| Instruction | | OpCode | RegDest | Jump | Branch | MemRead | MemtoReq | MemWrite | AluSrc | RegWrite | AluCtrl | Hexadeciamal |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| add | rd rs rt | 0000 | 0 | 0 | 0 | 0 | 0 | 0 | 010 | 1 | 010 | 002a |
| sub | rd rs rt | 0001 | 0 | 0 | 0 | 0 | 0 | 0 | 010 | 1 | 110 | 002e |
| lw | rd rs rt | 0010 | 0 | 0 | 0 | 1 | 1 | 0 | 010 | 1 | 010 | 032a |
| and | rd rs rt | 0100 | 0 | 0 | 0 | 0 | 0 | 0 | 010 | 1 | 000 | 0028 |
| or | rd rs rt | 0101 | 0 | 0 | 0 | 0 | 0 | 0 | 010 | 1 | 001 | 0029 |
| addi | rd imm 8 | 1000 | 1 | 0 | 0 | 0 | 0 | 0 | 100 | 1 | 010 | 104a |
| sw | rd rs rt | 0011 | 0 | 0 | 0 | 0 | 0 | 1 | 010 | 0 | 010 | 00a2 |
| j | imm 12 | 1111 | 0 | 1 | 0 | 0 | 0 | 0 | 100 | 0 | 010 | 0842 |
| not | rd rs rt | 0110 | 0 | 0 | 0 | 0 | 0 | 0 | 010 | 1 | 011 | 002b |
| ldhi | rd imm 8 | 1001 | 1 | 0 | 1 | 0 | 0 | 0 | 100 | 0 | 100 | 1444 |
| bz | rd imm 8 | 1010 | 1 | 0 | 1 | 0 | 0 | 0 | 100 | 0 | 010 | 1442 |
| bnz | rd imm 8 | 1011 | 1 | 0 | 0 | 0 | 0 | 0 | 100 | 0 | 010 | 1442 |
| jal | rd imm 8 | 1101 | 1 | 1 | 0 | 0 | 0 | 0 | 100 | 0 | 010 | 1842 |
| jr | rd | 1110 | 1 | 1 | 0 | 0 | 0 | 0 | 000 | 0 | 010 | 1802 |
| | | | | | | | | | | | | |