Target Board	CPU	SD card interface	SDIO ?	SPI Implementation		
BTT_SKR_PRO	STM32F407ZG	hardware SPI	not available	HARD		
F103ZE_Pro	STM32F103ZE	SDIO	SDIO	SOFT		
STM32F407VETX_Blac	STM32F407VET	SDIO	SDIO	not needed		
nucleo-F746ZG	STM32F746ZG	SDIO & HARD SPI	SDIO	HARD		
nucleo_H753ZI	STM32H753ZI	SDIO & HARD SPI	SDIO	HARD		
nucleo-G0B1RE	STM32G0B1RE	HARD SPI & SOFT SPI	not supported	HARD & SOFT		
nucleo-G474RE	STM32G474RE	HARD SPI & SOFT SPI	not supported	HARD & SOFT		
Cohesion3D Remixed	LPC1769	HARD SPI & SOFT SPI	not available	HARD		
			lowest			
			load			
REPOSITORY	BRANCH	image size (HEX)	point	Target Board	notes	
bootloader_SPI	SPI_soft_F103_based-on-fatfs-smt32-master	3FD0	8004000	STM32F103 (F103ZE_Pro)		
bootloader_SPI	SPI_F407ZG_based-on-fatfs-stm32-master	3D60	8004000	STM32F407ZG BTT SKR PRO		
bootloader_SPI	SPI_F746_STM32cubeIDE	72A0	8008000	nucleo_STM32F746ZG	has exFAT support	
bootloader_SPI	SPI_H753ZI_STM32cubeIDE	8CF0	8020000	nucleo_H753ZI	has exFAT support	
bootloader_SPI	SPI_G0B1_based-on-fatfs-stm32-master	47F8	8005000	nucleo_G0B1RE	HARD/SOFT SPI selectable	
bootloader_SPI	SPI_G474_based-on-fatfs-stm32-master	4EF8	8005000	nucleo-G474RE	HARD/SOFT SPI selectable	
bootloader_SPI	SPI_LPC1769_based-on-fatfs-stm32-master	5E1C	0006000	LPC1769	Marlin default load is at 400	00 so tl
bootloader_SDIO	SDIO_STM32F103ZE_Black	56E4	8005800	STM32F103 (F103ZE_Pro)		
bootloader_SDIO	SDIO_F407VE_fatfs-stm32-master	3E54	8004000	STM32F407VETX_Black		
bootloader_SDIO	SDIO_F746_STM32cube	7C70	8008000	nucleo_STM32F746ZG	has exFAT support	
bootloader_SDIO	SDIO_F103_based-on-fatfs-smt32-master	3FA8	8004000	STM32F103 (F103ZE_Pro)		
bootloader_SDIO	SDIO_H753ZI_STM32cubeIDE	9A18	8020000	nucleo_H753ZI	has exFAT support	