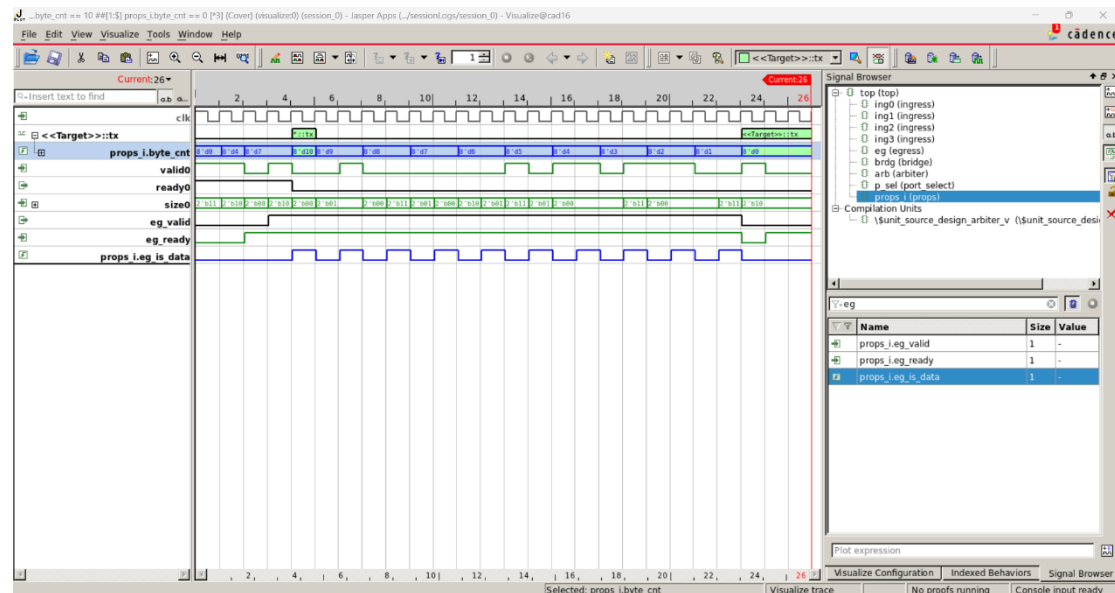
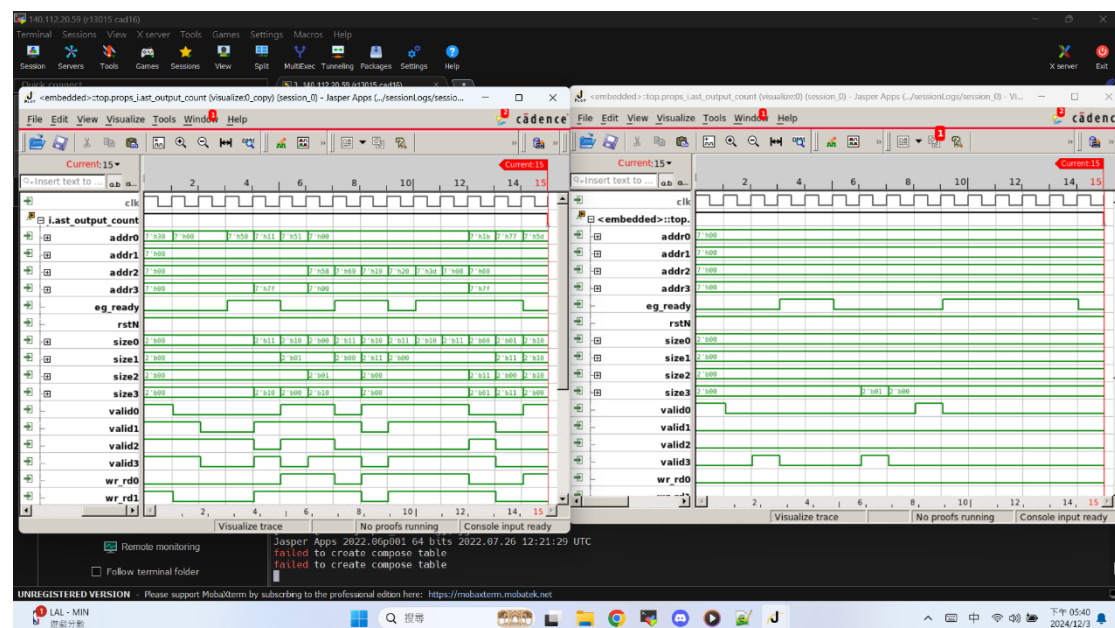


## Lab 8 r13943015 張根齊

1. Take a screenshot of the waveform from Visualize after running the visualize command. (You can select the useful signals on the right-hand side to inspect the byte\_cnt behavior further, as shown in the following figure)



2. Take a screenshot of the comparison between non-quiet (left) and quiet (right) visualize windows.



3. Take a screenshot of the property table after disabling reads and running a formal proof.

The screenshot shows the Cadence Formal Property Verifier interface. The main window displays the 'Property Table' with a list of properties and their verification results. The 'Design Hierarchy' pane on the left shows the project structure. The 'Coverage' pane at the bottom shows the summary of coverage results.

**Property Table:**

Type	Name	Engine	Bound	Time	Task	Traces	Source
Assert	top.props.last_output_count	B	21	28.4	<embedded>	0	Analysis Session
Cover (related)	top.props.last_output_count precondition1	N	1	0.1	<embedded>	1	Analysis Session
Assume	asm_no_read_0	?		0.0	<embedded>	0	Analysis Session
Assume	asm_no_read_1	?		0.0	<embedded>	0	Analysis Session
Assume	asm_no_read_2	?		0.0	<embedded>	0	Analysis Session
Assume	asm_no_read_3	?		0.0	<embedded>	0	Analysis Session
Assume	asm_no_size0_4	?		0.0	<embedded>	0	Analysis Session
Assume	asm_no_size1_4	?		0.0	<embedded>	0	Analysis Session
Assume	asm_no_size2_4	?		0.0	<embedded>	0	Analysis Session
Assume	asm_no_size3_4	?		0.0	<embedded>	0	Analysis Session

**Coverage Summary:**

- undetermined : 1 (100%)
- unknown : 0 (0%)
- error : 0 (0%)
- covers : 1
- unreachable : 0 (0%)
- bounded unreachable (user) : 0 (0%)
- covered : 1 (100%)
- ar: covered : 0 (0%)
- undetermined : 0 (0%)
- unknown : 0 (0%)
- error : 0 (0%)

The bottom status bar indicates 'No proofs running' and 'Console input ready'.