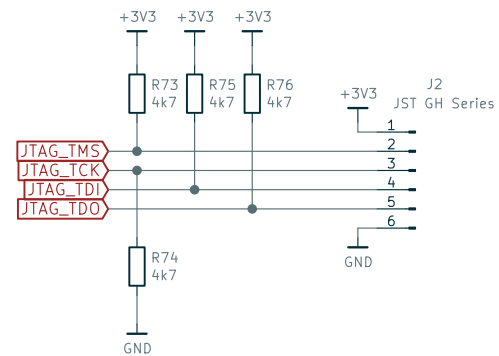
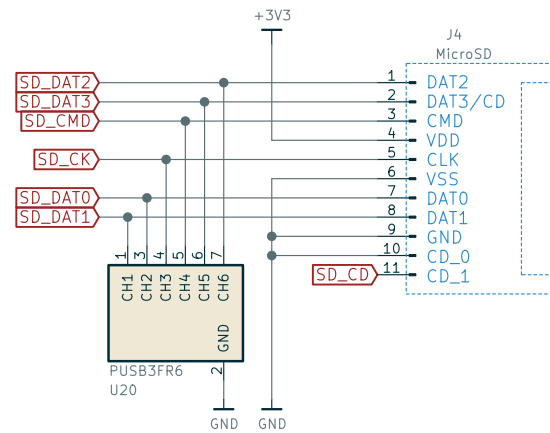
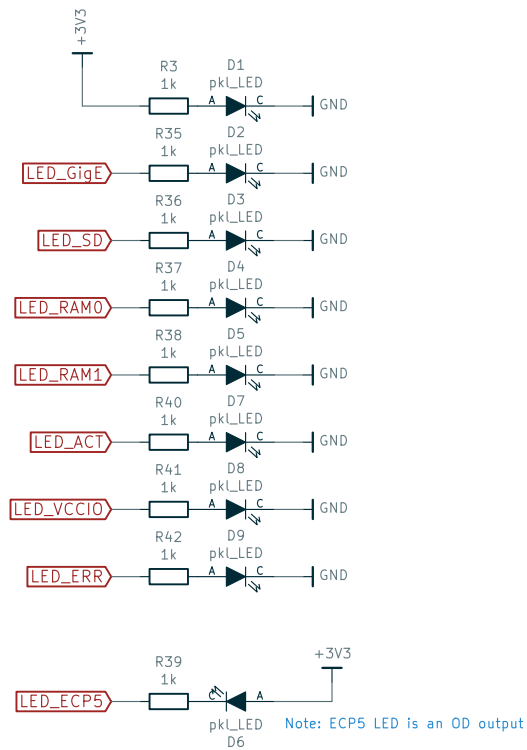


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Size: A4	Date: 2019-06-14	Rev: r0.2
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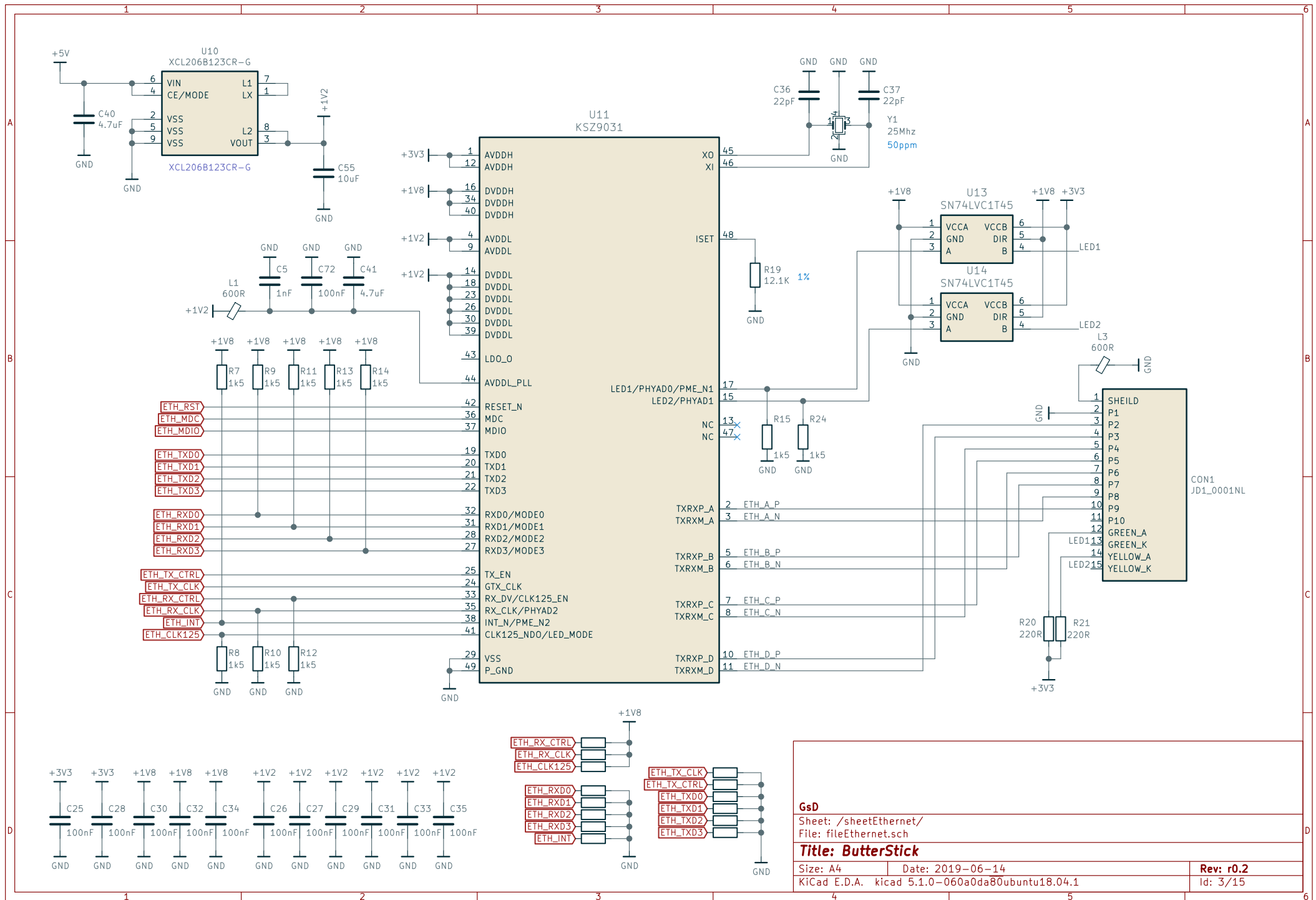
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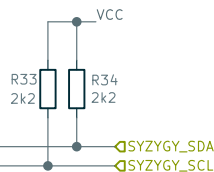
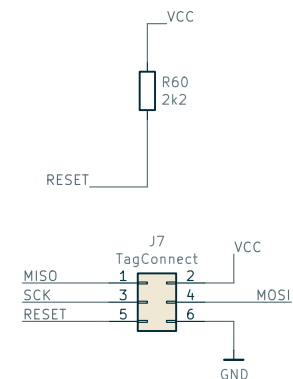
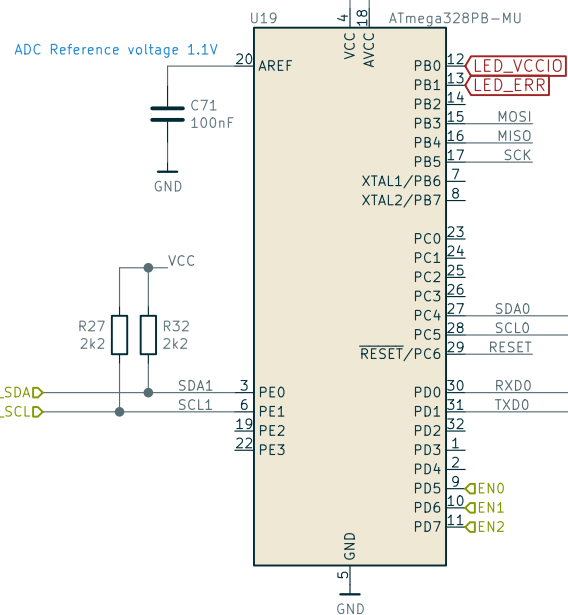
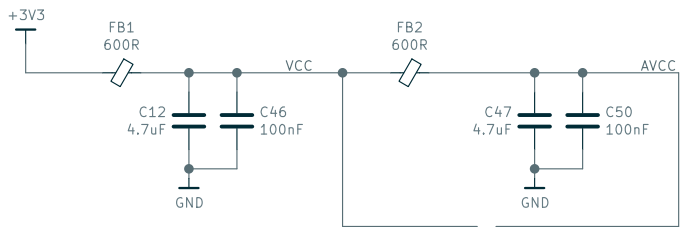
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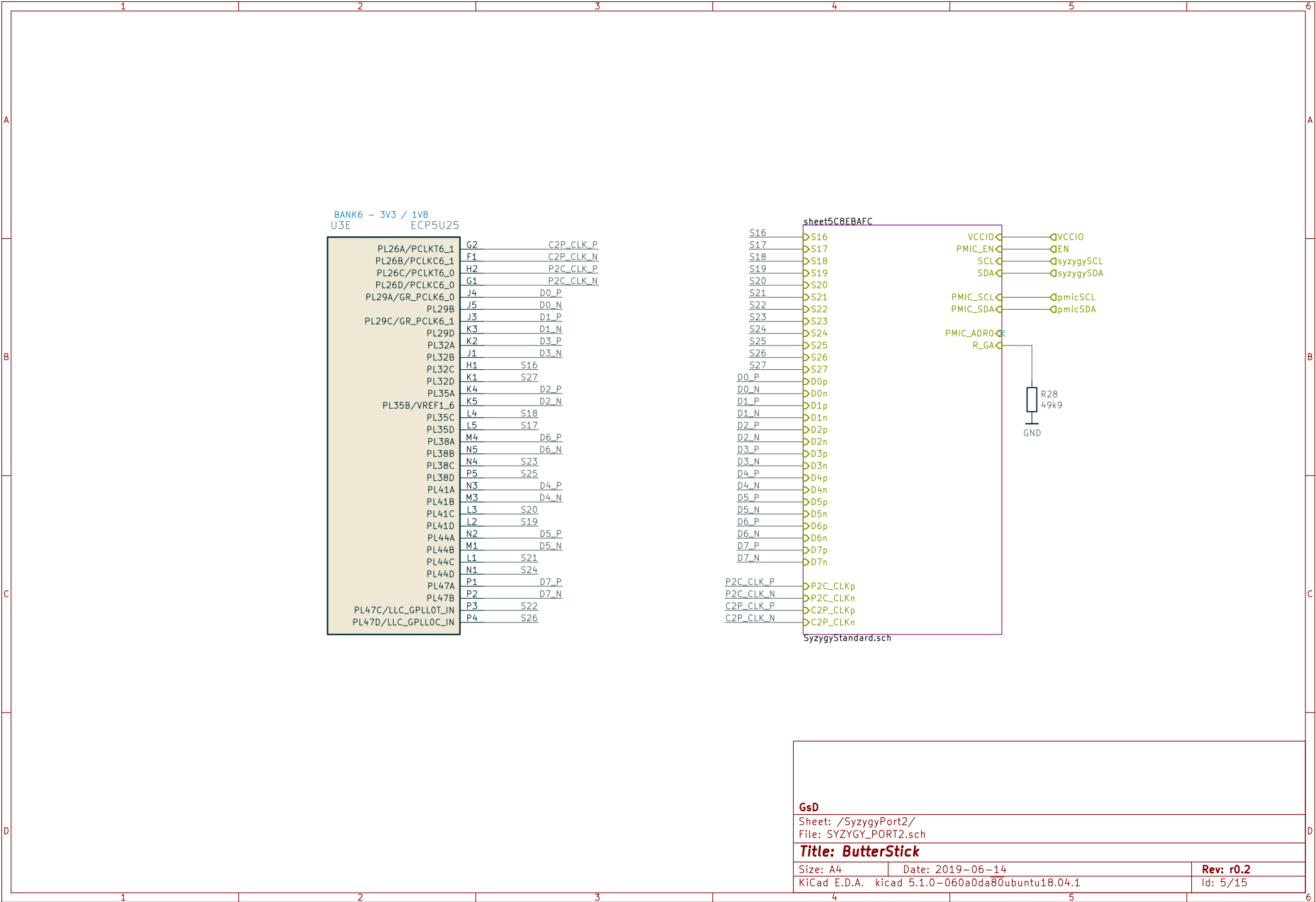
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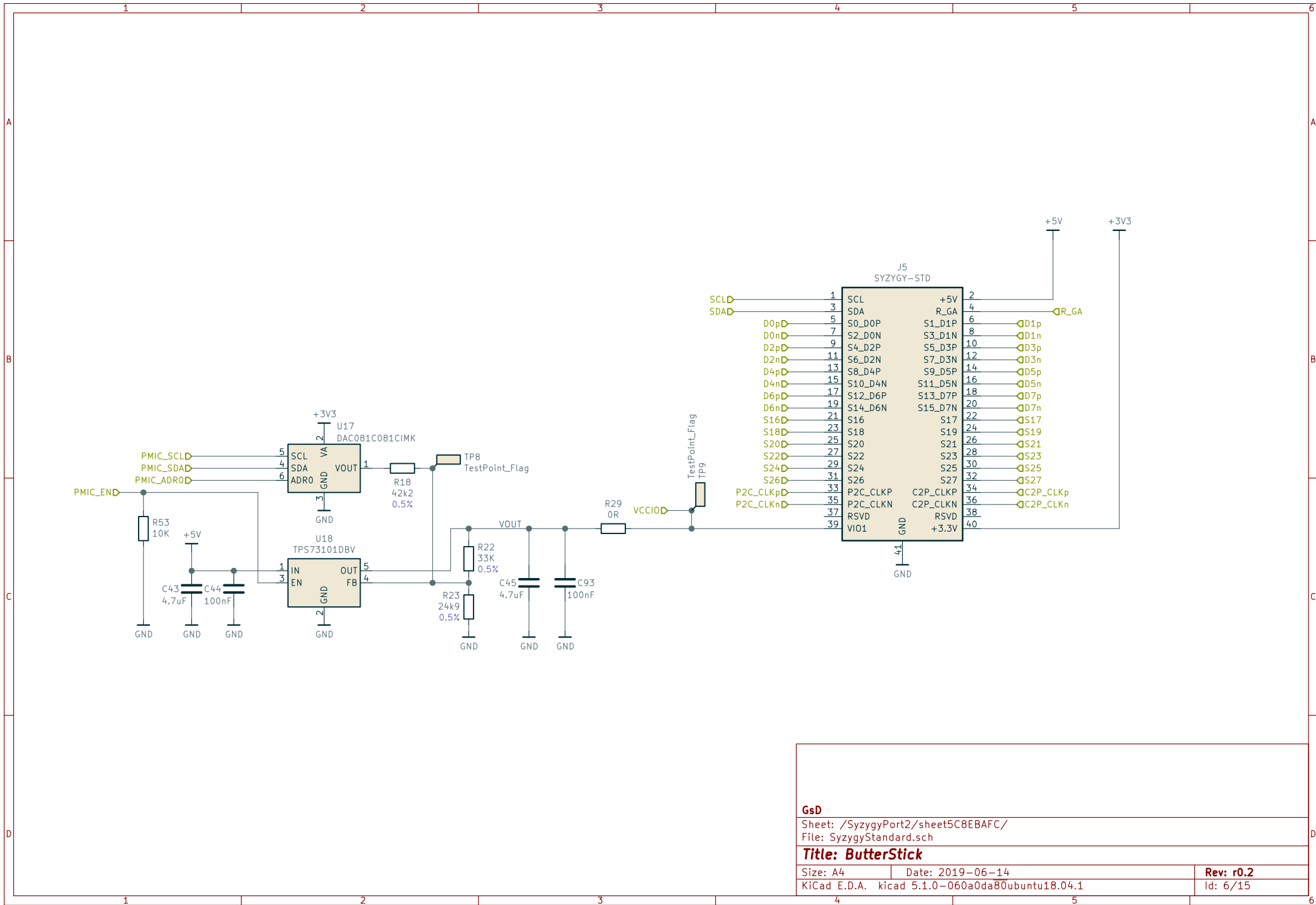
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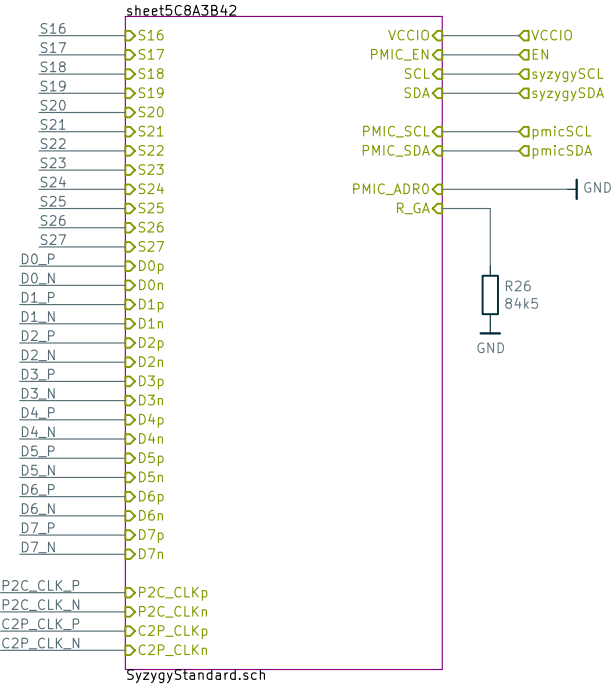
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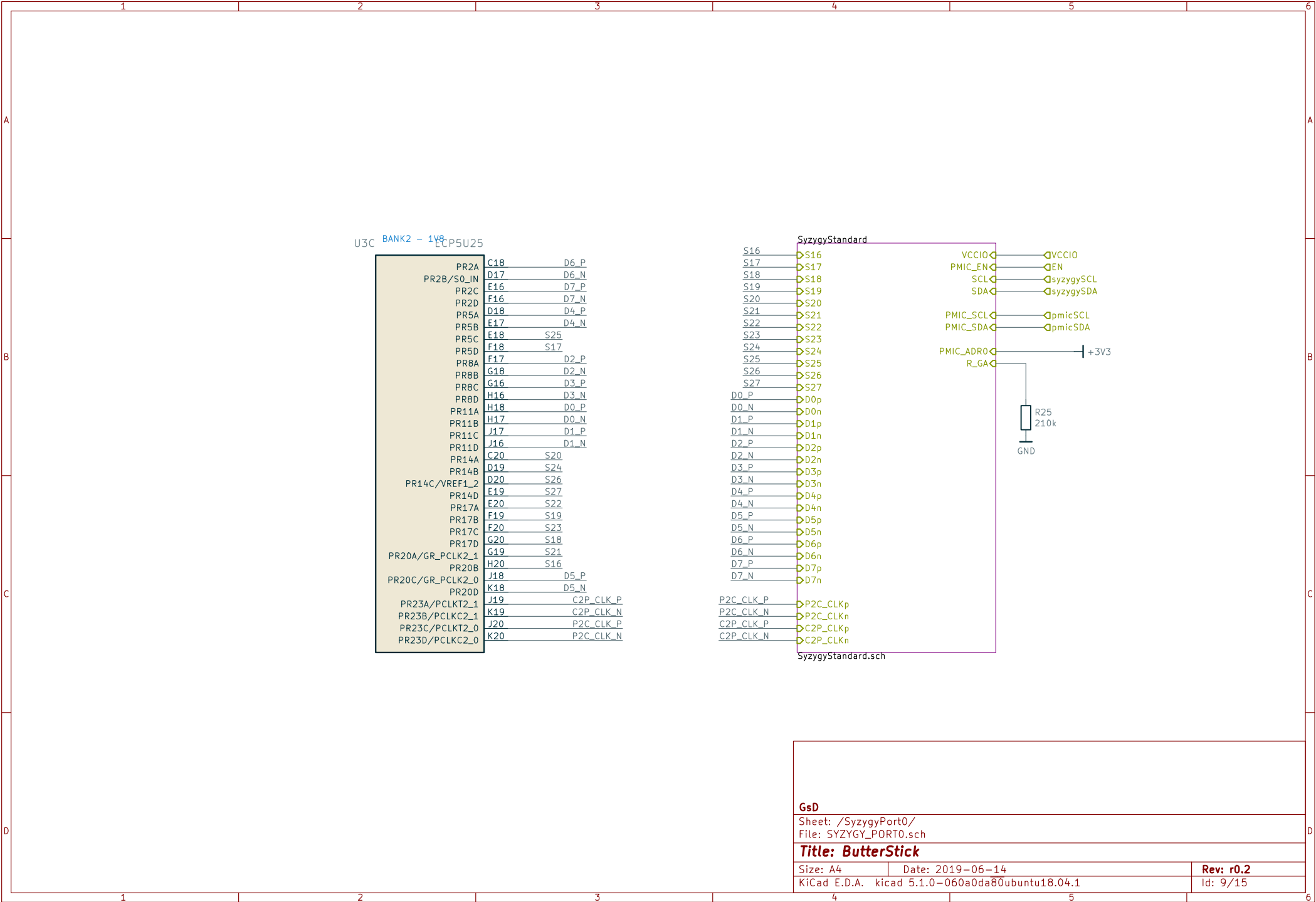
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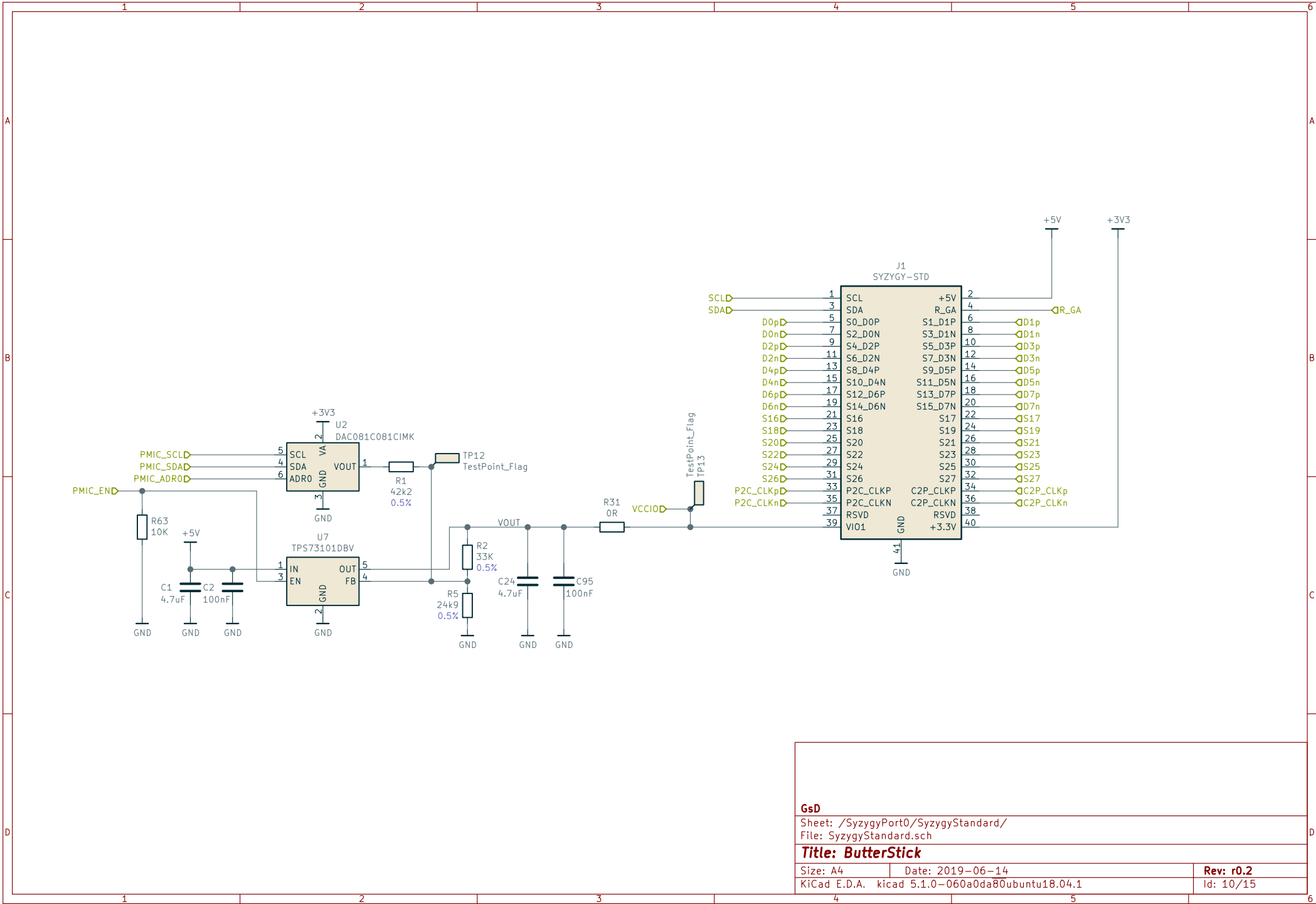
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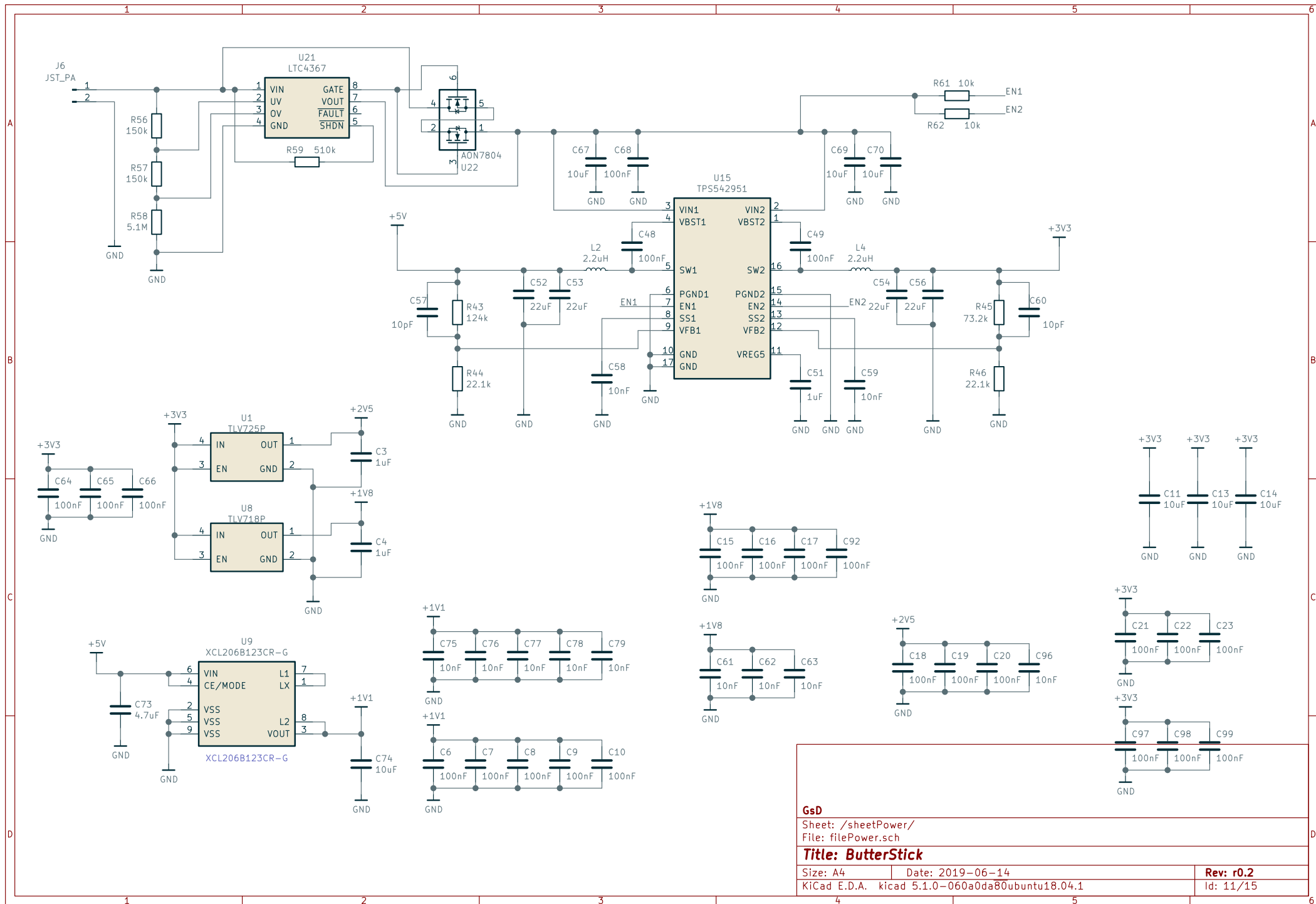
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Rev: r0.2

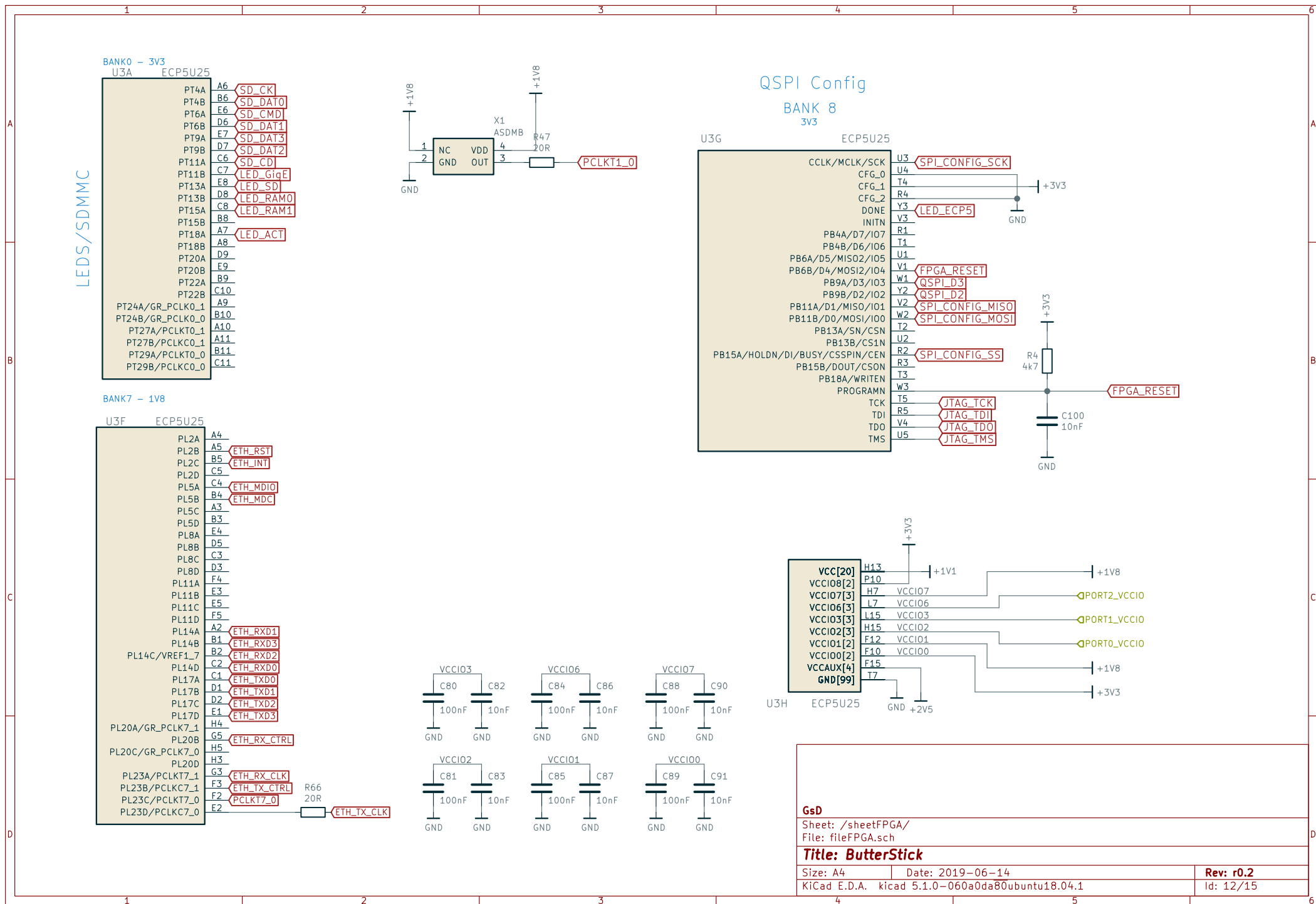
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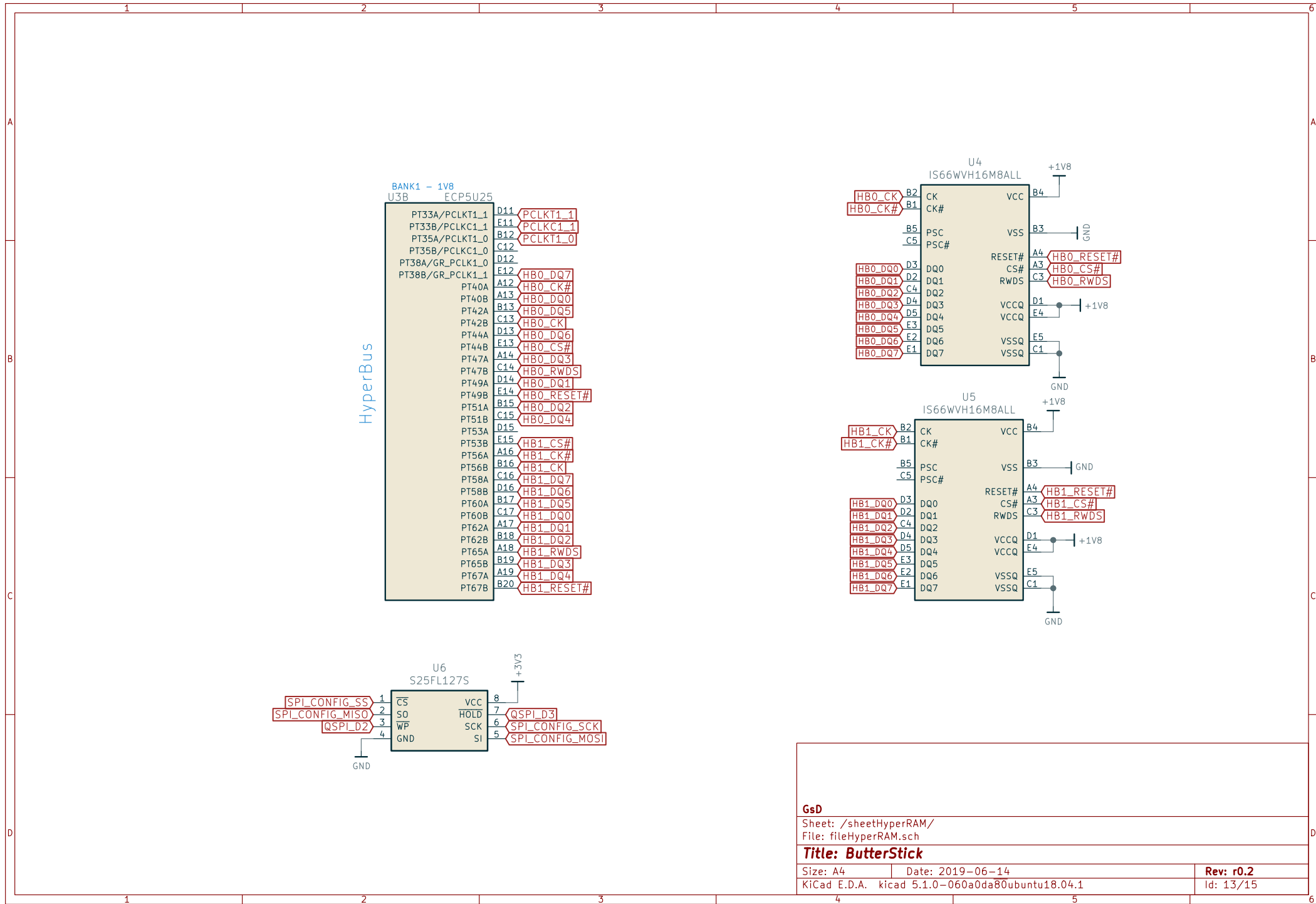


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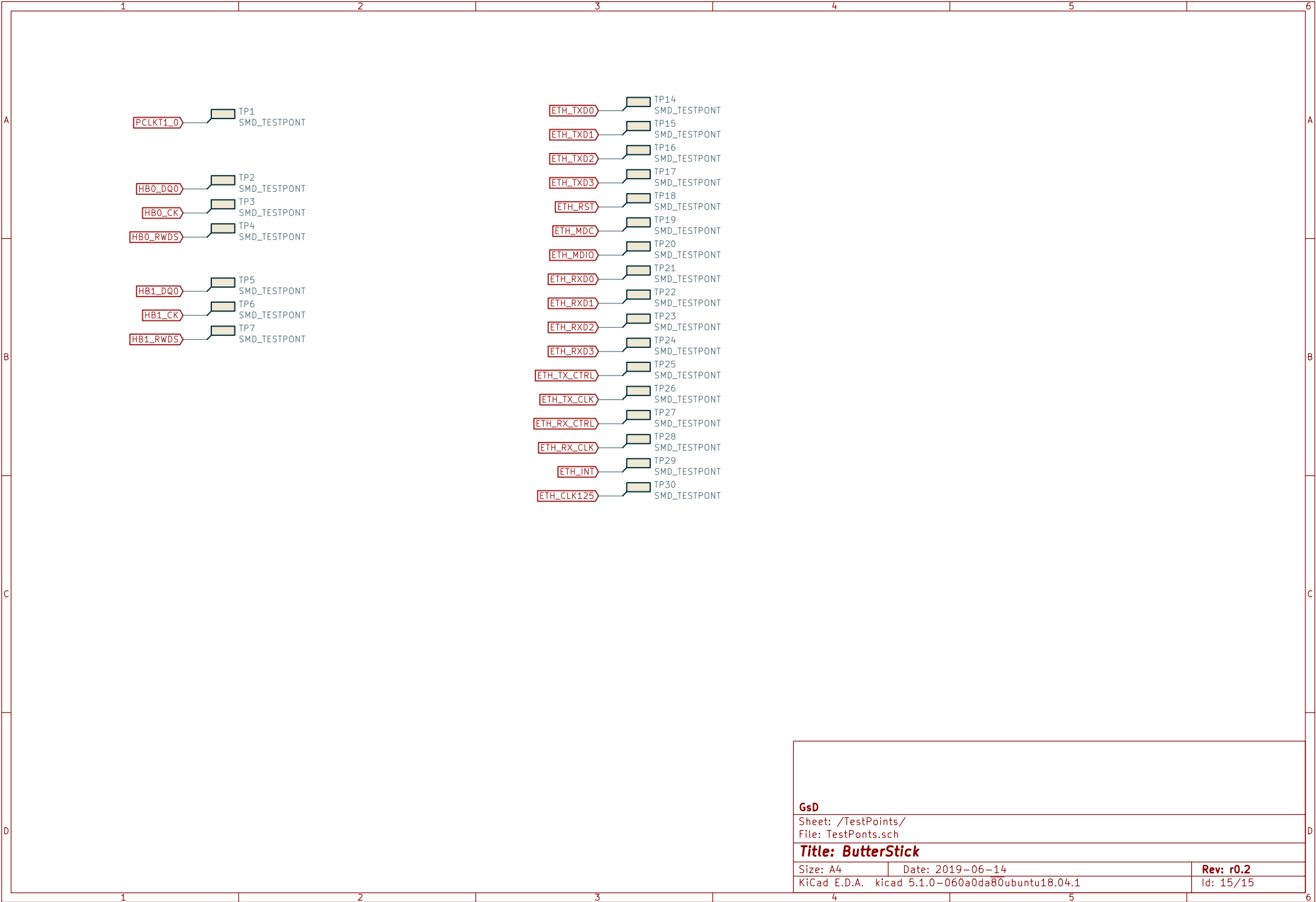
Rev: r0.2

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Technical Specification:

Number of Layers: 6
Base Material Generic FR4.
Finished Board Thickness: 62 mil +/- 10%
Apply White silkscreen ledgend top & bottom pads.
Layer Order shown in table.
Green Soldermask on top and bottom sides of board
Surface Finish: ENIG
Impedance control, values shown in table

Stackup		Single Ended Impedance		Differential Impedance		
Layer Name	Thickness	Trace Width (mil)	Impedance (Ohms)	Trace Width (mil)	Design Space (mil)	Impedance (Ohms)
TOP	1/2 oz + plating	4.0	50 +/- 10%	3.95	3.95	100 +/- 10%
prepreg	4 mil					
L2_GND	1/2 oz					
core	4 mil					
L3_SIG	1/2 oz	4.0	50 +/- 10%	3.95	3.95	100 +/- 10%
prepreg						
L4_PWR	1/2 oz					
core	4 mil					
L5_GND	1/2 oz					
prepreg	4 mil					
BOTTOM	1/2 oz + plating	4.0	50 +/- 10%			



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Sheet: /TestPoints/		
File: TestPonts.sch		
Title: ButterStick		
Size: A4	Date: 2019-06-14	Rev: r0.2
KiCad E.D.A. kicad 5.1.0-060a0da80ubuntu18.04.1		Id: 15/15