



PiRa-Smart

Power in Responsive Applications with BLE and RTC sleep mode.
Designed as a Raspberry Pi Zero HAT.

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PAGE2 - POWER, RTC, RPI

Version Revision:

v0.1 - 23.03.2018.
v0.2 - 11.05.2018.
v2.0 - 19.11.2018.
v2.1 - 26.03.2019.

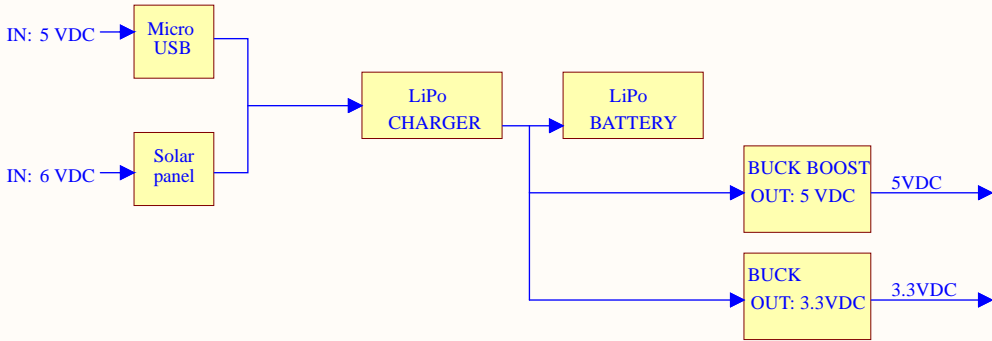
DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational
design notes.

DESIGN NOTE:
Example text for critical
design notes.

LAYOUT NOTE:
Example text for critical
layout guidelines.

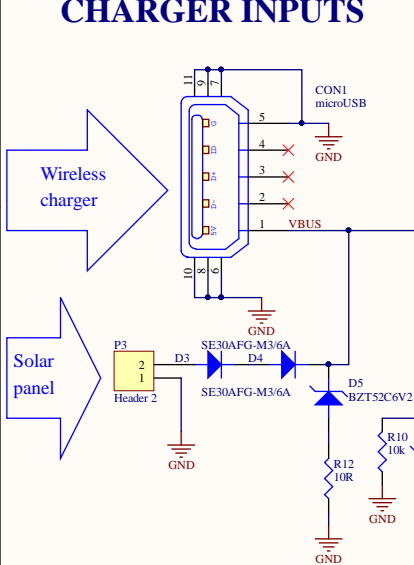
POWER DIAGRAM



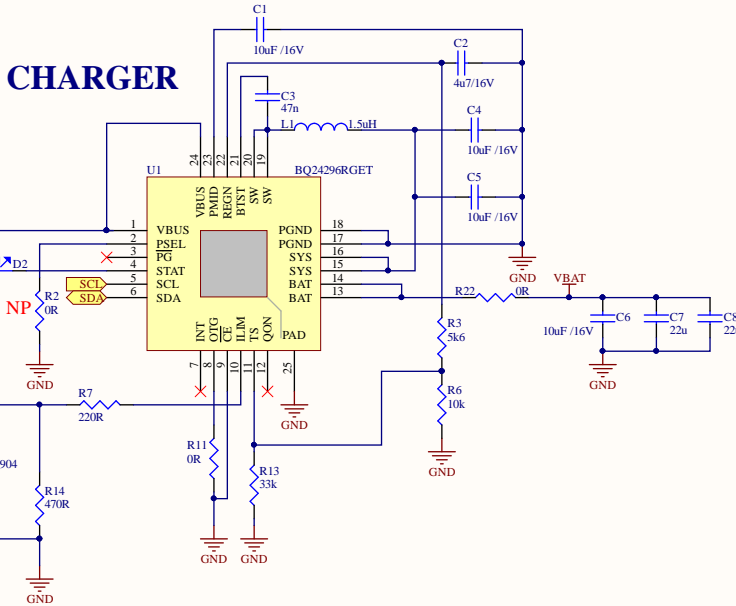
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Title: pira-smart-PCB.PrjPcb			
Page Contents: PAGE1-CONTENTS.SchDoc			
Size:	DWG NO		Revision: v2_1
Date: *	Sheet 1 of 2		

POWER SHEET

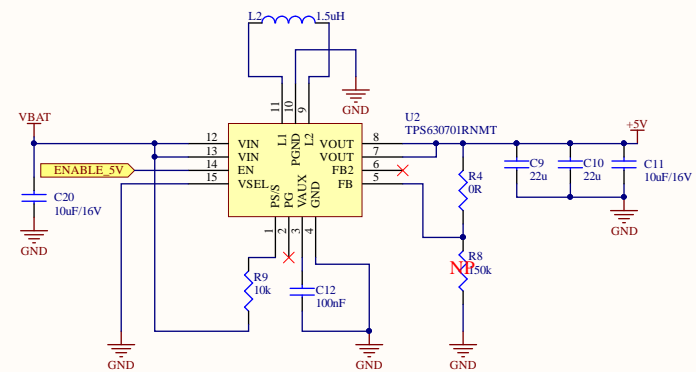
CHARGER INPUTS



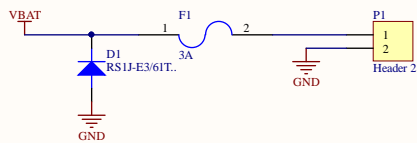
CHARGER



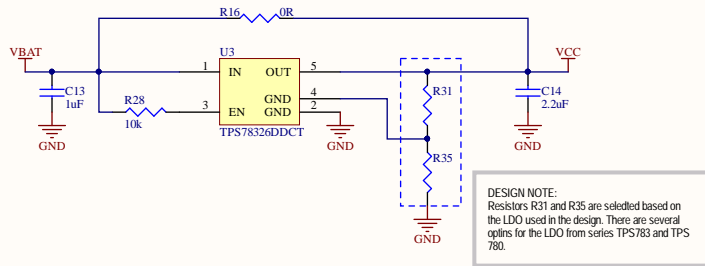
BOOST CONVERTER



BATTERY CONNECTOR



LDO REGULATOR



DESIGN NOTE:
Resistors R31 and R35 are selected based on the LDO used in the design. There are several options for the LDO from series TPS783 and TPS780.

