2022 Digital IC Design

Final Project

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| NAME | |  | | | | |
| Student ID | |  | | | | |
| **Functional Simulation Result of LZ77 Encoder** | | | | | | |
| Testing Pattern 0 | Pass or Fail | | Testing Pattern 1 | Pass or Fail | Testing Pattern 2 | Pass or Fail |
| your pre-sim result of test patterns | | | | | | |
| **Functional Simulation Result of LZ77 Decoder** | | | | | | |
| Testing Pattern 0 | Pass or Fail | | Testing Pattern 1 | Pass or Fail | Testing Pattern 2 | Pass or Fail |
| your pre-sim result of test patterns | | | | | | |
| **Quality of Interpolated Results** | | | | | | |
| Testing Pattern 0 | PSNR | | Testing Pattern 1 | PSNR | Testing Pattern 2 | PSNR |
| **Description of your design** | | | | | | |
|  | | | | | | |

*Scoring = Pattern 0 PSNR + Pattern 1 PSNR + Pattern 2 PSNR*

*The higher, the better.*