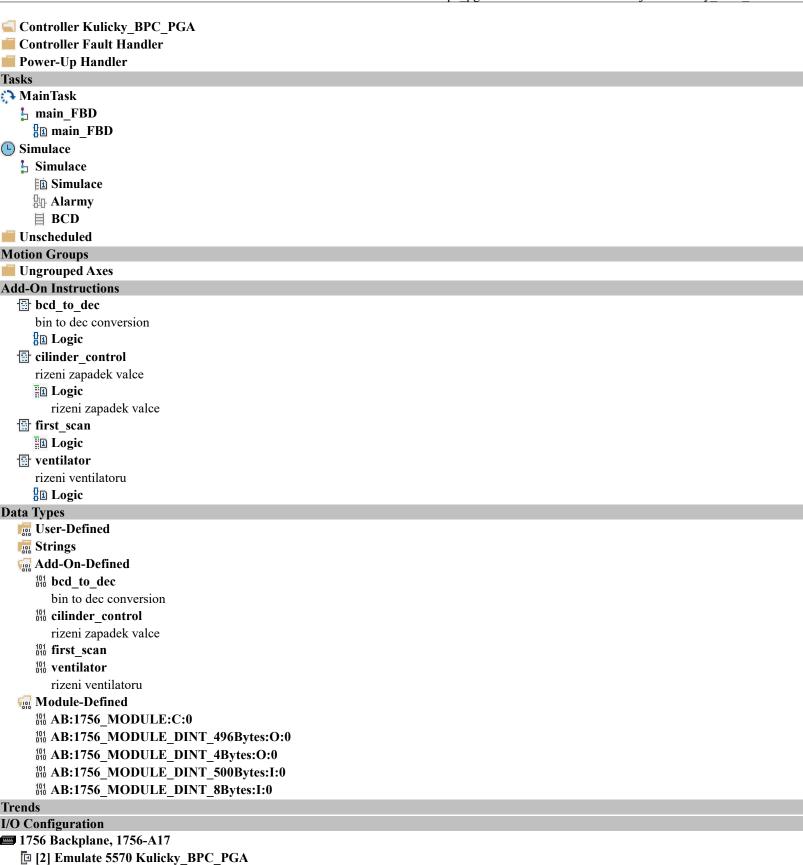
[] [3] 1756-MODULE SIM_I_O



C:\Users\bpc_pga\Documents\Studio 5000\Projects\Kulicky_BPC_PGA.ACD

AlarmH	Value	Data Type	Scope
	0	BOOL	Simulace
AliasFor:	Alarmy_SIM.1		
Base Tag:	Alarmy_SIM.1		
Constant	No		
External Access:	Read/Write		
AlarmH - Simulace/Alarmy	- *Unknown Protection(Unknown P	rotection), *Unknown Protection(Unkn	nown Protection)
AlarmH1	0	BOOL	Kulicky_BPC_PGA
Signalizace, že No Balls1 j	je větší než 9		·
AliasFor:	Alarms.1		
Base Tag:	Alarms.1		
Constant	No		
External Access:	Read/Write		
AlarmH1 - main_FBD/main 2-B4(IREF,AlarmH1)	n_FBD - *2-C2(OREF,AlarmH1), 2- ace - *Unknown Protection(Unknown	B2(bcd_to_dec,bcd_to_dec_01.alarm n Protection)	H), 2-B3(BOR,BOR_04.In4),
AlarmH2	0	BOOL	Kulicky BPC_PGA
Signalizace, že No Balls2 j			7 — —
AliasFor:	Alarms.3		
Base Tag:	Alarms.3		
Constant	No		
External Access:	Read/Write		
AlarmH2 - main_FBD/main 2-D2(bcd_to_dec,bcd_to_d	n_FBD - *2-E2(OREF,AlarmH2), 2-1	B3(BOR,BOR_04.In5), 2-B4(IREF,Alar	rmH2),
	`	,	
AlarmH3	0	BOOL	Kulicky_BPC_PGA
Signalizace, že No_Balls3 j			
AliasFor:	Alarms.5		
Base Tag:	Alarms.5		
Constant	No		
External Access: AlarmH3 - main_FBD/main 2-F2(bcd to dec,bcd to dec.bcd)		B3(BOR,BOR_04.In6), 2-B4(IREF,Alan	rmH3),
	ace - *Unknown Protection(Unknown	n Protection)	
	0	BOOL	Simulace
AlarmI		BOOL	Simulace
AliasFor			
AliasFor:	Alarmy_SIM.0		
AliasFor: Base Tag:	Alarmy_SIM.0		
AliasFor: Base Tag: Constant	Alarmy_SIM.0 No		
AliasFor: Base Tag: Constant External Access:	Alarmy_SIM.0 No Read/Write	rotection), *Unknown Protection(Unkn	own Protection)
AliasFor: Base Tag: Constant External Access: AlarmL - Simulace/Alarmy	Alarmy_SIM.0 No Read/Write - *Unknown Protection(Unknown Protection)		
AliasFor: Base Tag: Constant External Access: AlarmL - Simulace/Alarmy AlarmL1	Alarmy_SIM.0 No Read/Write - *Unknown Protection(Unknown Protection)	rotection), *Unknown Protection(Unkno BOOL	own Protection) Kulicky_BPC_PGA
AliasFor: Base Tag: Constant External Access: AlarmL - Simulace/Alarmy AlarmL1 Signalizace, že No_Balls1 j	Alarmy_SIM.0 No Read/Write - *Unknown Protection(Unknown Protection)		
AliasFor: Base Tag: Constant External Access: AlarmL - Simulace/Alarmy AlarmL1 Signalizace, že No_Balls1 j AliasFor:	Alarmy_SIM.0 No Read/Write - *Unknown Protection(Unknown Protection) 0 je menší než 0 Alarms.0		
AliasFor: Base Tag: Constant External Access: AlarmL - Simulace/Alarmy AlarmL1 Signalizace, že No_Balls1 j AliasFor: Base Tag:	Alarmy_SIM.0 No Read/Write - *Unknown Protection(Unknown Protection) 0 je menší než 0 Alarms.0 Alarms.0		
AliasFor: Base Tag: Constant External Access: AlarmL - Simulace/Alarmy AlarmL1 Signalizace, že No_Balls1 j AliasFor: Base Tag: Constant	Alarmy_SIM.0 No Read/Write - *Unknown Protection(Unknown Protection) je menší než 0 Alarms.0 Alarms.0 No		
AliasFor: Base Tag: Constant External Access: AlarmL - Simulace/Alarmy AlarmL1 Signalizace, že No_Balls1 j AliasFor: Base Tag: Constant External Access: AlarmL1 - main_FBD/main 2-B4(IREF,AlarmL1)	Alarmy_SIM.0 No Read/Write - *Unknown Protection(Unknown Protection) 0 je menší než 0 Alarms.0 Alarms.0 No Read/Write m_FBD - *2-C2(OREF,AlarmL1), 2-B	BOOL B2(bcd_to_dec,bcd_to_dec_01.alarm_L	Kulicky_BPC_PGA
AliasFor: Base Tag: Constant External Access: AlarmL - Simulace/Alarmy AlarmL1 Signalizace, že No_Balls1 j AliasFor: Base Tag: Constant External Access: AlarmL1 - main_FBD/main 2-B4(IREF,AlarmL1)	Alarmy_SIM.0 No Read/Write - *Unknown Protection(Unknown Protection) je menší než 0 Alarms.0 Alarms.0 No Read/Write	BOOL B2(bcd_to_dec,bcd_to_dec_01.alarm_L	Kulicky_BPC_PGA
AliasFor: Base Tag: Constant External Access: AlarmL - Simulace/Alarmy AlarmL1 Signalizace, že No_Balls1 j AliasFor: Base Tag: Constant External Access: AlarmL1 - main_FBD/main 2-B4(IREF,AlarmL1) AlarmL1 - Simulace/Simula	Alarmy_SIM.0 No Read/Write - *Unknown Protection(Unknown Protection(Unknown Protection) je menší než 0 Alarms.0 Alarms.0 No Read/Write n_FBD - *2-C2(OREF,AlarmL1), 2-Bace - *Unknown Protection(Unknown)	BOOL B2(bcd_to_dec,bcd_to_dec_01.alarm_L	Kulicky_BPC_PGA
AliasFor: Base Tag: Constant External Access: AlarmL - Simulace/Alarmy AlarmL1 Signalizace, že No_Balls1 j AliasFor: Base Tag: Constant External Access: AlarmL1 - main_FBD/main 2-B4(IREF,AlarmL1) AlarmL1 - Simulace/Simula AlarmL2 Signalizace, že No_Balls2 j	Alarmy_SIM.0 No Read/Write - *Unknown Protection(Unknown Protection(Un	BOOL 32(bcd_to_dec,bcd_to_dec_01.alarm_L 1 Protection)	Kulicky_BPC_PGA L), 2-B3(BOR,BOR_04.In1),
AliasFor: Base Tag: Constant External Access: AlarmL - Simulace/Alarmy AlarmL1 Signalizace, že No_Balls1 j AliasFor: Base Tag: Constant External Access: AlarmL1 - main_FBD/main 2-B4(IREF,AlarmL1) AlarmL1 - Simulace/Simula	Alarmy_SIM.0 No Read/Write - *Unknown Protection(Unknown Protection(Unknown Protection) je menší než 0 Alarms.0 Alarms.0 No Read/Write n_FBD - *2-C2(OREF,AlarmL1), 2-Bace - *Unknown Protection(Unknown)	BOOL 32(bcd_to_dec,bcd_to_dec_01.alarm_L 1 Protection)	Kulicky_BPC_PGA L), 2-B3(BOR,BOR_04.In1),
AliasFor: Base Tag: Constant External Access: AlarmL - Simulace/Alarmy AlarmL1 Signalizace, že No_Balls1 j AliasFor: Base Tag: Constant External Access: AlarmL1 - main_FBD/main 2-B4(IREF,AlarmL1) AlarmL1 - Simulace/Simula AlarmL2 Signalizace, že No_Balls2 j	Alarmy_SIM.0 No Read/Write - *Unknown Protection(Unknown Protection(Un	BOOL 32(bcd_to_dec,bcd_to_dec_01.alarm_L 1 Protection)	Kulicky_BPC_PGA L), 2-B3(BOR,BOR_04.In1),
AliasFor: Base Tag: Constant External Access: AlarmL - Simulace/Alarmy AlarmL1 Signalizace, že No_Balls1 j AliasFor: Base Tag: Constant External Access: AlarmL1 - main_FBD/main 2-B4(IREF,AlarmL1) AlarmL1 - Simulace/Simula AlarmL2 Signalizace, že No_Balls2 j AliasFor:	Alarmy_SIM.0 No Read/Write - *Unknown Protection(Unknown Protection(Un	BOOL 32(bcd_to_dec,bcd_to_dec_01.alarm_L 1 Protection)	Kulicky_BPC_PGA L), 2-B3(BOR,BOR_04.In1),
AliasFor: Base Tag: Constant External Access: AlarmL - Simulace/Alarmy AlarmL1 Signalizace, že No_Balls1 j AliasFor: Base Tag: Constant External Access: AlarmL1 - main_FBD/main 2-B4(IREF,AlarmL1) AlarmL1 - Simulace/Simula AlarmL2 Signalizace, že No_Balls2 j AliasFor: Base Tag:	Alarmy_SIM.0 No Read/Write - *Unknown Protection(Unknown Protection(Un	BOOL 32(bcd_to_dec,bcd_to_dec_01.alarm_L 1 Protection)	Kulicky_BPC_PGA L), 2-B3(BOR,BOR_04.In1),

Kulicky_BPC_PGA (Controller)

BOOL AlarmL3 0 Kulicky BPC PGA Signalizace, že No Balls3 je menší než 0 AliasFor: Base Tag: Alarms.4 Constant No **External Access:** Read/Write AlarmL3 - main FBD/main FBD - *2-G2(OREF, AlarmL3), 2-B3(BOR, BOR, 04.In3), 2-B4(IREF, AlarmL3), 2-F2(bcd to dec,bcd to dec 03.alarm L) AlarmL3 - Simulace/Simulace - *Unknown Protection(Unknown Protection) Alarms DINT Kulicky_BPC_PGA Alarmy BCD Constant No Read/Write External Access: **BOOL** Alarms.0 Alarmy BCD AlarmL1 - main FBD/main FBD - *2-C2(OREF, AlarmL1), 2-B2(bcd to dec, bcd to dec 01.alarm L), 2-B3(BOR, BOR 04.In1), 2-B4(IREF,AlarmL1) AlarmL1 - Simulace/Simulace - *Unknown Protection(Unknown Protection) Alarms.1 **BOOL** Alarmy BCD AlarmH1 - main FBD/main FBD - *2-C2(OREF, AlarmH1), 2-B2(bcd to dec, bcd to dec 01.alarm H), 2-B3(BOR, BOR 04.In4), 2-B4(IREF,AlarmH1) AlarmH1 - Simulace/Simulace - *Unknown Protection(Unknown Protection) Alarms.2 BOOL Alarmy BCD AlarmL2 - main FBD/main FBD - *2-E2(OREF,AlarmL2), 2-B3(BOR,BOR 04.In2), 2-B4(IREF,AlarmL2), 2-D2(bcd to dec,bcd to dec 02.alarm L) AlarmL2 - Simulace/Simulace - *Unknown Protection(Unknown Protection) Alarms.3 **BOOL** Alarmy BCD AlarmH2 - main FBD/main FBD - *2-E2(OREF,AlarmH2), 2-B3(BOR,BOR 04.In5), 2-B4(IREF,AlarmH2), 2-D2(bcd to dec,bcd to dec 02.alarm H) AlarmH2 - Simulace/Simulace - *Unknown Protection(Unknown Protection) Alarms.4 **BOOL** Alarmy BCD AlarmL3 - main FBD/main FBD - *2-G2(OREF,AlarmL3), 2-B3(BOR,BOR 04.In3), 2-B4(IREF,AlarmL3), 2-F2(bcd to dec,bcd to dec 03.alarm L) AlarmL3 - Simulace/Simulace - *Unknown Protection(Unknown Protection) Alarms.5 **BOOL** Alarmy BCD AlarmH3 - main FBD/main FBD - *2-G2(OREF,AlarmH3), 2-B3(BOR,BOR 04.In6), 2-B4(IREF,AlarmH3), 2-F2(bcd to dec,bcd to dec 03.alarm H) AlarmH3 - Simulace/Simulace - *Unknown Protection(Unknown Protection) 0 Simulace Alarmy SIM DINT Constant No Read/Write External Access: Alarmy SIM.0 BOOL. AlarmL - Simulace/Alarmy - *Unknown Protection(Unknown Protection), *Unknown Protection(Unknown Protection) Alarmy SIM.1 **BOOL** AlarmH - Simulace/Alarmy - *Unknown Protection(Unknown Protection), *Unknown Protection(Unknown Protection) **ALARM** ALM 01 Simulace Constant No **External Access:** Read/Write ALM 01 - Simulace/Alarmy - *Unknown Protection(Unknown Protection), *Unknown Protection(Unknown Protection), *Unknown Protection(Unknown Protection), *Unknown Protection(Unknown Protection), *Unknown Protection(Unknown P Protection(Unknown Protection) main_FBD BAND 01 FBD BOOLEAN AND Constant No Read/Write External Access:

BAND 01 (Continued)

BAND 01 - main FBD/main FBD - *1-B5(OREF, ZLED), *1-E2(cilinder control, cilinder control 01.ready), *1-E3(cilinder control,cilinder control 02.ready), *1-E5(cilinder control,cilinder control 03.ready), *1-G2(BAND,BAND 01), *1-G3(IREF,S4), *1-H2(BAND,BAND 03.In1)

FBD BOOLEAN AND BAND 02 main FBD

Constant No

Read/Write External Access:

BAND 02 - main FBD/main FBD - *1-A1(OSRI,OSRI 01.OutputBit), *1-B1(BNOT,BNOT 01.Out), *1-C1(BAND,BAND 02),

*I-E2(cilinder control,cilinder control 01.start), *1-E3(cilinder control,cilinder control 02.start),

*1-E5(cilinder control, cilinder control 03.start), *1-H2(BAND, BAND 03.In2)

BAND 03 FBD BOOLEAN AND main FBD

Constant No External Access: Read/Write

BAND 03 - main FBD/main FBD - *1-C1(BAND,BAND 02.Out), *1-E2(cilinder control,cilinder control 01.activate),

*1-E3(cilinder control,cilinder control 02.activate), *1-E5(cilinder control,cilinder control 03.activate), *1-G2(BAND,BAND 01.Out),

*1-H2(BAND,BAND 03)

0 **BCD** DINT Simulace

No Constant

Read/Write External Access:

BCD - Simulace/BCD - *Unknown Protection(Unknown Protection), Unknown Protection(Unknown Protection)

BCD L 0 0 **BOOL** Kulicky BPC PGA

BCD levý válec

AliasFor: BCDX.0 Base Tag: BCDX.0 Constant No

External Access: Read/Write

BCD L 0 - main FBD/main FBD - *2-B1(OREF,BCD L 0), 2-B1(IREF,IN BCD L0), 2-B2(bcd to dec,bcd to dec 01.BCD0),

2-B2(IREF,BCD L 0)

BCDX - Simulace/Simulace - *Unknown Protection(Unknown Protection), *Unknown Protection(Unknown Pr

Protection(Unknown Protection)

BCD_L_1 0 **BOOL** Kulicky BPC PGA

BCD levý válec

AliasFor: BCDX.1 Base Tag: BCDX.1 Constant No External Access: Read/Write

BCD L 1 - main FBD/main FBD - *2-B1(OREF,BCD L 1), 2-B1(IREF,IN BCD L1), 2-B2(bcd to dec,bcd to dec 01.BCD1), 2-B2(IREF,BCD L 1)

BCDX - Simulace/Simulace - *Unknown Protection(Unknown Protection), *Unknown Protection(Unknown Protection), *Unknown

Protection(Unknown Protection)

■ BCD L 2 0 **BOOL** Kulicky BPC PGA

BCD levý válec

AliasFor: BCDX.2 Base Tag: BCDX.2 Constant No

External Access: Read/Write

BCD L 2 - main FBD/main FBD - *2-B1(OREF,BCD L 2), 2-B1(IREF,IN BCD L2), 2-B2(bcd to dec,bcd to dec 01.BCD2),

2-B2(IREF,BCD L 2)

BCDX - Simulace/Simulace - *Unknown Protection(Unknown Protection), *Unknown Protection(Unknown Protection), *Unknown

Protection(Unknown Protection)

BOOL BCD L 3 1 Kulicky BPC PGA

BCD levý válec

AliasFor: BCDX.3 Base Tag: BCDX.3 Constant Read/Write External Access:

i

BCD_R_1
BCD pravý válec
AliasFor:

0

BCDX.9

iicky_bi c_i GA (Collifoliei) 	C:\Users\bpc_pga\Documents\St	udio 5000\Projects\Kulicky_BPC_PGA
2-B2(IREF,BCD_L_3)		L_3), 2-B1(IREF,IN_BCD_L3), 2-B2(bcd_to own Protection), *Unknown Protection(Un	
Protection(Unknown Prote		om Protection), Chimiom Protection(Ch	mioni Protectiony, Chimoni
BCD_M_0	0	BOOL	Kulicky_BPC_PGA
BCD střední válec	DCDW 4		
AliasFor:	BCDX.4		
Base Tag: Constant	BCDX.4 No		
External Access:	Read/Write		
BCD_M_0 - main_FBD/mc 2-D2(IREF,BCD_M_0)	ain_FBD - *2-D1(OREF,BCD_	M_0), 2-D1(IREF,IN_BCD_M0), 2-D2(bca own Protection), *Unknown Protection(Un	
Protection(Unknown Prote		own I rotection), Chimown I rotection(Chi	known i rotection), Onknown
BCD M 1	0	BOOL	Kulicky BPC PGA
BCD střední válec			v = _
AliasFor:	BCDX.5		
Base Tag:	BCDX.5		
Constant	No		
External Access:	Read/Write		
	ain_FBD - *2-D1(OREF,BCD_	(M_1) , 2-D1(IREF,IN_BCD_M1), 2-D2(bca	l_to_dec,bcd_to_dec_02.BCD1),
2-D2(IREF,BCD_M_1)			
BCDX - Simulace/Simulace Protection(Unknown Prote		own Protection), *Unknown Protection(Un	known Protection), *Unknown
BCD M 2	1	BOOL	Kulicky BPC PGA
BCD_M_2 BCD střední válec	1	BOOL	Runcky_B1 C_1 GA
AliasFor:	BCDX.6		
Base Tag:	BCDX.6		
Constant	No		
External Access:	Read/Write		
BCD_M_2 - main_FBD/ma 2-D2(IREF,BCD_M_2)	ain_FBD - *2-D1(OREF,BCD_	M_2), 2-D1(IREF,IN_BCD_M2), 2-D2(bca	!_to_dec,bcd_to_dec_02.BCD2),
BCDX - Simulace/Simulace		own Protection), *Unknown Protection(Un	known Protection), *Unknown
Protection(Unknown Prote	гспоп)		
BCD_M_3	0	BOOL	Kulicky_BPC_PGA
BCD střední válec	DCDV 7		
AliasFor:	BCDX.7 BCDX.7		
Base Tag: Constant	No		
External Access:	Read/Write		
		M 3), 2-D1(IREF,IN BCD M3), 2-D2(bca	to dec hed to dec 02 RCD3)
$2-D2(IREF,BCD\ M\ 3)$	im_PBD - 2-DI(OREI,BCD_	[M_3), 2-D1(IRE1,IIV_BCD_M3), 2-D2(000	touec,ocu_touec02.bCD3),
· /	e - *Unknown Protection(Unkn	own Protection), *Unknown Protection(Un	known Protection) *Unknown
Protection(Unknown Prote		om 1 roleemon, emmon 1 roleemon(em	mionii 1 iotociiony, Cinaionii
BCD R 0	0	BOOL	Kulicky BPC PGA
BCD pravý válec			, – –
AliasFor:	BCDX.8		
Base Tag:	BCDX.8		
Constant	No		
External Access:	Read/Write		
BCD_R_0 - main_FBD/ma 2-F2(IREF,BCD_R_0)	in_FBD - *2-G1(OREF,BCD_	R_0), 2-F1(IREF,IN_BCD_R0), 2-F2(bcd_1	o_dec,bcd_to_dec_03.BCD0),
BCDX - Simulace/Simulace Protection(Unknown Prote		own Protection), *Unknown Protection(Un	known Protection), *Unknown

BOOL

Kulicky_BPC_PGA

BCD_R_1 (Continued)

Base Tag: BCDX.9
Constant No

External Access: Read/Write

 $BCD_R_1 - main_FBD/main_FBD - *2-G1(OREF, BCD_R_1), \ 2-F1(IREF, IN_BCD_R1), \ 2-F2(bcd_to_dec, bcd_to_dec_03.BCD1), \ 2-F2(bcd_to_dec, bcd_to_dec_03.BCD1), \ 2-F2(bcd_to_dec, bcd_to_dec_03.BCD1), \ 2-F2(bcd_to_dec, bcd_to_dec_03.BCD1), \ 2-F2(bcd_to_dec_03.BCD1), \ 2-F2(bcd_to_d$

2-F2(IREF,BCD_R_1)

BCDX - Simulace/Simulace - *Unknown Protection(Unknown Protection), *Unknown Protection(Unknown Protection), *Unknown

Protection(Unknown Protection)

BOOL Kulicky_BPC_PGA

BCD pravý válec

AliasFor: BCDX.10
Base Tag: BCDX.10
Constant No

External Access: Read/Write

BCD_R_2 - main_FBD/main_FBD - *2-G1(OREF,BCD_R_2), 2-F1(IREF,IN_BCD_R2), 2-F2(bcd_to_dec,bcd_to_dec_03.BCD2),

2-F2(IREF,BCD_R_2)

BCDX - Simulace/Simulace - *Unknown Protection(Unknown Protection), *Unknown Protection(Unknown Protection(Unknown Protection), *Unknown Protection(Unknown Protection(Unknown Protection), *Unknown Protection(Unknown Protection(Unkn

Protection(Unknown Protection)

BOOL Kulicky_BPC_PGA

BCD pravý válec

AliasFor: BCDX.11
Base Tag: BCDX.11
Constant No
External Access: Read/Write

 $BCD_R_3 - main_FBD/main_FBD - *2-G1(OREF, BCD_R_3), \ 2-F1(IREF, IN_BCD_R3), \ 2-F2(bcd_to_dec, bcd_to_dec_03.BCD3), \ 2-F2(bcd_to_dec, bcd_to_dec_03.BCD3), \ 2-F2(bcd_to_dec, bcd_to_dec_03.BCD3), \ 2-F2(bcd_to_dec, bcd_to_dec_03.BCD3), \ 2-F2(bcd_to_dec_03.BCD3), \ 2-F2(bcd_to_d$

2-F2(IREF,BCD R 3)

BCDX - Simulace/Simulace - *Unknown Protection(Unknown Protection), *Unknown Protection(Unknown Protection), *Unknown

Protection(Unknown Protection)

bcd_to_dec_01 bcd_to_dec main_FBD

bin to dec conversion

Constant No

External Access: Read/Write

bcd_to_dec_01 - main_FBD/main_FBD - *2-B2(bcd_to_dec,bcd_to_dec_01), *2-B2(IREF,BCD_L_0), *2-B2(IREF,BCD_L_1),
*2-B2(IREF,BCD_L_2), *2-B2(IREF,BCD_L_1), *2-

*2-B2(IREF,BCD_L_2), *2-B2(IREF,BCD_L_3), *2-C2(OREF,AlarmH1), *2-C2(OREF,AlarmL1), *2-C2(OREF,No_Balls1)

bcd_to_dec_01.EnableIn 1 BOOL

bin to dec conversion Enable Input - System Defined Parameter

bcd_to_dec_01.EnableOut 1 BOOL

bin to dec conversion Enable Output - System Defined Parameter

bcd to dec 01.BCD0 0 BOOL

bin to dec conversion

bcd to dec 01.BCD1 0 BOOL

tu_to_ucc_or.bcb1

bin to dec conversion

cd to dec 01.BCD2 0 BOOL

bin to dec conversion

bcd to dec 01.BCD3 1 BOOL

bin to dec conversion

bcd to dec 01.dec 8 DINT

bin to dec conversion

bcd to dec 01.alarm L 0 BOOL

bin to dec conversion

bcd to dec 01.alarm H 0 BOOL

bin to dec conversion

bcd to dec 02 bcd to dec main FBD

bin to dec conversion

Constant No External Access: Read/Write

bcd_to_dec_02 - main_FBD/main_FBD - *2-D2(bcd_to_dec,bcd_to_dec_02), *2-D2(IREF,BCD_M_0), *2-D2(IREF,BCD_M_1), *2-D2(IREF,BCD_M_2), *2-D2(IREF,BCD_M_3), *2-E2(OREF,AlarmH2), *2-

bcd to dec 02.EnableIn 1 BOOL

bin to dec conversion Enable Input - System Defined Parameter

Logix Designer

Kulicky_BPC_PGA (Controller) 2022-04-25 20:39:25 C:\Users\bpc pga\Documents\Studio 5000\Projects\Kulicky BPC PGA.ACD bcd to dec 02 (Continued) bcd_to_dec_02.EnableOut **BOOL** 1 bin to dec conversion Enable Output - System Defined Parameter bcd to dec 02.BCD0 **BOOL** bin to dec conversion 0 **BOOL** bcd to dec 02.BCD1 bin to dec conversion **BOOL** bcd to dec 02.BCD2 1 bin to dec conversion 0 bcd to dec 02.BCD3 **BOOL** bin to dec conversion bcd_to_dec_02.dec DINT bin to dec conversion bcd to dec 02.alarm L **BOOL** bin to dec conversion bcd_to_dec_02.alarm_H 0 **BOOL** bin to dec conversion bcd to dec 03 bcd to dec main FBD bin to dec conversion Constant No External Access: Read/Write bcd to dec 03 - main FBD/main FBD - *2-F2(bcd to dec,bcd to dec 03), *2-F2(IREF,BCD R 0), *2-F2(IREF,BCD R 1), *2-F2(IREF,BCD_R_2), *2-F2(IREF,BCD_R_3), *2-G2(OREF,AlarmH3), *2-G2(OREF,AlarmL3), *2-G2(OREF,No_Balls3) bcd to dec 03.EnableIn BOOL bin to dec conversion Enable Input - System Defined Parameter bcd to dec 03.EnableOut BOOL. bin to dec conversion Enable Output - System Defined Parameter bcd to dec 03.BCD0 **BOOL** bin to dec conversion bcd to dec 03.BCD1 0 **BOOL** bin to dec conversion bcd to dec 03.BCD2 0 **BOOL** bin to dec conversion 0 bcd to dec 03.BCD3 **BOOL** bin to dec conversion bcd to dec 03.dec DINT bin to dec conversion bcd to dec 03.alarm L 0 **BOOL** bin to dec conversion bcd to dec 03.alarm H **BOOL** bin to dec conversion BCD1 0 DINT Simulace Constant No External Access: Read/Write BCD1 - Simulace/Simulace - *Unknown Protection(Unknown Protection), Unknown Protection(Unknown Protection) BCD2 0 DINT Simulace Constant No **External Access:** Read/Write BCD2 - Simulace/Simulace - *Unknown Protection(Unknown Protection), Unknown Protection(Unknown Protection) BCD3 DINT Simulace Constant No Read/Write **External Access:** BCD3 - Simulace/Simulace - *Unknown Protection(Unknown Protection), Unknown Protection(Unknown Protection) **BCDX** 72 DINT Kulicky BPC PGA Hodnoty BCD Constant No External Access: Read/Write BCDX - Simulace/Simulace - *Unknown Protection(Unknown Protection), *Unknown Protection(Unknown Pr Protection(Unknown Protection)

Kulicky_BPC_PGA (Controller)

```
BCDX (Continued)
                               0
                                                                 BOOL
BCDX.0
 Hodnoty BCD
 BCD L 0 - main FBD/main FBD - *2-B1(OREF,BCD L 0), 2-B1(IREF,IN BCD L0), 2-B2(bcd to dec,bcd to dec 01.BCD0),
  2-B2(IREF,BCD L 0)
                               0
BCDX.1
                                                                 BOOL
 Hodnoty BCD
 BCD L 1 - main FBD/main FBD - *2-B1(OREF,BCD L 1), 2-B1(IREF,IN BCD L1), 2-B2(bcd to dec,bcd to dec 01.BCD1),
  2-B2(IREF,BCD L 1)
                               0
BCDX.2
                                                                 BOOL
 Hodnoty BCD
 BCD L 2 - main FBD/main FBD - *2-B1(OREF,BCD L 2), 2-B1(IREF,IN BCD L2), 2-B2(bcd to dec,bcd to dec 01.BCD2),
  2-B2(IREF,BCD L 2)
BCDX.3
                               1
                                                                 BOOL
 Hodnoty BCD
 BCD L 3 - main FBD/main FBD - *2-B1(OREF,BCD L 3), 2-B1(IREF,IN BCD L3), 2-B2(bcd to dec,bcd to dec 01.BCD3),
  2-B2(IREF,BCD_L_3)
                               0
BCDX.4
                                                                 BOOL
 Hodnoty BCD
 BCD M 0 - main FBD/main FBD - *2-D1(OREF,BCD M 0), 2-D1(IREF,IN BCD M0), 2-D2(bcd to dec,bcd to dec 02.BCD0),
  2-D2(IREF,BCD\ M\ 0)
BCDX.5
                                                                 BOOL
 Hodnoty BCD
 BCD M 1 - main FBD/main FBD - *2-D1(OREF,BCD M_1), 2-D1(IREF,IN BCD_M1), 2-D2(bcd_to_dec,bcd_to_dec_02.BCD1),
  2-D2(IREF,BCD M 1)
BCDX.6
                                                                 BOOL
 Hodnoty BCD
 BCD M 2 - main FBD/main FBD - *2-D1(OREF,BCD M 2), 2-D1(IREF,IN BCD_M2), 2-D2(bcd_to_dec,bcd_to_dec_02.BCD2),
  2-D2(IREF,BCD M 2)
BCDX.7
                               0
                                                                 BOOL
 Hodnoty BCD
 BCD M 3 - main FBD/main FBD - *2-D1(OREF,BCD M 3), 2-D1(IREF,IN BCD M3), 2-D2(bcd to dec,bcd to dec 02.BCD3),
  2-D2(IREF,BCD M 3)
BCDX.8
                               0
                                                                 BOOL
 Hodnoty BCD
 BCD R 0 - main FBD/main FBD - *2-G1(OREF,BCD R 0), 2-F1(IREF,IN BCD R0), 2-F2(bcd to dec,bcd to dec 03.BCD0),
  2-F2(IREF,BCD R 0)
BCDX.9
                               0
                                                                 BOOL
 Hodnoty BCD
 BCD R 1 - main FBD/main FBD - *2-G1(OREF,BCD R 1), 2-F1(IREF,IN BCD R1), 2-F2(bcd to dec,bcd to dec 03.BCD1),
  2-F2(IREF,BCD R 1)
BCDX.10
                               0
                                                                 BOOL
 Hodnoty BCD
 BCD R 2 - main FBD/main FBD - *2-G1(OREF,BCD R 2), 2-F1(IREF,IN BCD R2), 2-F2(bcd to dec,bcd to dec 03.BCD2),
  2-F2(IREF,BCD R 2)
BCDX.11
                               0
                                                                 BOOL
 Hodnoty BCD
 BCD R 3 - main FBD/main FBD - *2-G1(OREF,BCD R 3), 2-F1(IREF,IN BCD R3), 2-F2(bcd to dec,bcd to dec 03.BCD3),
  2-F2(IREF,BCD R 3)
BNOT 01
                                                                 FBD BOOLEAN NOT
                                                                                                   main FBD
 Constant
                               No
 External Access:
                               Read/Write
 BNOT 01 - main FBD/main FBD - *1-B1(BNOT,BNOT 01), *1-C1(BAND,BAND 02.In2), *2-B3(BOR,BOR 04.Out)
                                                                 FBD BOOLEAN OR
BOR 01
                                                                                                   main FBD
 Constant
                               No
 External Access:
                               Read/Write
 BOR 01 - main FBD/main FBD - *3-E1(ventilator, ventilator 01.fault), *3-E2(ventilator, ventilator 02.fault),
  *3-E3(ventilator, ventilator 03.fault), *3-G1(BOR,BOR 01), *3-G1(OREF,fault)
BOR 02
                                                                 FBD BOOLEAN OR
                                                                                                   main FBD
 Constant
                               No
                               Read/Write
 External Access:
```

cilinder_control_02

```
BOR 02 (Continued)
   BOR 02 - main FBD/main FBD - *1-A2(OSRI,OSRI 02.OutputBit), *1-A2(OSRI,OSRI 04.OutputBit), *1-B2(BOR,BOR 02),
    *I-E2(cilinder control,cilinder control 01.stop), *I-E3(cilinder control,cilinder control 02.stop),
    *1-E5(cilinder control, cilinder control 03.stop)
  BOR 03
                                                                        FBD BOOLEAN OR
                                                                                                             main FBD
   Constant
                                    No
                                    Read/Write
   External Access:
   BOR 03 - main FBD/main FBD - *3-A2(OSRI,OSRI 05.OutputBit), *3-B1(BOR,BOR 03), *3-B2(IREF,clr fault),
    *3-C1(OSRI,OSRI 03.InputBit)
  BOR_04
                                                                        FBD_BOOLEAN_OR
                                                                                                             main_FBD
   Constant
                                    No
                                    Read/Write
   External Access:
   BOR 04 - main FBD/main FBD - *1-B1(BNOT,BNOT 01.In), *2-B3(BOR,BOR 04), *2-B4(IREF,AlarmH1), *2-B4(IREF,AlarmH2),
    *2-B4(IREF,AlarmH3), *2-B4(IREF,AlarmL1), *2-B4(IREF,AlarmL2), *2-B4(IREF,AlarmL3)
C LED
                                                                        BOOL
                                                                                                             Kulicky_BPC_PGA
   NEPOUŽÍVAT!!!
   AliasFor:
                                    SIM Outputs Data.31
   Base Tag:
                                    Local:3:O.Data[0].31
   Constant
   External Access:
                                    Read/Write
   C LED - main FBD/main FBD - *1-B5(OREF,C LED), 1-A5(EQU,EQU 01.Dest)
  cilinder control 01
                                                                        cilinder control
                                                                                                             main FBD
   rizeni zapadek valce
   Constant
                                    No
   External Access:
                                    Read/Write
   cilinder control 01 - main FBD/main FBD - *1-A5(EQU,EQU 01.SourceA), *1-B2(BOR,BOR 02.Out), *1-C1(BAND,BAND 02.Out),
    *I-D2(IREF,No Balls1), *I-D2(IREF,TOC), *I-E2(cilinder control,cilinder control 01), *I-E2(IREF,S1), *I-F2(OREF,M1D),
    *I-F2(OREF,MIU), *I-G2(BAND,BAND 01.In1), *I-H2(BAND,BAND 03.Out), *3-E1(ventilator,ventilator 01.on)
 cilinder control 01.EnableIn
                                                                        BOOL
   rizeni zapadek valce Enable Input - System Defined Parameter
                                                                        BOOL
 cilinder control 01.EnableOut
   rizeni zapadek valce Enable Output - System Defined Parameter
 cilinder control 01.senzor
                                                                        BOOL
   rizeni zapadek valce
 cilinder control 01.num balls
                                    8
                                                                        DINT
   rizeni zapadek valce
 cilinder control 01.start
                                                                        BOOL
   rizeni zapadek valce
 cilinder control 01.stop
                                    0
                                                                        BOOL
   rizeni zapadek valce
 cilinder control 01.activate
                                    0
                                                                        BOOL
   rizeni zapadek valce
                                    0
 cilinder control 01.zapadka h
                                                                        BOOL
   rizeni zapadek valce
 cilinder control 01.zapadka 1
                                    0
                                                                        BOOL
   rizeni zapadek valce
 cilinder control 01.ball cntr
                                    0
                                                                        DINT
   rizeni zapadek valce
 cilinder control 01.ready
                                    0
                                                                        BOOL
   rizeni zapadek valce
 cilinder control 01.run
                                    0
                                                                        BOOL
   rizeni zapadek valce
 cilinder control 01.dose period
                                    2000
                                                                        DINT
   rizeni zapadek valce
                                                                        BOOL
 cilinder control 01.complete
   rizeni zapadek valce
                                    0
                                                                        DINT
 cilinder control 01.state
   rizeni zapadek valce
```

cilinder_control

main FBD

cilinder_control_02 (Continued)

mizani zanadali valas			
rizeni zapadek valce Constant	No		
	Read/Write		
External Access:		\$1.C1/D4ND D4ND 02.Out) *1.D4/IDEENs D	~11~2\
		*1-C1(BAND,BAND_02.Out), *1-D4(IREF,No_Bo EF,S2), *1-F4(OREF,M2D), *1-F4(OREF,M2U),	111S2),
	-H2(BAND,BAND_03.Out), *3-E2(ventile		
cilinder_control_02.EnableIn	Crystom Defined Donometer	BOOL	
rizeni zapadek valce Enable Input	- System Defined Parameter	DOOL	
cilinder_control_02.EnableOut		BOOL	
rizeni zapadek valce Enable Outpu	ıı - System Defined Parameter	DOOL	
cilinder_control_02.senzor	1	BOOL	
rizeni zapadek valce	4	DNIT	
cilinder_control_02.num_balls	4	DINT	
rizeni zapadek valce	0	DOOL	
cilinder_control_02.start	0	BOOL	
rizeni zapadek valce	0	DOOL	
cilinder_control_02.stop	0	BOOL	
rizeni zapadek valce	0	DOOL	
cilinder_control_02.activate	0	BOOL	
rizeni zapadek valce	0	DOOI	
cilinder_control_02.zapadka_h	0	BOOL	
rizeni zapadek valce	0	DOOL	
cilinder_control_02.zapadka_l	0	BOOL	
rizeni zapadek valce	0	DINT	
cilinder_control_02.ball_cntr	0	DINT	
rizeni zapadek valce	0	BOOL	
cilinder_control_02.ready	0	BOOL	
rizeni zapadek valce	0	DOOI	
cilinder_control_02.run	0	BOOL	
rizeni zapadek valce	2000	DINT	
cilinder_control_02.dose_period	2000	DINI	
rizoni zonodale valoa			
rizeni zapadek valce	0	ROOI	
cilinder_control_02.complete	0	BOOL	
cilinder_control_02.complete rizeni zapadek valce			
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state	0 0	BOOL DINT	
cilinder_control_02.complete rizeni zapadek valce			
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce		DINT	FRD
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03			FBD
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce	0	DINT	FBD
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant	0 No	DINT	FBD
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access:	0 No Read/Write	DINT cilinder_control main_l	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n	0 No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), *	DINT cilinder_control main_l *1-C1(BAND,BAND_02.Out), *1-D5(IREF,No_Ba	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n *1-D6(IREF,TOC), *1-E5(cilinde	0 No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), * r_control,cilinder_control_03), *1-E5(IR	DINT cilinder_control main_l f1-C1(BAND,BAND_02.Out), *1-D5(IREF,No_Ba EF,S3), *1-F5(OREF,M3D), *1-F5(OREF,M3U),	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n *1-D6(IREF,TOC), *1-E5(cilinde *1-G2(BAND,BAND_01.In3), *1-	0 No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), *	DINT cilinder_control main_l *1-C1(BAND,BAND_02.Out), *1-D5(IREF,No_Ba EF,S3), *1-F5(OREF,M3D), *1-F5(OREF,M3U), ator,ventilator_03.on)	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n *1-D6(IREF,TOC), *1-E5(cilinde *1-G2(BAND,BAND_01.In3), *1- cilinder_control_03.EnableIn	No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), ' r_control,cilinder_control_03), *1-E5(IR -H2(BAND,BAND_03.Out), *3-E3(ventila	DINT cilinder_control main_l f1-C1(BAND,BAND_02.Out), *1-D5(IREF,No_Ba EF,S3), *1-F5(OREF,M3D), *1-F5(OREF,M3U),	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n *1-D6(IREF,TOC), *1-E5(cilinde *1-G2(BAND,BAND_01.In3), *1- cilinder_control_03.EnableIn rizeni zapadek valce Enable Input	No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), ' r_control,cilinder_control_03), *1-E5(IR -H2(BAND,BAND_03.Out), *3-E3(ventila	DINT cilinder_control main_l *1-C1(BAND,BAND_02.Out), *1-D5(IREF,No_Be EF,S3), *1-F5(OREF,M3D), *1-F5(OREF,M3U), ator;ventilator_03.on) BOOL	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n *1-D6(IREF,TOC), *1-E5(cilinde *1-G2(BAND,BAND_01.In3), *1- cilinder_control_03.EnableIn rizeni zapadek valce Enable Input cilinder_control_03.EnableOut	No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), * r_control,cilinder_control_03), *1-E5(IR-H2(BAND,BAND_03.Out), *3-E3(ventila) 1 - System Defined Parameter 1	DINT cilinder_control main_l *1-C1(BAND,BAND_02.Out), *1-D5(IREF,No_Ba EF,S3), *1-F5(OREF,M3D), *1-F5(OREF,M3U), ator,ventilator_03.on)	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n *1-D6(IREF,TOC), *1-E5(cilinde *1-G2(BAND,BAND_01.In3), *1- cilinder_control_03.EnableIn rizeni zapadek valce Enable Input cilinder_control_03.EnableOut rizeni zapadek valce Enable Output	No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), * r_control,cilinder_control_03), *1-E5(IR-H2(BAND,BAND_03.Out), *3-E3(ventila) 1 - System Defined Parameter 1	DINT cilinder_control main_l f1-C1(BAND,BAND_02.Out), *1-D5(IREF,No_Ba EF,S3), *1-F5(OREF,M3D), *1-F5(OREF,M3U), ator;ventilator_03.on) BOOL BOOL	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n *1-D6(IREF,TOC), *1-E5(cilinde *1-G2(BAND,BAND_01.In3), *1- cilinder_control_03.EnableIn rizeni zapadek valce Enable Input cilinder_control_03.EnableOut rizeni zapadek valce Enable Output cilinder_control_03.senzor	No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), * r_control,cilinder_control_03), *1-E5(IR-H2(BAND,BAND_03.Out), *3-E3(ventila) 1 - System Defined Parameter 1	DINT cilinder_control main_l *1-C1(BAND,BAND_02.Out), *1-D5(IREF,No_Be EF,S3), *1-F5(OREF,M3D), *1-F5(OREF,M3U), ator;ventilator_03.on) BOOL	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n *1-D6(IREF,TOC), *1-E5(cilinde *1-G2(BAND,BAND_01.In3), *1- cilinder_control_03.EnableIn rizeni zapadek valce Enable Input cilinder_control_03.EnableOut rizeni zapadek valce Enable Output cilinder_control_03.senzor rizeni zapadek valce	No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), * r_control,cilinder_control_03), *1-E5(IR -H2(BAND,BAND_03.Out), *3-E3(ventila) 1 - System Defined Parameter 1 nt - System Defined Parameter 1	DINT cilinder_control main_l *1-C1(BAND,BAND_02.Out), *1-D5(IREF,No_Bo EF,S3), *1-F5(OREF,M3D), *1-F5(OREF,M3U), ator,ventilator_03.on) BOOL BOOL BOOL	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n *1-D6(IREF,TOC), *1-E5(cilinde *1-G2(BAND,BAND_01.In3), *1- cilinder_control_03.EnableIn rizeni zapadek valce Enable Input cilinder_control_03.EnableOut rizeni zapadek valce Enable Output cilinder_control_03.senzor rizeni zapadek valce cilinder_control_03.num_balls	No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), * r_control,cilinder_control_03), *1-E5(IR-H2(BAND,BAND_03.Out), *3-E3(ventila) 1 - System Defined Parameter 1	DINT cilinder_control main_l f1-C1(BAND,BAND_02.Out), *1-D5(IREF,No_Ba EF,S3), *1-F5(OREF,M3D), *1-F5(OREF,M3U), ator;ventilator_03.on) BOOL BOOL	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n *1-D6(IREF,TOC), *1-E5(cilinde *1-G2(BAND,BAND_01.In3), *1- cilinder_control_03.EnableIn rizeni zapadek valce Enable Input cilinder_control_03.EnableOut rizeni zapadek valce Enable Outpu cilinder_control_03.senzor rizeni zapadek valce cilinder_control_03.num_balls rizeni zapadek valce	No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), ' r_control,cilinder_control_03), *1-E5(IR -H2(BAND,BAND_03.Out), *3-E3(ventile) 1 - System Defined Parameter 1 at - System Defined Parameter 1 0	DINT cilinder_control main_l *1-C1(BAND,BAND_02.Out), *1-D5(IREF,No_Backer,S3), *1-F5(OREF,M3D), *1-F5(OREF,M3U), ator,ventilator_03.on) BOOL BOOL BOOL DINT	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n *1-D6(IREF,TOC), *1-E5(cilinde *1-G2(BAND,BAND_01.In3), *1- cilinder_control_03.EnableIn rizeni zapadek valce Enable Input cilinder_control_03.EnableOut rizeni zapadek valce Enable Output cilinder_control_03.senzor rizeni zapadek valce cilinder_control_03.num_balls rizeni zapadek valce cilinder_control_03.start	No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), * r_control,cilinder_control_03), *1-E5(IR -H2(BAND,BAND_03.Out), *3-E3(ventila) 1 - System Defined Parameter 1 nt - System Defined Parameter 1	DINT cilinder_control main_l *1-C1(BAND,BAND_02.Out), *1-D5(IREF,No_Bo EF,S3), *1-F5(OREF,M3D), *1-F5(OREF,M3U), ator,ventilator_03.on) BOOL BOOL BOOL	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n *1-D6(IREF,TOC), *1-E5(cilinde *1-G2(BAND,BAND_01.In3), *1- cilinder_control_03.EnableIn rizeni zapadek valce Enable Input cilinder_control_03.EnableOut rizeni zapadek valce Enable Output cilinder_control_03.senzor rizeni zapadek valce cilinder_control_03.num_balls rizeni zapadek valce cilinder_control_03.start rizeni zapadek valce	No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), ' r_control,cilinder_control_03), *1-E5(IR -H2(BAND,BAND_03.Out), *3-E3(ventile) 1 - System Defined Parameter 1 at - System Defined Parameter 1 0	cilinder_control main_l \$1-C1(BAND,BAND_02.Out), *1-D5(IREF,No_Backer,S3), *1-F5(OREF,M3D), *1-F5(OREF,M3U), ator; ventilator_03.on) BOOL BOOL BOOL DINT BOOL	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n *1-D6(IREF,TOC), *1-E5(cilinde *1-G2(BAND,BAND_01.In3), *1- cilinder_control_03.EnableIn rizeni zapadek valce Enable Input cilinder_control_03.SenableOut rizeni zapadek valce Enable Output cilinder_control_03.senzor rizeni zapadek valce cilinder_control_03.num_balls rizeni zapadek valce cilinder_control_03.start rizeni zapadek valce cilinder_control_03.start rizeni zapadek valce cilinder_control_03.stop	No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), * r_control,cilinder_control_03), *1-E5(IR -H2(BAND,BAND_03.Out), *3-E3(ventile) 1 - System Defined Parameter 1 ut - System Defined Parameter 1 0 0	DINT cilinder_control main_l *1-C1(BAND,BAND_02.Out), *1-D5(IREF,No_Backer,S3), *1-F5(OREF,M3D), *1-F5(OREF,M3U), ator,ventilator_03.on) BOOL BOOL BOOL DINT	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n *1-D6(IREF,TOC), *1-E5(cilinde *1-G2(BAND,BAND_01.In3), *1- cilinder_control_03.EnableIn rizeni zapadek valce Enable Input cilinder_control_03.senzor rizeni zapadek valce Enable Output cilinder_control_03.senzor rizeni zapadek valce cilinder_control_03.num_balls rizeni zapadek valce cilinder_control_03.start rizeni zapadek valce cilinder_control_03.stop rizeni zapadek valce	No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), * r_control,cilinder_control_03), *1-E5(IR -H2(BAND,BAND_03.Out), *3-E3(ventile) 1 - System Defined Parameter 1 ut - System Defined Parameter 1 0 0	cilinder_control main_l *I-C1(BAND,BAND_02.Out), *I-D5(IREF,No_Backer,S3), *I-F5(OREF,M3D), *I-F5(OREF,M3U), nor,ventilator_03.on) BOOL BOOL BOOL DINT BOOL BOOL BOOL BOOL	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n *1-D6(IREF,TOC), *1-E5(cilinde *1-G2(BAND,BAND_01.In3), *1- cilinder_control_03.EnableIn rizeni zapadek valce Enable Input cilinder_control_03.EnableOut rizeni zapadek valce Enable Output cilinder_control_03.senzor rizeni zapadek valce cilinder_control_03.num_balls rizeni zapadek valce cilinder_control_03.start rizeni zapadek valce cilinder_control_03.stop rizeni zapadek valce cilinder_control_03.activate	No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), * r_control,cilinder_control_03), *1-E5(IR -H2(BAND,BAND_03.Out), *3-E3(ventile) 1 - System Defined Parameter 1 at - System Defined Parameter 1 0 0 0	cilinder_control main_l \$1-C1(BAND,BAND_02.Out), *1-D5(IREF,No_Backer,S3), *1-F5(OREF,M3D), *1-F5(OREF,M3U), ator; ventilator_03.on) BOOL BOOL BOOL DINT BOOL	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n *1-D6(IREF,TOC), *1-E5(cilinde *1-G2(BAND,BAND_01.In3), *1- cilinder_control_03.EnableIn rizeni zapadek valce Enable Input cilinder_control_03.EnableOut rizeni zapadek valce Enable Output cilinder_control_03.senzor rizeni zapadek valce cilinder_control_03.num_balls rizeni zapadek valce cilinder_control_03.start rizeni zapadek valce cilinder_control_03.stop rizeni zapadek valce cilinder_control_03.activate rizeni zapadek valce cilinder_control_03.activate	No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), * r_control,cilinder_control_03), *1-E5(IR -H2(BAND,BAND_03.Out), *3-E3(ventile) 1 - System Defined Parameter 1 at - System Defined Parameter 1 0 0 0	cilinder_control main_l *I-C1(BAND,BAND_02.Out), *I-D5(IREF,No_Backer,S3), *I-F5(OREF,M3D), *I-F5(OREF,M3U), nor,ventilator_03.on) BOOL BOOL BOOL DINT BOOL BOOL BOOL BOOL	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n *1-D6(IREF,TOC), *1-E5(cilinde *1-G2(BAND,BAND_01.In3), *1- cilinder_control_03.EnableIn rizeni zapadek valce Enable Input cilinder_control_03.EnableOut rizeni zapadek valce Enable Output cilinder_control_03.senzor rizeni zapadek valce cilinder_control_03.num_balls rizeni zapadek valce cilinder_control_03.start rizeni zapadek valce cilinder_control_03.stop rizeni zapadek valce cilinder_control_03.activate rizeni zapadek valce cilinder_control_03.activate rizeni zapadek valce cilinder_control_03.zapadka_h	No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), * r_control,cilinder_control_03), *1-E5(IR -H2(BAND,BAND_03.Out), *3-E3(ventile) 1 - System Defined Parameter 1 ut - System Defined Parameter 1 0 0 0	DINT cilinder_control main_l *1-C1(BAND,BAND_02.Out), *1-D5(IREF,No_Ba EF,S3), *1-F5(OREF,M3D), *1-F5(OREF,M3U), ator,ventilator_03.on) BOOL BOOL BOOL DINT BOOL BOOL BOOL BOOL BOOL BOOL BOOL	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n *1-D6(IREF,TOC), *1-E5(cilinde *1-G2(BAND,BAND_01.In3), *1- cilinder_control_03.EnableIn rizeni zapadek valce Enable Input cilinder_control_03.EnableOut rizeni zapadek valce Enable Output cilinder_control_03.senzor rizeni zapadek valce cilinder_control_03.num_balls rizeni zapadek valce cilinder_control_03.start rizeni zapadek valce cilinder_control_03.stop rizeni zapadek valce cilinder_control_03.activate rizeni zapadek valce cilinder_control_03.activate	No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), * r_control,cilinder_control_03), *1-E5(IR -H2(BAND,BAND_03.Out), *3-E3(ventile) 1 - System Defined Parameter 1 ut - System Defined Parameter 1 0 0 0	DINT cilinder_control main_l *1-C1(BAND,BAND_02.Out), *1-D5(IREF,No_Ba EF,S3), *1-F5(OREF,M3D), *1-F5(OREF,M3U), ator,ventilator_03.on) BOOL BOOL BOOL DINT BOOL BOOL BOOL BOOL BOOL BOOL BOOL	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n *1-D6(IREF,TOC), *1-E5(cilinde *1-G2(BAND,BAND_01.In3), *1- cilinder_control_03.EnableIn rizeni zapadek valce Enable Input cilinder_control_03.EnableOut rizeni zapadek valce Enable Output cilinder_control_03.senzor rizeni zapadek valce cilinder_control_03.num_balls rizeni zapadek valce cilinder_control_03.start rizeni zapadek valce cilinder_control_03.stop rizeni zapadek valce cilinder_control_03.activate rizeni zapadek valce cilinder_control_03.zapadka_h rizeni zapadek valce	No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), * r_control,cilinder_control_03), *1-E5(IR -H2(BAND,BAND_03.Out), *3-E3(ventile) 1 - System Defined Parameter 1 at - System Defined Parameter 1 0 0 0 0	cilinder_control main_l *1-C1(BAND,BAND_02.Out), *1-D5(IREF,No_Bo EF,S3), *1-F5(OREF,M3D), *1-F5(OREF,M3U), ator,ventilator_03.on) BOOL BOOL BOOL DINT BOOL BOOL BOOL BOOL BOOL BOOL BOOL BOO	
cilinder_control_02.complete rizeni zapadek valce cilinder_control_02.state rizeni zapadek valce cilinder_control_03 rizeni zapadek valce Constant External Access: cilinder_control_03 - main_FBD/n *1-D6(IREF,TOC), *1-E5(cilinde *1-G2(BAND,BAND_01.In3), *1- cilinder_control_03.EnableIn rizeni zapadek valce Enable Input cilinder_control_03.EnableOut rizeni zapadek valce Enable Output cilinder_control_03.senzor rizeni zapadek valce cilinder_control_03.num_balls rizeni zapadek valce cilinder_control_03.start rizeni zapadek valce cilinder_control_03.stop rizeni zapadek valce cilinder_control_03.activate rizeni zapadek valce cilinder_control_03.zapadka_h rizeni zapadek valce cilinder_control_03.zapadka_l	No Read/Write main_FBD - *1-B2(BOR,BOR_02.Out), * r_control,cilinder_control_03), *1-E5(IR -H2(BAND,BAND_03.Out), *3-E3(ventile) 1 - System Defined Parameter 1 at - System Defined Parameter 1 0 0 0 0	cilinder_control main_l *1-C1(BAND,BAND_02.Out), *1-D5(IREF,No_Bo EF,S3), *1-F5(OREF,M3D), *1-F5(OREF,M3U), ator,ventilator_03.on) BOOL BOOL BOOL DINT BOOL BOOL BOOL BOOL BOOL BOOL BOOL BOO	

Kulicky_BPC_PGA (Controller) 2022-04-25 20:39:26 C:\Users\bpc pga\Documents\Studio 5000\Projects\Kulicky BPC PGA.ACD cilinder control 03 (Continued) rizeni zapadek valce cilinder control 03.ready 0 **BOOL** rizeni zapadek valce cilinder control 03.run 0 **BOOL** rizeni zapadek valce cilinder control 03.dose period 2000 DINT rizeni zapadek valce cilinder control 03.complete 0 **BOOL** rizeni zapadek valce cilinder control 03.state 0 DINT rizeni zapadek valce 0 de clr fault **BOOL** Kulicky_BPC_PGA NEPOUŽÍVAT!!! SIM_Iputs_Data.14 AliasFor: Base Tag: Local:3:I.Data[1].14 Constant No Read/Write External Access: clr fault - main FBD/main FBD - 3-B1(BOR,BOR 03.In1), 3-B2(IREF,clr fault) IN BCD R3 - main FBD/main FBD - 2-F1(IREF,IN BCD R3), 2-G1(OREF,BCD R 3) EQU 01 FBD COMPARE main FBD Constant No External Access: Read/Write EQU 01 - main FBD/main FBD - *1-A5(EQU,EQU 01), *1-A6(IREF,6), *1-B5(OREF,C LED), *1-E2(cilinder control,cilinder control 01.state) fault 16#0 BOOL Kulicky BPC PGA NEPOUŽÍVAT!!! AliasFor: SIM Outputs Data.14 Base Tag: Local:3:O.Data[0].14 Constant **External Access:** Read/Write fault - main FBD/main FBD - *3-G1(OREF, fault), 1-A2(OSRI, OSRI 04. InputBit), 1-A3(IREF, fault), 3-G1(BOR, BOR 01. Out) Hodnota DINT Simulace Constant No Read/Write External Access: Hodnota - Simulace/Alarmy - *Unknown Protection(Unknown Protection), *Unknown Protection(Unknown Protection) Hodnota - Simulace/BCD - *Unknown Protection(Unknown Protection), Unknown Protection(Unknown Protection), Unknown Protection(Unknown Protection), Unknown Protection(Unknown Protection) IN BCD L0 **BOOL** Kulicky BPC PGA NEPOUŽÍVAT!!! AliasFor: SIM Iputs Data.3 Base Tag: Local:3:I.Data[1].3 Constant **External Access:** Read/Write IN BCD L0 - main FBD/main FBD - 2-B1(IREF,IN BCD L0), 2-B1(OREF,BCD L 0) IN BCD L1 **BOOL** Kulicky BPC PGA NEPOUŽÍVAT!!! AliasFor: SIM Iputs Data.4 Base Tag: Local:3:I.Data[1].4 Constant No Read/Write External Access: IN BCD L1 - main FBD/main FBD - 2-B1(IREF,IN BCD L1), 2-B1(OREF,BCD L 1) IN BCD L2 0 **BOOL** Kulicky BPC PGA NEPOUŽÍVAT!!! AliasFor: SIM Iputs Data.5 Base Tag: Local:3:I.Data[1].5 Constant No

IN BCD L2 (Continued) Read/Write External Access: IN BCD L2 - main FBD/main FBD - 2-B1(IREF,IN BCD L2), 2-B1(OREF,BCD L 2) IN BCD L3 **BOOL** Kulicky BPC PGA NEPOUŽÍVAT!!! AliasFor: SIM Iputs Data.6 Base Tag: Local:3:I.Data[1].6 Constant Read/Write External Access: IN BCD L3 - main FBD/main FBD - 2-B1(IREF,IN BCD L3), 2-B1(OREF,BCD L 3) IN BCD M0 **BOOL** Kulicky_BPC_PGA NEPOUŽÍVAT!!! SIM Iputs Data.7 AliasFor: Local:3:I.Data[1].7 Base Tag: Constant No Read/Write External Access: IN BCD M0 - main FBD/main FBD - 2-D1(IREF,IN BCD M0), 2-D1(OREF,BCD M 0) 0 IN BCD M1 **BOOL** Kulicky BPC PGA NEPOUŽÍVAT!!! AliasFor: SIM Iputs Data.8 Base Tag: Local:3:I.Data[1].8 Constant No External Access: Read/Write IN BCD M1 - main FBD/main FBD - 2-D1(IREF,IN BCD M1), 2-D1(OREF,BCD M 1) IN BCD M2 **BOOL** Kulicky BPC PGA NEPOUŽÍVAT!!! AliasFor: SIM Iputs Data.9 Base Tag: Local:3:I.Data[1].9 Constant **External Access:** Read/Write IN BCD M2 - main FBD/main FBD - 2-D1(IREF,IN BCD M2), 2-D1(OREF,BCD M 2) IN BCD M3 **BOOL** Kulicky_BPC_PGA NEPOUŽÍVAT!!! AliasFor: SIM Iputs Data.10 Base Tag: Local:3:I.Data[1].10 Constant No External Access: Read/Write IN BCD M3 - main FBD/main FBD - 2-D1(IREF,IN BCD M3), 2-D1(OREF,BCD M 3) IN BCD R0 **BOOL** Kulicky_BPC_PGA NEPOUŽÍVAT!!! SIM Iputs Data.11 AliasFor: Base Tag: Local:3:I.Data[1].11 Constant No Read/Write External Access: IN BCD R0 - main FBD/main FBD - 2-F1(IREF,IN BCD R0), 2-G1(OREF,BCD R 0) IN BCD R1 0 **BOOL** Kulicky BPC PGA NEPOUŽÍVAT!!! AliasFor: SIM Iputs Data.12 Base Tag: Local:3:I.Data[1].12 Constant No External Access: Read/Write IN BCD R1 - main FBD/main FBD - 2-F1(IREF,IN BCD R1), 2-G1(OREF,BCD R 1) IN BCD R2 **BOOL** Kulicky BPC PGA NEPOUŽÍVAT!!! SIM_Iputs Data.13 AliasFor: Base Tag: Local:3:I.Data[1].13

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Kulicky_BPC_PGA (Controller)
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NEPOUŽÍVAT!!!

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IN BCD R2 (Continued)
   Constant
                                 No
                                 Read/Write
   External Access:
   IN BCD R2 - main FBD/main FBD - 2-F1(IREF,IN BCD R2), 2-G1(OREF,BCD R 2)
IN BCD R3
                                 0
                                                                   BOOL
                                                                                                     Kulicky BPC PGA
  NEPOUŽÍVAT!!!
   AliasFor:
                                 SIM Iputs Data.14
                                 Local:3:I.Data[1].14
   Base Tag:
   Constant
                                 No
   External Access:
                                 Read/Write
   IN BCD R3 - main FBD/main FBD - 2-F1(IREF,IN BCD R3), 2-G1(OREF,BCD R 3)
   clr fault - main FBD/main FBD - 3-B1(BOR,BOR 03.In1), 3-B2(IREF,clr fault)
Local:3:I
                                                                   AB:1756_MODULE_DINT_8Bytes:I:0
                                                                                                     Kulicky_BPC_PGA
   NEPOUŽÍVAT!!!
   Constant
                                 No
                                 Read/Write
   External Access:
                                                                   DINT
 Local:3:I.Data
   NEPOUŽÍVAT!!!
 Local:3:I.Data[0]
                                 0
                                                                   DINT
   NEPOUŽÍVAT!!!
 Local:3:I.Data[1]
                                 583
                                                                   DINT
   NEPOUŽÍVAT!!!
 Local:3:I.Data[1].3
                                                                   BOOL
   NEPOUŽÍVAT!!!
   IN BCD L0 - main FBD/main FBD - 2-B1(IREF,IN BCD L0), 2-B1(OREF,BCD L 0)
 Local:3:I.Data[1].4
   NEPOUŽÍVAT!!!
   IN BCD L1 - main FBD/main FBD - 2-B1(IREF,IN BCD L1), 2-B1(OREF,BCD L 1)
 Local:3:I.Data[1].5
                                                                   BOOL
   NEPOUŽÍVAT!!!
   IN BCD L2 - main FBD/main FBD - 2-B1(IREF,IN BCD L2), 2-B1(OREF,BCD L 2)
 Local:3:I.Data[1].6
   NEPOUŽÍVAT!!!
   IN BCD L3 - main FBD/main FBD - 2-B1(IREF,IN BCD L3), 2-B1(OREF,BCD L 3)
 Local:3:I.Data[1].7
                                                                   BOOL
   NEPOUŽÍVAT!!!
   IN BCD M0 - main FBD/main FBD - 2-D1(IREF,IN BCD M0), 2-D1(OREF,BCD M 0)
 Local:3:I.Data[1].8
   NEPOUŽÍVAT!!!
   IN BCD M1 - main FBD/main FBD - 2-D1(IREF,IN BCD M1), 2-D1(OREF,BCD M 1)
 Local:3:I.Data[1].9
   NEPOUŽÍVAT!!!
   IN BCD M2 - main FBD/main FBD - 2-D1(IREF,IN BCD M2), 2-D1(OREF,BCD M 2)
 Local:3:I.Data[1].10
                                                                   BOOL
   NEPOUŽÍVAT!!!
   IN BCD M3 - main FBD/main FBD - 2-D1(IREF,IN BCD M3), 2-D1(OREF,BCD M 3)
 Local:3:I.Data[1].11
   NEPOUŽÍVAT!!!
   IN BCD R0 - main FBD/main FBD - 2-F1(IREF,IN BCD R0), 2-G1(OREF,BCD R 0)
 Local:3:I.Data[1].12
                                 0
                                                                   BOOL
   NEPOUŽÍVAT!!!
   IN BCD R1 - main FBD/main FBD - 2-F1(IREF,IN BCD R1), 2-G1(OREF,BCD R 1)
 Local:3:I.Data[1].13
                                                                   BOOL
   NEPOUŽÍVAT !!!
   IN BCD R2 - main FBD/main FBD - 2-F1(IREF,IN BCD R2), 2-G1(OREF,BCD R 2)
 Local:3:I.Data[1].14
   NEPOUŽÍVAT!!!
   clr fault - main FBD/main FBD - 3-B1(BOR,BOR 03.In1), 3-B2(IREF,clr fault)
   IN BCD R3 - main FBD/main FBD - 2-F1(IREF,IN BCD R3), 2-G1(OREF,BCD R 3)
 Local:3:I.Data[1].16
                                                                   BOOL
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Kulicky_BPC_PGA (Controller)
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M1U

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2022-04-25 20:39:27 C:\Users\bpc pga\Documents\Studio 5000\Projects\Kulicky BPC PGA.ACD Local:3:I (Continued) ZH1 - main FBD/main FBD - 3-E1(IREF,ZH1), 3-E1(ventilator, ventilator 01.zh) Local:3:I.Data[1].17 BOOL NEPOUŽÍVAT!!! ZH2 - main FBD/main FBD - 3-E2(IREF,ZH2), 3-E2(ventilator, ventilator 02.zh) **BOOL** Local:3:I.Data[1].18 NEPOUŽÍVAT!!! ZH3 - main FBD/main FBD - 3-E3(IREF,ZH3), 3-E3(ventilator, ventilator 03.zh) Local:3:0 AB:1756 MODULE DINT 4Bytes:O:0 Kulicky BPC PGA NEPOUŽÍVAT!!! No Constant Read/Write External Access: Local:3:O.Data DINT NEPOUŽÍVAT!!! 0 Local:3:O.Data[0] DINT NEPOUŽÍVAT!!! **BOOL** Local:3:O.Data[0].0 NEPOUŽÍVAT!!! MIU - main FBD/main FBD - *1-F2(OREF,MIU), 1-E2(cilinder control,cilinder control 01.zapadka h) Local:3:O.Data[0].1 NEPOUŽÍVAT!!! M2U - main FBD/main FBD - *1-F4(OREF,M2U), 1-E3(cilinder control,cilinder control 02.zapadka h) Local:3:O.Data[0].2 BOOL NEPOUŽÍVAT!!! M3U - main FBD/main FBD - *1-F5(OREF,M3U), 1-E5(cilinder control, cilinder control 03.zapadka h) Local:3:O.Data[0].14 BOOL NEPOUŽÍVAT!!! fault - main FBD/main FBD - *3-G1(OREF, fault), 1-A2(OSRI, OSRI 04. InputBit), 1-A3(IREF, fault), 3-G1(BOR, BOR 01. Out) Local:3:O.Data[0].15 **BOOL** NEPOUŽÍVAT!!! Z LED - main FBD/main FBD - *1-B5(OREF,Z LED), 1-G2(BAND,BAND 01.Out) Local:3:O.Data[0].16 BOOL NEPOUŽÍVAT !!! MID - main FBD/main FBD - *1-F2(OREF,MID), 1-E2(cilinder control,cilinder control 01.zapadka l) Local:3:O.Data[0].17 **BOOL** NEPOUŽÍVAT!!! M2D - main FBD/main FBD - *1-F4(OREF,M2D), 1-E3(cilinder control,cilinder control 02.zapadka l) Local:3:O.Data[0].18 **BOOL** NEPOUŽÍVAT!!! M3D - main FBD/main FBD - *1-F5(OREF,M3D), 1-E5(cilinder control,cilinder control 03.zapadka l) Local:3:O.Data[0].19 **BOOL** NEPOUŽÍVAT!!! Vel - main FBD/main FBD - *3-F1(OREF, Vel), 3-E1(ventilator, ventilator 01.vent) Local:3:O.Data[0].20 BOOL NEPOUZIVAT !!! Ve2 - main FBD/main FBD - *3-F2(OREF, Ve2), 3-E2(ventilator, ventilator 02.vent) Local:3:O.Data[0].21 0 BOOL NEPOUŽÍVAT!!! Ve3 - main FBD/main FBD - *3-F3(OREF, Ve3), 3-E3(ventilator, ventilator 03.vent) Local:3:O.Data[0].31 NEPOUŽÍVAT!!! C LED - main FBD/main FBD - *1-B5(OREF,C LED), 1-A5(EQU,EQU 01.Dest) **■** M1D **BOOL** Kulicky_BPC_PGA NEPOUŽÍVAT!!! SIM Outputs Data.16 AliasFor: Base Tag: Local:3:O.Data[0].16 Constant No Read/Write External Access: MID - main FBD/main FBD - *1-F2(OREF,MID), 1-E2(cilinder control,cilinder control 01.zapadka l)

BOOL

Kulicky BPC PGA

		C. losers tope_pga Documents to	budio 5000 a rojecis akuneky_bi c_i GA.Ac
Mati (C) (C) (D)			
M1U (Continued)			
NEPOUŽÍVAT !!!			
AliasFor:	SIM_Outputs_Data.0		
Base Tag:	Local:3:O.Data[0].0		
Constant	No		
External Access:	Read/Write		
	$BD - *1-F2(OREF,M1U), 1-E2(cilinder_$	_control,cilinder_control_01.zap	adka_h)
Man	0	DOOL	Wallalan DDC DCA
M2D	0	BOOL	Kulicky_BPC_PGA
NEPOUŽÍVAT !!!			
AliasFor:	SIM_Outputs_Data.17		
Base Tag:	Local:3:O.Data[0].17		
Constant	No		
External Access:	Read/Write		
M2D - main_FBD/main_F	$BD - *1-F4(OREF, M2D), 1-E3(cilinder_$	_control,cilinder_control_02.zap	adka_l)
M2U	0	BOOL	Kulicky BPC PGA
NEPOUŽÍVAT !!!	U	BOOL	Kulicky_BFC_FGA
AliasFor:	SIM_Outputs_Data.1		
Base Tag:	Local:3:O.Data[0].1		
Constant	No		
External Access:	Read/Write		
M2U - main_FBD/main_F	BD - *1-F4(OREF,M2U), 1-E3(cilinder_	_control,cilinder_control_02.zap	adka_h)
M3D	0	BOOL	Kulicky BPC PGA
NEPOUŽÍVAT !!!	O	BOOL	Runcky_DI C_I GA
AliasFor:	SIM Outputs Data 19		
	SIM_Outputs_Data.18		
Base Tag:	Local:3:O.Data[0].18		
Constant	No		
External Access:	Read/Write		
M3D - main_FBD/main_F	BD - *1-F5(OREF,M3D), 1-E5(cilinder_	_control,cilinder_control_03.zap	adka_l)
M3U	0	BOOL	Kulicky BPC PGA
NEPOUŽÍVAT !!!			7
AliasFor:	SIM Outputs Data.2		
Base Tag:	Local:3:O.Data[0].2		
Constant	No		
	Read/Write		
External Access:		control cilia des control 02 zan	adra h
M3U - main_FBD/main_F.	BD - *1-F5(OREF,M3U), 1-E5(cilinder_	_control,cuinaer_control_03.zap	aaka_n)
No_Balls1	0	DINT	Kulicky_BPC_PGA
Počet kuliček, kolik se má	odpočítav v levém válci		
Constant	No		
External Access:	Read/Write		
)2(IREF No Ralls 1) 1-E2(cilind	er control,cilinder control 01.num balls),
2-B2(bcd to dec,bcd to d		·2(II:E1,110_Batts1), 1	
	ilace - Unknown Protection(Unknown P.	rotection), Unknown Protection(Unknown Protection)
No_Balls2	0	DINT	Kulicky_BPC_PGA
Počet kuliček, kolik se má	odpočítav v středním válci		
Constant	No		
External Access:	Read/Write		
No Balls2 - main FBD/ma	in FBD - *2-E2(OREF,No Balls2), 1-L	04(IREF,No Balls2), 1-E3(cilind	er control,cilinder control 02.num balls),
$2-\overline{D}$ 2(bcd to dec,bcd to		_ //	
	nlace - Unknown Protection(Unknown P	rotection), Unknown Protection(Unknown Protection)
No Rolls2	0	DINT	Vulida DDC DCA
No_Balls3		DINI	Kulicky_BPC_PGA
Počet kuliček, kolik se má			
Constant	No		
External Access:	Read/Write		
)5(IREF,No_Balls3), 1-E5(cilind	ler_control,cilinder_control_03.num_balls),
2-F2(bcd_to_dec,bcd_to_d			
No_Balls3 - Simulace/Simi	ılace - Unknown Protection(Unknown P	rotection), Unknown Protection(Unknown Protection)

SIM Iputs Data.4

SIM Iputs Data.5

Použití simulovaných vstupů

Použití simulovaných vstupů

IN BCD L1 - main FBD/main FBD - 2-B1(IREF,IN BCD L1), 2-B1(OREF,BCD L 1)

Kulicky_BPC_PGA (Controller) 2022-04-25 20:39:27 C:\Users\bpc pga\Documents\Studio 5000\Projects\Kulicky BPC PGA.ACD OSRI 01 FBD ONESHOT main FBD Constant No Read/Write External Access: OSRI 01 - main FBD/main FBD - *1-A1(IREF,START), *1-A1(OSRI,OSRI 01), *1-C1(BAND,BAND 02.In1) OSRI 02 FBD ONESHOT main FBD Constant No External Access: Read/Write OSRI 02 - main FBD/main FBD - *1-A2(IREF,STOP), *1-A2(OSRI,OSRI 02), *1-B2(BOR,BOR 02.In1) OSRI 03 FBD ONESHOT main_FBD Constant No Read/Write External Access: OSRI 03 - main FBD/main FBD - *3-B1(BOR,BOR 03.Out), *3-C1(OSRI,OSRI 03), *3-E1(ventilator,ventilator 01.clr fault), *3-E2(ventilator, ventilator 02.clr fault), *3-E3(ventilator, ventilator 03.clr fault) OSRI 04 FBD_ONESHOT main_FBD Constant No Read/Write External Access: OSRI 04 - main FBD/main FBD - *1-A2(OSRI,OSRI 04), *1-A3(IREF,fault), *1-B2(BOR,BOR 02.In2) OSRI 05 FBD ONESHOT main FBD Constant No External Access: Read/Write OSRI_05 - main_FBD/main_FBD - *3-A2(IREF,STOP), *3-A2(OSRI,OSRI_05), *3-B1(BOR,BOR_03.In2) **■** S1 0 **BOOL** Kulicky BPC PGA snimac L Constant No **External Access:** Read/Write S1 - main FBD/main FBD - 1-E2(cilinder control, cilinder control 01.senzor), 1-E2(IREF,S1) **■** S2 **BOOL** Kulicky_BPC_PGA snimac M Constant No External Access: Read/Write S2 - main FBD/main FBD - 1-E3(cilinder control, cilinder control 02.senzor), 1-E4(IREF,S2) **S3** 0 **BOOL** Kulicky BPC PGA snimac R Constant No External Access: Read/Write S3 - main FBD/main FBD - 1-E5(cilinder control, cilinder control 03.senzor), 1-E5(IREF,S3) **■** S4 0 BOOL. Kulicky BPC PGA spinac krabice Constant No Read/Write External Access: S4 - main FBD/main FBD - 1-G2(BAND,BAND 01.In4), 1-G3(IREF,S4) SIM Iputs Data 16#0000 0247 DINT Kulicky BPC PGA Použití simulovaných vstupů Local:3:I.Data[1] AliasFor: Base Tag: Local:3:I.Data[1] Constant No External Access: Read/Write **BOOL** SIM Iputs Data.3 Použití simulovaných vstupů IN BCD L0 - main FBD/main FBD - 2-B1(IREF,IN BCD L0), 2-B1(OREF,BCD L 0)

BOOL

BOOL

Kulicky_BPC_PGA (Controller)

```
SIM Iputs Data (Continued)
   IN BCD L2 - main FBD/main_FBD - 2-B1(IREF,IN_BCD_L2), 2-B1(OREF,BCD_L_2)
SIM Iputs Data.6
   Použití simulovaných vstupů
   IN BCD L3 - main FBD/main FBD - 2-B1(IREF,IN BCD L3), 2-B1(OREF,BCD L 3)
SIM Iputs Data.7
                                                                     BOOL
   Použití simulovaných vstupů
   IN BCD M0 - main FBD/main FBD - 2-D1(IREF,IN BCD M0), 2-D1(OREF,BCD M 0)
 SIM Iputs Data.8
   Použití simulovaných vstupů
   IN BCD M1 - main FBD/main FBD - 2-D1(IREF,IN BCD M1), 2-D1(OREF,BCD M 1)
 SIM Iputs Data.9
                                                                     BOOL
   Použití simulovaných vstupů
   IN BCD M2 - main FBD/main FBD - 2-D1(IREF,IN BCD M2), 2-D1(OREF,BCD M 2)
SIM Iputs Data.10
                                                                     BOOL
   Použití simulovaných vstupů
   IN BCD M3 - main FBD/main FBD - 2-D1(IREF,IN BCD M3), 2-D1(OREF,BCD M 3)
 SIM Iputs Data.11
   Použití simulovaných vstupů
   IN BCD R0 - main FBD/main FBD - 2-F1(IREF,IN BCD R0), 2-G1(OREF,BCD R 0)
                                                                     BOOL
 SIM Iputs Data.12
   Použití simulovaných vstupů
   IN BCD R1 - main FBD/main FBD - 2-F1(IREF,IN BCD R1), 2-G1(OREF,BCD R 1)
 SIM Iputs Data.13
                                                                     BOOL
   Použití simulovaných vstupů
   IN BCD R2 - main FBD/main FBD - 2-F1(IREF,IN_BCD_R2), 2-G1(OREF,BCD_R_2)
 SIM Iputs Data.14
   Použití simulovaných vstupů
   clr fault - main FBD/main FBD - 3-B1(BOR,BOR 03.In1), 3-B2(IREF,clr fault)
   IN BCD R3 - main FBD/main FBD - 2-F1(IREF,IN BCD R3), 2-G1(OREF,BCD R 3)
SIM Iputs Data.16
                                                                     BOOL
   Použití simulovaných vstupů
   ZHI - main FBD/main FBD - 3-EI(IREF,ZHI), 3-EI(ventilator, ventilator 01.zh)
SIM Iputs Data.17
                                                                     BOOL
   Použití simulovaných vstupů
   ZH2 - main FBD/main FBD - 3-E2(IREF,ZH2), 3-E2(ventilator, ventilator 02.zh)
SIM Iputs Data.18
   Použití simulovaných vstupů
   ZH3 - main FBD/main FBD - 3-E3(IREF,ZH3), 3-E3(ventilator, ventilator 03.zh)
SIM Outputs Data
                                  16#0000 0000
                                                                     DINT
                                                                                                        Kulicky BPC PGA
   Použití simulovaných výstupů
   AliasFor:
                                  Local:3:O.Data[0]
   Base Tag:
                                  Local:3:O.Data[0]
   Constant
                                  No
                                  Read/Write
   External Access:
                                                                     BOOL
 SIM Outputs Data.1
   Použití simulovaných výstupů
  M2U - main FBD/main FBD - *1-F4(OREF,M2U), 1-E3(cilinder control,cilinder control 02.zapadka h)
 SIM Outputs Data.2
                                                                     BOOL
   Použití simulovaných výstupů
   M3U - main FBD/main FBD - *1-F5(OREF,M3U), 1-E5(cilinder control, cilinder control 03.zapadka h)
SIM Outputs Data.14
                                  0
                                                                     BOOL
   Použití simulovaných výstupů
   fault - main FBD/main FBD - *3-G1(OREF,fault), 1-A2(OSRI,OSRI 04.InputBit), 1-A3(IREF,fault), 3-G1(BOR,BOR 01.Out)
 SIM Outputs Data.15
                                                                     BOOL
   Použití simulovaných výstupů
   Z LED - main FBD/main FBD - *1-B5(OREF,Z LED), 1-G2(BAND,BAND 01.Out)
SIM Outputs Data.16
   Použití simulovaných výstupů
   MID - main FBD/main FBD - *1-F2(OREF,MID), 1-E2(cilinder control, cilinder control 01.zapadka 1)
SIM Outputs Data.17
                                                                     BOOL
   Použití simulovaných výstupů
  M2D - main FBD/main FBD - *1-F4(OREF,M2D), 1-E3(cilinder control,cilinder control 02.zapadka l)
```

```
SIM Outputs Data (Continued)
                                    0
                                                                         BOOL
 SIM Outputs Data.18
   Použití simulovaných výstupů
   M3D - main FBD/main FBD - *1-F5(OREF,M3D), 1-E5(cilinder control, cilinder control 03.zapadka l)
 SIM Outputs Data.19
   Použití simulovaných výstupů
   Ve1 - main FBD/main FBD - *3-F1(OREF, Ve1), 3-E1(ventilator, ventilator 01.vent)
 SIM Outputs Data.20
   Použití simulovaných výstupů
   Ve2 - main FBD/main FBD - *3-F2(OREF, Ve2), 3-E2(ventilator, ventilator 02.vent)
 SIM Outputs Data.21
   Použití simulovaných výstupů
   Ve3 - main FBD/main FBD - *3-F3(OREF, Ve3), 3-E3(ventilator, ventilator 03.vent)
 SIM Outputs Data.31
   Použití simulovaných výstupů
   C LED - main FBD/main FBD - *1-B5(OREF,C LED), 1-A5(EQU,EQU 01.Dest)
■ START
                                    0
                                                                         BOOL
                                                                                                              Kulicky_BPC_PGA
                                    No
   Constant
                                    Read/Write
   External Access:
   START - main FBD/main FBD - 1-A1(IREF,START), 1-A1(OSRI,OSRI 01.InputBit)
■ STOP
                                    0
                                                                         BOOL
                                                                                                              Kulicky BPC PGA
   Constant
                                    No
   External Access:
                                    Read/Write
   STOP - main FBD/main FBD - 1-A2(IREF,STOP), 1-A2(OSRI,OSRI 02.InputBit), 3-A2(IREF,STOP), 3-A2(OSRI,OSRI 05.InputBit)
• TOC
                                    1000
                                                                         DINT
                                                                                                              Kulicky BPC PGA
   Constant
                                    No
   External Access:
                                    Read/Write
   TOC - main FBD/main FBD - 1-D2(IREF,TOC), 1-D4(IREF,TOC), 1-D6(IREF,TOC), 1-E2(cilinder control, cilinder control 01.dose period),
    1-E3(cilinder control, cilinder control 02.dose period), 1-E5(cilinder control, cilinder control 03.dose period)
TZH
                                    5000
                                                                         DINT
                                                                                                              Kulicky BPC PGA
   Constant
                                    No
   External Access:
                                    Read/Write
   TZH - main FBD/main FBD - 3-E1(ventilator, ventilator 01.max delay), 3-E2(IREF, TZH), 3-E2(IREF, TZH),
    3-E2(ventilator, ventilator 02.max delay), 3-E3(IREF,TZH), 3-E3(ventilator, ventilator 03.max delay)

■ Ve1

                                    0
                                                                         BOOL
                                                                                                              Kulicky BPC PGA
   NEPOUŽÍVAT!!!
   AliasFor:
                                    SIM Outputs Data.19
   Base Tag:
                                    Local:3:O.Data[0].19
   Constant
                                    No
   External Access:
                                    Read/Write
   Vel - main FBD/main FBD - *3-F1(OREF, Ve1), 3-E1(ventilator, ventilator_01.vent)

■ Ve2

                                                                         BOOL
                                                                                                              Kulicky_BPC_PGA
   NEPOUŽÍVAT!!!
   AliasFor:
                                    SIM Outputs Data.20
   Base Tag:
                                    Local:3:O.Data[0].20
   Constant
                                    No
   External Access:
                                    Read/Write
   Ve2 - main FBD/main FBD - *3-F2(OREF, Ve2), 3-E2(ventilator, ventilator 02.vent)
■ Ve3
                                                                         BOOL
                                                                                                              Kulicky BPC PGA
   NEPOUŽÍVAT!!!
   AliasFor:
                                    SIM Outputs Data.21
   Base Tag:
                                    Local:3:O.Data[0].21
   Constant
                                    No
                                    Read/Write
   External Access:
   Ve3 - main FBD/main FBD - *3-F3(OREF, Ve3), 3-E3(ventilator, ventilator 03.vent)
  ventilator 01
                                                                         ventilator
                                                                                                              main FBD
```

ventilator_01 (Continued)			
rizeni ventilatoru			
Constant	No		
External Access:	Read/Write		
ventilator 01 - main FBD/main	FBD - *1-E2(cilinder control,cilinder	_control_01.run), *3-C1(OSRI,OSRI_03.0	OutputBit), *3-E1(IREF,ZH1),
	*3-E2(IREF,TZH), *3-F1(OREF,Ve1),	*3-G1(BOR,BOR_01.In1)	
ventilator_01.EnableIn	1	BOOL	
rizeni ventilatoru Enable Input -	- System Defined Parameter		
ventilator_01.EnableOut	1	BOOL	
rizeni ventilatoru Enable Output	t - System Defined Parameter		
ventilator_01.on	0	BOOL	
rizeni ventilatoru	2	Door	
ventilator_01.zh	0	BOOL	
rizeni ventilatoru	0	DOOL	
ventilator_01.clr_fault rizeni ventilatoru	0	BOOL	
ventilator 01.fault	0	BOOL	
rizeni ventilatoru	V	BOOL	
ventilator_01.vent	0	BOOL	
rizeni ventilatoru	·	BOOL	
ventilator_01.max_delay	5000	DINT	
rizeni ventilatoru		_ · -	
112011			
ventilator 02		ventilator	main FBD
rizeni ventilatoru			_
Constant	No		
External Access:	Read/Write		
		_control_02.run), *3-C1(OSRI,OSRI_03.0	OutputBit), *3-E2(IREF,TZH),
	ator,ventilator_02), *3-F2(OREF,Ve2), ¹	*3-G1(BOR,BOR_01.In2)	
ventilator_02.EnableIn	1	BOOL	
rizeni ventilatoru Enable Input -	- System Defined Parameter		
ventilator_02.EnableOut	1	BOOL	
rizeni ventilatoru Enable Output	t - System Defined Parameter	Door	
ventilator_02.on	0	BOOL	
rizeni ventilatoru	0	BOOL	
ventilator_02.zh rizeni ventilatoru	0	BOOL	
ventilator 02.clr fault	0	BOOL	
rizeni ventilatoru	V	BOOL	
ventilator_02.fault	0	BOOL	
rizeni ventilatoru	· ·	Book	
ventilator_02.vent	0	BOOL	
rizeni ventilatoru			
ventilator_02.max_delay	5000	DINT	
rizeni ventilatoru			
ventilator_03		ventilator	main_FBD
rizeni ventilatoru			
Constant	No		
External Access:	Read/Write	1 00) the GI/OGDI OGDI 00	
		_control_03.run), *3-C1(OSRI,OSRI_03.0	OutputBit), *3-E3(IREF,TZH),
	ator,ventilator_03), *3-F3(OREF,Ve3),		
ventilator_03.EnableIn	Crystom Defined Denomentar	BOOL	
rizeni ventilatoru Enable Input -	- System Defined Parameter	BOOL	
ventilator_03.EnableOut rizeni ventilatoru Enable Output	t - System Defined Parameter	DOOL	
ventilator_03.on		BOOL	
rizeni ventilatoru	~	DOOL	
ventilator_03.zh	0	BOOL	
rizeni ventilatoru	•		
ventilator_03.clr_fault	0	BOOL	
rizeni ventilatoru			
ventilator_03.fault	0	BOOL	
rizeni ventilatoru			

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ventilator_03 (Continued)

ventilator 03.vent 0 BOOL

rizeni ventilatoru

ventilator 03.max delay 5000 DINT

rizeni ventilatoru

□ Z LED 0 BOOL Kulicky BPC PGA

NEPOUŽÍVAT!!!

AliasFor: SIM_Outputs_Data.15
Base Tag: Local:3:O.Data[0].15

Constant No

External Access: Read/Write

Z LED - main FBD/main FBD - *1-B5(OREF,Z LED), 1-G2(BAND,BAND 01.Out)

□ ZH1 0 BOOL Kulicky_BPC_PGA

NEPOUŽÍVAT !!!

AliasFor: SIM_Iputs_Data.16
Base Tag: Local:3:1.Data[1].16

Constant No External Access: Read/Write

ZH1 - main FBD/main FBD - 3-E1(IREF,ZH1), 3-E1(ventilator, ventilator 01.zh)

■ ZH2 0 BOOL Kulicky_BPC_PGA

NEPOUŽÍVAT!!!

AliasFor: SIM_Iputs_Data.17
Base Tag: Local:3:I.Data[1].17

Constant No

External Access: Read/Write

ZH2 - main FBD/main FBD - 3-E2(IREF,ZH2), 3-E2(ventilator, ventilator 02.zh)

□ ZH3 0 BOOL Kulicky_BPC_PGA

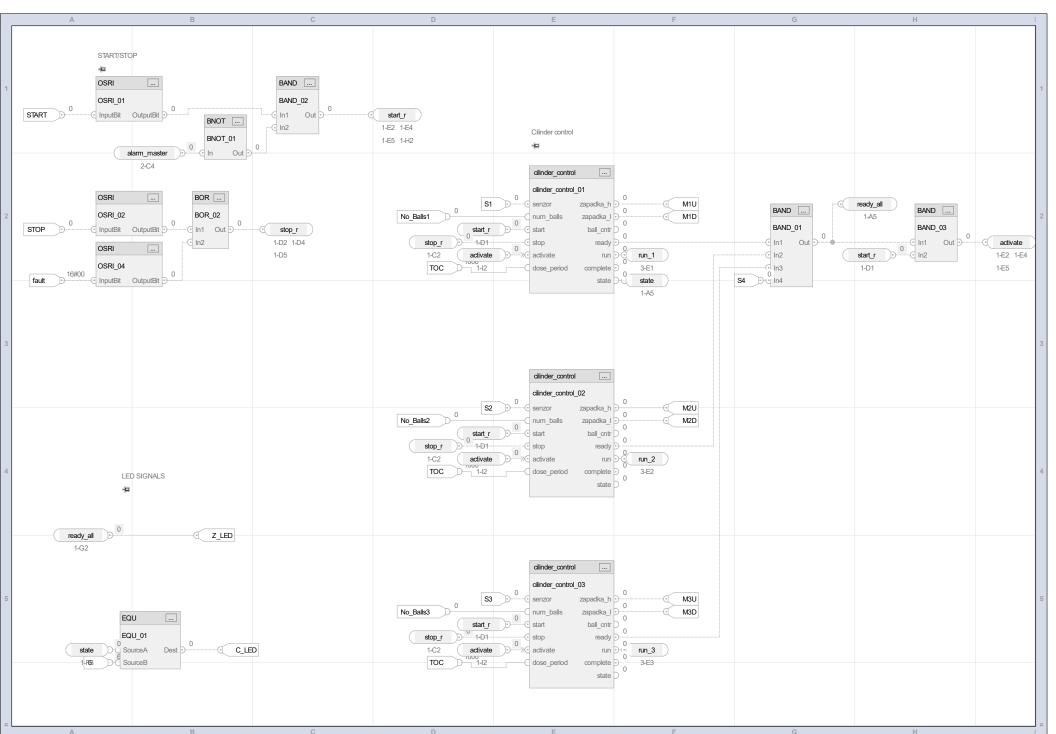
NEPOUŽÍVAT!!!

AliasFor: SIM_Iputs_Data.18
Base Tag: Local:3:1.Data[1].18

Constant No

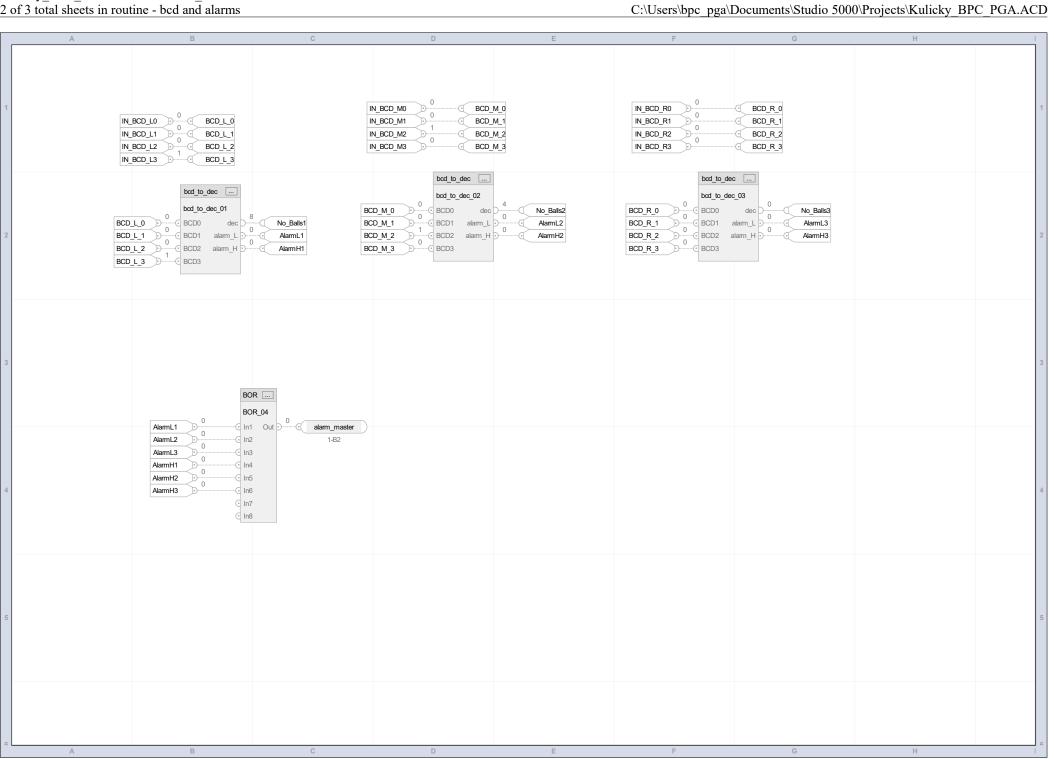
External Access: Read/Write

ZH3 - main_FBD/main_FBD - 3-E3(IREF,ZH3), 3-E3(ventilator,ventilator_03.zh)

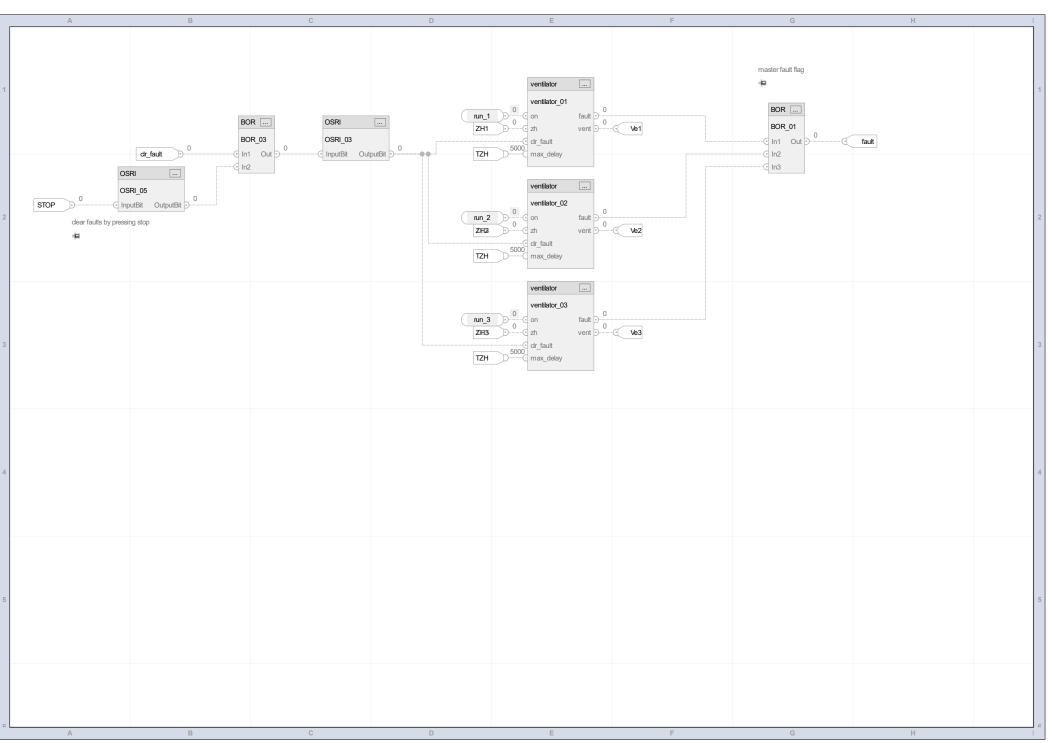


main FBD - Function Block Diagram Kulicky_BPC_PGA:MainTask:main_FBD

2022-04-25 20:39:44 C:\Users\bpc pga\Documents\Studio 5000\Projects\Kulicky BPC PGA.ACD



C:\Users\bpc pga\Documents\Studio 5000\Projects\Kulicky BPC PGA.ACD



Alarmy - Routine Listing (Source Not Available) Kulicky_BPC_PGA:Simulace:Simulace

2022-04-25 20:39:51

Routine Listing (Source Not Available)

C:\Users\bpc pga\Documents\Studio 5000\Projects\Kulicky BPC PGA.ACD

Page 24

Unable to print the Routine: Source not available

BCD - Routine Listing (Source Not Available)
Kulicky_BPC_PGA:Simulace:Simulace

Page 25 2022-04-25 20:39:51

C:\Users\bpc pga\Documents\Studio 5000\Projects\Kulicky BPC PGA.ACD

Routine Listing (Source Not Available)

Unable to print the Routine: Source not available

Logix Designer

Simulace - Routine Listing (Source Not Available) Kulicky_BPC_PGA:Simulace:Simulace

Page 26 2022-04-25 20:39:52

Routine Listing (Source Not Available)

C:\Users\bpc pga\Documents\Studio 5000\Projects\Kulicky BPC PGA.ACD

Unable to print the Routine: Source not available

Kulicky_BPC_PGA Add-On Instruction Signature Listing

Page 27 2022-04-25 20:39:52

C:\Users\bpc_pga\Documents\Studio 5000\Projects\Kulicky_BPC_PGA.ACD

Data Context: bcd_to_dec <definition>

Butta Contenti oca_to_ucc | definition

Signature Listing

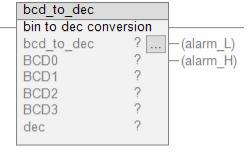
Data Context: bcd to dec <definition>

bcd_to_dec v1.0

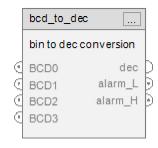
bin to dec conversion

Available Languages





🛂 Function Block



Structured Text bcd_to_dec();

Parameters

Required	Name	Data Type	Usage	Description
X	bcd_to_dec	bcd_to_dec	InOut	bin to dec conversion
	- 11 T	DOOL	.	
	EnableIn	BOOL	Input	
	EnableOut	BOOL	Output	
	BCD0	BOOL	Input	
	BCD1	BOOL	Input	
	BCD2	BOOL	Input	
	BCD3	BOOL	Input	
	dec	DINT	Output	
	alarm_L	BOOL	Output	
	alarm H	BOOL	Output	

Extended Description

Execution

Condition Description

EnableIn is true

Revision v1.0 Notes

Add-On Instructions - Instruction Definition
Kulicky_BPC_PGA:Add-On Instructions
Data Context: bcd_to_dec <definition>

Page 29 2022-04-25 20:39:55

C:\Users\bpc_pga\Documents\Studio 5000\Projects\Kulicky_BPC_PGA.ACD

dec.3 - bcd to dec/Logic - *1-B2(OREF,dec.3), 1-B2(IREF,BCD3)

Kulicky_BPC_PGA:Add-On Instructions:bcd_to_dec

Data Type Size: 36 byte (s)
Data Context: bcd_to_dec <definition>

Data Context: bcd_to_dec <d< th=""><th>efinition></th><th>1 10</th><th>, , , , , , , , , , , , , , , , , , ,</th></d<>	efinition>	1 10	, , , , , , , , , , , , , , , , , , ,
Name	Default	Data Type	Scope
alarm_H	0	BOOL	bcd_to_dec
Usage:	Output Parameter		
Required:	No		
Visible:	Yes		
External Access:	Read/Write		
alarm_H - bcd_to_dec/L	ogic - *1-E2(OREF,alarm_H), 1-D2(GR	T,GRT_01.Dest)	
alarm L	0	BOOL	bed to dec
Usage:	Output Parameter		
Required:	No		
Visible:	Yes		
External Access:	Read/Write		
	ogic - *1-E2(OREF,alarm_L), 1-D1(LES	(,LES_01.Dest)	
BCD0	0	BOOL	bed to dee
Usage:	Input Parameter		
Required:	No		
Visible:	Yes		
External Access:	Read/Write		
	ic - 1-B1(IREF,BCD0), 1-B1(OREF,dec.0	9)	
BCD1	0	BOOL	bcd_to_dec
Usage:	Input Parameter	BOOL	oca_to_acc
Required:	No		
Visible:	Yes		
External Access:	Read/Write		
	ic - 1-B1(IREF,BCD1), 1-B1(OREF,dec	1)	
BCD2	0	BOOL	bcd to dec
Usage:	Input Parameter	BOOL	oed_to_dee
Required:	No		
Visible:	Yes		
External Access:	Read/Write		
	ic - 1-B1(IREF,BCD2), 1-B1(OREF,dec.2	2)	
DCD2	0	DOOL	h.d 4. d
BCD3	O	BOOL	bcd_to_dec
Usage:	Input Parameter		
Required:	No V		
Visible:	Yes		
External Access:	Read/Write	2)	
BCD3 - bcd_to_dec/Logi	ic - 1-B2(IREF,BCD3), 1-B2(OREF,dec.:	3)	
dec	0	DINT	bcd_to_dec
Usage:	Output Parameter		
Required:	No		
Visible:	Yes		
External Access:	Read Only		
dec - bcd_to_dec/Logic -	\cdot 1-D1(LES,LES_01.SourceA), 1-D2(GR)		<i>1-D2(IREF,dec)</i>
dec.0	0	BOOL	
dec.0 - bcd_to_dec/Logic	c - *1-B1(OREF,dec.0), 1-B1(IREF,BCD		
dec.1	0	BOOL	
	c - *1-B1(OREF,dec.1), 1-B1(IREF,BCD		
dec.2	0	BOOL	
dec.2 - bcd_to_dec/Logic	c - *1-B1(OREF,dec.2), 1-B1(IREF,BCD		
dec.3	0	BOOL	
don 2 had to do -/1	*1 D2/ODEE dos 2\ 1 D2/IDEE DCD	2)	

bcd_to_dec Instruction Definition - Local Tag Listing Kulicky_BPC_PGA:Add-On Instructions:bcd_to_dec

Page 31 2022-04-25 20:39:55

Data Context: bcd to dec <definition>

C:\Users\bpc_	_pga\Documents\Studio	5000\Projects\Ku	iicky_BPC_PGA.AC	٦,

Name	Default	Data Type	Scope	
GRT_01		FBD_COMPARE	bcd_to_dec	
Usage:	Local Tag			
External Access:	None			
GRT_01 - bcd_to_dec/Lo	gic - *1-D2(GRT,GRT_01), *1-D2	(IREF,9), *1-D2(IREF,dec), *1-E2(OREF,alarm_	<i>H</i>)	
LES_01		FBD_COMPARE	bcd_to_dec	
Usage:	Local Tag			
External Access:	None			

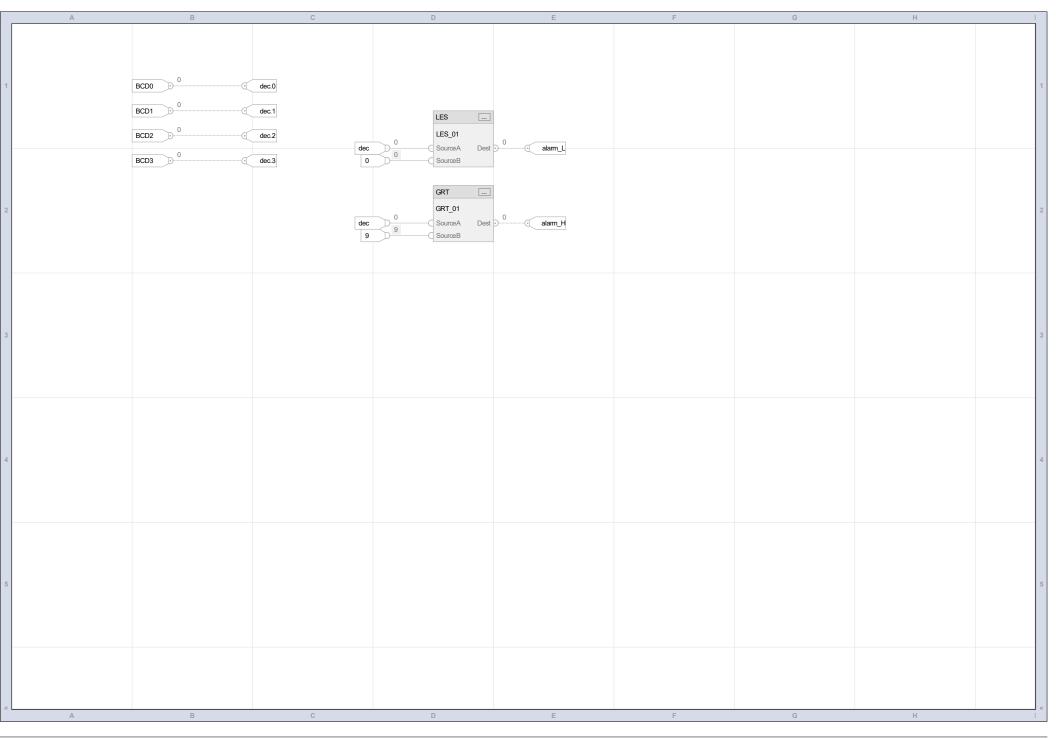
 $LES_01 - bcd_to_dec/Logic - *1-D1(LES, LES_01), *1-D2(IREF, 0), *1-D2(IREF, dec), *1-E2(OREF, alarm_L)$

bcd_to_dec Instruction Definition - Logic Routine

Kulicky_BPC_PGA:Add-On Instructions:bcd_to_dec:Logic

Page 32 2022-04-25 20:40:02 C:\Users\bpc pga\Documents\Studio 5000\Projects\Kulicky BPC PGA.ACD

1 of 1 total sheets in routine Data Context: bcd to dec <definition>



Data Context: cilinder_control <definition>

🖶 cilinder_control v1.0

rizeni zapadek valce

Available Languages

📋 Relay Ladder



🛂 Function Block



Structured Text cilinder_control();

Parameters

Required	Name	Data Type	Usage	Description
X	cilinder_control	cilinder_control	InOut	rizeni zapadek valce
	EnableIn	BOOL	Input	
	EnableOut	BOOL	Output	
	senzor	BOOL	Input	
	num_balls	DINT	Input	
	start	BOOL	Input	
	stop	BOOL	Input	
	activate	BOOL	Input	
	zapadka_h	BOOL	Output	
	zapadka_l	BOOL	Output	
	ball_cntr	DINT	Output	
	ready	BOOL	Output	
	run	BOOL	Output	
	dose_period	DINT	Input	
	complete	BOOL	Output	
	state	DINT	Output	
			_	

Add-On Instructions - Instruction Definition Kulicky_BPC_PGA:Add-On Instructions

Page 34 2022-04-25 20:40:03 C:\Users\bpc pga\Documents\Studio 5000\Projects\Kulicky BPC PGA.ACD

Data Context: cilinder control <definition>

Extended Description

Execution

Description Condition EnableIn is true

rizeni zapadek valce

Revision v1.0 Notes

cilinder_control Instruction Definition - Parameter Listing
Kulicky_BPC_PGA:Add-On Instructions:cilinder_control
Data Type Size: 120 byte (s)
Data Context: cilinder_control <definition>

2022-04-25 20:40:03 C:\Users\bpc_pga\Documents\Studio 5000\Projects\Kulicky_BPC_PGA.ACD

Name	Default	Data Type	Scope
ball_cntr	0	DINT	cilinder_control
Usage:	Output Parameter		
Required:	No		
Visible:	Yes		
External Access:	Read/Write		
ball_cntr - cilinder_control/Logic -			
buii_cmi - ciiinaei_coniioi/Logic	- #09, #70, #40, #09, #99		
complete	0	BOOL	cilinder_control
Usage:	Output Parameter	2002	= = = = = = = = = = = = = = = = = = =
Required:	No		
Visible:	Yes		
External Access:	Read/Write		
complete - cilinder control/Logic -			
	,,		
dose_period	1000	DINT	cilinder_control
Usage:	Input Parameter		
Required:	No		
Visible:	Yes		
External Access:	Read/Write		
dose_period - cilinder_control/Log	gic - #20, *#19		
	_	DD 77	111 1
num_balls	5	DINT	cilinder_control
Usage:	Input Parameter		
Required:	No		
Visible:	Yes		
External Access:	Read/Write		
num_balls - cilinder_control/Logic	e - #70		
ready	0	BOOL	cilinder_control
Usage:	Output Parameter	BOOL	clinidel_condoi
Required:	No		
Visible:	Yes		
External Access:	Read/Write		
ready - cilinder control/Logic - #4			
reday - clithaer_control/Logic - #4	0, 1#12, 1#14, 1#/		
run	0	BOOL	cilinder_control
Usage:	Output Parameter		_
Required:	No		
Visible:	Yes		
External Access:	Read/Write		
run - cilinder control/Logic - *#40			
senzor	0	BOOL	cilinder_control
Usage:	Input Parameter		
Required:	No		
Visible:	Yes		
External Access:	Read/Write		
senzor - cilinder_control/Logic - #	11		
stant	0	POOL	ailindan ct1
start	0	BOOL	cilinder_control
Usage:	Input Parameter		
Required:	No		
Visible:	Yes		
External Access: start - cilinder control/Logic - #10	Read/Write		
Start - Carmaci_ComfonDogic - #10	2, 111, 11TO		
state	0	DINT	cilinder_control
Usage:	Output Parameter		
Required:	No		
Visible:	Yes		
External Access:	Read/Write		
state - cilinder control/Logic - *#I			
_ = =			

cilinder control Instruction Definition - Parameter Listing

Kulicky_BPC_PGA:Add-On Instructions:cilinder_control Data Type Size: 120 byte (s)

C:\Users\bpc pga\Documents\Studio 5000\Projects\Kulicky BPC PGA.ACD

Page 36

2022-04-25 20:40:03

Data Context: cilinder control <definition>

BOOL cilinder control stop Usage:

Input Parameter Required: No

Visible: Yes External Access: Read/Write

stop - cilinder control/Logic - #27

zapadka_h **BOOL** cilinder_control

Output Parameter Usage:

Required: No Visible: Yes External Access: Read/Write

zapadka_h - cilinder_control/Logic - *#3, *#38, *#43, *#52, *#62, *#76, *#85, *#94

BOOL cilinder_control zapadka l

Output Parameter Usage:

Required: Visible: Yes **External Access:** Read/Write

zapadka l-cilinder control/Logic - *#39, *#4, *#44, *#53, *#63, *#77, *#86, *#95

Default

Data Context: cilinder control <definition>

Name

C:\Users\bpc pga\Documents\Studio 5000\Projects\Kulicky BPC PGA.ACD

Scope

Maine	Delault	Data Type	Scope
_force_cleaning	0	BOOL	cilinder_control
Usage:	Local Tag		
External Access:	None		
_force_cleaning - cilinder_	control/Logic - #97, *#29, *#31		
_state	0	DINT	cilinder_control
Usage:	Local Tag		
External Access:	None		
_state - cilinder_control/Lo	egic - #108, #11, #28, #36, *#103	, *#106, *#2, *#32, *#41, *#47, *#59, *#68,	*#71, *#82, *#91, *#98
_tim1		FBD_TIMER	cilinder_control
Usage:	Local Tag		
External Access:	None		
_tim1 - cilinder_control/Lo	gic - *#22		
_tim1.TimerEnable	0	BOOL	
_tim1.TimerEnable - cilinde	er_control/Logic	#64, *#66, *#78, *#80, *#87, *#89	
_tim1.PRE	0	DINT	
_tim1.PRE - cilinder_contr	ol/Logic - *#20		
_tim1.Reset	0	BOOL	
	rol/Logic - *#21, *#23, *#58, *#6	57, *#81, *#90	
_tim1.DN	0	BOOL	
_tim1.DN - cilinder_contro	l/Logic - #56, #65, #79, #88		
tim2		FBD_TIMER	cilinder control
Usage:	Local Tag	_	_
External Access:	None		
tim2 - cilinder control/Lo	gic - *#24		
tim2.Reset	0	BOOL	
tim2.Reset - cilinder cont	rol/Logic - *#25		

Data Type

Kulicky_BPC_PGA:Add-On Instructions:cilinder_control:Logic Total number of lines in routine: 110

Data Context: cilinder_control <definition>

```
1 if S:FS then //init.
       <u>_state</u> := 0; //idle
 3
       zapadka_h := 0;
       zapadka_1 := 0;
 4
 5
       run := 0;
       complete := 0;
 7
       ready := 0;
 8 end_if;
 9
10 ///READY signaů
11 if(senzor and _state=1) then
12
       ready := 1;
13 else
       ready :=0;
14
15 end_if;
16
17 ///TIMERS
18 //casovac davkovani
19 dose_period := 2000;
20 tim1.PRE := dose period/4;
21 _tim1.Reset := S:FS;
22 TONR(_tim1);
23 tim1.Reset := 0;
24 TONR(_tim2);
25 _tim2.Reset := 0;
26
27 if stop then
28
       if _state = 6 then
29
            _force_cleaning :=1; //second stop press
30
       else
31
            _force_cleaning:=0;
32
            _state := 6; //cleaning
       end_if;
33
34 end_if;
35
36 case _state of
       0: //idle
37
38
           zapadka_h :=0;
39
           zapadka 1 :=0;
40
           run :=0;
41
           if start then _state:=1; end_if;
42
       1: // plneni valce
43
           zapadka_h :=0;
           zapadka_l :=1;
44
45
           run :=0;
46
           if start and ready then
47
                _state:=2; //start cycle
                ball_cntr :=0; //reset cntr
48
49
                complete :=0; //clear flag
50
           end if;
51
       2: //zavreno_1
52
           zapadka_h :=1;
53
            zapadka_l :=1;
54
            run :=1;
55
            _tim1.TimerEnable :=1;
           if _tim1.DN then
56
57
                _tim1.TimerEnable :=0;
                _tim1.Reset :=1;
58
```

Data Context: cilinder control <definition>

Total number of lines in routine: 110

```
59
                 state :=3;
 60
            end_if;
 61
        3: //vypadnuti kulicky
             zapadka_h :=1;
 62
 63
             zapadka 1 :=0;
 64
             tim1.TimerEnable :=1;
            if _tim1.DN then
 65
                 _tim1.TimerEnable :=0;
 66
 67
                _tim1.Reset :=1;
                 _state :=4;
 68
                 ball_cntr := ball_cntr+1;
 69
                 if ball_cntr >= num_balls then
 70
 71
                     _state :=1; //konec cyklu
 72
                     complete :=1;
 73
                 end_if;
 74
            end_if;
 75
        4: //zavreno 2
            zapadka_h :=1;
 76
 77
            zapadka_l :=1;
 78
             tim1.TimerEnable :=1;
 79
            if _tim1.DN then
                 _tim1.TimerEnable :=0;
 80
                 _tim1.Reset :=1;
 81
 82
                 _state :=5;
 83
            end if;
        5: //vpadnuti kulicky
 84
 85
            zapadka h :=0;
 86
            zapadka l :=1;
 87
             tim1.TimerEnable :=1;
            if _tim1.DN then
 88
 89
                 _tim1.TimerEnable :=0;
                 _tim1.Reset :=1;
 90
                 _state :=2;
 91
            end_if;
 92
        6: //cleaning - waiting
 93
 94
            zapadka h :=0;
 95
             zapadka_l :=1;
 96
             run:=0;
 97
             if _force_cleaning then //manual or automat cleaning
                 _state := 0; //idle state
 98
                 ball_cntr :=0;
 99
                 complete :=0;
100
101
            end_if;
102
             if start then
                 state:=1; //cancel cleaning by pressing start
103
            end_if;
104
105
        else
             _state :=0;
106
107 end case;
108 state :=_state;
109
110
```

Data Context: first_scan <definition>

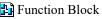
first_scan v1.0

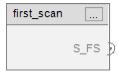
Available Languages











Structured Text first_scan();

Parameters

Required	Name	Data Type	Usage	Description
X	first_scan	first_scan	InOut	
	EnableIn	BOOL	Input	
	EnableOut	BOOL	Output	
	S_FS	BOOL	Output	

Extended Description

Execution

Condition Description

EnableIn is true

Revision v1.0 Notes

first_scan Instruction Definition - Parameter Listing

Kulicky_BPC_PGA:Add-On Instructions:first_scan

C:\Users\bpc_pga\Documents\Studio 5000\Projects\Kulicky_BPC_PGA.ACD

Page 41

2022-04-25 20:40:09

Data Type Size: 4 byte (s)

Data Context: first_scan <definition>

ame	Default	Data Type	Scope
S_FS	0	BOOL	first_scan
Usage:	Output Parameter		
Required:	No		
Visible:	Yes		
External Access:	Read Only		
S_FS - first_scan/Logic - *#1	·		

 first_scan Instruction Definition - Local Tag Listing

 Kulicky_BPC_PGA:Add-On Instructions:first_scan

 Data Context: first_scan <definition>
 C:\User:

Page 42 2022-04-25 20:40:09

C:\Users\bpc_pga\Documents\Studio 5000\Projects\Kulicky_BPC_PGA.ACD

Name Default Data Type Scope

No Tags Exist

first_scan Instruction Definition - Logic Routine

Kulicky_BPC_PGA:Add-On Instructions:first_scan:Logic Total number of lines in routine: 1
Data Context: first_scan <definition>

2022-04-25 20:40:11 C:\Users\bpc_pga\Documents\Studio 5000\Projects\Kulicky_BPC_PGA.ACD

Page 43

1 S_FS:= S:FS;

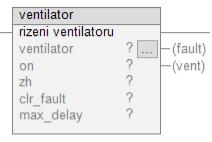
Data Context: ventilator <definition>

🖶 ventilator v1.0

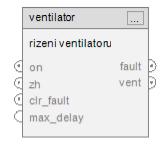
rizeni ventilatoru

Available Languages





🛂 Function Block



Structured Text ventilator();

Parameters

Name	Data Type	Usage	Description
ventilator	ventilator	InOut	rizeni ventilatoru
EnableIn	BOOL	Input	
EnableOut	BOOL	Output	
on	BOOL	Input	
zh	BOOL	Input	
clr_fault	BOOL	Input	
fault	BOOL	Output	
vent	BOOL	Output	
max_delay	DINT	Input	
	ventilator EnableIn EnableOut on zh clr_fault fault vent	ventilator ventilator EnableIn BOOL EnableOut BOOL on BOOL zh BOOL clr_fault BOOL fault BOOL vent BOOL	ventilatorventilatorInOutEnableInBOOLInputEnableOutBOOLOutputonBOOLInputzhBOOLInputclr_faultBOOLInputfaultBOOLOutputventBOOLOutput

Extended Description

Execution

Condition Description

EnableIn is true

Revision v1.0 Notes

ventilator Instruction Definition - Parameter Listing

Kulicky_BPC_PGA:Add-On Instructions:ventilator

2022-04-25 20:40:11 C:\Users\bpc pga\Documents\Studio 5000\Projects\Kulicky BPC PGA.ACD

Page 45

Data Type Size: 172 byte (s)

External Access:

Read/Write

 $zh-ventilator/Logic-1-B1(TONR,TONR_01.Reset),\ 1-B2(IREF,zh),\ 1-B3(BNOT,BNOT_02.In),\ 1-B3(IREF,zh)$

Data Context: ventilator <definition>

Name	Default	Data Type	Scope
clr_fault	0	BOOL	ventilator
Usage:	Input Parameter		
Required:	No		
Visible:	Yes		
External Access:	Read/Write		
clr_fault - ventilator/Logic - 1-	E2(BOR,BOR_02.In2), 1-E3(IREF,	clr_fault)	
fault	0	BOOL	ventilator
Usage:	Output Parameter		
Required:	No		
Visible:	Yes		
External Access:	Read/Write		
fault - ventilator/Logic - *1-F2	(OREF,fault), 1-F2(SETD,SETD_0	l.Out)	
max delay	5000	DINT	ventilator
Usage:	Input Parameter		
Required:	No		
Visible:	Yes		
External Access:	Read/Write		
max_delay - ventilator/Logic -	1-B1(TONR,TONR_01.PRE), 1-B2	TREF,max_delay), 1-C2(TONR,Te	ONR_02.PRE), 1-C3(IREF,max_delay)
on	0	BOOL	ventilator
Usage:	Input Parameter		
Required:	No		
Visible:	Yes		
External Access:	Read/Write		
on - ventilator/Logic - 1-B1(IR 1-C1(OREF,vent)	EF,on), 1-B1(TONR,TONR_01.Time	erEnable), 1-B2(BNOT,BNOT_01.	.In), 1-B2(IREF,on), 1-B3(IREF,on),
vent	0	BOOL	ventilator
Usage:	Output Parameter		
Required:	No		
Visible:	Yes		
External Access:	Read/Write		
vent - ventilator/Logic - *1-C1			
zh	0	BOOL	ventilator
Usage:	Input Parameter		
Required:	No		
Visible:	Yes		

ventilator Instruction Definition - Local Tag Listing Kulicky_BPC_PGA:Add-On Instructions:ventilator

Page 46 2022-04-25 20:40:11

Data Context: ventilator <definition>

C:\Users\bpc pga\Documents\Studio 5000\Projects\Kulicky BPC PGA.ACD

Default Name **Data Type** Scope ventilator BNOT 01 FBD BOOLEAN NOT Local Tag Usage: **External Access:** None

BNOT 01 - ventilator/Logic - *1-B2(BNOT,BNOT 01), *1-B3(IREF,on), *1-C2(TONR,TONR 02.TimerEnable)

BNOT 02 FBD BOOLEAN NOT ventilator

Local Tag Usage: External Access: None

BNOT 02 - ventilator/Logic - *1-B3(BNOT,BNOT 02), *1-B3(IREF,zh), *1-C2(TONR,TONR 02.Reset)

BOR 01 FBD BOOLEAN OR ventilator

Local Tag Usage: External Access: None

BOR 01 - ventilator/Logic - *1-B1(TONR, TONR 01.DN), *1-C2(TONR, TONR 02.DN), *1-D1(BOR, BOR 01), *1-F2(SETD, SETD 01.Set)

BOR 02 FBD BOOLEAN OR ventilator

Local Tag Usage: External Access: None

BOR 02 - ventilator/Logic - *1-D2(first scan, first scan 01.S FS), *1-E2(BOR, BOR 02), *1-E3(IREF, clr fault), *1-F2(SETD, SETD 01.Reset)

ventilator first scan 01 first_scan

Usage: Local Tag **External Access:** None

first scan 01 - ventilator/Logic - *1-D2(first scan, first scan 01), *1-E2(BOR, BOR 02.In1)

first scan 01.EnableIn 1 BOOL

Enable Input - System Defined Parameter

first scan 01.EnableOut **BOOL**

Enable Output - System Defined Parameter

SETD 01 DOMINANT SET ventilator

Usage: Local Tag **External Access:** None

SETD 01 - ventilator/Logic - *1-D1(BOR,BOR 01.Out), *1-E2(BOR,BOR 02.Out), *1-F2(OREF,fault), *1-F2(SETD,SETD 01)

TONR 01 FBD_TIMER ventilator

Usage: Local Tag External Access: None

TONR 01 - ventilator/Logic - *1-B1(TONR, TONR 01), *1-B2(IREF, max delay), *1-B2(IREF, on), *1-B2(IREF, zh), *1-D1(BOR, BOR 01. In1)

TONR 02 FBD TIMER ventilator

Usage: Local Tag External Access: None

TONR 02 - ventilator/Logic - *1-B2(BNOT,BNOT 01.Out), *1-B3(BNOT,BNOT 02.Out), *1-C2(TONR,TONR 02), *1-C3(IREF,max delay),

*1-D1(BOR,BOR 01.In2)

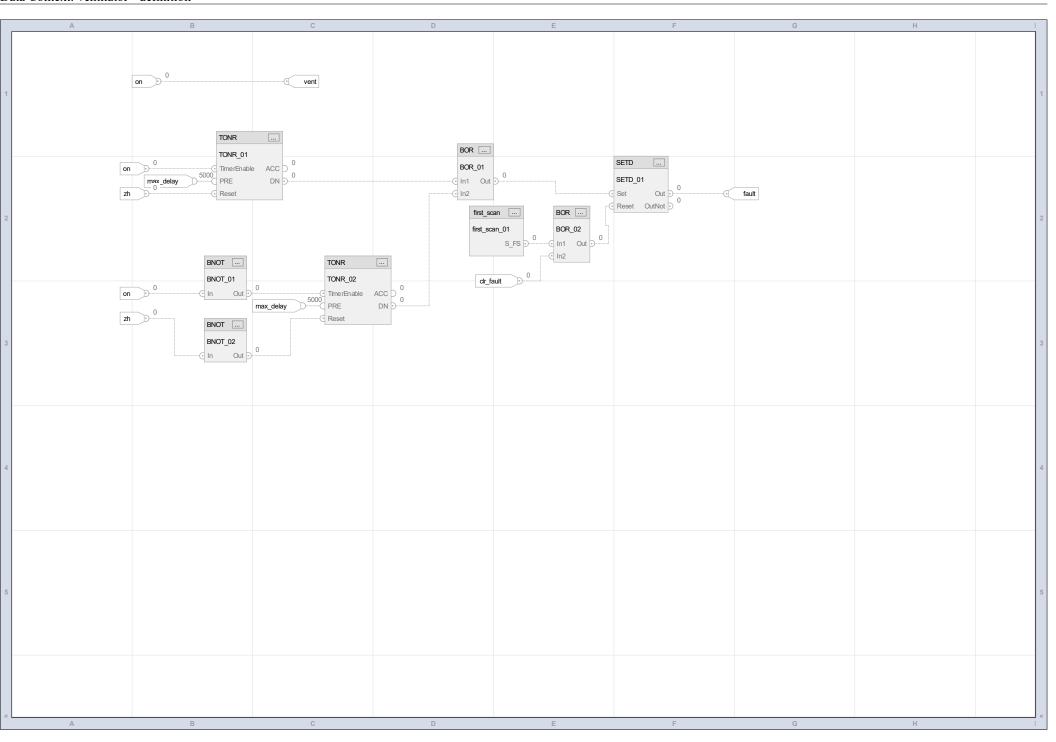
ventilator Instruction Definition - Logic Routine

Kulicky_BPC_PGA:Add-On Instructions:ventilator:Logic

1 of 1 total sheets in routine

Data Context: ventilator <definition>

2022-04-25 20:40:18 C:\Users\bpc_pga\Documents\Studio 5000\Projects\Kulicky_BPC_PGA.ACD



Page 47

Read/Write

Data type Name: bcd_to_dec

Description:

bin to dec conversion

Size: 36 byte(s)

External Access:

Name	Value	Data Type	Style	
EnableIn	, uiuc	BOOL	Decimal	
Enable Input - System De	fined Parameter			
External Access:	Read Only			
EnableOut		BOOL	Decimal	
Enable Output - System D	Defined Parameter			
External Access:	Read Only			
BCD0		BOOL	Decimal	
External Access:	Read/Write			
BCD1		BOOL	Decimal	
External Access:	Read/Write			
BCD2		BOOL	Decimal	
External Access:	Read/Write			
BCD3		BOOL	Decimal	
External Access:	Read/Write	Bool	Beelmar	
dec		DINT	Decimal	
External Access:	Read Only	DINI	Decimal	
alarm L		BOOL	Decimal	
External Access:	Read/Write	DOOL	Decimal	
alaum II		BOOL	Decimal	
alarm H		DUUL	Decimal	

Data type Name: cilinder_control

Description:

rizeni zapadek valce

Size: 120 byte(s)

Name	Value	Data Type	Style	
EnableIn Enable Input - System Def External Access:	ined Parameter Read Only	BOOL	Decimal	
EnableOut Enable Output - System Do External Access:	efined Parameter Read Only	BOOL	Decimal	
senzor External Access:	Read/Write	BOOL	Decimal	
num_balls External Access:	Read/Write	DINT	Decimal	
start External Access:	Read/Write	BOOL	Decimal	
stop External Access:	Read/Write	BOOL	Decimal	
activate External Access:	Read/Write	BOOL	Decimal	
zapadka_h External Access:	Read/Write	BOOL	Decimal	
zapadka_l External Access:	Read/Write	BOOL	Decimal	
ball_cntr External Access:	Read/Write	DINT	Decimal	
ready External Access:	Read/Write	BOOL	Decimal	
run External Access:	Read/Write	BOOL	Decimal	
dose_period External Access:	Read/Write	DINT	Decimal	
complete External Access:	Read/Write	BOOL	Decimal	
state External Access:	Read/Write	DINT	Decimal	

first_scan - Add-On-Defined Data Type Kulicky_BPC_PGA (Controller)

2022-04-25 20:40:19 C:\Users\bpc_pga\Documents\Studio 5000\Projects\Kulicky_BPC_PGA.ACD

Page 50

Data type Name: first_scan

Description:

Size: 4 byte(s)

Name	Value	Data Type	Style	
EnableIn		BOOL	Decimal	
Enable Input - System Det	fined Parameter			
External Access:	Read Only			
EnableOut		BOOL	Decimal	
Enable Output - System D	efined Parameter			
External Access:	Read Only			
S_FS		BOOL	Decimal	
External Access:	Read Only			

Data type Name: ventilator

Description: rizeni ventilatoru

Size: 172 byte(s)

External Access:

Read/Write

Name	Value	Data Type	Style
EnableIn		BOOL	Decimal
Enable Input - System Defined Par	ameter		
External Access:	Read Only		
EnableOut		BOOL	Decimal
Enable Output - System Defined Pa			
External Access:	Read Only		
on		BOOL	Decimal
External Access:	Read/Write		
zh		BOOL	Decimal
External Access:	Read/Write		
clr_fault		BOOL	Decimal
External Access:	Read/Write		
fault		BOOL	Decimal
External Access:	Read/Write		
vent		BOOL	Decimal
External Access:	Read/Write		
max_delay		DINT	Decimal
l ₁			

Local

1

1756 Backplane, 1756-A17 : Local Modules

Local: [2] Emulate 5570 Kulicky_BPC_PGA

Type: Emulate 5570 Studio 5000® Logix

EmulateTM Controller

Vendor: Rockwell Automation/Allen-Bradley

Slot:2Electronic Keying:Exact MatchRevision:33.11Status:StandbyModule Fault:OfflineInhibit FlagOff

Parent:

Vendor ID:

1 Local: [3] 1756-MODULE SIM_I_O

Type: 1756-MODULE Generic 1756 Module Parent: Local Vendor: Rockwell Automation/Allen-Bradley Vendor ID: 1

Slot:3Electronic Keying:DisabledRevision:1.1Status:StandbyModule Fault:OfflineInhibit FlagOff

Use Unicast: No

Module Defined Value Data Type

Configuration Tag

Local:3:C AB:1756_MODULE:C:0

.Data SINT[400]

Module Properties

Kulicky_BPC_PGA	1
Label does not exist	
Tag Listing	2
MainTask	
main_FBD	
main_FBD	
Function Block Diagram	21
Simulace	
Simulace	
Alarmy	
Routine Listing (Source Not Available)	24
BCD	
Routine Listing (Source Not Available)	25
Simulace	
Routine Listing (Source Not Available)	26
Add-On Instruction Signature Listing	
Add-On Instructions	
Instruction Definition	28
bcd_to_dec	
Parameter Listing	30
Local Tag Listing	31
Logic Routine	32
Instruction Definition	33
cilinder control	
Parameter Listing	35
Local Tag Listing	
Logic Routine	
Instruction Definition	
first scan	
Parameter Listing	41
Local Tag Listing	
Logic Routine	
Instruction Definition	
ventilator	
Parameter Listing	45
Local Tag Listing	
Logic Routine	
Data Types	
Add-On-Defined Data Type	18
rad on Defined Data Type	0