Group Logbook

20th of March 2015 2IO70 Version 1

The purpose of this document is to record how much time the group, and each member individually, has spent on any given part of the project.

Group 16

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Dat Phung

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Validation

We validate the work plan by comparing the expected time it would take to finish each task in the schedule with the time spent working on the corresponding task in the collective logbook. The logbook serves as an indication of how much time was spent on a certain task by each member of the group.

Total time spent

Project	Rolf	Wigger	Stefan	Tudor	Maarten	Dat	Total
5.2c	4	8	19	0	0	0	31
Work Plan	16	8	2	8	16	18	68
Machine Design	20	12	24	6	5	4	71
Presentation	0	0	0	5	9	6	15
Software Specification	20	10	20	9	9	18	86
Software Design	6	7	5	4	3	7	27
Total	66	45	65	27	42	53	298

Conclusion

It is clear from these numbers that we so far overestimated, or underreported, the amount of work needed for each part of the project. It must however be said that the table above only includes work towards the major deliverables, this means that work on individual assignments (such as the the abstract and maintaining the logbooks) and group talks (to resolve issues) have not been included, leading to an underestimation of the amount of time spent on the project. Either way, it seems that some members have contributed significantly less than others. This has now been addressed in the group however, and going forward we will try to create a more even workload for all.

Dat Phung

Date	Time Start	Time Stop Action	Time
5-Feb		16:00 Individual Assignment	5:00
6-Feb		10:50 Meeting	0:20
6-Feb		15:15 Work Plan	4:15
10-Feb		13:00 Meeting	0:30
10-Feb		14:30 Work Plan	1:20
11-Feb		14:00 Orientation	1:15
13-Feb		11:15 Meeting	0:35
13-Feb		14:45 Work Plan	3:30
24-Feb		11:30 Meeting	1:00
24-Feb	11:30	15:30 Design of the Sorting Machine	4:00
25-Feb		17:10 Preparation presentation	4:10
26-Feb	14:30	16:00 Preparation presentation	1:30
27-Feb	8:15	8:45 Practising presentation	0:30
27-Feb	10:39	10:54 Presentation	0:15
27-Feb	11:00	11:30 Meeting	0:30
27-Feb	11:30	15:00 Work Plan	3:30
3-Mar	10:30	10:50 Meeting	0:20
3-Mar	11:00	14:00 Work Plan	3:00
3-Mar	15:00	18:00 Finished Work Plan	3:00
4-Mar	13:30	15:00 Inventarisation of states for SS	1:30
4-Mar	15:30	17:00 Validation high level specification to SLR for MD	1:30
5-Mar	15:00	16:00 Cross-reading Stefan's abstract	1:00
6-Mar	11:45	13:00 Inventarisation of all states	1:15
6-Mar	13:00	15:30 Elaborating all states with Tudor	2:30
6-Mar	18:30	19:30 Cross-reading Machine Design	1:00
10-Mar	10:30	11:00 Meeting	0:30
10-Mar	11:00	11:30 Writing purpose for SS	0:30
10-Mar	12:30	13:00 Added dependency of display	0:30
10-Mar		14:45 Testing and defining timers	1:45
10-Mar		22:00 Peer assessment	0:15
11-Mar		12:45 Inventarisation of tasks to be done for SS	0:05
11-Mar		14:00 Better definition timers, relation previous/current phas	
11-Mar		15:00 Validation safety properties	1:00
11-Mar		18:00 Finishing SS	3:00
11-Mar		20:00 Cross-reading SS, validation use-case	0:30
13-Mar		11:30 Meeting	1:00
13-Mar		12:00 Defining tasks to be done, updating I/O tables	0:30
13-Mar		12:30 Design decisions	0:30
13-Mar		14:00 Description of states and I/O	1:30
13-Mar		16:30 Wrote outline of tasks for SD, wrote purpose	1:30
17-Mar		11:15 Meeting	0:45
17-Mar	11:15	11:30 Tasks	0:15

17-Mar	11:30	15:19 Coding standard	3:49
17-Mar	15:40	16:00 Coding standard	0:20
18-Mar	10:45	11:30 Cross-reading Stefan's java	0:45
18-Mar	11:30	15:00 Dinges	3:30
18-Mar	21:40	21:49 Loop invariant termination	0:09
20-Mar	10:30	11:00 Meeting	0:30
20-Mar	11:00	12:30 Dividing subjects presentation	1:30
20-Mar	12:30	Preparation presentation	0:00

Maarten Keet

Date	Time Start	Time Stop Action	Time
3-Feb	8:45	9:30 Visit lecture	0:45
3-Feb	9:30	11:30 Meeting group	2:00
4-Feb	13:45	14:15 Visit group meeting	0:30
5-Feb	20:30	1:15 Writing abstract	4:45
6-Feb	10:30	11:00 Visit tutor meeting	0:30
6-Feb	11:00	11:19 Visit personal meeting	0:19
10-Feb	11:00	12:19 Constructing Work Plan	1:19
10-Feb	12:30	13:15 Tutor meeting	0:45
10-Feb	14:50	16:30 Getting material and checking it	1:40
10-Feb	21:25	23:00 Reading slides	1:35
11-Feb	12:30	12:49 Attending group meeting	0:19
11-Feb	12:49	14:00 Adjusting Work Plan	1:10
13-Feb	10:30	11:30 Attending tutor meeting	1:00
13-Feb	11:30	13:45 Work Plan	2:15
14-Feb	0:00	15:40 Work Plan	1:40
14-Feb	16:15	17:25 Work Plan	1:10
15-Feb	15:15	16:45 Work Plan	1:30
24-Feb	10:30	11:30 Tutor meeting	1:00
24-Feb	11:30	15:30 Machine design	4:00
24-Feb	21:55	23:15 Minutes	1:20
25-Feb	13:30	17:10 Presentation	3:40
25-Feb	22:40	23:19 Presentation	0:39
25-Feb	23:19	0:00 Practise presentation	0:40
26-Feb	14:19	14:30 Presentation	0:10
26-Feb	15:49	17:10 Presentation	1:20
26-Feb	18:00	19:19 Presentation	1:19
26-Feb	22:15	0:04 Presentation	1:49
27-Feb	8:15	8:49 Practise presentation	0:34
27-Feb	9:20	9:49 Preparing together	0:29
27-Feb	10:00	10:30 Attending presentation	0:30
27-Feb	10:30	10:45 Giving presentation	0:15
27-Feb	0:00	11:30 Tutor meeting	0:30
27-Feb	11:30	14:19 Work plan	2:49
27-Feb	17:30	18:00 Cross reading Validation Machine Design	0:30
1-Mar	15:49	17:30 Minutes	1:40
1-Mar	19:25	19:50 Logbook	0:25
1-Mar	21:20	21:55 Logbook	0:35
2-Mar	0:00	0:40 Logbook	0:40
2-Mar	13:09	13:25 Agenda	0:15
2-Mar	16:45	16:50 Agenda	0:05
2-Mar	19:34	20:30 Work Plan	0:55
3-Mar	10:45	10:54 Meeting	0:09

11:04	11:15 Work Plan	0:10
11:30	13:09 Work Plan	1:39
15:30	17:19 Work Plan	1:49
13:40	13:45 Fixing git	0:05
13:45	13:54 Reread Software Specification	0:09
14:04	15:10 Inventory States	1:05
15:30	16:00 Make test cases Priorities to Machine Design	0:30
16:30	16:50 Make a validation	0:20
15:34	16:00 Agenda, prepare meeting	0:25
10:34	11:15 Meeting	0:40
11:49	12:00 Installing UPPAAL	0:10
12:04	13:15 Inventory states	1:10
10:30	10:54 Meeting	0:24
12:34	12:49 Added dependency display	0:15
13:00	13:39 Logbook	0:39
14:00	15:00 Validation and testing	1:00
10:30	11:24 Meeting	0:54
11:55	12:45 Design Decisions Software Specification	0:50
13:00	13:20 Describing states RunT - WWT	0:20
14:55	15:00 Checking what to do	0:05
15:04	16:30 Trying to start with Software Design	1:25
11:30	14:00 Making minor changes to Software Spec document	2:30
10:15	10:20 Reading minutes last meeting	0:05
11:10	11:45 Putting Machine Design together	0:35
12:00	12:30 Preparing presentation	0:30
	11:30 15:30 13:40 13:45 14:04 15:30 16:30 15:34 10:34 11:49 12:04 10:30 12:34 13:00 14:00 10:30 11:55 13:00 14:55 15:04 11:30 10:15 11:10	11:30 13:09 Work Plan 15:30 17:19 Work Plan 13:40 13:45 Fixing git 13:45 13:54 Reread Software Specification 14:04 15:10 Inventory States 15:30 16:00 Make test cases Priorities to Machine Design 16:30 16:50 Make a validation 15:34 16:00 Agenda, prepare meeting 10:34 11:15 Meeting 11:49 12:00 Installing UPPAAL 12:04 13:15 Inventory states 10:30 10:54 Meeting 12:34 12:49 Added dependency display 13:00 13:39 Logbook 14:00 15:00 Validation and testing 10:30 11:24 Meeting 11:55 12:45 Design Decisions Software Specification 13:00 13:20 Describing states RunT - WWT 14:55 15:00 Checking what to do 15:04 16:30 Trying to start with Software Design 11:30 14:00 Making minor changes to Software Spec document 10:15 10:20 Reading minutes last meeting 11:10 11:45 Putting Machine Design together

Rolf Verschuuren

Date	Time Start	Time Stop Action	Time
5-Feb	11:00	16:00 Individual Assignment (Abstract Project Guide)	5:00
6-Feb	10:30	10:50 Meeting	0:20
6-Feb	11:00	15:15 Orientation	4:15
9-Feb	11:30	14:00 Misc, logbook	2:30
10-Feb	10:45	12:20 Misc	1:35
10-Feb	12:30	13:00 Meeting	0:30
10-Feb	13:10	14:30 Work Plan + Orientation	1:20
13-Feb	10:40	11:15 Meeting	0:35
13-Feb	11:15	14:45 Work Plan	3:30
14-Feb	11:00	11:30 Work Plan	0:30
14-Feb	11:30	12:00 Misc, logbook	0:30
15-Feb	13:45	15:00 Work Plan	1:15
24-Feb	10:30	11:30 Meeting	1:00
25-Feb	11:30	15:30 Machine Design	4:00
25-Feb	12:30	17:30 Design of the Sorting Machine	5:00
25-Feb	9:00	10:00 Design of the Sorting Machine	1:00
27-Feb	11:00	11:45 Meeting	0:45
27-Feb	11:45	17:00 Design of the Sorting Machine + Work Plan	5:15
3-Mar	10:30	11:30 Meeting	1:00
3-Mar	11:30	17:30 Work Plan v3	6:00
		Machine design validation and testing, work plan v3,	
4-Mar		1	6:00
6-Mar	11:00	<u> </u>	0:45
c M	11 47	Software specification inputs and outputs, finite state	F 15
6-Mar		,	5:15
10-Mar		O .	1:00
10-Mar	11:30	•	5:00
11-Mar	12:30	Software specification finalizing document, writing 18:30 validations, documenting decisions	6:00
13-Mar			0:30
13-Mar		<u> </u>	5:00
17-Mar			0:30
17-Mar		<u> </u>	4:00
18-Mar		<u> </u>	4:30
20-Mar		<u> </u>	0:00

Stefan van den Berg

Date	Time Start	Time Stop Action	Time
3-Feb	9:30	12:00 Orientation	2:30
4-Feb	12:45	14:10 Indivual Assignment	1:25
5-Feb	12:00	16:00 Indivual Assignment	4:00
6-Feb	8:30	15:15 Orientation	6:45
10-Feb	10:45	12:19 Orientation	1:34
10-Feb	12:30	13:00 Meeting	0:30
10-Feb	13:10	16:30 Orientation	3:20
11-Feb	12:45	18:19 Orientation	5:34
11-Feb	18:19	19:00 Orientation	0:40
12-Feb	16:30	16:45 Orientation	0:15
12-Feb	17:45	18:09 Orientation	0:24
13-Feb	13:00	13:25 Misc (Reading Minutes)	0:25
15-Feb	10:30	10:45 Design of the Sorting Machine	0:15
24-Feb	10:30	11:30 Meeting	1:00
24-Feb	11:30	16:30 Design of the Sorting Machine	5:00
25-Feb	12:00	17:00 Design of the Sorting Machine	5:00
25-Feb	22:00	23:00 Design of the Sorting Machine	1:00
27-Feb	9:00	9:45 Presentation	0:45
27-Feb	10:00	11:00 Presentation	1:00
27-Feb	11:00	11:30 Meeting	0:30
27-Feb	11:30	16:30 Design of the Sorting Machine	5:00
28-Feb	11:30	12:30 Design of the Sorting Machine	1:00
28-Feb	17:45	18:15 Design of the Sorting Machine	0:30
3-Mar	10:30	10:50 Meeting	0:20
3-Mar	11:00	13:30 Worked on Machine interface	2:30
3-Mar	13:15	14:30 Worked on Machine interface	1:15
3-Mar	14:30	14:45 Helped tudor with Test cases	0:15
3-Mar	14:45	17:00 Help on the Work plan	2:15
4-Mar	13:00	15:00 Inputs and outputs + dependencies	2:00
4-Mar	15:00	17:00 Worked on validation and test case of the Machine Design	2:00
4-Mar	19:00	19:20 Put together files for the Machine Design	0:20
6-Mar	10:30	11:30 Meeting	1:00
6-Mar	11:30	13:00 Helped with the state diagram	1:30
6-Mar	13:15	15:30 Learned UPPAAL worked on the UPPAAL model	2:15
8-Mar	11:00	11:30 Worked on UPPAAL	0:30
10-Mar	10:30	11:00 Meeting	0:30
10-Mar	11:00	16:00 Worked on UPPAAL	5:00
11-Mar	12:45	13:30 Finished UPPAAL	0:45
11-Mar	13:30	18:00 Helped finishing software specifications	4:30
13-Mar	10:30		0:45
13-Mar	11:15	16:00 Improved software specifications	4:45

Tudor Petrescu

Date	Time Start	Time Stop Action	Time
6-Feb	10:30	10:50 Meeting	0:20
6-Feb	11:00	15:15 Work Plan	4:15
13-Feb	11:15	14:45 Work Plan	3:30
3-Mar	10:30	10:50 Meeting	0:20
3-Mar	11:00	15:30 Machine Design	4:30
4-Mar	16:00	17:00 System Validation and Testing (Machine Design)	1:00
6-Mar	10:30	11:30 Meeting	1:00
6-Mar	11:39	15:00 Software Specification	2:21
10-Mar	10:30	10:54 Meeting	0:24
10-Mar	12:00	14:15 Software Specification	2:15
10-Mar	15:19	16:00 Software Specification	1:40
13-Mar	10:30	11:30 Meeting	1:00
13-Mar	11:30	15:00 Software Specification	2:30
17-Mar	10:30	11:10 Meeting	0:40
17-Mar	11:10	15:09 Software Design	3:00
17-Mar	15:30	16:00 Software Design	0:30

Wigger Boelens

Date T	Time Start Tim	ne Stop Action	Time
6-feb.	10:30	10:50 Meeting	0:20
6-feb.	11:00	15:15 Orientation	4:15
6-feb.	15:20	16:45 Individual Assignment	1:25
10-feb.	12:30	13:00 Meeting	0:30
10-feb.	13:10	14:30 Orientation	1:20
11-feb.	12:45	14:00 Orientation	1:15
13-feb.	10:30	11:30 Attending tutor meeting	1:00
13-feb.	11:30	13:45 Working on 5.2c	2:15
14-feb.	14:00	15:40 Working on 5.2c	1:40
14-feb.	16:15	17:25 Working on 5.2c	1:10
15-feb.	15:15	16:45 Working on 5.2c	1:30
24-feb.	10:30	11:30 Tutor meeting	1:00
24-feb.	11:30	15:30 Machine design	4:00
25-feb.	13:30	17:10 Machine design	3:40
25-feb.	22:40	23:20 Machine design	0:40
25-feb.	23:20	0:00 Machine design	0:40
26-feb.	14:20	14:30 Machine design	0:10
26-feb.	15:50	17:10 Machine design	1:20
26-feb.	18:00	19:20 Machine design	1:20
27-feb.	10:00	10:45 Attending presentation	0:45
27-feb.	11:00	11:30 Tutor meeting	0:30
27-feb.	11:30	14:20 Work plan	2:50
1-mrt.	19:25	19:50 Logbook	0:25
1-mrt.	21:20	21:55 Logbook	0:35
2-mrt.	0:00	0:40 Logbook	0:40
2-mrt.	13:10	13:25 Work Plan	0:15
2-mrt.	16:45	16:50 Work Plan	0:05
2-mrt.	19:35	20:30 Work Plan	0:55
3-mrt.	10:45	10:55 Meeting	0:10
3-mrt.	11:05	11:15 Work Plan	0:10
3-mrt.	11:30	13:10 Work Plan	1:40
3-mrt.	15:30	17:20 Work Plan	1:50
4-mrt.	13:40	13:45 Sketching States	0:05
4-mrt.	13:45	13:55 Reread Software Specification	0:10
4-mrt.	14:05	15:10 Sketching States	1:05
4-mrt.	15:30	16:00 Validating	0:30
4-mrt.	16:30	16:50 Validating	0:20
6-mrt.	10:35	11:15 Meeting	0:40
6-mrt.	11:50	12:00 Installing UPPAAL	0:10
6-mrt.	12:05	13:15 Sketching States	1:10
10-mrt.	10:35	11:00 Meeting	0:25
10-mrt.	11:00	13:35 Updating the group logbook	2:35
10-mrt.	13:35	13:50 updating the sketches	0:15
10-mrt. 11-mrt.	12:45	13:30 Validating	0:45
11-mrt.	13:30	18:00 Software Specifications	4:30
13-mrt.	10:30	11:15 Meeting	0:45
17-mrt.	10:30	11:10 Meeting	0:40
17-1111t. 17-mrt.	11:10	18:00 Software Design	6:50
17 -1111 (.	11.10	10.00 Doltware Design	0.50