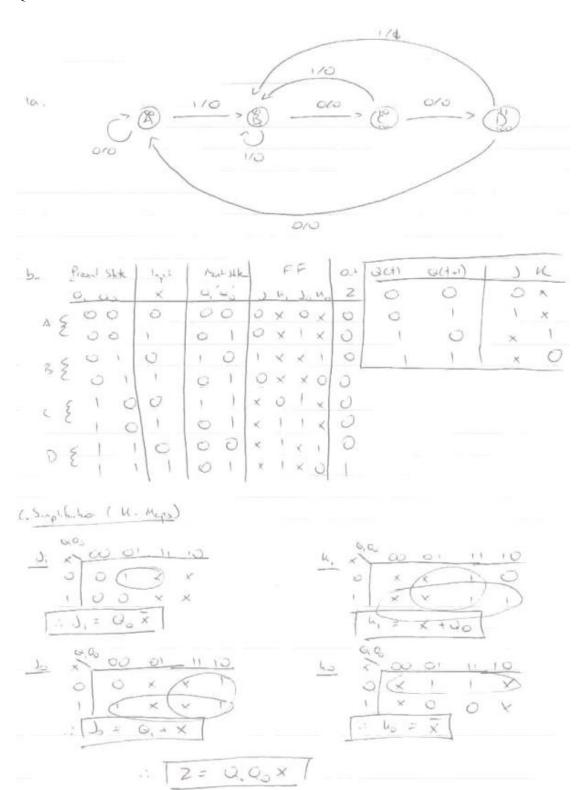
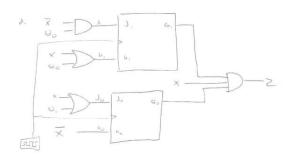
## Homework 2 - Solutions

Q1.





Q2.

## (1) Assume:

input: X; output: Y

state variables:

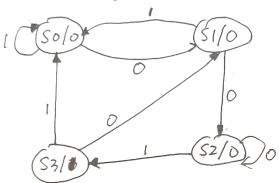
So: no valid input; Y=0

SI: "O" was detected; Y=0

S2: "00" was detected; Y=0

53: 'oo!' was detected; Y=1

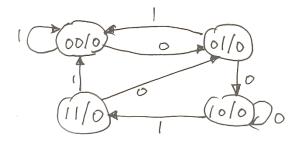
## (2) state diagram:

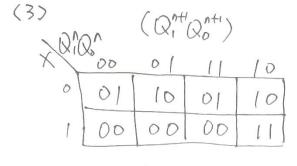


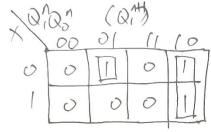
minimized states

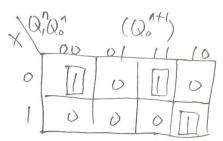
There are 4 states totally, so the number of bits for FF is n=2,

Assign: So=00, SI=01, SZ=10, S3=11









$$\begin{cases} Q_{i}^{n+1} = Q_{i}^{n} \overline{Q}_{o}^{n} + \overline{X} \overline{Q}_{i}^{n} Q_{o}^{n} \\ Q_{o}^{n+1} = X Q_{i}^{n} \overline{Q}_{o}^{n} + \overline{X} (Q_{i}^{n} \odot Q_{o}^{n}) \end{cases}$$

The simplified state equations.

We can derive the exitation equations:

$$\begin{cases} D_{i} = Q_{i}^{n} \overline{Q}_{0}^{n} + \overline{\chi} \overline{Q}_{i}^{n} Q_{0}^{n} \\ D_{0} = \chi Q_{i}^{n} \overline{Q}_{0}^{n} + \overline{\chi} (Q_{i}^{n} O Q_{0}^{n}) \end{cases}$$

- (4) .....
- (5) There is on invalid state for the circuit, so it can startup automatically.