

# 1. Description

## 1.1. Project

Project Name	LineFollower_RE
Board Name	custom
Generated with:	STM32CubeMX 6.8.0
Date	04/01/2023

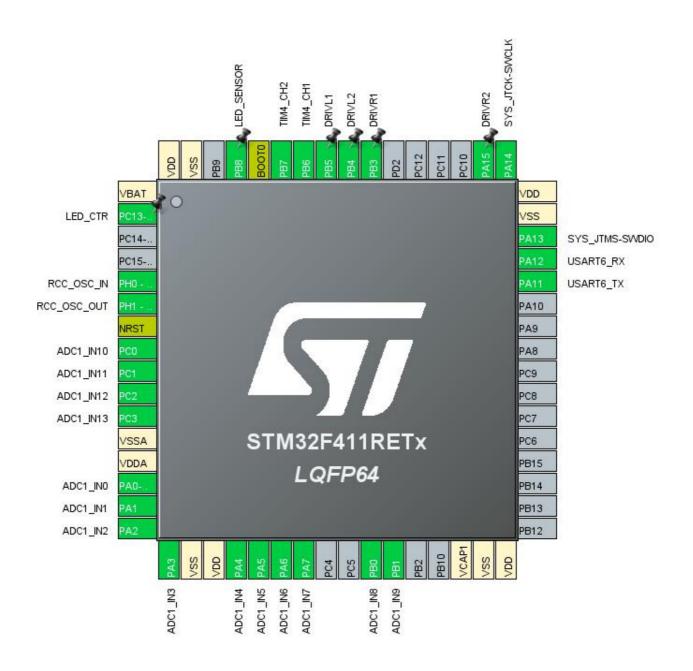
## 1.2. MCU

MCU Series	STM32F4
MCU Line	STM32F411
MCU name	STM32F411RETx
MCU Package	LQFP64
MCU Pin number	64

## 1.3. Core(s) information

Core(s)	Arm Cortex-M4

# 2. Pinout Configuration



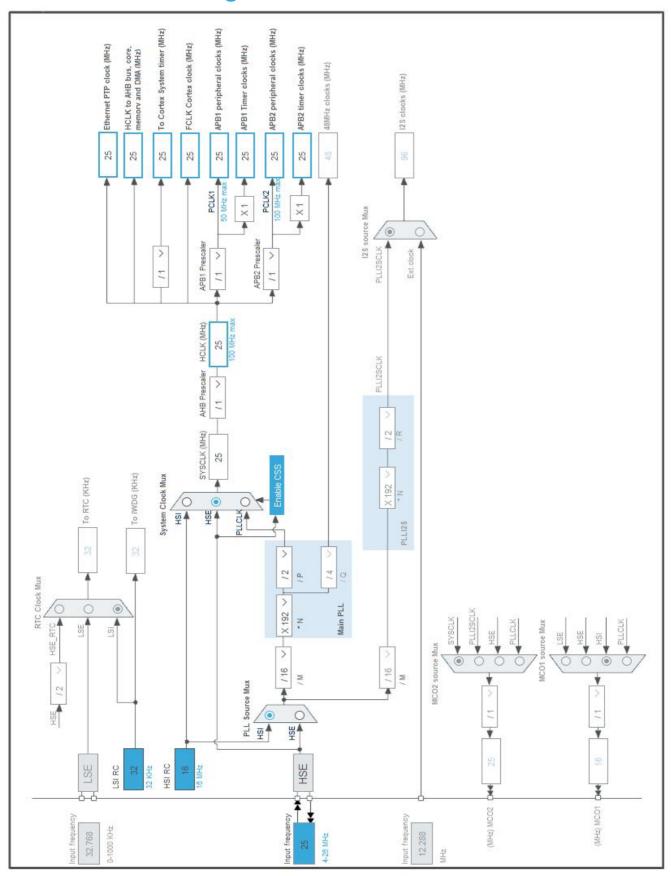
# 3. Pins Configuration

Pin Number	Pin Name	Pin Type	Alternate	Label
LQFP64	(function after reset)		Function(s)	
1	VBAT	Power		
2	PC13-ANTI_TAMP *	I/O	GPIO_Output	LED_CTR
5	PH0 - OSC_IN	I/O	RCC_OSC_IN	
6	PH1 - OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
8	PC0	I/O	ADC1_IN10	
9	PC1	I/O	ADC1_IN11	
10	PC2	I/O	ADC1_IN12	
11	PC3	I/O	ADC1_IN13	
12	VSSA	Power		
13	VDDA	Power		
14	PA0-WKUP	I/O	ADC1_IN0	
15	PA1	I/O	ADC1_IN1	
16	PA2	I/O	ADC1_IN2	
17	PA3	I/O	ADC1_IN3	
18	VSS	Power		
19	VDD	Power		
20	PA4	I/O	ADC1_IN4	
21	PA5	I/O	ADC1_IN5	
22	PA6	I/O	ADC1_IN6	
23	PA7	I/O	ADC1_IN7	
26	PB0	I/O	ADC1_IN8	
27	PB1	I/O	ADC1_IN9	
30	VCAP1	Power		
31	VSS	Power		
32	VDD	Power		
44	PA11	I/O	USART6_TX	
45	PA12	I/O	USART6_RX	
46	PA13	I/O	SYS_JTMS-SWDIO	
47	VSS	Power	<del>-</del>	
48	VDD	Power		
49	PA14	I/O	SYS_JTCK-SWCLK	
50	PA15 *	I/O	GPIO_Output	DRIVR2
55	PB3 *	I/O	GPIO_Output	DRIVR1
56	PB4 *	I/O	GPIO_Output	DRIVL2
57	PB5 *	I/O	GPIO_Output	DRIVL1

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
58	PB6	I/O	TIM4_CH1	
59	PB7	I/O	TIM4_CH2	
60	воото	Boot		
61	PB8 *	I/O	GPIO_Output	LED_SENSOR
63	VSS	Power		
64	VDD	Power		

<sup>\*</sup> The pin is affected with an I/O function

# 4. Clock Tree Configuration



# 5. Software Project

## 5.1. Project Settings

Name	Value
Project Name	LineFollower_RE
Project Folder	C:\Users\Bgs\Documents\studia\6sem\linefollower\LineFollower_RE
Toolchain / IDE	Makefile
Firmware Package Name and Version	STM32Cube FW_F4 V1.27.1
Application Structure	Advanced
Generate Under Root	No
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

## 5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	
Enable Full Assert	No

## 5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_ADC1_Init	ADC1
4	MX_TIM4_Init	TIM4
5	MX_USART6_UART_Init	USART6

# 6. Power Consumption Calculator report

### 6.1. Microcontroller Selection

Series	STM32F4
Line	STM32F411
мси	STM32F411RETx
Datasheet	DS10314_Rev6

### 6.2. Parameter Selection

Temperature	25
Vdd	1.7

## 6.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

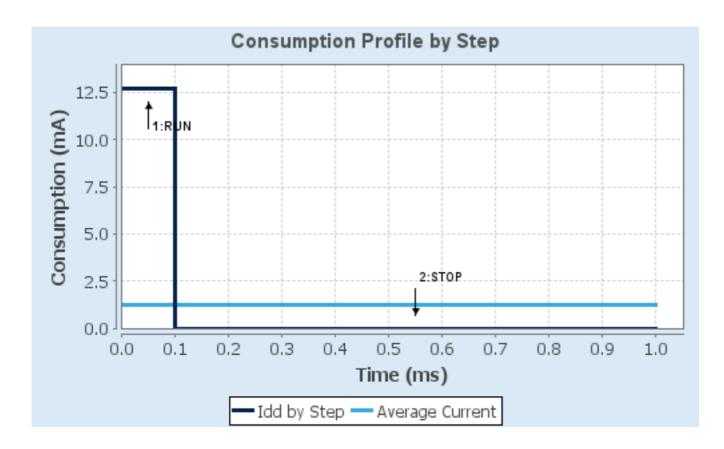
## 6.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP
Vdd	1.7	1.7
Voltage Source	Battery	Battery
Range	Scale1-High	No Scale
Fetch Type	SRAM	n/a
CPU Frequency	100 MHz	0 Hz
Clock Configuration	HSE PLL	Regulator_LPLV Flash- PwrDwn
Clock Source Frequency	4 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	12.7 mA	9 μΑ
Duration	0.1 ms	0.9 ms
DMIPS	125.0	0.0
Ta Max	103.99	105
Category	In DS Table	In DS Table

## 6.5. Results

Sequence Time	1 ms	Average Current	1.28 mA
Battery Life	3 months, 19	Average DMIPS	125.0 DMIPS
	days, 6 hours		

## 6.6. Chart



## 7. Peripherals and Middlewares Configuration

7.1. ADC1 mode: IN0 mode: IN1 mode: IN2 mode: IN3 mode: IN4 mode: IN5 mode: IN6 mode: IN7 mode: IN8

mode: IN9 mode: IN10 mode: IN11

mode: IN12 mode: IN13

### 7.1.1. Parameter Settings:

#### ADCs\_Common\_Settings:

Mode Independent mode

ADC\_Settings:

Clock Prescaler PCLK2 divided by 2

Resolution 12 bits (15 ADC Clock cycles)

Data Alignment Right alignment

Scan Conversion Mode Enabled Continuous Conversion Mode Disabled Disabled Discontinuous Conversion Mode **DMA Continuous Requests** Disabled

End Of Conversion Selection EOC flag at the end of single channel conversion

ADC\_Regular\_ConversionMode:

**Number Of Conversion** 14 \*

**External Trigger Conversion Source** Regular Conversion launched by software

External Trigger Conversion Edge None Rank

Channel 0 Channel Sampling Time

112 Cycles \*

Rank 2 \*

Channel

Channel 1 \*

Sampling Time 112 Cycles \*

<u>Rank</u> 3 \*

Channel 2 \*
Sampling Time Channel 2 \*

<u>Rank</u> **4** \*

Channel 3 \*
Sampling Time Channel 3 \*

Rank 5

Channel Channel 4 \*
Sampling Time 112 Cycles \*

<u>Rank</u> 6 \*

Channel 5 \*
Sampling Time 112 Cycles \*

<u>Rank</u> 7 \*

Channel Channel 6 \*
Sampling Time 112 Cycles \*

<u>Rank</u> 8 \*

Channel Channel 7 \*
Sampling Time 112 Cycles \*

<u>Rank</u> **9** \*

Channel 8 \*
Sampling Time 112 Cycles \*

<u>Rank</u> **10** \*

Channel 9 \*
Sampling Time 112 Cycles \*

Rank 11 \*

Rank 12 \*

Channel 11 \*
Sampling Time 112 Cycles \*

Rank 13 \*

Channel 12 \*
Sampling Time Channel 12 \*

Rank 14 \*

Channel 13 \*

Sampling Time 112 Cycles \*

ADC\_Injected\_ConversionMode:

Number Of Conversions 0

WatchDog:

Enable Analog WatchDog Mode false

#### 7.2. RCC

#### High Speed Clock (HSE): Crystal/Ceramic Resonator

#### 7.2.1. Parameter Settings:

#### **System Parameters:**

VDD voltage (V) 3.3
Instruction Cache Enabled
Prefetch Buffer Enabled
Data Cache Enabled

Flash Latency(WS) 0 WS (1 CPU cycle)

**RCC Parameters:** 

HSI Calibration Value 16

TIM Prescaler Selection Disabled

HSE Startup Timout Value (ms) 100

LSE Startup Timout Value (ms) 5000

**Power Parameters:** 

Power Regulator Voltage Scale Power Regulator Voltage Scale 1

#### 7.3. SYS

**Debug: Serial Wire** 

**Timebase Source: TIM10** 

#### 7.4. TIM4

Channel1: PWM Generation CH1 Channel2: PWM Generation CH2

#### 7.4.1. Parameter Settings:

#### **Counter Settings:**

Prescaler (PSC - 16 bits value) 15 \*

Counter Mode Up

Counter Period (AutoReload Register - 16 bits value ) 19 \*

Internal Clock Division (CKD)

auto-reload preload

Enable \*

**Trigger Output (TRGO) Parameters:** 

Master/Slave Mode (MSM bit) Disable (Trigger input effect not delayed)

Trigger Event Selection Reset (UG bit from TIMx\_EGR)

**PWM Generation Channel 1:** 

Mode PWM mode 1

Pulse (16 bits value) 0

Output compare preload Enable

Fast Mode Disable

CH Polarity High

**PWM Generation Channel 2:** 

Mode PWM mode 1

Pulse (16 bits value) 0

Output compare preload Enable

Fast Mode Disable

CH Polarity High

#### **7.5. USART6**

#### **Mode: Asynchronous**

#### 7.5.1. Parameter Settings:

#### **Basic Parameters:**

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

**Advanced Parameters:** 

Data Direction Receive and Transmit

Over Sampling 16 Samples

#### 7.6. FREERTOS

Interface: CMSIS\_V1

#### 7.6.1. Config parameters:

API:

FreeRTOS API CMSIS v1

Versions:

FreeRTOS version 10.3.1 CMSIS-RTOS version 1.02

MPU/FPU:

ENABLE\_MPU Disabled ENABLE\_FPU Disabled

Kernel settings:

USE\_PREEMPTION Enabled

CPU\_CLOCK\_HZ SystemCoreClock

TICK\_RATE\_HZ 1000
MAX\_PRIORITIES 7
MINIMAL\_STACK\_SIZE 128
MAX\_TASK\_NAME\_LEN 16

USE\_16\_BIT\_TICKS Disabled
IDLE\_SHOULD\_YIELD Enabled
USE\_MUTEXES Enabled
USE\_RECURSIVE\_MUTEXES Disabled
USE\_COUNTING\_SEMAPHORES Disabled
QUEUE\_REGISTRY\_SIZE 8

USE\_APPLICATION\_TASK\_TAG Disabled
ENABLE\_BACKWARD\_COMPATIBILITY Enabled
USE\_PORT\_OPTIMISED\_TASK\_SELECTION Enabled
USE\_TICKLESS\_IDLE Disabled
USE\_TASK\_NOTIFICATIONS Enabled

Memory management settings:

RECORD\_STACK\_HIGH\_ADDRESS

Memory Allocation Dynamic / Static

TOTAL\_HEAP\_SIZE 15360

Memory Management scheme heap\_4

Hook function related definitions:

USE\_IDLE\_HOOK Disabled
USE\_TICK\_HOOK Disabled
USE\_MALLOC\_FAILED\_HOOK Disabled
USE\_DAEMON\_TASK\_STARTUP\_HOOK Disabled
CHECK\_FOR\_STACK\_OVERFLOW Disabled

Run time and task stats gathering related definitions:

GENERATE\_RUN\_TIME\_STATS Disabled USE\_TRACE\_FACILITY Disabled

Disabled

USE\_STATS\_FORMATTING\_FUNCTIONS Disabled

Co-routine related definitions:

USE\_CO\_ROUTINES Disabled MAX\_CO\_ROUTINE\_PRIORITIES 2

Software timer definitions:

USE\_TIMERS Disabled

Interrupt nesting behaviour configuration:

LIBRARY\_LOWEST\_INTERRUPT\_PRIORITY 15
LIBRARY\_MAX\_SYSCALL\_INTERRUPT\_PRIORITY 5

Added with 10.2.1 support:

MESSAGE\_BUFFER\_LENGTH\_TYPE size\_t
USE\_POSIX\_ERRNO Disabled

#### 7.6.2. Include parameters:

#### Include definitions:

vTaskPrioritySet Enabled uxTaskPriorityGet Enabled vTaskDelete Enabled vTaskCleanUpResources Disabled vTaskSuspend Enabled vTaskDelayUntil Disabled vTaskDelay Enabled xTaskGetSchedulerState Enabled xTaskResumeFromISR Enabled xQueueGetMutexHolder Disabled xSemaphoreGetMutexHolder Disabled pcTaskGetTaskName Disabled uxTaskGetStackHighWaterMark Disabled xTaskGetCurrentTaskHandle Disabled eTaskGetState Disabled xEventGroupSetBitFromISR Disabled xTimerPendFunctionCall Disabled xTaskAbortDelay Disabled Disabled xTaskGetHandle Disabled uxTaskGetStackHighWaterMark2

#### 7.6.3. Advanced settings:

#### Newlib settings (see parameter description first):

USE\_NEWLIB\_REENTRANT Disabled

Project settings (see parameter des	cription first):
Use FW pack heap file	Enabled
* User modified value	

# 8. System Configuration

## 8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull	Max	User Label
				down	Speed	
ADC1	PC0	ADC1_IN10	Analog mode	No pull-up and no pull-down	n/a	
	PC1	ADC1_IN11	Analog mode	No pull-up and no pull-down	n/a	
	PC2	ADC1_IN12	Analog mode	No pull-up and no pull-down	n/a	
	PC3	ADC1_IN13	Analog mode	No pull-up and no pull-down	n/a	
	PA0-WKUP	ADC1_IN0	Analog mode	No pull-up and no pull-down	n/a	
	PA1	ADC1_IN1	Analog mode	No pull-up and no pull-down	n/a	
	PA2	ADC1_IN2	Analog mode	No pull-up and no pull-down	n/a	
	PA3	ADC1_IN3	Analog mode	No pull-up and no pull-down	n/a	
	PA4	ADC1_IN4	Analog mode	No pull-up and no pull-down	n/a	
	PA5	ADC1_IN5	Analog mode	No pull-up and no pull-down	n/a	
	PA6	ADC1_IN6	Analog mode	No pull-up and no pull-down	n/a	
	PA7	ADC1_IN7	Analog mode	No pull-up and no pull-down	n/a	
	PB0	ADC1_IN8	Analog mode	No pull-up and no pull-down	n/a	
	PB1	ADC1_IN9	Analog mode	No pull-up and no pull-down	n/a	
RCC	PH0 - OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PH1 - OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SYS	PA13	SYS_JTMS- SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK- SWCLK	n/a	n/a	n/a	
TIM4	PB6	TIM4_CH1	Alternate Function Push Pull	No pull-up and no pull-down	Low	
	PB7	TIM4_CH2	Alternate Function Push Pull	No pull-up and no pull-down	Low	
USART6	PA11	USART6_TX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
	PA12	USART6_RX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
GPIO	PC13- ANTI_TAMP	GPIO_Output	Output Push Pull	Pull-up *	Low	LED_CTR
	PA15	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DRIVR2
	PB3	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DRIVR1
	PB4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DRIVL2
	PB5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DRIVL1
	PB8	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LED_SENSOR

8.2.	<b>DMA</b>	confia	uration
<b>U.</b>		0011119	M. M

nothing configured in DMA service

## 8.3. NVIC configuration

## 8.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Pre-fetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	15	0
System tick timer	true	15	0
TIM1 update interrupt and TIM10 global interrupt	true	15	0
TIM4 global interrupt	true	10	0
USART6 global interrupt	true	5	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
ADC1 global interrupt	unused		
FPU global interrupt	unused		

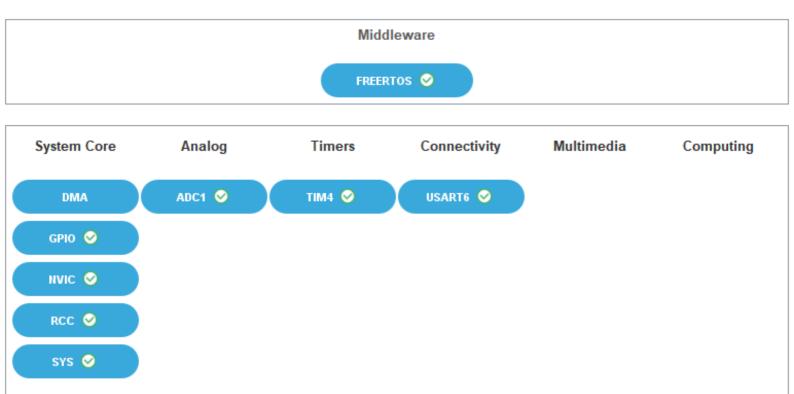
## 8.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Pre-fetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	false	false
Debug monitor	false	true	false
Pendable request for system service	false	false	false
System tick timer	false	false	true
TIM1 update interrupt and TIM10 global interrupt	false	true	true
TIM4 global interrupt	false	true	true
USART6 global interrupt	false	true	true

\* User modified value

# 9. System Views

- 9.1. Category view
- 9.1.1. Current



## 10. Docs & Resources

Type Link

BSDL files https://www.st.com/resource/en/bsdl\_model/stm32f411\_bsdl.zip

IBIS models https://www.st.com/resource/en/ibis\_model/stm32f411\_ibis.zip

System View https://www.st.com/resource/en/svd/stm32f4\_svd.zip

Description

BSDL files https://www.st.com/resource/en/bsdl\_model/stm32f411\_bsdl.zip

IBIS models https://www.st.com/resource/en/ibis\_model/stm32f411\_ibis.zip

System View https://www.st.com/resource/en/svd/stm32f4\_svd.zip

Description

Presentations https://www.st.com/resource/en/product\_presentation/stm32-

stm8\_embedded\_software\_solutions.pdf

Presentations https://www.st.com/resource/en/product\_presentation/stm32\_eval-

tools\_portfolio.pdf

Presentations https://www.st.com/resource/en/product\_presentation/stm32\_stm8\_functi

onal-safety-packages.pdf

Presentations https://www.st.com/resource/en/product\_presentation/stm32-

stm8\_software\_development\_tools.pdf

Training Material https://www.st.com/resource/en/sales\_guide/sg\_sc2154.pdf

Flyers https://www.st.com/resource/en/flyer/flstm32f4x1.pdf

Flyers https://www.st.com/resource/en/flyer/flstm32nucleo.pdf

Flyers https://www.st.com/resource/en/flyer/flstmcsuite.pdf

Flyers https://www.st.com/resource/en/flyer/flstm32trust.pdf

Product https://www.st.com/resource/en/certification\_document/stm32\_authenticat

Certifications ion\_can.pdf

Application Notes https://www.st.com/resource/en/application\_note/an1181-electrostatic-

discharge-sensitivity-measurement-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application\_note/an1709-emc-design-

guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application\_note/an2606-stm32-

- microcontroller-system-memory-boot-mode-stmicroelectronics.pdf

  Application Notes https://www.st.com/resource/en/application\_note/an2639-solderingrecommendations-and-package-information-for-leadfree-ecopack-mcusand-mpus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an2834-how-to-get-the-best-adc-accuracy-in-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an2867-oscillator-design-guide-for-stm8afals-stm32-mcus-and-mpus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an2945-stm8s-and-stm32-mcus-a-consistent-832bit-product-line-for-painless-migration-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3070-managing-the-driver-enable-signal-for-rs485-and-iolink-communications-with-the-stm32s-usart-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3154-can-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3155-usart-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3156-usb-dfu-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3364-migration-and-compatibility-guidelines-for-stm32-microcontroller-applications-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3997-audio-playback-and-recording-using-the-stm32f4discovery-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an3998-pdm-audio-software-decoding-on-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4013-stm32-crossseries-timer-overview-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4031-using-the-

- stm32f2-stm32f4-and-stm32f7-series-dma-controller-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4221-i2c-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4229-how-to-implement-a-vocoder-solution-using-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4230-stm32-microcontroller-random-number-generation-validation-using-the-nist-statistical-test-suite-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4277-using-stm32-device-pwm-shutdown-features-for-motor-control-and-digital-power-conversion-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4286-spi-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4488-getting-started-with-stm32f4xxxx-mcu-hardware-development-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4566-extending-the-dac-performance-of-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4616-migrating-from-stm32f401-and-stm32f411-lines-to-stm32l4-series-and-stm32l4-series-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4646-peripheral-interconnections-on-stm32f401-and-stm32f411-lines-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4655-virtually-increasing-the-number-of-serial-communication-peripherals-in-stm32-applications-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4739-stm32cube-firmware-examples-for-stm32f4-series-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4750-handling-of-soft-errors-in-stm32-applications-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4759-using-the-

- hardware-realtime-clock-rtc-and-the-tamper-management-unit-tamp-with-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4776-generalpurpose-timer-cookbook-for-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4803-highspeed-si-simulations-using-ibis-and-boardlevel-simulations-using-hyperlynx-si-on-stm32-mcus-and-mpus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4838-managing-memory-protection-unit-in-stm32-mcus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4850-stm32-mcus-spreadspectrum-clock-generation-principles-properties-and-implementation-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4879-usb-hardware-and-pcb-guidelines-using-stm32-mcus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4904-migration-of-microcontroller-applications-from-stm32f1-series-to-stm32f4-access-lines-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4908-stm32-usart-automatic-baud-rate-detection-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4989-stm32-microcontroller-debug-toolbox-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4995-using-anelectromyogram-technique-to-detect-muscle-activitystmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an5027-interfacing-pdm-digital-microphones-using-stm32-mcus-and-mpus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an5036-thermal-management-guidelines-for-stm32-applications-stmicroelectronics.pdf
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