

SpecTec

Update and Vote

Andreas Rossberg

with Dongjun Youn, Wonho Shin, Jaehyun Lee, Sukyoung Ryu,
Joachim Breitner, Philippa Gardner, Sam Lindley, Matija Pretnar,
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a DSL for authoring the Wasm spec

Spec authoring today

Duplicate work to write *both* formal and prose rules

Writing prose is particularly laborious

Both formats are verbose and terrible for code reviews

Error-prone due to lack of meta-level error checking (syntax, rules, cross-links, etc.)

No macro facilities in Sphinx

Spec authoring with SpecTec

SpecTec is (close to) **WYSIWYG** ASCII for the math rules

Easy to **write**, **read**, **diff**, and **review**, with meta-level **error checking**

Single **source of truth** for auto-generating:

- ...math in **Latex**

- ...prose in **Sphinx**

- ...mechanised **Coq** and co (future work)

Generated math and prose can be **spliced** into spec document

$\text{select } (t^*)^?$

1. Assert: due to **validation**, a value of **value type** **i32** is on the top of the stack.
2. Pop the value **i32.const c** from the stack.
3. Assert: due to **validation**, two more values (of the same **value type**) are on the top of the stack.
4. Pop the value val_2 from the stack.
5. Pop the value val_1 from the stack.
6. If c is not 0, then:
 - a. Push the value val_1 back to the stack.
7. Else:
 - a. Push the value val_2 back to the stack.

$$\begin{aligned} \text{val}_1 \text{ val}_2 (\text{i32.const } c) (\text{select } t^?) &\hookrightarrow \text{val}_1 \quad (\text{if } c \neq 0) \\ \text{val}_1 \text{ val}_2 (\text{i32.const } c) (\text{select } t^?) &\hookrightarrow \text{val}_2 \quad (\text{if } c = 0) \end{aligned}$$

```
rule Step_pure/select-true:
  val_1 val_2 (CONST I32 c) (SELECT t*?) ~> val_1
  -- if c != 0

rule Step_pure/select-false:
  val_1 val_2 (CONST I32 c) (SELECT t*?) ~> val_2
  -- if c = 0
```

```
.. _exec-select:
```

```
:math:\SELECTION~(t^*)^?
```

```
.....
```

1. Assert: due to **validation** `<valid-select>`, a **value** `<syntax-value>` of **value type** `<syntax-valtype>` `I32` is on the top of the **stack** `<syntax-stack>`.

2. Pop the value `I32.CONST~c` from the **stack** `<syntax-stack>`.

3. Assert: due to **validation** `<valid-select>`, two more **values** `<syntax-value>` (of the same **value type** `<syntax-valtype>`) are on the top of the stack.

4. Pop the **value** `<syntax-value>` `val_2` from the **stack** `<syntax-stack>`.

5. Pop the **value** `<syntax-value>` `val_1` from the **stack** `<syntax-stack>`.

6. If `c` is not `0`, then:

a. Push the **value** `<syntax-value>` `val_1` back to the **stack** `<syntax-stack>`.

7. Else:

a. Push the **value** `<syntax-value>` `val_2` back to the **stack** `<syntax-stack>`.

```
.. math::
```

```
\begin{array}{lcl@{\quad}l}
```

```
\val_1~\val_2~(\I32\K{.}\CONST~c)~(\SELECTION~t^?) &\stepto& \val_1
```

```
& (\iff c \neq 0) \\\
```

```
\val_1~\val_2~(\I32\K{.}\CONST~c)~(\SELECTION~t^?) &\stepto& \val_2
```

```
& (\iff c = 0) \\\
```

```
\end{array}
```


select (t*)?

- 1. Assert: due to validation, a value of value type i32 is on the top of the stack.
- 2. Pop the value i32.const c from the stack.
- 3. Assert: due to validation, two more values (of the same value type) are on the top of the stack.
- 4. Pop the value val₂ from the stack.
- 5. Pop the value val₁ from the stack.
- 6. If c is not 0, then:
 - a. Push the value val₁ back to the stack.
- 7. Else:
 - a. Push the value val₂ back to the stack.

$$val_1\ val_2\ (i32.const\ c)\ (select\ t^?)\ \hookrightarrow\ val_1\ \text{ (if } c \neq 0)$$
$$val_1\ val_2\ (i32.const\ c)\ (select\ t^?)\ \hookrightarrow\ val_2\ \text{ (if } c = 0)$$

```
rule Step_pure/select-true:
  val_1 val_2 (CONST I32 c) (SELECT t*?) ~> val_1
  -- if c != 0

rule Step_pure/select-false:
  val_1 val_2 (CONST I32 c) (SELECT t*?) ~> val_2
  -- if c = 0
```

```
.. _exec-select:

${rule-prose: exec/select}

${rule: {Step_pure/select-*}}
```



```
.. _exec-select:

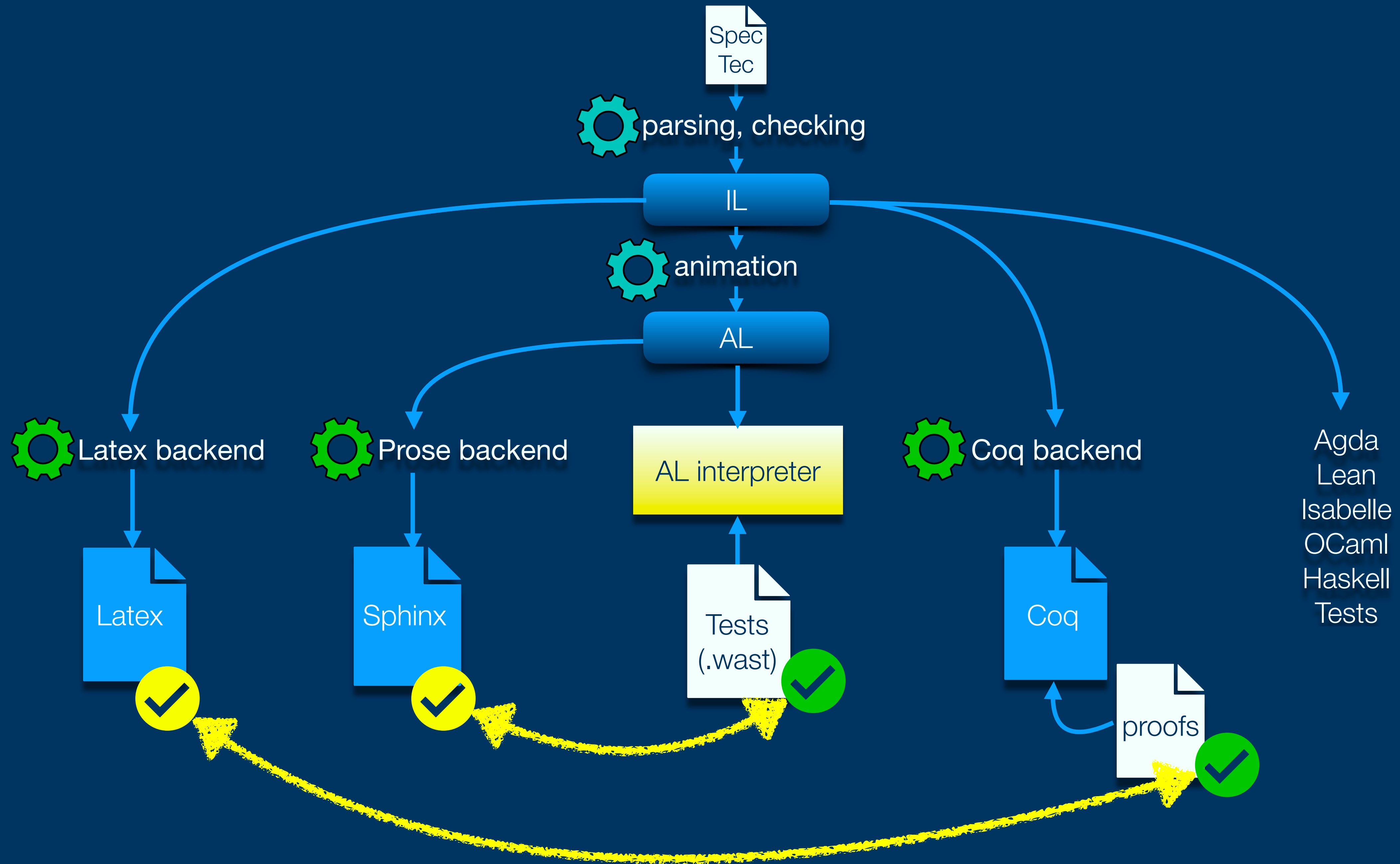
:math:\SELECT~{({t^{\ast}})^?}\`
.....

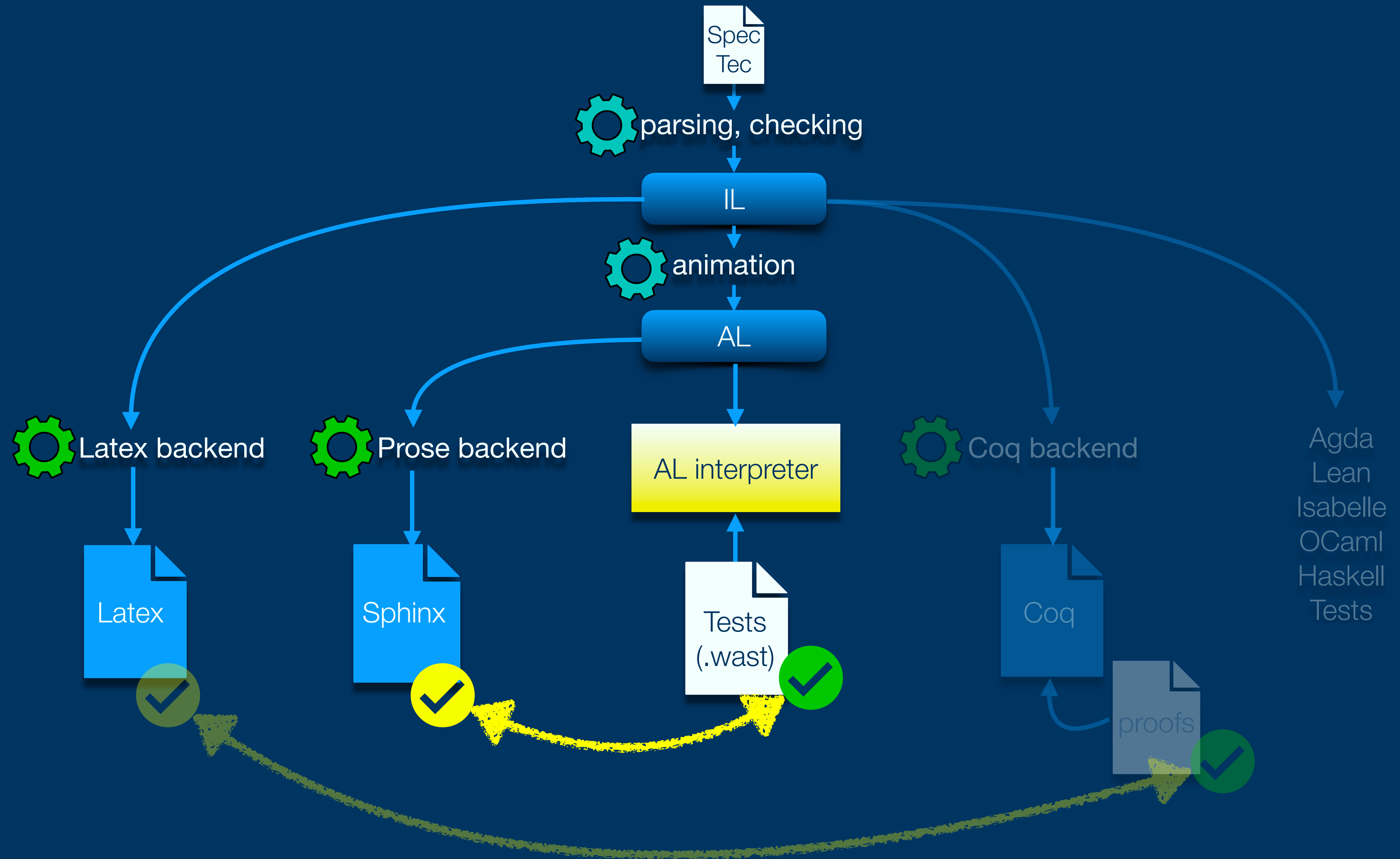
1. Assert: Due to validation, a value of value type :math:\I32` is on the top of the stack.
#. Pop the value :math:\(\I32\{.\}\CONST~c)` from the stack.
#. Assert: Due to validation, a value is on the top of the stack.
#. Pop the value :math:\{\val\}_2` from the stack.
#. Assert: Due to validation, a value is on the top of the stack.
#. Pop the value :math:\{\val\}_1` from the stack.
#. If :math:`c` is not :math:`0`, then:
  a. Push the value :math:\{\val\}_1` to the stack.
#. Else:
  a. Push the value :math:\{\val\}_2` to the stack.

.. math::
  \begin{array}{@{}l@{}rcl@{}l@{}}
    & \& \{\val\}_1\sim\{\val\}_2\sim(\I32\{.\}\CONST~c)\sim(\SELECT~{({t^{\ast}})^?}) & \& \stepto& \{\val\}_1 \\
    & \& \quad \mbox{if}~c \neq 0 \quad \& \\
    & \& \{\val\}_1\sim\{\val\}_2\sim(\I32\{.\}\CONST~c)\sim(\SELECT~{({t^{\ast}})^?}) & \& \stepto& \{\val\}_2 \\
    & \& \quad \mbox{if}~c = 0 \quad \& \\
  \end{array}
  \end{array}
```

Meta Interpreter

Algorithmic prose is derived from user-defined declarative reduction rules
via an intermediate representation that's an AST for the prose
and we can interpret “programs” in this AST, i.e., the Wasm spec
...and thereby indirectly run actual Wasm with actual Wasm spec
Passes 100% of applicable Wasm test suite





Updates

implemented generation of **hyperlinking** and customis

fixes and **rendering improvements**

started writing a **tutorial**

converted more of the spec document

Bringing the WebAssembly Standard up to Speed with SpecTec

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WebAssembly (Wasm) is a portable low-level bytecode language and virtual machine that has seen increasing use in a variety of ecosystems. Its specification is unusually rigorous – including a full formal semantics for the language – and every new feature must be specified in this formal semantics, in prose, and in the official reference interpreter before it can be standardized. With the growing size of the language, this manual process with its redundancies has become laborious and error-prone, and in this work, we offer a solution.

We present SpecTec, a domain-specific language (DSL) and toolchain that facilitates both the Wasm specification and the generation of artifacts necessary to standardize new features. SpecTec serves as a single source of truth – from a SpecTec definition of the Wasm semantics, we can generate a typeset specification, including formal definitions and prose pseudocode descriptions, and a meta-level interpreter. Further backends for test generation and interactive theorem proving are planned. We evaluate SpecTec’s ability to represent the latest Wasm 2.0 and show that the generated meta-level interpreter passes 100% of the applicable official test suite. We show that SpecTec is highly effective at discovering and preventing errors by detecting historical errors in the specification that have been corrected and ten errors in five proposals ready for inclusion in the next version of Wasm. Our ultimate aim is that SpecTec should be adopted by the Wasm standards community and used to specify future versions of the standard.

CCS Concepts: • **Theory of computation** → **Program specifications**; • **Software and its engineering** → **Syntax**; **Semantics**; **Specification languages**; **Domain specific languages**.

Additional Key Words and Phrases: WebAssembly, language specification, executable prose, DSL

ACM Reference Format:

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<https://people.mpi-sws.org/~rossberg/papers/spectec1.pdf>

	syntax	validation	execution	binary	text	appendix
conventions						
types						
values						
instructions						
modules						
			numerics			

Wasm 2.0 + tail calls + multi memory + function references + garbage collection

select $(t^*)^?$

1. Assert: due to **validation**, a value of **value type i32** is on the top of the stack.
2. Pop the value **i32.const c** from the stack.
3. Assert: due to **validation**, two more values (of the same **value type**) are on the top of the stack.
4. Pop the value val_2 from the stack.
5. Pop the value val_1 from the stack.
6. If c is not 0, then:
 - a. Push the value val_1 back to the stack.
7. Else:
 - a. Push the value val_2 back to the stack.

$$\begin{aligned} val_1 \ val_2 \ (i32.const \ c) \ (select \ t^?) &\hookrightarrow val_1 \quad (\text{if } c \neq 0) \\ val_1 \ val_2 \ (i32.const \ c) \ (select \ t^?) &\hookrightarrow val_2 \quad (\text{if } c = 0) \end{aligned}$$

select $(t^*)^?$

1. Assert: Due to validation, a value of value type **i32** is on the top of the stack.
2. Pop the value **(i32.const c)** from the stack.
3. Assert: Due to validation, a value is on the top of the stack.
4. Pop the value val_2 from the stack.
5. Assert: Due to validation, a value is on the top of the stack.
6. Pop the value val_1 from the stack.
7. If c is not 0, then:
 - a. Push the value val_1 to the stack.
8. Else:
 - a. Push the value val_2 to the stack.

$$\begin{aligned} val_1 \ val_2 \ (i32.const \ c) \ (select \ (t^*)^?) &\hookrightarrow val_1 && \text{if } c \neq 0 \\ val_1 \ val_2 \ (i32.const \ c) \ (select \ (t^*)^?) &\hookrightarrow val_2 && \text{if } c = 0 \end{aligned}$$

i31.get_sx

1. Assert: due to **validation**, a **value** of **type** (**ref null i31**) is on the top of the stack.
2. Pop the value *ref* from the stack.
3. If *ref* is **ref.null** *t*, then:
 - a. Trap.
4. Assert: due to **validation**, a *ref* is a **scalar** reference.
5. Let **ref.i31** *i* be the reference value *ref*.
6. Let *j* be the result of computing $\text{extend}_{31,32}^{sx}(i)$.
7. Push the value **i32.const** *j* to the stack.

$$\begin{aligned} (\text{ref.i31 } i) \text{ i31.get_sx} &\hookrightarrow (\text{i32.const extend}_{31,32}^{sx}(i)) \\ (\text{ref.null } t) \text{ i31.get_sx} &\hookrightarrow \text{trap} \end{aligned}$$

i31.get_sx

1. Assert: Due to validation, a value is on the top of the stack.
2. Pop the value instr_{u0} from the stack.
3. If instr_{u0} is of the case **ref.null**, then:
 - a. Trap.
4. If instr_{u0} is of the case **ref.i31**, then:
 - a. Let (**ref.i31** *i*) be instr_{u0} .
 - b. Push the value (**i32.const** $\text{extend}_{31,32}^{sx}(i)$) to the stack.

$$\begin{aligned} (\text{ref.null } ht) (\text{i31.get_sx}) &\hookrightarrow \text{trap} \\ (\text{ref.i31 } i) (\text{i31.get_sx}) &\hookrightarrow (\text{i32.const extend}_{31,32}^{sx}(i)) \end{aligned}$$

$$\begin{aligned}
z; (\text{ref.null } ht) \text{ (i32.const } i) \text{ val (array.set } x) &\hookrightarrow z; \text{trap} \\
z; (\text{ref.array } a) \text{ (i32.const } i) \text{ val (array.set } x) &\hookrightarrow z; \text{trap} \quad \text{if } i \geq |z.\text{arrays}[a].\text{fields}| \\
z; (\text{ref.array } a) \text{ (i32.const } i) \text{ val (array.set } x) &\hookrightarrow z[\text{.arrays}[a].\text{fields}[i] = \text{pack}_{zt}(\text{val})]; \epsilon \\
&\quad \text{if } z.\text{types}[x] \approx \text{array (mut? } zt)
\end{aligned}$$

$$\begin{aligned}
z; (\text{ref.null } ht) \text{ (i32.const } i) \text{ val (array.set } x) &\hookrightarrow z; \text{trap} \\
z; (\text{ref.array } a) \text{ (i32.const } i) \text{ val (array.set } x) &\hookrightarrow z; \text{trap} \\
z; (\text{ref.array } a) \text{ (i32.const } i) \text{ val (array.set } x) &\hookrightarrow \text{\textcolor{red}{\multicolumn{2}{l}@} } z[\text{.arrays}[a].\text{fields}[i] = \text{pack}_{zt}(\text{val})]; \epsilon \\
&\quad \text{\textcolor{red}{\multicolumn{2}{l}@} } \text{ if } z.\text{types}[x] \approx \text{array (mut? } zt)
\end{aligned}$$

Current Limitations

burndown list: <https://github.com/wasm-dsl/spectec/issues/67>

user experience still is rather rough

especially wrt robustness and error reporting in backends

output still needs some polishing

tutorial and documentation is WIP

... best intro for now is overview in the PLDI paper

Limitations of Math Renderer

layout control is somewhat whacky

hard-codes knowledge about some notation of relations

... recognises \vdash and \rightsquigarrow to decide rendering, falls back otherwise

Current Limitations of Prose Renderer

conditionals that should be `assertions` (guaranteed by validation)

- ...leads to spurious nesting

- ...interpreter just falls through on missing rules instead of aborting

produced `phrasing` not always ideal

- ...`naming` of generated meta-variables

- ...noise from `helper functions` that should be “inlined”

- ...`iterations` not unpacked

- ...missing special cases (e.g., “absent” instead “has length 0” for x ?)

`hard-codes` various definitional choices that can break when spec changes

Current Limitations of Meta Interpreter

can do *only execution*, hooks into reference interpreter for everything else

- ...parsing, decoding, validation, numerics

still *fragile* on errors and structural spec changes

- ...esp. depends on various naming conventions being followed by user

a *lot* of *hard-coded knowledge* about various aspects of the spec

- ...often inevitable, e.g., must convert from reference interpreter's AST

requires *adjustment* for non-trivial spec extensions

On the up side

found and eliminated various **spec bugs**

syntax errors, type errors, omissions, hyperlinks, layout, typos, ...

... detected either by SpecTec error checking

... or prevented by construction thanks to generation

more **consistent style** and layout, without manual tuning

non-trivial proposals like GC were **expressible** out of the box

much **faster turn-around** for spec'ing new features (when it works...)

it's more fun :)

Open Questions

Better *trade-off* between WYSIWYG and ambiguity / frontend complexity?

Evaluation contexts (currently, we avoid them)

Reducing amount of hard-coded *assumptions* in backends

More *self-contained meta interpreter*

Meta-theory of SpecTec

Practical Questions

Hierarchy of amount of assumptions about s

frontend < Latex backend < prose back

Latter parts more likely to break on spec change

... interpreter particularly brittle and has to

Need process and infrastructure that allow t

Contingency plan with permanent fallback ir

... interpreter not on critical path, could si

... Latex & prose backend can generate s

```
.. _exec-select:

${rule-prose: exec/select}

${rule: {Step_pure/select-*}}
```



```

.. _exec-select:

 $\backslash$ SELECT~(({tast})^?)`
.....

1. Assert: Due to validation, a value of value type  $\backslash$ I32` is on the top of the stack.

#. Pop the value  $\backslash$ (I32{.}\CONST~c)` from the stack.

#. Assert: Due to validation, a value is on the top of the stack.

#. Pop the value  $\backslash$ {val}_2` from the stack.

#. Assert: Due to validation, a value is on the top of the stack.

#. Pop the value  $\backslash$ {val}_1` from the stack.

#. If  $\backslash$ c` is not  $\backslash$ 0`, then:

    a. Push the value  $\backslash$ {val}_1` to the stack.

#. Else:

    a. Push the value  $\backslash$ {val}_2` to the stack.

.. math::
\begin{array}{@{}l@{}rcl@{}l@{}}
& \backslash\text{val}_1\sim\backslash\text{val}_2\sim(\backslash\text{I32}\{.\}\backslash\text{CONST}\sim\text{c})\sim(\backslash\text{SELECT}\sim(({t^{\text{ast}}})^?)) & \&\text{stepto}& \backslash\text{val}_1 \\
& \&\text{quad} & \backslash\text{mbox}\{\text{if}\}\sim\text{c} & \neq 0 & \backslash\backslash \\
& \backslash\text{val}_1\sim\backslash\text{val}_2\sim(\backslash\text{I32}\{.\}\backslash\text{CONST}\sim\text{c})\sim(\backslash\text{SELECT}\sim(({t^{\text{ast}}})^?)) & \&\text{stepto}& \backslash\text{val}_2 \\
& \&\text{quad} & \backslash\text{mbox}\{\text{if}\}\sim\text{c} & = 0 & \backslash\backslash \\
& \backslash\text{end}\{\text{array}\}
\end{array}

```

Levels of Risks

1. The **output** produced by SpecTec is inferior to the hand-written document
⇒ we are close, and already surpass it on some aspects (esp. correctness)
2. The **user experience** of the tool is not good enough
⇒ not as good yet as we'd like, maturation will require iteration and user feedback
3. Tool **maintenance** becomes a burden, and the CG may lack sufficient expertise
⇒ definite risk, some backends are not robust against cross-cutting spec changes
“big feature” proposals will hopefully “saturate” functionality over time
contingency plan applies, fall back to status quo

What we offer

The current team will finish **converting** the “3.0” spec

- ... ETA is \approx 6 months

- ... that includes everything in burndown list on <https://github.com/Wasm-DSL/spectec/issues/67>

- ... and most stage 4 proposals (biggest current omission: threads)

The current team will continue to incrementally **document**, **improve/fix**, and **extend** the tool

- ... including polishing prose output

- ... improving the user experience

- ... adding yet missing features such as support for specifying numerics

The current team will be available to **maintain** the tool for at least 2 years

- ... and help with knowledge transfer where necessary for a future hand-over

We will invest into building theorem **prover backends**

- ... especially porting the existing **Coq** development and taking it beyond Wasm 2.0

Longer-term

Theorem prover backends, especially Coq

Generate additional parts of the spec document

- ...macros, cross-reference anchors, indices

Handle more aspects of the spec in meta interpreter

- ...decoding, parsing, validation

Meta interpreter for declarative semantics

Semantics-guided test fuzzing

Why CG sign-off matters for the project

Resources: more than 1 year of engineering work so far; justify further time investment by knowing that it is going to matter.

Impact: applied research has been (and will be) involved; needs some practical impact to be valued and considered publication-worthy.

Funding: some of us need to find funding for continuing this project; for the grant game it again is (even more) important to demonstrate relevance and impact.

Momentum: well-defined semantics has been key to the academic community's growing interest in Wasm; formalisation has to keep up with the language to preserve momentum and keep research relevant to the Wasm community.

Poll

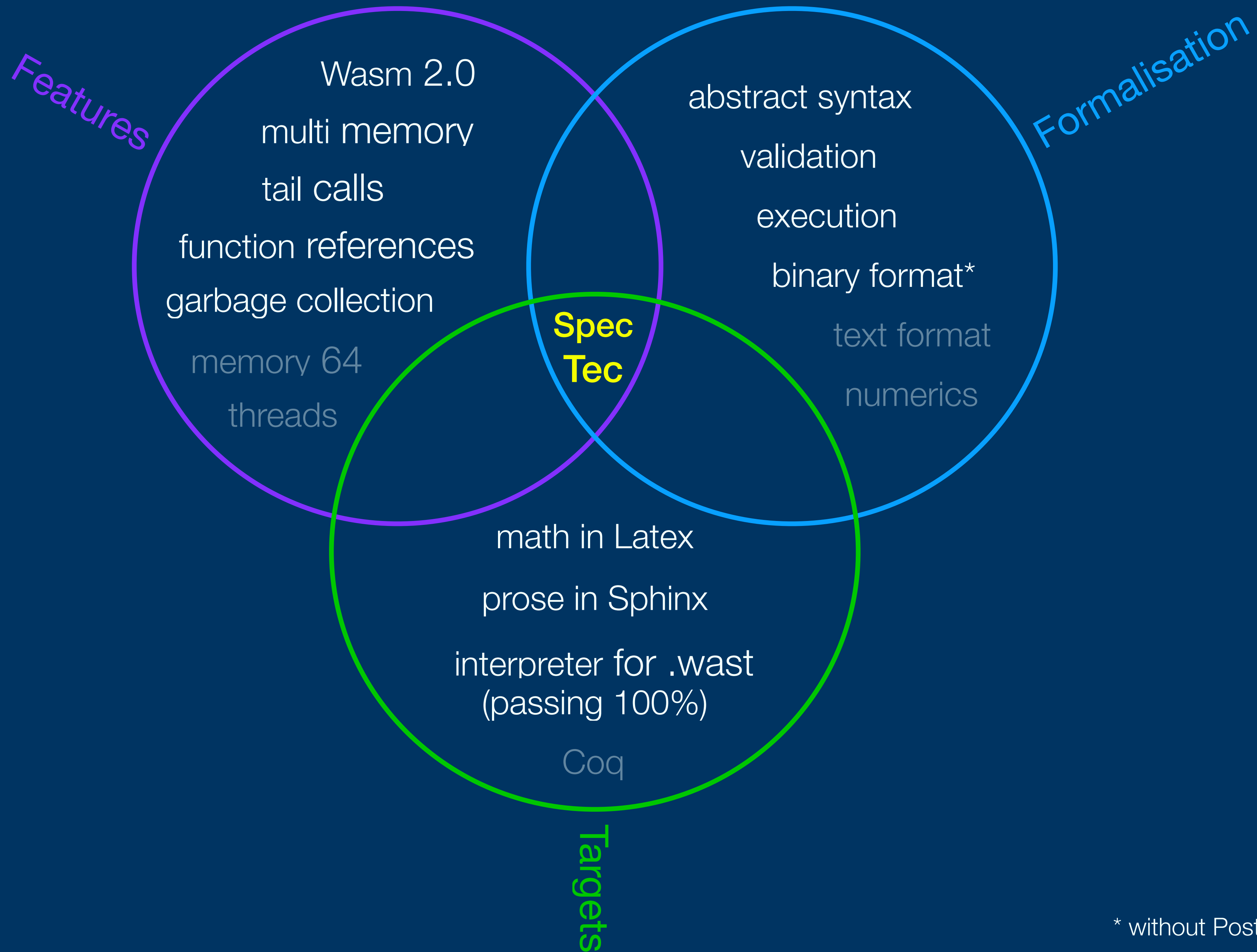
Adopt SpecTec (once it's ready) as the toolchain for authoring the spec.

Repo: <https://github.com/wasm-dsl/spectec>

Render: <https://wasm-dsl.github.io/spectec/core/>

Paper: <https://mpi-sws.org/~rossberg/papers/spectec1.pdf>

Outtakes



* without Post-2.0 syntax

Bringing the WebAssembly Standard up to Speed with SpecTec

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We present SpecTec, a domain-specific language (DSL) and toolchain that facilitates both the Wasm specification and the generation of artifacts necessary to standardize new features. SpecTec serves as a single source of truth — from a SpecTec definition of the Wasm semantics, we can generate a typeset specification, including formal definitions and prose pseudocode descriptions, and a meta-level interpreter. Further backends for test generation and interactive theorem proving are planned. We evaluate SpecTec’s ability to represent the latest Wasm 2.0 and show that the generated meta-level interpreter passes 100% of the applicable official test suite. We show that SpecTec is highly effective at discovering and preventing errors by detecting historical errors in the specification that have been corrected and ten errors in five proposals ready for inclusion in the next version of Wasm. Our ultimate aim is that SpecTec should be adopted by the Wasm standards community and used to specify future versions of the standard.

CCS Concepts: • **Theory of computation** → **Program specifications**; • **Software and its engineering** → **Syntax**; **Semantics**; **Specification languages**; **Domain specific languages**.

Additional Key Words and Phrases: WebAssembly, language specification, executable prose, DSL

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```
.. index:: heap type, type identifier
   pair: validation; heap type
   single: abstract syntax; heap type
.. _valid-heaptype:
```

Heap Types
~~~~~

Concrete **:ref:**`Heap types <syntax-heaptype>` are only valid when the **:ref:**`type index <syntax-typeidx>` is.

```
:math:`\absheaptype`
.....
```

\* The heap type is valid.

```
.. math::
   \frac{
     }{
       C \vdash\text{heaptype} \absheaptype \text{ok}
     }

```

```
:math:`\typeidx`
.....
```

\* The type **:math:**`C.\CTYPES[\typeidx]` must be defined in the context.

\* Then the heap type is valid.

```
.. math::
   \frac{
     C.\CTYPES[\typeidx] = \deftype
   }{
     C \vdash\text{heaptype} \typeidx \text{ok}
   }

```

```
.. index:: reference type, heap type
   pair: validation; reference type
   single: abstract syntax; reference type
.. _valid-reftype:
```

Reference Types  
~~~~~

:ref:`Reference types <syntax-reftype>` are valid when the referenced **:ref:**`heap type <syntax-heaptype>` is.

```
:math:`\REF~\NULL^?~\heaptype`
.....
```

* The heap type **:math:**`\heaptype` must be **:ref:**`valid <valid-heaptype>`.

* Then the reference type is valid.

```
.. math::
   \frac{
     C \vdash\text{reftype} \heaptype \text{ok}
   }{
     C \vdash\text{reftype} \REF~\NULL^?~\heaptype \text{ok}
   }

```



```
.. index:: heap type, type identifier
   pair: validation; heap type
   single: abstract syntax; heap type
.. _valid-heaptype:
```

Heap Types
~~~~~

Concrete **:ref:**`Heap types <syntax-heaptype>` are only valid when the **:ref:**`type index <syntax-typeidx>` is.

```
$$\{prose: Heaptype_ok/\absheaptype\}
```

```
$$\{rule: Heaptype_ok/\absheaptype\}
```

```
$$\{prose: Heaptype_ok/typeidx\}
```

```
$$\{rule: Heaptype_ok/typeidx\}
```

```
.. index:: reference type, heap type
   pair: validation; reference type
   single: abstract syntax; reference type
.. _valid-reftype:
```

Reference Types  
~~~~~

:ref:`Reference types <syntax-reftype>` are valid when the referenced **:ref:**`heap type <syntax-heaptype>` is.

```
$$\{prose: Reftype_ok\}
```

```
$$\{rule: Reftype_ok\}
```

```
.. index:: heap type, type identifier
   pair: validation; heap type
   single: abstract syntax; heap type
.. _valid-heaptype:
```

Heap Types
~~~~~

Concrete **:ref:**`Heap types <syntax-heaptype>` are only valid when the **:ref:**`type index <syntax-typeidx>` is.

```
:math:`\absheaptype`
.....
```

\* The heap type is valid.

```
.. math::
   \frac{
     }{
       C \vdash\heaptype \absheaptype \ok
     }

```

```
:math:`\typeidx`
.....
```

\* The type **:math:**`C.\CTYPES[\typeidx]` must be defined in the context.

\* Then the heap type is valid.

```
.. math::
   \frac{
     C.\CTYPES[\typeidx] = \deftype
   }{
     C \vdash\heaptype \typeidx \ok
   }

```

```
.. index:: reference type, heap type
   pair: validation; reference type
   single: abstract syntax; reference type
.. _valid-reftype:
```

Reference Types  
~~~~~

:ref:`Reference types <syntax-reftype>` are valid when the referenced **:ref:**`heap type <syntax-heaptype>` is.

```
:math:`\REF~\NULL^?~\heaptype`
.....
```

* The heap type **:math:**`\heaptype` must be **:ref:**`valid <valid-heaptype>`.

* Then the reference type is valid.

```
.. math::
   \frac{
     C \vdash\reftype \heaptype \ok
   }{
     C \vdash\reftype \REF~\NULL^?~\heaptype \ok
   }

```



Heap Types
~~~~~

Concrete **:ref:**`Heap types <syntax-heaptype>` are only valid when the **:ref:**`type index <syntax-typeidx>` is.

\$\$\{prose: Heaptype\\_ok/\absheaptype\}

\$\$\{rule: Heaptype\\_ok/\absheaptype\}

\$\$\{prose: Heaptype\\_ok/typeidx\}

\$\$\{rule: Heaptype\\_ok/typeidx\}

Reference Types  
~~~~~

:ref:`Reference types <syntax-reftype>` are valid when the referenced **:ref:**`heap type <syntax-heaptype>` is.

\$\$\{prose: Reftype_ok\}

\$\$\{rule: Reftype_ok\}


```
syntax instr hint(desc "instruction") =
| UNREACHABLE
| NOP
| DROP
| SELECT valtype?
| BLOCK blocktype instr*
| LOOP blocktype instr*
| IF blocktype instr* ELSE instr*
| BR labelidx
| BR_IF labelidx
| BR_TABLE labelidx* labelidx
| CALL funcidx
| CALL_INDIRECT tableidx functype
| RETURN
| CONST numtype c_numtype hint(show %.CONST %)
| UNOP numtype unop_numtype hint(show %.%)
| BINOP numtype binop_numtype hint(show %.%)
| TESTOP numtype testop_numtype hint(show %.%)
| RELOP numtype relop_numtype hint(show %.%)
| EXTEND numtype n hint(show %.EXTEND#%)
| CVTOP numtype cvtop numtype sx? hint(show %.%#_#%#_#%)
| ...
```

```
instr ::= unreachable
| nop
| drop
| select valtype?
| block blocktype instr*
| loop blocktype instr*
| if blocktype instr* else instr*
| br labelidx
| br_if labelidx
| br_table labelidx* labelidx
| call funcidx
| call_indirect tableidx functype
| return
| numtype.const c_numtype
| numtype.unop_numtype
| numtype.binop_numtype
| numtype.testop_numtype
| numtype.relop_numtype
| numtype.extendn
| numtype.cvtop_numtype_sx?
```

```
relation Instr_ok: context |- instr : functype hint(show "T")
```

```
rule Instr_ok/nop:
  C |- NOP : epsilon -> epsilon
```

```
rule Instr_ok/block:
  C |- BLOCK bt instr* : t_1* -> t_2*
  -- Blocktype_ok: C |- bt : t_1* -> t_2*
  -- Instrs_ok: C, LABEL t_2* |- instr* : t_1* -> t_2*
```

```
rule Instr_ok/loop:
  C |- LOOP bt instr* : t_1* -> t_2*
  -- Blocktype_ok: C |- bt : t_1* -> t_2*
  -- Instrs_ok: C, LABEL t_1* |- instr* : t_1* -> t_2
```

```
rule Instr_ok/br:
  C |- BR l : t_1* t* -> t_2*
  -- if C.LABEL[l] = t*
```

```
rule Instr_ok/br_if:
  C |- BR_IF l : t* I32 -> t*
  -- if C.LABEL[l] = t*
```

```
rule Instr_ok/br_table:
  C |- BR_TABLE l* l' : t_1* t* -> t_2*
  -- (Resulttype_sub: |- t* <: C.LABEL[l])*
```

$context \vdash instr : functype$

$\frac{}{C \vdash nop : \epsilon \rightarrow \epsilon} [T-NOP]$

$\frac{C \vdash bt : t_1^* \rightarrow t_2^* \quad C, label\ t_2^* \vdash instr^* : t_1^* \rightarrow t_2^*}{C \vdash block\ bt\ instr^* : t_1^* \rightarrow t_2^*} [T-BLOCK]$

$\frac{C \vdash bt : t_1^* \rightarrow t_2^* \quad C, label\ t_1^* \vdash instr^* : t_1^* \rightarrow t_2}{C \vdash loop\ bt\ instr^* : t_1^* \rightarrow t_2} [T-LOOP]$

$\frac{C.label[l] = t^*}{C \vdash br\ l : t_1^* t^* \rightarrow t_2^*} [T-BR] \quad \frac{C.label[l] = t^*}{C \vdash br_if\ l : t^* i32 \rightarrow t^*} [T-BR_IF]$

$\frac{(\vdash t^* \leq C.label[l])^* \quad \vdash t^* \leq C.label[l']}{C \vdash br_table\ l^* l' : t_1^* t^* \rightarrow t_2^*} [T-BR_TABLE]$


```
relation Step_pure: config ~> config

rule Step_pure/nop:
  NOP ~> epsilon

rule Step_pure/block:
  val^k (BLOCK bt instr*) ~> (LABEL_n`{epsilon} val^k instr*)
  -- if bt = t_1^k -> t_2^n

rule Step_pure/loop:
  val^k (LOOP bt instr*) ~> (LABEL_n`{LOOP bt instr*} val^k instr*)
  -- if bt = t_1^k -> t_2^n

rule Step_pure/br-zero:
  (LABEL_n`{instr'*} val'* val^n (BR 0) instr*) ~> val^n instr'*

rule Step_pure/br-succ:
  (LABEL_n`{instr'*} val* (BR $(l+1)) instr*) ~> val* (BR l)

rule Step_pure/br_if-true:
  (CONST I32 c) (BR_IF l) ~> (BR l)
  -- if c != 0

rule Step_pure/br_if-false:
  (CONST I32 c) (BR_IF l) ~> epsilon
  -- if c = 0

rule Step_pure/br_table-lt:
  (CONST I32 i) (BR_TABLE l* l') ~> (BR l*[i])
  -- if i < |l*|

rule Step_pure/br_table-le:
  (CONST I32 i) (BR_TABLE l* l') ~> (BR l')
  -- if i >= |l*|
```

<div>$instr^* \hookrightarrow instr^*$</div>		
nop	\hookrightarrow	ϵ
val^k (block bt $instr^*$)	\hookrightarrow	$(label_n\{\epsilon\} val^k instr^*)$ if $bt = t_1^k \rightarrow t_2^n$
val^k (loop bt $instr^*$)	\hookrightarrow	$(label_n\{\text{loop } bt \text{ } instr^*\} val^k instr^*)$ if $bt = t_1^k \rightarrow t_2^n$
$(label_n\{instr'^*\} val'^* val^n$ (br 0) $instr^*$)	\hookrightarrow	$val^n instr'^*$
$(label_n\{instr'^*\} val^*$ (br $l + 1$) $instr^*$)	\hookrightarrow	val^* (br l)
$(i32.const\ c)$ (br_if l)	\hookrightarrow	(br l) if $c \neq 0$
$(i32.const\ c)$ (br_if l)	\hookrightarrow	ϵ if $c = 0$
$(i32.const\ i)$ (br_table $l^* l'$)	\hookrightarrow	(br $l^*[i]$) if $i < l^* $
$(i32.const\ i)$ (br_table $l^* l'$)	\hookrightarrow	(br l') if $i \geq l^* $

```

relation Step_pure: config ~> config

rule Step_pure/nop:
  NOP ~> epsilon

rule Step_pure/block:
  val^k (BLOCK bt instr*) ~> (LABEL_n`{epsilon} val^k instr*)
  -- if bt = t_1^k -> t_2^n

rule Step_pure/loop:
  val^k (LOOP bt instr*) ~> (LABEL_n`{LOOP bt instr*} val^k instr*)
  -- if bt = t_1^k -> t_2^n

rule Step_pure/br-zero:
  (LABEL_n`{instr'*} val'* val^n (BR 0) instr*) ~> val^n instr'*

rule Step_pure/br-succ:
  (LABEL_n`{instr'*} val* (BR $(l+1)) instr*) ~> val* (BR l)

rule Step_pure/br_if-true:
  (CONST I32 c) (BR_IF l) ~> (BR l)
  -- if c != 0

rule Step_pure/br_if-false:
  (CONST I32 c) (BR_IF l) ~> epsilon
  -- if c = 0

rule Step_pure/br_table-lt:
  (CONST I32 i) (BR_TABLE l* l') ~> (BR l*[i])
  -- if i < |l*|

rule Step_pure/br_table-le:
  (CONST I32 i) (BR_TABLE l* l') ~> (BR l')
  -- if i >= |l*|

```

nop

1. Do nothing.

$$[E\text{-NOP}]\text{nop} \hookrightarrow \epsilon$$

block $bt\ instr^*$

1. Let $t_1^k \rightarrow t_2^n$ be bt .
2. Assert: Due to [validation](#), there are at least k values on the top of the stack.
3. Pop val^k from the stack.
4. Let L be the label whose arity is n and whose continuation is ϵ .
5. Push L to the stack.
6. Push val^k to the stack.
7. Jump to $instr^*$.

$$[E\text{-BLOCK}]\text{val}^k (\text{block } bt\ instr^*) \hookrightarrow (\text{label}_n\{\epsilon\} \text{val}^k instr^*) \text{ if } bt = t_1^k \rightarrow t_2^n$$

loop $bt\ instr^*$

1. Let $t_1^k \rightarrow t_2^n$ be bt .
2. Assert: Due to [validation](#), there are at least k values on the top of the stack.
3. Pop val^k from the stack.
4. Let L be the label whose arity is k and whose continuation is `loop $bt\ instr^*$` .
5. Push L to the stack.
6. Push val^k to the stack.

$$[E\text{-LOOP}]\text{val}^k (\text{loop } bt\ instr^*) \hookrightarrow (\text{label}_k\{\text{loop } bt\ instr^*\} \text{val}^k instr^*) \text{ if } bt = t_1^k \rightarrow t_2^n$$

br x_0

1. Let L be the current label.
2. Let n be the arity of L .
3. Let $instr'^*$ be the continuation of L .
4. Pop all values x_1^* from the stack.
5. Exit current context.
6. If x_0 is 0 and the length of x_1^* is greater than or equal to n , then:
 - a. Let $val'^* val^n$ be x_1^* .
 - b. Push val^n to the stack.
 - c. Execute the sequence $instr'^*$.
7. If x_0 is greater than or equal to 1, then:
 - a. Let l be $x_0 - 1$.
 - b. Let val^* be x_1^* .
 - c. Push val^* to the stack.
 - d. Execute `br l` .

$$[E\text{-BR-ZERO}](\text{label}_n\{instr'^*\} \text{val}'^* \text{val}^n (\text{br } 0) instr^*) \hookrightarrow \text{val}^n instr'^*$$

$$[E\text{-BR-SUCC}](\text{label}_n\{instr'^*\} \text{val}^* (\text{br } l + 1) instr^*) \hookrightarrow \text{val}^* (\text{br } l)$$

grammar Binstr/control: instr =

```
| 0x00                                     => UNREACHABLE
| 0x01                                     => NOP
| 0x02 bt:Bblocktype (in:Binstr)* 0x0B    => BLOCK bt in*
| 0x03 bt:Bblocktype (in:Binstr)* 0x0B    => LOOP bt in*
| 0x04 bt:Bblocktype (in:Binstr)* 0x0B    => IF bt in* epsilon
| 0x04 bt:Bblocktype (in_1:Binstr)* 0x0B => IF bt in_1 in* epsilon
| 0x0C l:Blabelidx
| 0x0D l:Blabelidx
| 0x0E l*:Bvec(Blabelidx) l_N
| 0x0F
| 0x10 x:Bfuncidx
| 0x11 y:Btypeid x:Btableidx
| ...
```

instr

```
\begin{array}{llcllll}
\production{instruction} & \text{\Binstr} & \text{\&::=}& & & & \\
\text{\hex{00}} & \text{\&\rightarrow} & \text{\UNREACHABLE} & \text{\&\&} & \text{\&} & & \\
\text{\hex{01}} & \text{\&\rightarrow} & \text{\NOP} & \text{\&\&} & \text{\&} & & \\
\text{\hex{02}} & \sim\text{\X{bt}}\{:\}\text{\Bblocktype}\sim(\text{\X{in}}\{:\}\text{\Binstr})^{\text{\ast}}\sim\text{\hex{0B}} & & & & & \\
& \text{\&\rightarrow} & \text{\BLOCK}\sim\text{\X{bt}}\sim\text{\X{in}}^{\text{\ast}}\sim\text{\END} & \text{\&\&} & \text{\&} & & \\
\text{\hex{03}} & \sim\text{\X{bt}}\{:\}\text{\Bblocktype}\sim(\text{\X{in}}\{:\}\text{\Binstr})^{\text{\ast}}\sim\text{\hex{0B}} & & & & & \\
& \text{\&\rightarrow} & \text{\LOOP}\sim\text{\X{bt}}\sim\text{\X{in}}^{\text{\ast}}\sim\text{\END} & \text{\&\&} & \text{\&} & & \\
\text{\hex{04}} & \sim\text{\X{bt}}\{:\}\text{\Bblocktype}\sim(\text{\X{in}}\{:\}\text{\Binstr})^{\text{\ast}}\sim\text{\hex{0B}} & & & & & \\
& \text{\&\rightarrow} & \text{\IF}\sim\text{\X{bt}}\sim\text{\X{in}}^{\text{\ast}}\sim\text{\ELSE}\sim\text{\epsilon}\sim\text{\END} & \text{\&\&} & \text{\&} & & \\
\text{\hex{04}} & \sim\text{\X{bt}}\{:\}\text{\Bblocktype}\sim(\text{\X{in}}_1\{:\}\text{\Binstr})^{\text{\ast}}\sim & & & & & \\
& \text{\hex{05}} & \sim(\text{\X{in}}_2\{:\}\text{\Binstr})^{\text{\ast}}\sim\text{\hex{0B}} & & & & \\
& \text{\&\rightarrow} & & & & & \\
& \text{\IF}\sim\text{\X{bt}}\sim\text{\X{in}}_1^{\text{\ast}}\sim\text{\ELSE}\sim\text{\X{in}}_2^{\text{\ast}}\sim\text{\END} & \text{\&\&} & \text{\&} & & \\
\text{\hex{0C}} & \sim l\{:\}\text{\Blabelidx} & \text{\&\rightarrow} & \text{\BR}\sim l & \text{\&\&} & \text{\&} & \\
\text{\hex{0D}} & \sim l\{:\}\text{\Blabelidx} & \text{\&\rightarrow} & \text{\BRIF}\sim l & \text{\&\&} & \text{\&} & \\
\text{\hex{0E}} & \sim l^{\text{\ast}}\{:\}\text{\Bvec}(\text{\Blabelidx})\sim l_N\{:\}\text{\Blabelidx} & & & & & \\
& \text{\&\rightarrow} & \text{\BRTABLE}\sim l^{\text{\ast}}\sim l_N & \text{\&\&} & \text{\&} & & \\
\text{\hex{0F}} & \text{\&\rightarrow} & \text{\RETURN} & \text{\&\&} & \text{\&} & & \\
\text{\hex{10}} & \sim x\{:\}\text{\Bfuncidx} & \text{\&\rightarrow} & \text{\CALL}\sim x & \text{\&\&} & \text{\&} & \\
\text{\hex{11}} & \sim y\{:\}\text{\Btypeid}\sim x\{:\}\text{\Btableidx} & & & & & \\
& \text{\&\rightarrow} & \text{\CALLINDIRECT}\sim x\sim y & \text{\&\&} & & & \\
\end{array}
```

Bugs found in spec and proposals

type errors

- ...missing immediates or record fields in rules

semantic errors

- ...missing stack operands, stack mishandling, index errors

prose errors

- ...unbound variables, missing steps

editorial errors

- ...syntax errors, typos, layout errors, wrong hyperlinks, ...