

# SpecTec Update

**Andreas Rossberg**

with Dongjun Youn, Wonho Shin, Jaehyun Lee, Sukyoung Ryu,  
Joachim Breitner, Philippa Gardner, Sam Lindley, Matija Pretnar,  
Rao Xiaojia, Conrad Watt

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# Recap: spec authoring today

Duplicate work to write *both* formal and prose rules

Writing prose is particularly laborious

Both terrible for code reviews

- ...Latex math is a write-only language

- ...reStructuredText is verbose, repetitive, and diff-unfriendly (e.g. tables, indentation!)

No macro facilities in Sphinx

- ...had to build hacky Python scripts and Sphinx plugins on top (broke repeatedly)

# SpecTec

a DSL for authoring the Wasm spec

**select** ( $t^*$ )?

1. Assert: due to **validation**, a value of **value type i32** is on the top of the stack.
2. Pop the value **i32.const**  $c$  from the stack.
3. Assert: due to **validation**, two more values (of the same **value type**) are on the top of the stack.
4. Pop the value  $val_2$  from the stack.
5. Pop the value  $val_1$  from the stack.
6. If  $c$  is not 0, then:
  - a. Push the value  $val_1$  back to the stack.
7. Else:
  - a. Push the value  $val_2$  back to the stack.

$$\begin{aligned} val_1 \ val_2 \ (\text{i32.const } c) \ (\text{select } t^?) &\hookrightarrow val_1 \quad (\text{if } c \neq 0) \\ val_1 \ val_2 \ (\text{i32.const } c) \ (\text{select } t^?) &\hookrightarrow val_2 \quad (\text{if } c = 0) \end{aligned}$$

```
.. _exec-select:
```

```
:math:\`SELECT~(t^\ast)^?`
```

```
.....
```

1. Assert: due to :ref:`validation <valid-select>`, a :ref:`value <syntax-value>` of :ref:`value type <syntax-valtype>` I32 is on the top of the :ref:`stack <syntax-stack>`.

2. Pop the value :math:`\text{I32.CONST}c` from the :ref:`stack <syntax-stack>`.

3. Assert: due to :ref:`validation <valid-select>`, two more :ref:`values <syntax-value>` (of the same :ref:`value type <syntax-valtype>`) are on the top of the stack.

4. Pop the :ref:`value <syntax-value>` :math:`\text{val}\_2` from the :ref:`stack <syntax-stack>`.

5. Pop the :ref:`value <syntax-value>` :math:`\text{val}\_1` from the :ref:`stack <syntax-stack>`.

6. If :math:`c` is not :math:`0`, then:

a. Push the :ref:`value <syntax-value>` :math:`\text{val}\_1` back to the :ref:`stack <syntax-stack>`.

7. Else:

a. Push the :ref:`value <syntax-value>` :math:`\text{val}\_2` back to the :ref:`stack <syntax-stack>`.

```
.. math::
```

```
\begin{array}{lcl@{\quad}l}
```

```
\val_1~\val_2~(\text{I32}\text{K}\{.\}\text{CONST}c)~(\text{SELECT}t^?) \&\text{stepto} \& \text{val}_1
```

```
\& (\text{iff } c \neq 0) \\\
```

```
\val_1~\val_2~(\text{I32}\text{K}\{.\}\text{CONST}c)~(\text{SELECT}t^?) \&\text{stepto} \& \text{val}_2
```

```
\& (\text{iff } c = 0) \\\
```

```
\end{array}
```

```
rule Step_pure/select-true:
  val_1 val_2 (CONST I32 c) (SELECT t*?) ~> val_1
  -- if c != 0

rule Step_pure/select-false:
  val_1 val_2 (CONST I32 c) (SELECT t*?) ~> val_2
  -- if c = 0
```

[spectec/reduction.watsup](#)



**select**  $(t^*)^?$

1. Assert: Due to validation, a value of value type `i32` is on the top of the stack.
2. Pop the value `(i32.const c)` from the stack.
3. Assert: Due to validation, a value is on the top of the stack.
4. Pop the value  $val_2$  from the stack.
5. Assert: Due to validation, a value is on the top of the stack.
6. Pop the value  $val_1$  from the stack.
7. If  $c$  is not 0, then:
  - a. Push the value  $val_1$  to the stack.
8. Else:
  - a. Push the value  $val_2$  to the stack.

$$\begin{array}{ll} val_1 \ val_2 \ (i32.const \ c) \ (\text{select } (t^*)^?) & \hookrightarrow \ val_1 \quad \text{if } c \neq 0 \\ val_1 \ val_2 \ (i32.const \ c) \ (\text{select } (t^*)^?) & \hookrightarrow \ val_2 \quad \text{if } c = 0 \end{array}$$

# Recap: spec authoring with SpecTec

SpecTec is (mostly) **WYSIWYG** ASCII for the math rules

**single source of truth** for auto-generating:

- ...math in **Latex**

- ...prose in **Sphinx**

- ...mechanised **Coq**, Lean, Agda, Isabelle formalisations (future work)

Math and prose can be **spliced** into spec document

Plus, can run meta-level **interpreter** executing the prose on tests!



# Meta Interpreter

algorithmic prose is derived from user-defined declarative reduction rules

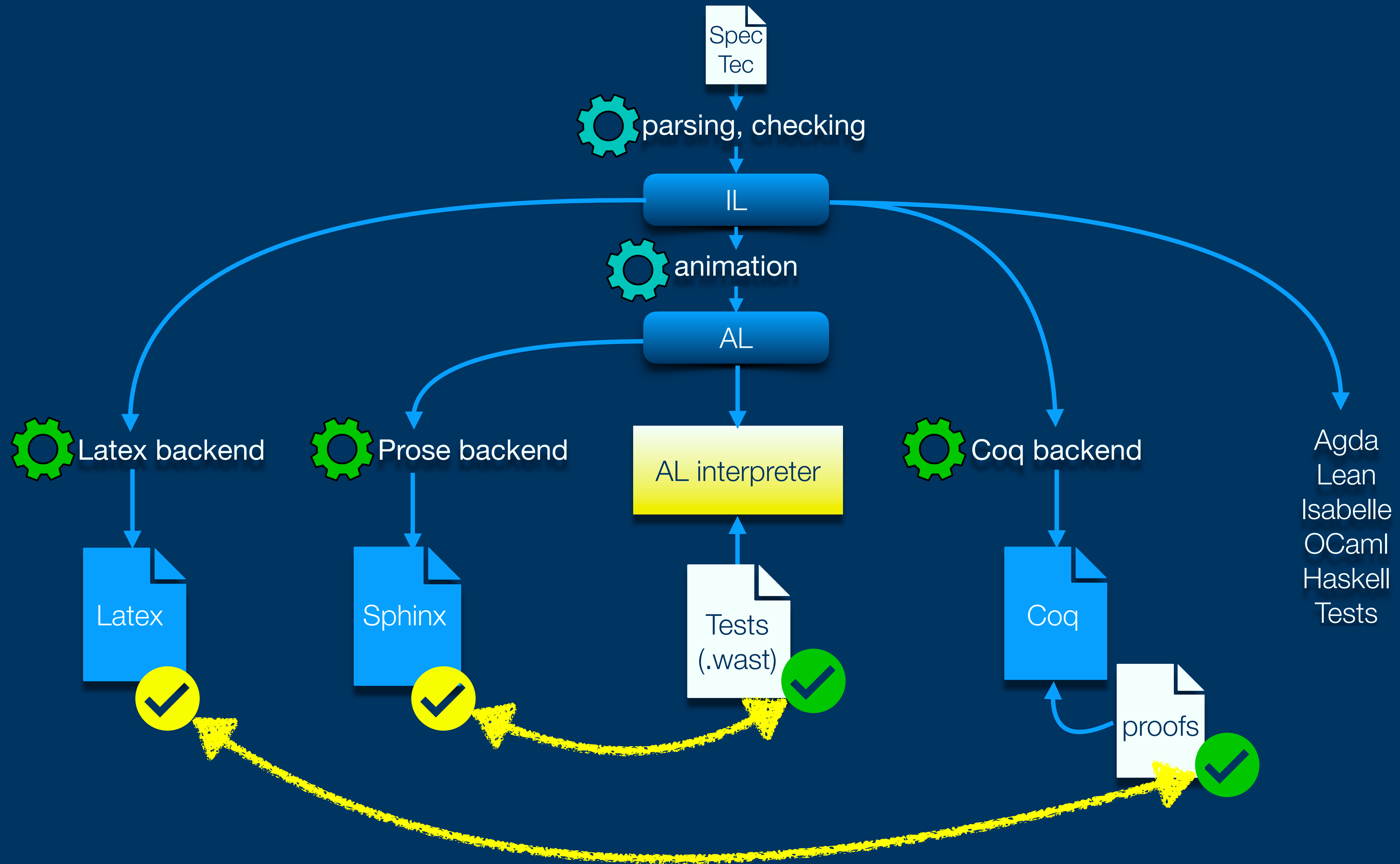
...via a transformation sometimes called *animation*

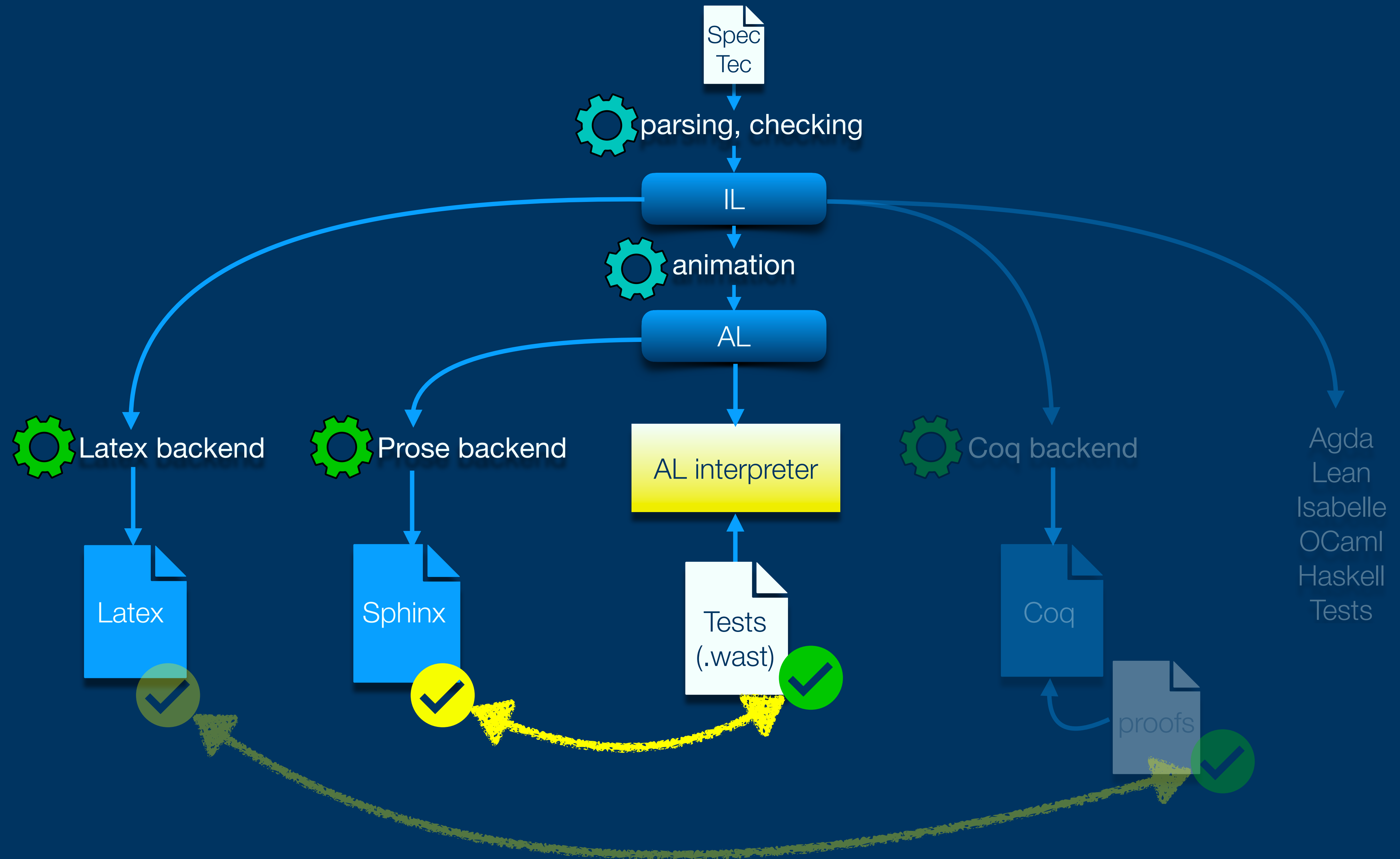
result is an IR called AL (algorithmic language)

...essentially, an AST for the prose

we can interpret this AL

...and thereby indirectly run actual Wasm with actual Wasm spec





# Updates

# Bringing the WebAssembly Standard up to Speed with SpecTec

DONGJUN YOUN, WONHO SHIN, JAEHYUN LEE, and SUKYOUNG RYU, KAIST, South Korea  
JOACHIM BREITNER, Independent, Germany  
PHILIPPA GARDNER, Imperial College London, United Kingdom  
SAM LINDLEY, The University of Edinburgh, United Kingdom  
MATIJA PRETNAR, University of Ljubljana, Slovenia  
XIAOJIA RAO, Imperial College London, United Kingdom  
CONRAD WATT, University of Cambridge, United Kingdom  
ANDREAS ROSSBERG, Independent, Germany

WebAssembly (Wasm) is a portable low-level bytecode language and virtual machine that has seen increasing use in a variety of ecosystems. Its specification is unusually rigorous – including a full formal semantics for the language – and every new feature must be specified in this formal semantics, in prose, and in the official reference interpreter before it can be standardized. With the growing size of the language, this manual process with its redundancies has become laborious and error-prone, and in this work, we offer a solution.

We present SpecTec, a domain-specific language (DSL) and toolchain that facilitates both the Wasm specification and the generation of artifacts necessary to standardize new features. SpecTec serves as a single source of truth — from a SpecTec definition of the Wasm semantics, we can generate a typeset specification, including formal definitions and prose pseudocode descriptions, and a meta-level interpreter. Further backends for test generation and interactive theorem proving are planned. We evaluate SpecTec’s ability to represent the latest Wasm 2.0 and show that the generated meta-level interpreter passes 100% of the applicable official test suite. We show that SpecTec is highly effective at discovering and preventing errors by detecting historical errors in the specification that have been corrected and ten errors in five proposals ready for inclusion in the next version of Wasm. Our ultimate aim is that SpecTec should be adopted by the Wasm standards community and used to specify future versions of the standard.

CCS Concepts: • **Theory of computation** → **Program specifications**; • **Software and its engineering** → **Syntax**; **Semantics**; **Specification languages**; **Domain specific languages**.

Additional Key Words and Phrases: WebAssembly, language specification, executable prose, DSL

## ACM Reference Format:

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Authors’ addresses: Dongjun Youn, f52985@kaist.ac.kr; Wonho Shin, new170527@kaist.ac.kr; Jaehyun Lee, 99jaehyunlee@kaist.ac.kr; Sukyoung Ryu, sryu.cs@kaist.ac.kr, KAIST, Daejeon, South Korea; Joachim Breitner, mail@joachim-breitner.de, Independent, Freiburg, Germany; Philippa Gardner, p.gardner@imperial.ac.uk, Imperial College London, London, United Kingdom; Sam Lindley, Sam.Lindley@ed.ac.uk, The University of Edinburgh, Edinburgh, United Kingdom; Matija Pretnar, matija.pretnar@fmf.uni-lj.si, University of Ljubljana, Ljubljana, Slovenia; Xiaojia Rao, xiaojia.rao19@imperial.ac.uk, Imperial College London, London, United Kingdom; Conrad Watt, conrad.watt@cl.cam.ac.uk, University of Cambridge, Cambridge, United Kingdom; Andreas Rossberg, rossberg@mpi-sws.org, Independent, Munich, Germany.

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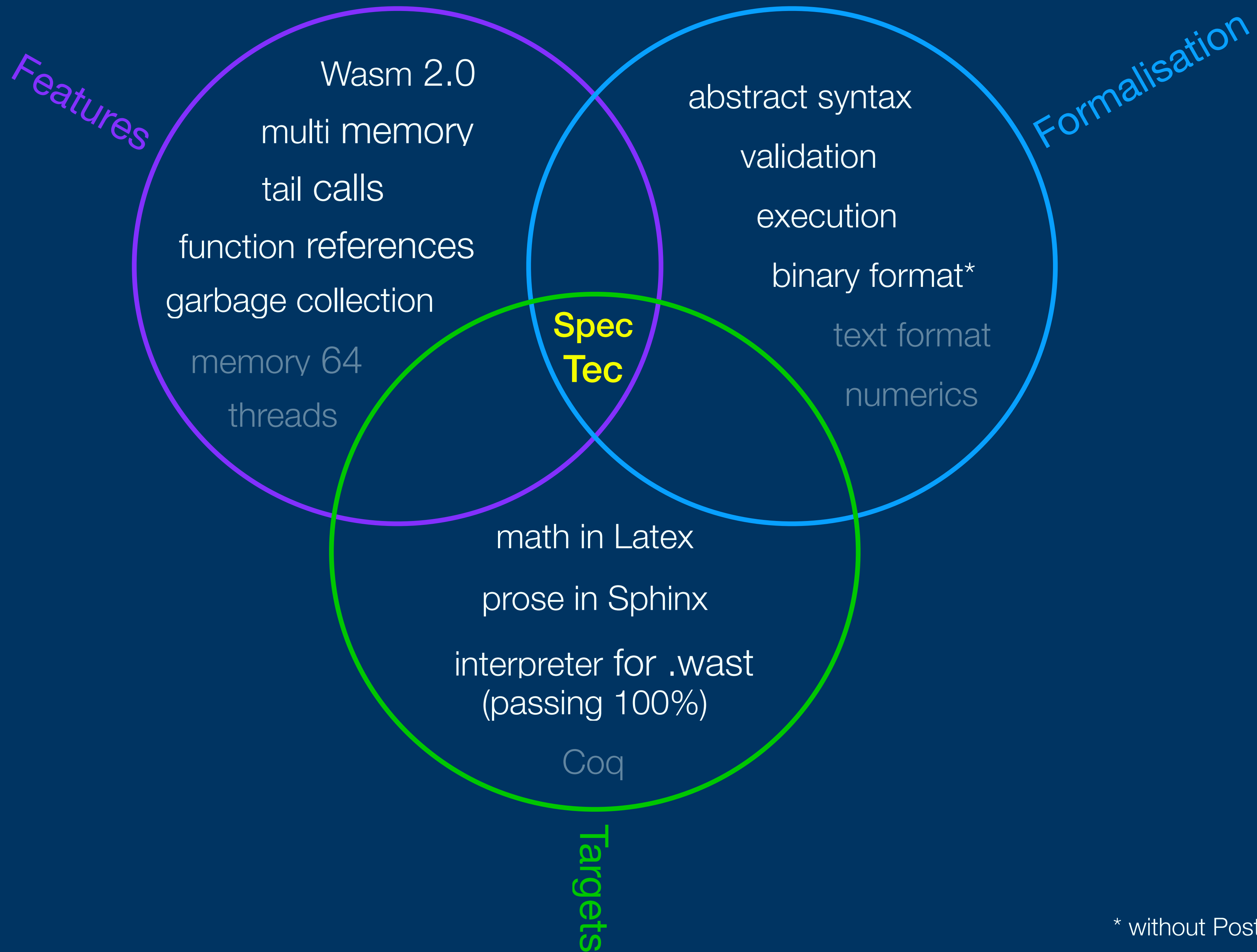
<https://doi.org/10.1145/3656440>

Proc. ACM Program. Lang., Vol. 8, No. PLDI, Article 210. Publication date: June 2024.

Code...



Status



\* without Post-2.0 syntax

# Converting the Document

extended spec build [infrastructure](#) with a SpecTec pass

conversion of document can now happen [incrementally](#), replacing text with splices

WIP, so far we have converted:

- all of [abstract syntax](#) Chapter (syntax/\*.rst)
- [instruction execution](#) (exec/instructions.rst) (math and prose)
- [instruction validation](#) (valid/instructions.rst) (math only)

# Current Limitations

# Current Limitations of Math Renderer

hyperlinks not working yet

layout heuristics are slightly hacky

some formulas need more fine-tuning

hard-codes known syntactic shapes of relations ( $\vdash$  and  $\rightsquigarrow$ )

# Current Limitations of Prose Renderer

conditionals that should be **assertions** (guaranteed by validation)

- ...leads to spurious nesting

- ...interpreter just falls through on missing rules instead of aborting

produced **phrasing** not always ideal

- ...**naming** of generated meta-variables

- ...noise from **helper functions** that should be “inlined”

- ...**iterations** not unpacked

- ...special cases (e.g., say “absent” instead “has length 0” for  $x$ ?)

**hard-codes** various definitional choices that can break when spec changes

**validation** prose has fallen behind a little



# Current Limitations of Meta Interpreter

can do *only execution*, hooks into reference interpreter for everything else

- ...parsing, decoding, validation, numerics

still *fragile* on errors and structural spec changes

- ...esp. depends on various naming conventions being followed by user

a *lot* of *hard-coded knowledge* about various aspects of the spec

- ...often inevitable, e.g., must convert from reference interpreter's AST

requires *adjustment* for non-trivial spec extensions

# Spec Bugs

# Bugs found in spec and proposals

## type errors

- ...missing immediates or record fields in rules

## semantic errors

- ...missing stack operands, stack mishandling, index errors

## prose errors

- ...unbound variables, missing steps

## editorial errors

- ...typos, layout errors, wrong hyperlinks, ...

### `elem. drop $x$`

1. Let  $F$  be the `current frame`.
2. Assert: due to `validation`,  $F.\text{module}.\text{elemaddrs}[x]$  exists.
3. Let  $a$  be the `element address`  $F.\text{module}.\text{elemaddrs}[x]$ .
4. Assert: due to `validation`,  $S.\text{elems}[a]$  exists.
5. Replace  $S.\text{elems}[a]$  with the `element instance`  $\{\text{elem } \epsilon\}$ .

$$S; F; (\text{elem. drop } x) \hookrightarrow S'; F; \epsilon$$

(if  $S' = S$  with  $\text{elems}[F.\text{module}.\text{elemaddrs}[x]] = \{\text{elem } \epsilon\}$ )

## Element Instances

An *element instance* is the runtime representation of an `element segment`. It holds a vector of references and their common `type`.

$$\text{eleminst} ::= \{\text{type } \text{reftype}, \text{elem } \text{vec}(\text{ref})\}$$

# Retroactive bug detection

collected fixed bugs in Wasm 2.0 spec from last 2 years

...and re-injected them into SpecTec formulation

type errors (3)

semantic errors (3)

prose errors (7)

editorial errors (numerous)

...see paper for details

Plan



# Current Focus

burndown list: <https://github.com/wasm-dsl/spectec/issues/67>

addressing renderer limitations

progress on converting the spec

improve robustness and error reporting

tutorial and documentation

(user experience still is rough at the moment...)

# Longer-term

Complete [spec conversion](#)

Generate more parts of the spec

- ...cross-reference anchors, indices, macros

Handle more aspects of the spec in meta interpreter

- ...decoding, parsing, validation

Improve user experience and stability

Theorem prover backends

# Moving Forward

Will have tool and spec in “good shape” by June F2F

- ...crucial parts of the spec converted and on par; tool usable for early adopters

Hoping for official CG sign-off at that point

- ...it is a lot of work, can only invest so much upfront

Actual switch to infrastructure can take longer

- ...and full spec conversion/coverage will be an incremental process anyway

We expect to continue working on SpecTec for at least the next couple of years

- ...improvements, extensions, maintenance, new ideas

What should be the **process**?

What should be the **acceptance criteria**?

Repo: <https://github.com/wasm-dsl/spectec>

Render: <https://wasm-dsl.github.io/spectec/core/>

Paper: <https://mpi-sws.org/~rossberg/papers/spectec1.pdf>

# Outtakes

```
syntax instr hint(desc "instruction") =
| UNREACHABLE
| NOP
| DROP
| SELECT valtype?
| BLOCK blocktype instr*
| LOOP blocktype instr*
| IF blocktype instr* ELSE instr*
| BR labelidx
| BR_IF labelidx
| BR_TABLE labelidx* labelidx
| CALL funcidx
| CALL_INDIRECT tableidx functype
| RETURN
| CONST numtype c_numtype hint(show %.CONST %)
| UNOP numtype unop_numtype hint(show %.%)
| BINOP numtype binop_numtype hint(show %.%)
| TESTOP numtype testop_numtype hint(show %.%)
| RELOP numtype relop_numtype hint(show %.%)
| EXTEND numtype n hint(show %.EXTEND#%)
| CVTOP numtype cvtop numtype sx? hint(show %.%#_#%#_#%)
| ...
```

```
instr ::= unreachable
| nop
| drop
| select valtype?
| block blocktype instr*
| loop blocktype instr*
| if blocktype instr* else instr*
| br labelidx
| br_if labelidx
| br_table labelidx* labelidx
| call funcidx
| call_indirect tableidx functype
| return
| numtype.const c_numtype
| numtype.unop_numtype
| numtype.binop_numtype
| numtype.testop_numtype
| numtype.relop_numtype
| numtype.extendn
| numtype.cvtop_numtype_sx?
```



```
relation Instr_ok: context |- instr : functype hint(show "T")
```

```
rule Instr_ok/nop:
  C |- NOP : epsilon -> epsilon
```

```
rule Instr_ok/block:
  C |- BLOCK bt instr* : t_1* -> t_2*
  -- Blocktype_ok: C |- bt : t_1* -> t_2*
  -- Instrs_ok: C, LABEL t_2* |- instr* : t_1* -> t_2*
```

```
rule Instr_ok/loop:
  C |- LOOP bt instr* : t_1* -> t_2*
  -- Blocktype_ok: C |- bt : t_1* -> t_2*
  -- Instrs_ok: C, LABEL t_1* |- instr* : t_1* -> t_2
```

```
rule Instr_ok/br:
  C |- BR l : t_1* t* -> t_2*
  -- if C.LABEL[l] = t*
```

```
rule Instr_ok/br_if:
  C |- BR_IF l : t* I32 -> t*
  -- if C.LABEL[l] = t*
```

```
rule Instr_ok/br_table:
  C |- BR_TABLE l* l' : t_1* t* -> t_2*
  -- (Resulttype_sub: |- t* <: C.LABEL[l])*
```

$context \vdash instr : functype$

$\frac{}{C \vdash nop : \epsilon \rightarrow \epsilon} [T-NOP]$

$\frac{C \vdash bt : t_1^* \rightarrow t_2^* \quad C, label\ t_2^* \vdash instr^* : t_1^* \rightarrow t_2^*}{C \vdash block\ bt\ instr^* : t_1^* \rightarrow t_2^*} [T-BLOCK]$

$\frac{C \vdash bt : t_1^* \rightarrow t_2^* \quad C, label\ t_1^* \vdash instr^* : t_1^* \rightarrow t_2^*}{C \vdash loop\ bt\ instr^* : t_1^* \rightarrow t_2^*} [T-LOOP]$

$\frac{C.label[l] = t^*}{C \vdash br\ l : t_1^* t^* \rightarrow t_2^*} [T-BR] \quad \frac{C.label[l] = t^*}{C \vdash br\_if\ l : t^* i32 \rightarrow t^*} [T-BR\_IF]$

$\frac{(\vdash t^* \leq C.label[l])^* \quad \vdash t^* \leq C.label[l']}{C \vdash br\_table\ l^* l' : t_1^* t^* \rightarrow t_2^*} [T-BR\_TABLE]$

```
relation Step_pure: config ~> config

rule Step_pure/nop:
  NOP ~> epsilon

rule Step_pure/block:
  val^k (BLOCK bt instr*) ~> (LABEL_n`{epsilon} val^k instr*)
  -- if bt = t_1^k -> t_2^n

rule Step_pure/loop:
  val^k (LOOP bt instr*) ~> (LABEL_n`{LOOP bt instr*} val^k instr*)
  -- if bt = t_1^k -> t_2^n

rule Step_pure/br-zero:
  (LABEL_n`{instr'*} val'* val^n (BR 0) instr*) ~> val^n instr'*

rule Step_pure/br-succ:
  (LABEL_n`{instr'*} val* (BR $(l+1)) instr*) ~> val* (BR l)

rule Step_pure/br_if-true:
  (CONST I32 c) (BR_IF l) ~> (BR l)
  -- if c != 0

rule Step_pure/br_if-false:
  (CONST I32 c) (BR_IF l) ~> epsilon
  -- if c = 0

rule Step_pure/br_table-lt:
  (CONST I32 i) (BR_TABLE l* l') ~> (BR l*[i])
  -- if i < |l*|

rule Step_pure/br_table-le:
  (CONST I32 i) (BR_TABLE l* l') ~> (BR l')
  -- if i >= |l*|
```

$instr^* \hookrightarrow instr^*$

nop	$\hookrightarrow$	$\epsilon$	
$val^k$ (block $bt$ $instr^*$ )	$\hookrightarrow$	$(label_n\{\epsilon\} val^k instr^*)$	if $bt = t_1^k \rightarrow t_2^n$
$val^k$ (loop $bt$ $instr^*$ )	$\hookrightarrow$	$(label_n\{\text{loop } bt \text{ } instr^*\} val^k instr^*)$	if $bt = t_1^k \rightarrow t_2^n$
$(label_n\{instr'^*\} val'^* val^n$ (br 0) $instr^*$ )	$\hookrightarrow$	$val^n instr'^*$	
$(label_n\{instr'^*\} val^*$ (br $l + 1$ ) $instr^*$ )	$\hookrightarrow$	$val^*$ (br $l$ )	
$(i32.const\ c)$ (br_if $l$ )	$\hookrightarrow$	$(br\ l)$	if $c \neq 0$
$(i32.const\ c)$ (br_if $l$ )	$\hookrightarrow$	$\epsilon$	if $c = 0$
$(i32.const\ i)$ (br_table $l^* l'$ )	$\hookrightarrow$	$(br\ l^*[i])$	if $i <  l^* $
$(i32.const\ i)$ (br_table $l^* l'$ )	$\hookrightarrow$	$(br\ l')$	if $i \geq  l^* $

```

relation Step_pure: config ~> config

rule Step_pure/nop:
  NOP ~> epsilon

rule Step_pure/block:
  val^k (BLOCK bt instr*) ~> (LABEL_n`{epsilon} val^k instr*)
  -- if bt = t_1^k -> t_2^n

rule Step_pure/loop:
  val^k (LOOP bt instr*) ~> (LABEL_n`{LOOP bt instr*} val^k instr*)
  -- if bt = t_1^k -> t_2^n

rule Step_pure/br-zero:
  (LABEL_n`{instr'*} val'* val^n (BR 0) instr*) ~> val^n instr'*

rule Step_pure/br-succ:
  (LABEL_n`{instr'*} val* (BR $(l+1)) instr*) ~> val* (BR l)

rule Step_pure/br_if-true:
  (CONST I32 c) (BR_IF l) ~> (BR l)
  -- if c != 0

rule Step_pure/br_if-false:
  (CONST I32 c) (BR_IF l) ~> epsilon
  -- if c = 0

rule Step_pure/br_table-lt:
  (CONST I32 i) (BR_TABLE l* l') ~> (BR l*[i])
  -- if i < |l*|

rule Step_pure/br_table-le:
  (CONST I32 i) (BR_TABLE l* l') ~> (BR l')
  -- if i >= |l*|

```

nop

1. Do nothing.

$$[E\text{-NOP}]\text{nop} \hookrightarrow \epsilon$$

block  $bt\ instr^*$

1. Let  $t_1^k \rightarrow t_2^n$  be  $bt$ .
2. Assert: Due to [validation](#), there are at least  $k$  values on the top of the stack.
3. Pop  $val^k$  from the stack.
4. Let  $L$  be the label whose arity is  $n$  and whose continuation is  $\epsilon$ .
5. Push  $L$  to the stack.
6. Push  $val^k$  to the stack.
7. Jump to  $instr^*$ .

$$[E\text{-BLOCK}]\text{val}^k (\text{block } bt\ instr^*) \hookrightarrow (\text{label}_n\{\epsilon\} \text{val}^k instr^*) \text{ if } bt = t_1^k \rightarrow t_2^n$$

loop  $bt\ instr^*$

1. Let  $t_1^k \rightarrow t_2^n$  be  $bt$ .
2. Assert: Due to [validation](#), there are at least  $k$  values on the top of the stack.
3. Pop  $val^k$  from the stack.
4. Let  $L$  be the label whose arity is  $k$  and whose continuation is `loop  $bt\ instr^*$` .
5. Push  $L$  to the stack.
6. Push  $val^k$  to the stack.

$$[E\text{-LOOP}]\text{val}^k (\text{loop } bt\ instr^*) \hookrightarrow (\text{label}_k\{\text{loop } bt\ instr^*\} \text{val}^k instr^*) \text{ if } bt = t_1^k \rightarrow t_2^n$$

br  $x_0$

1. Let  $L$  be the current label.
2. Let  $n$  be the arity of  $L$ .
3. Let  $instr'^*$  be the continuation of  $L$ .
4. Pop all values  $x_1^*$  from the stack.
5. Exit current context.
6. If  $x_0$  is 0 and the length of  $x_1^*$  is greater than or equal to  $n$ , then:
  - a. Let  $val'^* val^n$  be  $x_1^*$ .
  - b. Push  $val^n$  to the stack.
  - c. Execute the sequence  $instr'^*$ .
7. If  $x_0$  is greater than or equal to 1, then:
  - a. Let  $l$  be  $x_0 - 1$ .
  - b. Let  $val^*$  be  $x_1^*$ .
  - c. Push  $val^*$  to the stack.
  - d. Execute `br  $l$` .

$$[E\text{-BR-ZERO}](\text{label}_n\{instr'^*\} \text{val}'^* \text{val}^n (\text{br } 0) instr^*) \hookrightarrow \text{val}^n instr'^*$$

$$[E\text{-BR-SUCC}](\text{label}_n\{instr'^*\} \text{val}^* (\text{br } l + 1) instr^*) \hookrightarrow \text{val}^* (\text{br } l)$$



grammar Binstr/control: instr =

```
| 0x00                                     => UNREACHABLE
| 0x01                                     => NOP
| 0x02 bt:Bblocktype (in:Binstr)* 0x0B    => BLOCK bt in*
| 0x03 bt:Bblocktype (in:Binstr)* 0x0B    => LOOP bt in*
| 0x04 bt:Bblocktype (in:Binstr)* 0x0B    => IF bt in* epsilon
| 0x04 bt:Bblocktype (in_1:Binstr)* 0x0B => IF bt in_1 in* epsilon
| 0x0C l:Blabelidx                        => BR l
| 0x0D l:Blabelidx                        => BRIF l
| 0x0E l*:Bvec(Blabelidx) l_N:Blabelidx  => BRTABLE l l_N
| 0x0F                                     => RETURN
| 0x10 x:Bfuncidx                         => CALL x
| 0x11 y:Btypeidx x:Btableidx             => CALLINDIRECT x y
| ...
```

instr

```
\begin{array}{llcllll}
\production{instruction} & \Binstr & ::= & & & & \\
\hex{00} & \rightarrow & \text{UNREACHABLE} & \backslash \& \& | & \& \\
\hex{01} & \rightarrow & \text{NOP} & \backslash \& \& | & \& \\
\hex{02} & \sim \backslash X\{bt\}\{:\}\Bblocktype \sim (\backslash X\{in\}\{:\}\Binstr)^\wedge \text{ast} \sim \backslash \hex{0B} & & & & & \\
& \rightarrow & \text{BLOCK} \sim \backslash X\{bt\} \sim \backslash X\{in\}^\wedge \text{ast} \sim \text{END} & \backslash \& \& | & \& \\
\hex{03} & \sim \backslash X\{bt\}\{:\}\Bblocktype \sim (\backslash X\{in\}\{:\}\Binstr)^\wedge \text{ast} \sim \backslash \hex{0B} & & & & & \\
& \rightarrow & \text{LOOP} \sim \backslash X\{bt\} \sim \backslash X\{in\}^\wedge \text{ast} \sim \text{END} & \backslash \& \& | & \& \\
\hex{04} & \sim \backslash X\{bt\}\{:\}\Bblocktype \sim (\backslash X\{in\}\{:\}\Binstr)^\wedge \text{ast} \sim \backslash \hex{0B} & & & & & \\
& \rightarrow & \text{IF} \sim \backslash X\{bt\} \sim \backslash X\{in\}^\wedge \text{ast} \sim \text{ELSE} \sim \text{epsilon} \sim \text{END} & \backslash \& \& | & \& \\
\hex{04} & \sim \backslash X\{bt\}\{:\}\Bblocktype \sim (\backslash X\{in\}_1\{:\}\Binstr)^\wedge \text{ast} \sim & & & & & \\
& \backslash \hex{05} \sim (\backslash X\{in\}_2\{:\}\Binstr)^\wedge \text{ast} \sim \backslash \hex{0B} & & & & & \\
& \rightarrow & & & & & \\
& \text{IF} \sim \backslash X\{bt\} \sim \backslash X\{in\}_1^\wedge \text{ast} \sim \text{ELSE} \sim \backslash X\{in\}_2^\wedge \text{ast} \sim \text{END} & \backslash \& \& | & \& \\
\hex{0C} & \sim l\{:\}\Blabelidx & \rightarrow & \text{BR} \sim l & \backslash \& \& | & \& \\
\hex{0D} & \sim l\{:\}\Blabelidx & \rightarrow & \text{BRIF} \sim l & \backslash \& \& | & \& \\
\hex{0E} & \sim l^\wedge \text{ast}\{:\}\Bvec(\Blabelidx) \sim l_N\{:\}\Blabelidx & & & & & \\
& \rightarrow & \text{BRTABLE} \sim l^\wedge \text{ast} \sim l_N & \backslash \& \& | & \& \\
\hex{0F} & \rightarrow & \text{RETURN} & \backslash \& \& | & \& \\
\hex{10} & \sim x\{:\}\Bfuncidx & \rightarrow & \text{CALL} \sim x & \backslash \& \& | & \& \\
\hex{11} & \sim y\{:\}\Btypeidx \sim x\{:\}\Btableidx & & & & & \\
& \rightarrow & \text{CALLINDIRECT} \sim x \sim y & \backslash \& \& | & \& \\
\end{array}
```

```
.. index:: heap type, type identifier
   pair: validation; heap type
   single: abstract syntax; heap type
.. _valid-heaptype:
```

Heap Types  
~~~~~

Concrete **:ref:**`Heap types <syntax-heaptype>` are only valid when the **:ref:**`type index <syntax-typeidx>` is.

```
:math:`\absheaptype`
.....
```

\* The heap type is valid.

```
.. math::
   \frac{
     }{
       C \vdash\heaptype \absheaptype \ok
     }

```

```
:math:`\typeidx`
.....
```

\* The type **:math:**`C.\CTYPES[\typeidx]` must be defined in the context.

\* Then the heap type is valid.

```
.. math::
   \frac{
     C.\CTYPES[\typeidx] = \deftype
   }{
     C \vdash\heaptype \typeidx \ok
   }

```

```
.. index:: reference type, heap type
   pair: validation; reference type
   single: abstract syntax; reference type
.. _valid-reftype:
```

Reference Types  
~~~~~

**:ref:**`Reference types <syntax-reftype>` are valid when the referenced **:ref:**`heap type <syntax-heaptype>` is.

```
:math:`\REF~\NULL^?~\heaptype`
.....
```

\* The heap type **:math:**`\heaptype` must be **:ref:**`valid <valid-heaptype>`.

\* Then the reference type is valid.

```
.. math::
   \frac{
     C \vdash\reftype \heaptype \ok
   }{
     C \vdash\reftype \REF~\NULL^?~\heaptype \ok
   }

```



```
.. index:: heap type, type identifier
   pair: validation; heap type
   single: abstract syntax; heap type
.. _valid-heaptype:
```

Heap Types  
~~~~~

Concrete **:ref:**`Heap types <syntax-heaptype>` are only valid when the **:ref:**`type index <syntax-typeidx>` is.

```
:math:`\absheaptype`
.....
```

\$\$\{prose: Heaptype\\_ok/\absheaptype\}

\$\$\{rule: Heaptype\\_ok/\absheaptype\}

```
:math:`\typeidx`
.....
```

\$\$\{prose: Heaptype\\_ok/typeidx\}

\$\$\{rule: Heaptype\\_ok/typeidx\}

```
.. index:: reference type, heap type
   pair: validation; reference type
   single: abstract syntax; reference type
.. _valid-reftype:
```

Reference Types  
~~~~~

**:ref:**`Reference types <syntax-reftype>` are valid when the referenced **:ref:**`heap type <syntax-heaptype>` is.

```
:math:`\REF~\NULL^?~\heaptype`
.....
```

\$\$\{prose: Reftype\\_ok\}

\$\$\{rule: Reftype\\_ok\}

```
.. index:: heap type, type identifier
   pair: validation; heap type
   single: abstract syntax; heap type
.. _valid-heaptype:
```

Heap Types  
~~~~~

Concrete **:ref:**`Heap types <syntax-heaptype>` are only valid when the **:ref:**`type index <syntax-typeidx>` is.

```
:math:`\absheaptype`
.....
```

\* The heap type is valid.

```
.. math::
   \frac{
     }{
       C \vdash\heaptype \absheaptype \ok
     }

```

```
:math:`\typeidx`
.....
```

\* The type **:math:**`C.\CTYPES[\typeidx]` must be defined in the context.

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.. index:: reference type, heap type
   pair: validation; reference type
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Reference Types  
~~~~~

**:ref:**`Reference types <syntax-reftype>` are valid when the referenced **:ref:**`heap type <syntax-heaptype>` is.

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:math:`\REF~\NULL^?~\heaptype`
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```

\* The heap type **:math:**`\heaptype` must be **:ref:**`valid <valid-heaptype>`.

\* Then the reference type is valid.

```
.. math::
   \frac{
     C \vdash\reftype \heaptype \ok
   }{
     C \vdash\reftype \REF~\NULL^?~\heaptype \ok
   }

```



Heap Types  
~~~~~

Concrete **:ref:**`Heap types <syntax-heaptype>` are only valid when the **:ref:**`type index <syntax-typeidx>` is.

\$\$\{prose: Heaptype\_ok/\absheaptype\}

\$\$\{rule: Heaptype\_ok/\absheaptype\}

\$\$\{prose: Heaptype\_ok/typeidx\}

\$\$\{rule: Heaptype\_ok/typeidx\}

Reference Types  
~~~~~

**:ref:**`Reference types <syntax-reftype>` are valid when the referenced **:ref:**`heap type <syntax-heaptype>` is.

\$\$\{prose: Reftype\_ok\}

\$\$\{rule: Reftype\_ok\}