## **HD74HC148**

8-to-3-line Octal Priority Encoder

# **HITACHI**

### **Description**

The HD74HC148 encodes eight data lines to three-line (4-2-1) binary (octal). Cascading circuitry (enable input EI and enable output EO) is provided to allow octal expansion without the need for external circuitry. The data inputs and outputs are active at the low logic level.

### **Features**

• High Speed Operation:  $t_{pd}$  (0 - 7 to  $A_0$  -  $A_2$ ) = 15 ns typ ( $C_L$  = 50 pF)

• High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage:  $V_{CC} = 2$  to 6 V

• Low Input Current: 1 μA max

• Low Quiescent Supply Current:  $I_{CC}$  (static) = 4  $\mu$ A max (Ta = 25°C)

### **Function Table**

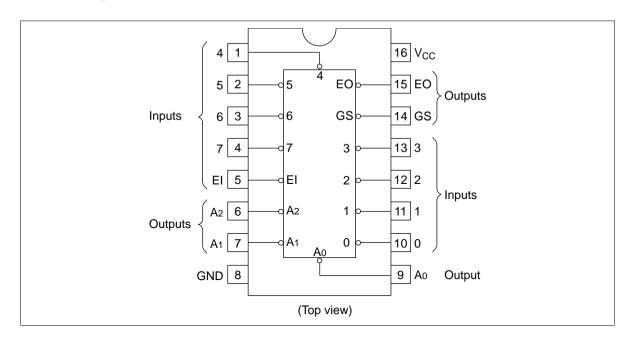
Inpu	uts								Outp	outs			
EI	0	1	2	3	4	5	6	7	$A_2$	<b>A</b> <sub>1</sub>	$A_0$	GS	EO
Н	Χ	Χ	Х	Χ	Χ	Х	Χ	Χ	Н	Н	Н	Н	Н
L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L
L	Χ	Χ	Χ	Χ	Χ	Χ	Χ	L	L	L	L	L	Н
L	Χ	Χ	Χ	Χ	Χ	Χ	L	Н	L	L	Н	L	Н
L	Х	Х	Х	Х	Х	L	Н	Н	L	Н	L	L	Н
L	Χ	Χ	Χ	Χ	L	Н	Н	Н	L	Н	Н	L	Н
L	Χ	Χ	Χ	L	Н	Н	Н	Н	Н	L	L	L	Н
L	Х	Х	L	Н	Н	Н	Н	Н	Н	L	Н	L	Н
L	Х	L	Н	Н	Н	Н	Н	Н	Н	Н	L	L	Н
L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	Н

H: High levelL: Low levelX: Irrelevant



## **HD74HC148**

## Pin Arrangement



## **DC** Characteristics

			Ta =	= 25°(		Ta = - +85°C	-40 to			
Item	Symbol	V <sub>cc</sub> (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions	
Input voltage	V <sub>IH</sub>	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	i —	_	3.15	_	=		
		6.0	4.2	_	_	4.2	_	=		
	V <sub>IL</sub>	2.0	_	_	0.5	_	0.5	V		
		4.5	_	_	1.35	_	1.35	_		
		6.0	_	_	1.8	_	1.8	=		
Output voltage	V <sub>OH</sub>	2.0	1.9	2.0		1.9	_	V	Vin = $V_{IH}$ or $V_{IL}$ $I_{OH} = -20 \mu$	ιΑ
		4.5	4.4	4.5	_	4.4	_	_		
		6.0	5.9	6.0	_	5.9	_	=		
		4.5	4.18	3 —		4.13	_	_	$I_{OH} = -4 \text{ m}.$	Α
		6.0	5.68	3 —	_	5.63	_	=	$I_{OH} = -5.2$	mA
	V <sub>OL</sub>	2.0	_	0.0	0.1	_	0.1	V	$Vin = V_{IH} \text{ or } V_{IL} I_{OL} = 20 \mu A$	١
		4.5	_	0.0	0.1	_	0.1	_		
		6.0	_	0.0	0.1	_	0.1	_		
		4.5	_	_	0.26	_	0.33	=	$I_{OL} = 4 \text{ mA}$	
		6.0	_	_	0.26	_	0.33	_	$I_{OL} = 5.2 \text{ m}$	Α
Input current	lin	6.0	_	_	±0.1	_	±1.0	μΑ	Vin = V <sub>CC</sub> or GND	
Quiescent supply current	I <sub>cc</sub>	6.0	_	_	4.0	_	40	μΑ	$Vin = V_{CC}$ or GND, lout = 0	μΑ

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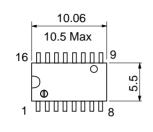
**AC Characteristics** ( $C_L = 50 \text{ pF}$ , Input  $t_r = t_f = 6 \text{ ns}$ )

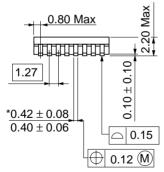
	Ta = -40 to
Ta = 25°C	+85°C

Item	Symbol	V <sub>cc</sub> (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t <sub>PLH</sub>	2.0	_	_	230	_	290	ns	0 - 7 to A <sub>0</sub> - A <sub>2</sub>
time	$t_{\tiny PHL}$	4.5	_	15	46	_	58		
		6.0	_	_	39	_	49		
	t <sub>PLH</sub>	2.0	_	_	250	_	315	ns	0 - 7 to EO
	$t_{\tiny PHL}$	4.5	_	16	50	_	63	_	
		6.0	_	_	43	_	54	_	
	t <sub>PLH</sub>	2.0	_	_	270	_	340	ns	0 - 7 to GS
	$t_{\tiny PHL}$	4.5	_	18	54	_	68	=	
		6.0	_	_	46	_	58	_	
	t <sub>PLH</sub>	2.0	_	_	230	_	290	ns	EI to A <sub>0</sub> - A <sub>2</sub>
	$t_{\tiny PHL}$	4.5	_	12	46	_	58	=	
		6.0	_	_	39	_	49	_	
	t <sub>PLH</sub>	2.0	_	_	250	_	315	ns	El to GS
	$t_{\tiny PHL}$	4.5	_	12	50	_	63	=	
		6.0	_	_	43	_	54	-	
	t <sub>PLH</sub>	2.0	_	_	270	_	340	ns	El to EO
	$t_{\tiny PHL}$	4.5	_	12	54	_	68	=	
		6.0	_	_	46	_	58	-	
Output rise/fall	t <sub>TLH</sub>	2.0	_	_	75	_	90	ns	
time	$t_{\text{THL}}$	4.5	_	5	15	_	19	_	
		6.0	_	_	13	_	16	_	
Input capacitance	Cin	_	_	5	10	_	10	pF	

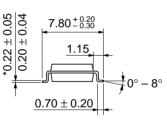
Unit: mm 19.20 20.00 Max 16 7.40 Max 6.30 1.3 1.11 Max 7.62 5.06 Max 2.54 Min 0.51 Min  $0.25^{+0.13}_{-0.05}$  $0.48 \pm 0.10$  $2.54\pm0.25$  $0^{\circ} - 15^{\circ}$ Hitachi Code DP-16 **JEDEC** Conforms EIAJ Conforms Weight (reference value) 1.07 g

Unit: mm





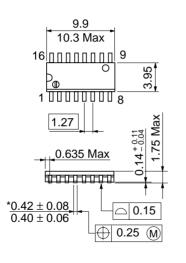


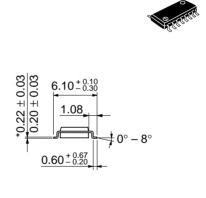


Hitachi Code	FP-16DA
JEDEC	_
EIAJ	Conforms
Weight (reference value)	0.24 g

\*Dimension including the plating thickness
Base material dimension

Unit: mm





\*Dimension including the plating thickness
Base material dimension

Hitachi Code	FP-16DN
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.15 g

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