



Additional Information

This notice includes information related to your MC68681 expansion product. Please read this sheet carefully and retain for your reference.

1. JP2 & JP3 Jumper Setting

JP2 & JP3 are optional jumpers that allows your rosco_m68k to receive power via the FTDI module.

These jumpers must only be shorted when the board is powered via the module.

Only one of these jumpers should be shorted at any given time.

Connecting this jumper when external power is also connected may damage your rosco_m68k, your FTDI module, power supply and any connected equipment.

Additionally, when powering the board via the FTDI module, you must ensure that the current requirements (500mA for the main board alone) are met. Where your computer or FTDI module are unable to supply the minimum current requirement, improper operation and (in rare cases) permanent damage may occur.

2. JP4 Jumper Setting

JP4 determines whether DTACK generation is done for the MC68681 board. This jumper should only be shorted when using the expansion with a revision 1.2 or higher main board.

When using with the revision 1 board, this jumper should be left open. Failure to observe this will cause improper operation and (in rare cases) may cause permanent damage.

3. Baud Rate & Line Coding

When used with the standard firmware 1.1 or 1.2, your terminal should be set for 115,200BPS, 8N1.