

National University Of Computer and Emerging Sciences

Assignment 3:

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SECTION - BCS(3A)

Subject - Computer Organization and Assembly Language

Question No. 1:

[org 0x0100]

mov ax, 1100010110100011b ; Test case 1

;mov ax, 11111111111100000b ; Test case 2

mov bx, 0 ; iteration counter

mov cx, 0; counter for ones

start:

mov dx, 0

shr ax, 1; Shift AX right by 1

jnc start; If no carry, jump back to start

add cx, 1; Increment CX for a counted one

cmp ax, 0; Check if AX is 0

jnz start ; If not, repeat

mov ax, cx ; Move CX (count of ones) to AX

add bx, 1; Increment BX (iteration counter)

cmp ax, 1; Check if AX equals 1

jz end ; If yes, jump to end

mov cx, 16; Reset CX to 16 for 16-bit binary length

11:

sub cx, 1; Decrement CX

rol ax, 1; Rotate AX left by 1

jnc 12; If no carry, jump to 12

add dx, 1; Increment DX for a counted one

12:

cmp cx, 0; Check if CX is 0

jnz l1; If not, repeat l1

mov cx, 0; Reset CX to 0

cmp dx, 2 ; Check if $DX \ge 2$

jge start ; If yes, jump back to start

add bx, 1; Increment BX for another iteration

13:

cmp ax, 1; Check if AX equals 1

je end ; If yes, jump to end

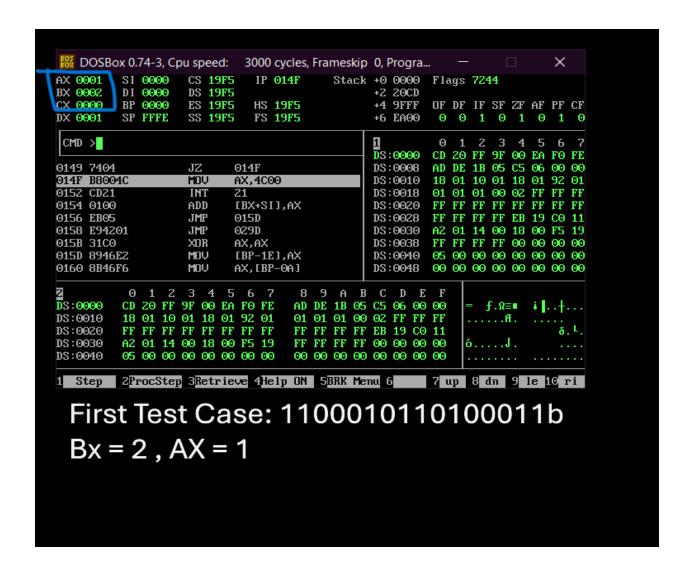
shr ax, 1; Shift AX right by 1

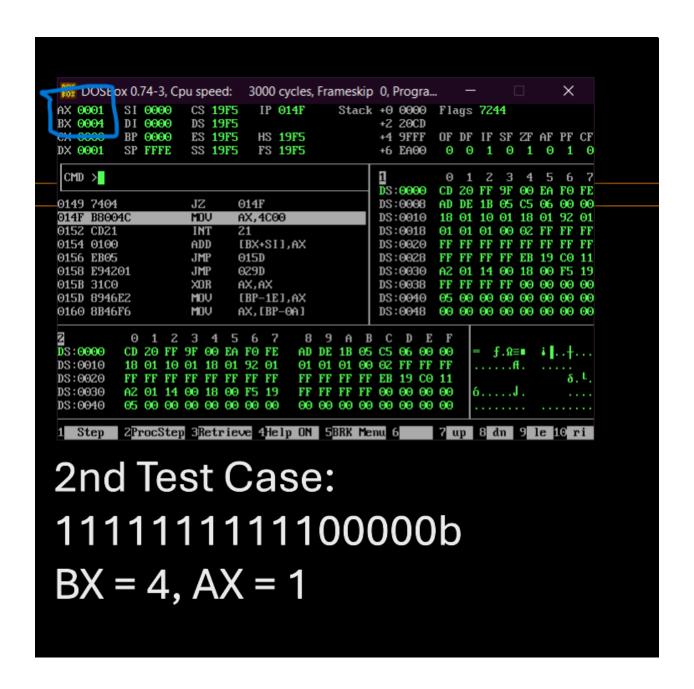
jmp l3 ; Repeat l3

end:

mov ax, 0x4c00

int 0x21



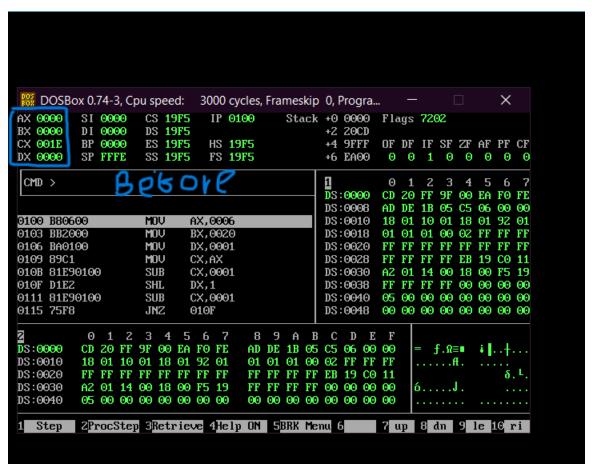


Question 2:

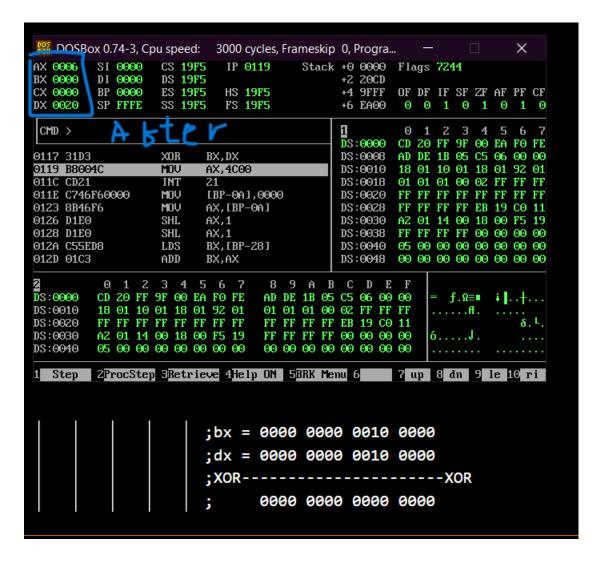
[org 0x0100)]	
mov ax,6 mov bx,32	;bx = 0000 0000 0010 0000	
mov dx,1	;dx = 0000 0000 0000 0001	
mov cx,ax		
sub cx,1		
11:		
shl dx,1 sub cx,1		
jnz l1		
xor bx,dx	$; dx = 0000 \ 0000 \ 0010 \ 0000$	
mov ax,0x4c00		
int 0x021		

```
;bx = 0000 0000 0010 0000
;dx = 0000 0000 0010 0000
;XOR-----XOR
: 0000 0000 0000 0000
```

Before:



After:



Question #3:

Instruction	AX register (value)	Carry Flag (value)
stc	0000	1
mov ax, 0x0559	022F	1
adc ah,'T'	572F	0
cmc	572F	1
xor ah, al	782F	0
mov cl, 4	782F	0
shr al, cl	7802	1
rcr ah, cl	1702	1