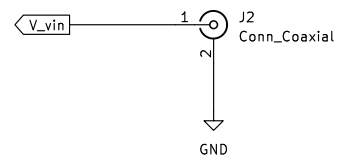
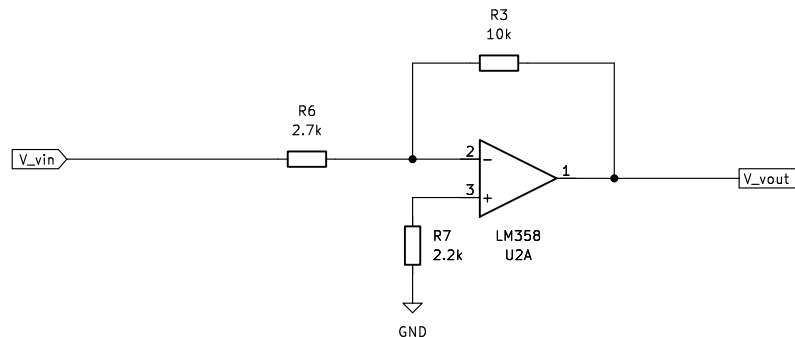


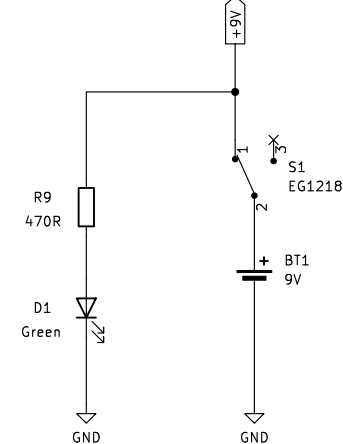
# HP141T Vertical Output -> BNC Adapter



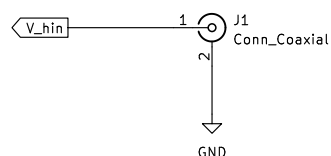
# Vertical Input Conditioning Circuit



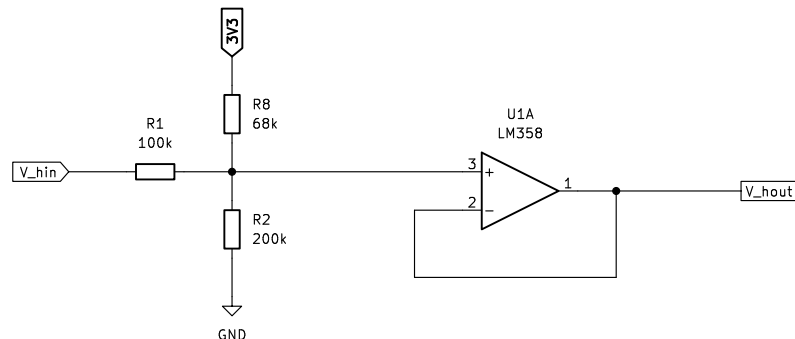
# Circuit Power



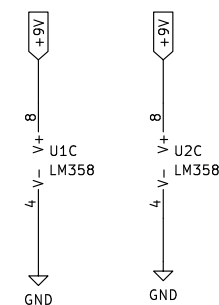
# HP141T Horizontal Output -> BNC Adapter



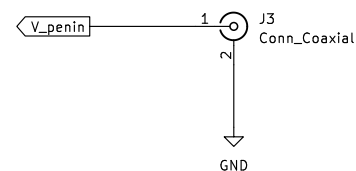
# HP141T Horizontal Output Conditioning Circuit



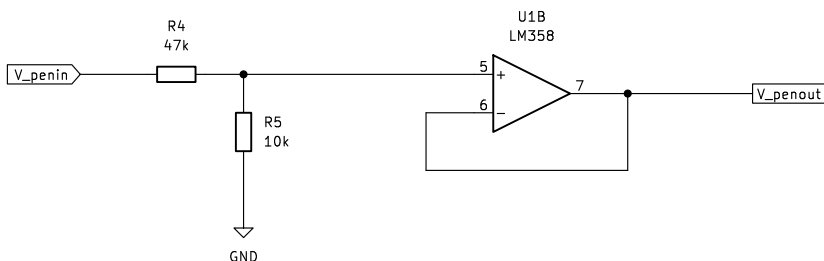
# Op-amp Positive and Negative Rails



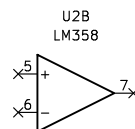
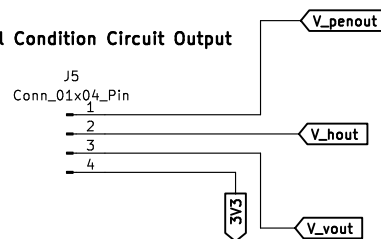
# HP141T Pen Lift Output -> BNC Adapter



# HP141T Pen Lift Output Conditioning Circuit



# Signal Condition Circuit Output



Vpenin = Input pen lift auxiliary signal from HP141T  
Vhin = Input horizontal auxiliary signal from HP141T  
Vvin = Input vertical auxiliary signal from HP141T

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**Title: Signal Conditioning Circuit for HP141T Auxiliary Outputs**

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