

```
state my_state0:  
if (VGA_source_bd_i/system_ila_0/U0/probe1_1 == 1'b1) then  
#test on v_tc_0_vsync_out  
goto my_state1;  
else  
goto my_state0;  
endif
```

```
state my_state1:  
if (VGA_source_bd_i/system_ila_0/U0/probe0_1 == 1'bR) then  
#test on v_tc_0_hsync_out  
reset_counter $counter0;  
goto count_LVAL;  
else  
goto my_state1;  
endif
```

```
state count_LVAL:  
if ($counter0 != 16'u150) then  
increment_counter $counter0;  
goto count_LVAL;  
else  
goto my_state2;  
endif
```

```
state my_state2:  
trigger;
```