BONNIE (GUANQING) HU

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EDUCATION

McGill University, Canada

January 2018 - Present

M.Eng., Electrical Engineering

· Specialization: Computer Vision (Supervised by Professor James Clark)

McGill University, Canada

September 2013 - December 2017

Bachelor of Engineering, Honors Electrical Engineering

· Cumulative GPA: 3.9/4.0

· Awards: 2014-2015 Douglas H. Macaulay Scholarship, 2015-2016 the Class of '83 Scholarships

SKILLS

Language English, Chinese (Mandarin)

Programming Languages Python, C, Embedded C, Java, Assembly, VHDL

Libraries and Knowledge Keras, Pandas, Django; machine learning, reinforcement learning, acoustic

WORK EXPERIENCES

HW/FW Developer Intern

May 2017 - July 2017

Ericsson, Beijing, China

- · Winner of Excellent Intern Award
- · Primarily worked on FPGA acceleration project, including development of command line interface and real-time monitoring dashboard. Developed data processing and automation tools.

Speech Science Intern

September 2016 – April 2017

Nuance Communications, Montréal, Canada

- · Worked on numerous Speech recognition projects, relating primarily to Biometrics suite of technologies.
- · Supported several large biometrics clients for system deployment and migration.
- · Effectively collaborated with remote staffs. Developed high-quality analysis and testing tools.

EXTRA CURRICULUM ACTIVITIES

Hackathons

- · McHacks 2018: Winner of Achievement Unlocked: the most fun and creative game dev hack. (VR game)
- · ConUHack 2017: Received honorable mention for the best efforts of using SAP Yaas API

Student Society and Club

- · VP Finance: McGill Electrical Engineering Graduate Student Society (January 2018 Present)
- · VP Finance: McGill ECSESS RoboElectronics Club (January 2015 April 2017)

ACADEMIC PROJECTS

- · Machine learning: Top 2 in Modified MNIST Classification Competition. Implemented modified VGGNet with image augmentation using Keras library.
- · Reinforcement learning: Dueling bandits algorithm implementation and analysis
- · Electrical engineering related: FPGA design in VHDL, microelectronic lab