

libcaer

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# Contents

<b>1</b>	<b>Data Structure Index</b>	<b>1</b>
1.1	Data Structures . . . . .	1
<b>2</b>	<b>File Index</b>	<b>3</b>
2.1	File List . . . . .	3
<b>3</b>	<b>Data Structure Documentation</b>	<b>5</b>
3.1	caer_bias_coarsefine Struct Reference . . . . .	5
3.1.1	Detailed Description . . . . .	5
3.2	caer_bias_shiftedsources Struct Reference . . . . .	5
3.2.1	Detailed Description . . . . .	6
3.3	caer_bias_vdac Struct Reference . . . . .	6
3.3.1	Detailed Description . . . . .	6
3.4	caer_davis_info Struct Reference . . . . .	6
3.4.1	Detailed Description . . . . .	8
3.5	caer_dvs128_info Struct Reference . . . . .	8
3.5.1	Detailed Description . . . . .	8
3.6	caer_dynapse_info Struct Reference . . . . .	8
3.6.1	Detailed Description . . . . .	9

<b>4 File Documentation</b>	<b>11</b>
4.1 devices/davis.h File Reference	11
4.1.1 Detailed Description	20
4.1.2 Macro Definition Documentation	20
4.1.2.1 CAER_DEVICE_DAVIS_FX2	20
4.1.2.2 CAER_DEVICE_DAVIS_FX3	20
4.1.2.3 DAVIS128_CONFIG_BIAS_ADCCOMPBP	20
4.1.2.4 DAVIS128_CONFIG_BIAS_ADCREFHIGH	21
4.1.2.5 DAVIS128_CONFIG_BIAS_ADCREFLOW	21
4.1.2.6 DAVIS128_CONFIG_BIAS_AEPDBN	21
4.1.2.7 DAVIS128_CONFIG_BIAS_AEPUXBP	21
4.1.2.8 DAVIS128_CONFIG_BIAS_AEPUYBP	22
4.1.2.9 DAVIS128_CONFIG_BIAS_APSCAS	22
4.1.2.10 DAVIS128_CONFIG_BIAS_APSOEVERFLOWLEVEL	22
4.1.2.11 DAVIS128_CONFIG_BIAS_APSROSFBN	22
4.1.2.12 DAVIS128_CONFIG_BIAS_BIASBUFFER	23
4.1.2.13 DAVIS128_CONFIG_BIAS_COLSELLOWBN	23
4.1.2.14 DAVIS128_CONFIG_BIAS_DACBUFBP	23
4.1.2.15 DAVIS128_CONFIG_BIAS_DIFFBN	23
4.1.2.16 DAVIS128_CONFIG_BIAS_IFREFRBN	24
4.1.2.17 DAVIS128_CONFIG_BIAS_IFTHRBN	24
4.1.2.18 DAVIS128_CONFIG_BIAS_LCOLTIMEOUTBN	24
4.1.2.19 DAVIS128_CONFIG_BIAS_LOCALBUFBN	24
4.1.2.20 DAVIS128_CONFIG_BIAS_OFFBN	25
4.1.2.21 DAVIS128_CONFIG_BIAS_ONBN	25
4.1.2.22 DAVIS128_CONFIG_BIAS_PADFOLLBN	25
4.1.2.23 DAVIS128_CONFIG_BIAS_PIXINVBN	25
4.1.2.24 DAVIS128_CONFIG_BIAS_PRBP	26
4.1.2.25 DAVIS128_CONFIG_BIAS_PRSFBP	26
4.1.2.26 DAVIS128_CONFIG_BIAS_READOUTBUFBP	26

4.1.2.27	DAVIS128_CONFIG_BIAS_REFRBP . . . . .	26
4.1.2.28	DAVIS128_CONFIG_BIAS_SSN . . . . .	27
4.1.2.29	DAVIS128_CONFIG_BIAS_SSP . . . . .	27
4.1.2.30	DAVIS128_CONFIG_CHIP_AERNAROW . . . . .	27
4.1.2.31	DAVIS128_CONFIG_CHIP_ANALOGMUX0 . . . . .	27
4.1.2.32	DAVIS128_CONFIG_CHIP_ANALOGMUX1 . . . . .	27
4.1.2.33	DAVIS128_CONFIG_CHIP_ANALOGMUX2 . . . . .	27
4.1.2.34	DAVIS128_CONFIG_CHIP_BIASMUX0 . . . . .	28
4.1.2.35	DAVIS128_CONFIG_CHIP_DIGITALMUX0 . . . . .	28
4.1.2.36	DAVIS128_CONFIG_CHIP_DIGITALMUX1 . . . . .	28
4.1.2.37	DAVIS128_CONFIG_CHIP_DIGITALMUX2 . . . . .	28
4.1.2.38	DAVIS128_CONFIG_CHIP_DIGITALMUX3 . . . . .	28
4.1.2.39	DAVIS128_CONFIG_CHIP_GLOBAL_SHUTTER . . . . .	28
4.1.2.40	DAVIS128_CONFIG_CHIP_RESETCALIBNEURON . . . . .	28
4.1.2.41	DAVIS128_CONFIG_CHIP_RESETTESTPIXEL . . . . .	28
4.1.2.42	DAVIS128_CONFIG_CHIP_SELECTGRAYCOUNTER . . . . .	29
4.1.2.43	DAVIS128_CONFIG_CHIP_TYPCALIBNEURON . . . . .	29
4.1.2.44	DAVIS128_CONFIG_CHIP_USEAOUT . . . . .	29
4.1.2.45	DAVIS208_CONFIG_BIAS_ADCCOMPBP . . . . .	29
4.1.2.46	DAVIS208_CONFIG_BIAS_ADCREFHIGH . . . . .	29
4.1.2.47	DAVIS208_CONFIG_BIAS_ADCREFLOW . . . . .	29
4.1.2.48	DAVIS208_CONFIG_BIAS_AEPDBN . . . . .	30
4.1.2.49	DAVIS208_CONFIG_BIAS_AEPUXBP . . . . .	30
4.1.2.50	DAVIS208_CONFIG_BIAS_AEPUYBP . . . . .	30
4.1.2.51	DAVIS208_CONFIG_BIAS_APSCAS . . . . .	30
4.1.2.52	DAVIS208_CONFIG_BIAS_APSEVERFLOWLEVEL . . . . .	31
4.1.2.53	DAVIS208_CONFIG_BIAS_APSROSFBN . . . . .	31
4.1.2.54	DAVIS208_CONFIG_BIAS_BIASBUFFER . . . . .	31
4.1.2.55	DAVIS208_CONFIG_BIAS_COLSELLOWBN . . . . .	31
4.1.2.56	DAVIS208_CONFIG_BIAS_DACBUFBP . . . . .	32

4.1.2.57	DAVIS208_CONFIG_BIAS_DIFFBN	32
4.1.2.58	DAVIS208_CONFIG_BIAS_IFREFRBN	32
4.1.2.59	DAVIS208_CONFIG_BIAS_IFTHRBN	32
4.1.2.60	DAVIS208_CONFIG_BIAS_LCOLTIMEOUTBN	33
4.1.2.61	DAVIS208_CONFIG_BIAS_LOCALBUFBN	33
4.1.2.62	DAVIS208_CONFIG_BIAS_OFFBN	33
4.1.2.63	DAVIS208_CONFIG_BIAS_ONBN	33
4.1.2.64	DAVIS208_CONFIG_BIAS_PADFOLLBN	34
4.1.2.65	DAVIS208_CONFIG_BIAS_PIXINVBN	34
4.1.2.66	DAVIS208_CONFIG_BIAS_PRBP	34
4.1.2.67	DAVIS208_CONFIG_BIAS_PRSFBP	34
4.1.2.68	DAVIS208_CONFIG_BIAS_READOUTBUFBP	35
4.1.2.69	DAVIS208_CONFIG_BIAS_REFRBP	35
4.1.2.70	DAVIS208_CONFIG_BIAS_REFSS	35
4.1.2.71	DAVIS208_CONFIG_BIAS_REFSSBN	35
4.1.2.72	DAVIS208_CONFIG_BIAS_REGBIASBP	36
4.1.2.73	DAVIS208_CONFIG_BIAS_RESETHIGHPASS	36
4.1.2.74	DAVIS208_CONFIG_BIAS_SSN	36
4.1.2.75	DAVIS208_CONFIG_BIAS_SSP	36
4.1.2.76	DAVIS208_CONFIG_CHIP_AERNAROW	37
4.1.2.77	DAVIS208_CONFIG_CHIP_ANALOGMUX0	37
4.1.2.78	DAVIS208_CONFIG_CHIP_ANALOGMUX1	37
4.1.2.79	DAVIS208_CONFIG_CHIP_ANALOGMUX2	37
4.1.2.80	DAVIS208_CONFIG_CHIP_BIASMUX0	37
4.1.2.81	DAVIS208_CONFIG_CHIP_DIGITALMUX0	37
4.1.2.82	DAVIS208_CONFIG_CHIP_DIGITALMUX1	37
4.1.2.83	DAVIS208_CONFIG_CHIP_DIGITALMUX2	37
4.1.2.84	DAVIS208_CONFIG_CHIP_DIGITALMUX3	38
4.1.2.85	DAVIS208_CONFIG_CHIP_GLOBAL_SHUTTER	38
4.1.2.86	DAVIS208_CONFIG_CHIP_RESETCALIBNEURON	38

4.1.2.87	DAVIS208_CONFIG_CHIP_RESETESTPIXEL . . . . .	38
4.1.2.88	DAVIS208_CONFIG_CHIP_SELECTBIASREFSS . . . . .	38
4.1.2.89	DAVIS208_CONFIG_CHIP_SELECTGRAYCOUNTER . . . . .	38
4.1.2.90	DAVIS208_CONFIG_CHIP_SELECTHIGHPASS . . . . .	38
4.1.2.91	DAVIS208_CONFIG_CHIP_SELECTPOSFB . . . . .	38
4.1.2.92	DAVIS208_CONFIG_CHIP_SELECTPREAMPAVG . . . . .	39
4.1.2.93	DAVIS208_CONFIG_CHIP_SELECTSENSE . . . . .	39
4.1.2.94	DAVIS208_CONFIG_CHIP_TYPENCALIBNEURON . . . . .	39
4.1.2.95	DAVIS208_CONFIG_CHIP_USEAOUT . . . . .	39
4.1.2.96	DAVIS240_CONFIG_BIAS_AEPDBN . . . . .	39
4.1.2.97	DAVIS240_CONFIG_BIAS_AEPUXBP . . . . .	39
4.1.2.98	DAVIS240_CONFIG_BIAS_AEPUYBP . . . . .	40
4.1.2.99	DAVIS240_CONFIG_BIAS_APSCASEPC . . . . .	40
4.1.2.100	DAVIS240_CONFIG_BIAS_APSOEVERFLOWLEVELBN . . . . .	40
4.1.2.101	DAVIS240_CONFIG_BIAS_APSROSFBN . . . . .	40
4.1.2.102	DAVIS240_CONFIG_BIAS_BIASBUFFER . . . . .	40
4.1.2.103	DAVIS240_CONFIG_BIAS_DIFFBN . . . . .	41
4.1.2.104	DAVIS240_CONFIG_BIAS_DIFFCASBNC . . . . .	41
4.1.2.105	DAVIS240_CONFIG_BIAS_IFREFRBN . . . . .	41
4.1.2.106	DAVIS240_CONFIG_BIAS_IFTHRBN . . . . .	41
4.1.2.107	DAVIS240_CONFIG_BIAS_LCOLTIMEOUTBN . . . . .	41
4.1.2.108	DAVIS240_CONFIG_BIAS_LOCALBUFBN . . . . .	42
4.1.2.109	DAVIS240_CONFIG_BIAS_OFFBN . . . . .	42
4.1.2.110	DAVIS240_CONFIG_BIAS_ONBN . . . . .	42
4.1.2.111	DAVIS240_CONFIG_BIAS_PADFOLLBN . . . . .	42
4.1.2.112	DAVIS240_CONFIG_BIAS_PIXINVBN . . . . .	42
4.1.2.113	DAVIS240_CONFIG_BIAS_PRBP . . . . .	43
4.1.2.114	DAVIS240_CONFIG_BIAS_PRSFBP . . . . .	43
4.1.2.115	DAVIS240_CONFIG_BIAS_REFRBP . . . . .	43
4.1.2.116	DAVIS240_CONFIG_BIAS_SSN . . . . .	43

4.1.2.117 DAVIS240_CONFIG_BIAS_SSP . . . . .	43
4.1.2.118 DAVIS240_CONFIG_CHIP_AERNAROW . . . . .	44
4.1.2.119 DAVIS240_CONFIG_CHIP_ANALOGMUX0 . . . . .	44
4.1.2.120 DAVIS240_CONFIG_CHIP_ANALOGMUX1 . . . . .	44
4.1.2.121 DAVIS240_CONFIG_CHIP_ANALOGMUX2 . . . . .	44
4.1.2.122 DAVIS240_CONFIG_CHIP_BIASMUX0 . . . . .	44
4.1.2.123 DAVIS240_CONFIG_CHIP_DIGITALMUX0 . . . . .	44
4.1.2.124 DAVIS240_CONFIG_CHIP_DIGITALMUX1 . . . . .	44
4.1.2.125 DAVIS240_CONFIG_CHIP_DIGITALMUX2 . . . . .	45
4.1.2.126 DAVIS240_CONFIG_CHIP_DIGITALMUX3 . . . . .	45
4.1.2.127 DAVIS240_CONFIG_CHIP_GLOBAL_SHUTTER . . . . .	45
4.1.2.128 DAVIS240_CONFIG_CHIP_RESETCALIBNEURON . . . . .	45
4.1.2.129 DAVIS240_CONFIG_CHIP_RESETTESTPIXEL . . . . .	45
4.1.2.130 DAVIS240_CONFIG_CHIP_SPECIALPIXELCONTROL . . . . .	45
4.1.2.131 DAVIS240_CONFIG_CHIP_TYPCALIBNEURON . . . . .	45
4.1.2.132 DAVIS240_CONFIG_CHIP_USEAOUT . . . . .	46
4.1.2.133 DAVIS346_CONFIG_BIAS_ADCCOMPBP . . . . .	46
4.1.2.134 DAVIS346_CONFIG_BIAS_ADCREFHIGH . . . . .	46
4.1.2.135 DAVIS346_CONFIG_BIAS_ADCREFLOW . . . . .	46
4.1.2.136 DAVIS346_CONFIG_BIAS_ADCTESTVOLTAGE . . . . .	46
4.1.2.137 DAVIS346_CONFIG_BIAS_AEPDBN . . . . .	47
4.1.2.138 DAVIS346_CONFIG_BIAS_AEPUXBP . . . . .	47
4.1.2.139 DAVIS346_CONFIG_BIAS_AEPUYBP . . . . .	47
4.1.2.140 DAVIS346_CONFIG_BIAS_APSCAS . . . . .	47
4.1.2.141 DAVIS346_CONFIG_BIAS_APSEVERFLOWLEVEL . . . . .	48
4.1.2.142 DAVIS346_CONFIG_BIAS_APSROSFBN . . . . .	48
4.1.2.143 DAVIS346_CONFIG_BIAS_BIASBUFFER . . . . .	48
4.1.2.144 DAVIS346_CONFIG_BIAS_COLSELLOWBN . . . . .	48
4.1.2.145 DAVIS346_CONFIG_BIAS_DACBUFBP . . . . .	49
4.1.2.146 DAVIS346_CONFIG_BIAS_DIFFBN . . . . .	49



4.1.2.147 DAVIS346_CONFIG_BIAS_IFREFRBN . . . . .	49
4.1.2.148 DAVIS346_CONFIG_BIAS_IFTHRBN . . . . .	49
4.1.2.149 DAVIS346_CONFIG_BIAS_LCOLTIMEOUTBN . . . . .	50
4.1.2.150 DAVIS346_CONFIG_BIAS_LOCALBUFBN . . . . .	50
4.1.2.151 DAVIS346_CONFIG_BIAS_OFFBN . . . . .	50
4.1.2.152 DAVIS346_CONFIG_BIAS_ONBN . . . . .	50
4.1.2.153 DAVIS346_CONFIG_BIAS_PADFOLLBN . . . . .	51
4.1.2.154 DAVIS346_CONFIG_BIAS_PIXINVBN . . . . .	51
4.1.2.155 DAVIS346_CONFIG_BIAS_PRBP . . . . .	51
4.1.2.156 DAVIS346_CONFIG_BIAS_PRSFBP . . . . .	51
4.1.2.157 DAVIS346_CONFIG_BIAS_READOUTBUFBP . . . . .	52
4.1.2.158 DAVIS346_CONFIG_BIAS_REFRBP . . . . .	52
4.1.2.159 DAVIS346_CONFIG_BIAS_SSN . . . . .	52
4.1.2.160 DAVIS346_CONFIG_BIAS_SSP . . . . .	52
4.1.2.161 DAVIS346_CONFIG_CHIP_AERNAROW . . . . .	53
4.1.2.162 DAVIS346_CONFIG_CHIP_ANALOGMUX0 . . . . .	53
4.1.2.163 DAVIS346_CONFIG_CHIP_ANALOGMUX1 . . . . .	53
4.1.2.164 DAVIS346_CONFIG_CHIP_ANALOGMUX2 . . . . .	53
4.1.2.165 DAVIS346_CONFIG_CHIP_BIASMUX0 . . . . .	53
4.1.2.166 DAVIS346_CONFIG_CHIP_DIGITALMUX0 . . . . .	53
4.1.2.167 DAVIS346_CONFIG_CHIP_DIGITALMUX1 . . . . .	53
4.1.2.168 DAVIS346_CONFIG_CHIP_DIGITALMUX2 . . . . .	53
4.1.2.169 DAVIS346_CONFIG_CHIP_DIGITALMUX3 . . . . .	54
4.1.2.170 DAVIS346_CONFIG_CHIP_GLOBAL_SHUTTER . . . . .	54
4.1.2.171 DAVIS346_CONFIG_CHIP_RESETCALIBNEURON . . . . .	54
4.1.2.172 DAVIS346_CONFIG_CHIP_RESETTESTPIXEL . . . . .	54
4.1.2.173 DAVIS346_CONFIG_CHIP_SELECTGRAYCOUNTER . . . . .	54
4.1.2.174 DAVIS346_CONFIG_CHIP_TESTADC . . . . .	54
4.1.2.175 DAVIS346_CONFIG_CHIP_TYPENCALIBNEURON . . . . .	54
4.1.2.176 DAVIS346_CONFIG_CHIP_USEAOUT . . . . .	54

4.1.2.177 DAVIS640_CONFIG_BIAS_ADCCOMPBP . . . . .	55
4.1.2.178 DAVIS640_CONFIG_BIAS_ADCREFHIGH . . . . .	55
4.1.2.179 DAVIS640_CONFIG_BIAS_ADCREFLOW . . . . .	55
4.1.2.180 DAVIS640_CONFIG_BIAS_ADCTESTVOLTAGE . . . . .	55
4.1.2.181 DAVIS640_CONFIG_BIAS_AEPDBN . . . . .	56
4.1.2.182 DAVIS640_CONFIG_BIAS_AEPUXBP . . . . .	56
4.1.2.183 DAVIS640_CONFIG_BIAS_AEPUYBP . . . . .	56
4.1.2.184 DAVIS640_CONFIG_BIAS_APSCAS . . . . .	56
4.1.2.185 DAVIS640_CONFIG_BIAS_APSOEVERFLOWLEVEL . . . . .	57
4.1.2.186 DAVIS640_CONFIG_BIAS_APSROSFBN . . . . .	57
4.1.2.187 DAVIS640_CONFIG_BIAS_BIASBUFFER . . . . .	57
4.1.2.188 DAVIS640_CONFIG_BIAS_COLSELLOWBN . . . . .	57
4.1.2.189 DAVIS640_CONFIG_BIAS_DACBUFBP . . . . .	58
4.1.2.190 DAVIS640_CONFIG_BIAS_DIFFBN . . . . .	58
4.1.2.191 DAVIS640_CONFIG_BIAS_IFREFRBN . . . . .	58
4.1.2.192 DAVIS640_CONFIG_BIAS_IFTHRBN . . . . .	58
4.1.2.193 DAVIS640_CONFIG_BIAS_LCOLTIMEOUTBN . . . . .	59
4.1.2.194 DAVIS640_CONFIG_BIAS_LOCALBUFBN . . . . .	59
4.1.2.195 DAVIS640_CONFIG_BIAS_OFFBN . . . . .	59
4.1.2.196 DAVIS640_CONFIG_BIAS_ONBN . . . . .	59
4.1.2.197 DAVIS640_CONFIG_BIAS_PADFOLLBN . . . . .	60
4.1.2.198 DAVIS640_CONFIG_BIAS_PIXINBN . . . . .	60
4.1.2.199 DAVIS640_CONFIG_BIAS_PRBP . . . . .	60
4.1.2.200 DAVIS640_CONFIG_BIAS_PRSFBP . . . . .	60
4.1.2.201 DAVIS640_CONFIG_BIAS_READOUTBUFBP . . . . .	61
4.1.2.202 DAVIS640_CONFIG_BIAS_REFRBP . . . . .	61
4.1.2.203 DAVIS640_CONFIG_BIAS_SSN . . . . .	61
4.1.2.204 DAVIS640_CONFIG_BIAS_SSP . . . . .	61
4.1.2.205 DAVIS640_CONFIG_CHIP_AERNAROW . . . . .	62
4.1.2.206 DAVIS640_CONFIG_CHIP_ANALOGMUX0 . . . . .	62

4.1.2.207 DAVIS640_CONFIG_CHIP_ANALOGMUX1 . . . . .	62
4.1.2.208 DAVIS640_CONFIG_CHIP_ANALOGMUX2 . . . . .	62
4.1.2.209 DAVIS640_CONFIG_CHIP_BIASMUX0 . . . . .	62
4.1.2.210 DAVIS640_CONFIG_CHIP_DIGITALMUX0 . . . . .	62
4.1.2.211 DAVIS640_CONFIG_CHIP_DIGITALMUX1 . . . . .	62
4.1.2.212 DAVIS640_CONFIG_CHIP_DIGITALMUX2 . . . . .	62
4.1.2.213 DAVIS640_CONFIG_CHIP_DIGITALMUX3 . . . . .	63
4.1.2.214 DAVIS640_CONFIG_CHIP_GLOBAL_SHUTTER . . . . .	63
4.1.2.215 DAVIS640_CONFIG_CHIP_RESETCALIBNEURON . . . . .	63
4.1.2.216 DAVIS640_CONFIG_CHIP_RESETTESTPIXEL . . . . .	63
4.1.2.217 DAVIS640_CONFIG_CHIP_SELECTGRAYCOUNTER . . . . .	63
4.1.2.218 DAVIS640_CONFIG_CHIP_TESTADC . . . . .	63
4.1.2.219 DAVIS640_CONFIG_CHIP_TYPCALIBNEURON . . . . .	63
4.1.2.220 DAVIS640_CONFIG_CHIP_USEAOUT . . . . .	63
4.1.2.221 DAVIS_CHIP_DAVIS128 . . . . .	64
4.1.2.222 DAVIS_CHIP_DAVIS208 . . . . .	64
4.1.2.223 DAVIS_CHIP_DAVIS240A . . . . .	64
4.1.2.224 DAVIS_CHIP_DAVIS240B . . . . .	64
4.1.2.225 DAVIS_CHIP_DAVIS240C . . . . .	64
4.1.2.226 DAVIS_CHIP_DAVIS346A . . . . .	64
4.1.2.227 DAVIS_CHIP_DAVIS346B . . . . .	64
4.1.2.228 DAVIS_CHIP_DAVIS346C . . . . .	64
4.1.2.229 DAVIS_CHIP_DAVIS640 . . . . .	64
4.1.2.230 DAVIS_CHIP_DAVISRGB . . . . .	64
4.1.2.231 DAVIS_CONFIG_APS . . . . .	65
4.1.2.232 DAVIS_CONFIG_APS_ADC_TEST_MODE . . . . .	65
4.1.2.233 DAVIS_CONFIG_APS_COLOR_FILTER . . . . .	65
4.1.2.234 DAVIS_CONFIG_APS_COLUMN_SETTLE . . . . .	65
4.1.2.235 DAVIS_CONFIG_APS_END_COLUMN_0 . . . . .	65
4.1.2.236 DAVIS_CONFIG_APS_END_COLUMN_1 . . . . .	65

4.1.2.237 DAVIS_CONFIG_APS_END_COLUMN_2 . . . . .	65
4.1.2.238 DAVIS_CONFIG_APS_END_COLUMN_3 . . . . .	65
4.1.2.239 DAVIS_CONFIG_APS_END_ROW_0 . . . . .	65
4.1.2.240 DAVIS_CONFIG_APS_END_ROW_1 . . . . .	66
4.1.2.241 DAVIS_CONFIG_APS_END_ROW_2 . . . . .	66
4.1.2.242 DAVIS_CONFIG_APS_END_ROW_3 . . . . .	66
4.1.2.243 DAVIS_CONFIG_APS_EXPOSURE . . . . .	66
4.1.2.244 DAVIS_CONFIG_APS_FRAME_DELAY . . . . .	66
4.1.2.245 DAVIS_CONFIG_APS_GLOBAL_SHUTTER . . . . .	66
4.1.2.246 DAVIS_CONFIG_APS_HAS_EXTERNAL_ADC . . . . .	66
4.1.2.247 DAVIS_CONFIG_APS_HAS_GLOBAL_SHUTTER . . . . .	66
4.1.2.248 DAVIS_CONFIG_APS_HAS_INTERNAL_ADC . . . . .	67
4.1.2.249 DAVIS_CONFIG_APS_HAS_QUAD_ROI . . . . .	67
4.1.2.250 DAVIS_CONFIG_APS_NULL_SETTLE . . . . .	67
4.1.2.251 DAVIS_CONFIG_APS_ORIENTATION_INFO . . . . .	67
4.1.2.252 DAVIS_CONFIG_APS_RAMP_RESET . . . . .	67
4.1.2.253 DAVIS_CONFIG_APS_RAMP_SHORT_RESET . . . . .	67
4.1.2.254 DAVIS_CONFIG_APS_RESET_READ . . . . .	67
4.1.2.255 DAVIS_CONFIG_APS_RESET_SETTLE . . . . .	67
4.1.2.256 DAVIS_CONFIG_APS_ROW_SETTLE . . . . .	68
4.1.2.257 DAVIS_CONFIG_APS_RUN . . . . .	68
4.1.2.258 DAVIS_CONFIG_APS_SAMPLE_ENABLE . . . . .	68
4.1.2.259 DAVIS_CONFIG_APS_SAMPLE_SETTLE . . . . .	68
4.1.2.260 DAVIS_CONFIG_APS_SIZE_COLUMNS . . . . .	68
4.1.2.261 DAVIS_CONFIG_APS_SIZE_ROWS . . . . .	68
4.1.2.262 DAVIS_CONFIG_APS_SNAPSHOT . . . . .	68
4.1.2.263 DAVIS_CONFIG_APS_START_COLUMN_0 . . . . .	68
4.1.2.264 DAVIS_CONFIG_APS_START_COLUMN_1 . . . . .	69
4.1.2.265 DAVIS_CONFIG_APS_START_COLUMN_2 . . . . .	69
4.1.2.266 DAVIS_CONFIG_APS_START_COLUMN_3 . . . . .	69

4.1.2.267 DAVIS_CONFIG_APS_START_ROW_0 . . . . .	69
4.1.2.268 DAVIS_CONFIG_APS_START_ROW_1 . . . . .	69
4.1.2.269 DAVIS_CONFIG_APS_START_ROW_2 . . . . .	69
4.1.2.270 DAVIS_CONFIG_APS_START_ROW_3 . . . . .	69
4.1.2.271 DAVIS_CONFIG_APS_USE_INTERNAL_ADC . . . . .	69
4.1.2.272 DAVIS_CONFIG_APS_WAIT_ON_TRANSFER_STALL . . . . .	70
4.1.2.273 DAVIS_CONFIG_BIAS . . . . .	70
4.1.2.274 DAVIS_CONFIG_CHIP . . . . .	70
4.1.2.275 DAVIS_CONFIG_DVS . . . . .	70
4.1.2.276 DAVIS_CONFIG_DVS_ACK_DELAY_COLUMN . . . . .	70
4.1.2.277 DAVIS_CONFIG_DVS_ACK_DELAY_ROW . . . . .	70
4.1.2.278 DAVIS_CONFIG_DVS_ACK_EXTENSION_COLUMN . . . . .	70
4.1.2.279 DAVIS_CONFIG_DVS_ACK_EXTENSION_ROW . . . . .	70
4.1.2.280 DAVIS_CONFIG_DVS_EXTERNAL_AER_CONTROL . . . . .	71
4.1.2.281 DAVIS_CONFIG_DVS_FILTER_BACKGROUND_ACTIVITY . . . . .	71
4.1.2.282 DAVIS_CONFIG_DVS_FILTER_BACKGROUND_ACTIVITY_DELTAT . . . . .	71
4.1.2.283 DAVIS_CONFIG_DVS_FILTER_PIXEL_0_COLUMN . . . . .	71
4.1.2.284 DAVIS_CONFIG_DVS_FILTER_PIXEL_0_ROW . . . . .	71
4.1.2.285 DAVIS_CONFIG_DVS_FILTER_PIXEL_1_COLUMN . . . . .	71
4.1.2.286 DAVIS_CONFIG_DVS_FILTER_PIXEL_1_ROW . . . . .	71
4.1.2.287 DAVIS_CONFIG_DVS_FILTER_PIXEL_2_COLUMN . . . . .	71
4.1.2.288 DAVIS_CONFIG_DVS_FILTER_PIXEL_2_ROW . . . . .	72
4.1.2.289 DAVIS_CONFIG_DVS_FILTER_PIXEL_3_COLUMN . . . . .	72
4.1.2.290 DAVIS_CONFIG_DVS_FILTER_PIXEL_3_ROW . . . . .	72
4.1.2.291 DAVIS_CONFIG_DVS_FILTER_PIXEL_4_COLUMN . . . . .	72
4.1.2.292 DAVIS_CONFIG_DVS_FILTER_PIXEL_4_ROW . . . . .	72
4.1.2.293 DAVIS_CONFIG_DVS_FILTER_PIXEL_5_COLUMN . . . . .	72
4.1.2.294 DAVIS_CONFIG_DVS_FILTER_PIXEL_5_ROW . . . . .	72
4.1.2.295 DAVIS_CONFIG_DVS_FILTER_PIXEL_6_COLUMN . . . . .	72
4.1.2.296 DAVIS_CONFIG_DVS_FILTER_PIXEL_6_ROW . . . . .	72

4.1.2.297 DAVIS_CONFIG_DVS_FILTER_PIXEL_7_COLUMN . . . . .	73
4.1.2.298 DAVIS_CONFIG_DVS_FILTER_PIXEL_7_ROW . . . . .	73
4.1.2.299 DAVIS_CONFIG_DVS_FILTER_ROW_ONLY_EVENTS . . . . .	73
4.1.2.300 DAVIS_CONFIG_DVS_HAS_BACKGROUND_ACTIVITY_FILTER . . . . .	73
4.1.2.301 DAVIS_CONFIG_DVS_HAS_PIXEL_FILTER . . . . .	73
4.1.2.302 DAVIS_CONFIG_DVS_HAS_TEST_EVENT_GENERATOR . . . . .	73
4.1.2.303 DAVIS_CONFIG_DVS_ORIENTATION_INFO . . . . .	73
4.1.2.304 DAVIS_CONFIG_DVS_RUN . . . . .	73
4.1.2.305 DAVIS_CONFIG_DVS_SIZE_COLUMNS . . . . .	74
4.1.2.306 DAVIS_CONFIG_DVS_SIZE_ROWS . . . . .	74
4.1.2.307 DAVIS_CONFIG_DVS_TEST_EVENT_GENERATOR_ENABLE . . . . .	74
4.1.2.308 DAVIS_CONFIG_DVS_WAIT_ON_TRANSFER_STALL . . . . .	74
4.1.2.309 DAVIS_CONFIG_EXTINPUT . . . . .	74
4.1.2.310 DAVIS_CONFIG_EXTINPUT_DETECT_FALLING_EDGES . . . . .	74
4.1.2.311 DAVIS_CONFIG_EXTINPUT_DETECT_FALLING_EDGES1 . . . . .	74
4.1.2.312 DAVIS_CONFIG_EXTINPUT_DETECT_FALLING_EDGES2 . . . . .	75
4.1.2.313 DAVIS_CONFIG_EXTINPUT_DETECT_PULSE_LENGTH . . . . .	75
4.1.2.314 DAVIS_CONFIG_EXTINPUT_DETECT_PULSE_LENGTH1 . . . . .	75
4.1.2.315 DAVIS_CONFIG_EXTINPUT_DETECT_PULSE_LENGTH2 . . . . .	75
4.1.2.316 DAVIS_CONFIG_EXTINPUT_DETECT_PULSE_POLARITY . . . . .	75
4.1.2.317 DAVIS_CONFIG_EXTINPUT_DETECT_PULSE_POLARITY1 . . . . .	75
4.1.2.318 DAVIS_CONFIG_EXTINPUT_DETECT_PULSE_POLARITY2 . . . . .	75
4.1.2.319 DAVIS_CONFIG_EXTINPUT_DETECT_PULSES . . . . .	76
4.1.2.320 DAVIS_CONFIG_EXTINPUT_DETECT_PULSES1 . . . . .	76
4.1.2.321 DAVIS_CONFIG_EXTINPUT_DETECT_PULSES2 . . . . .	76
4.1.2.322 DAVIS_CONFIG_EXTINPUT_DETECT_RISING_EDGES . . . . .	76
4.1.2.323 DAVIS_CONFIG_EXTINPUT_DETECT_RISING_EDGES1 . . . . .	76
4.1.2.324 DAVIS_CONFIG_EXTINPUT_DETECT_RISING_EDGES2 . . . . .	76
4.1.2.325 DAVIS_CONFIG_EXTINPUT_GENERATE_INJECT_ON_FALLING_EDGE . . .	76
4.1.2.326 DAVIS_CONFIG_EXTINPUT_GENERATE_INJECT_ON_RISING_EDGE . . . .	76

4.1.2.327 DAVIS_CONFIG_EXTINPUT_GENERATE_PULSE_INTERVAL . . . . .	77
4.1.2.328 DAVIS_CONFIG_EXTINPUT_GENERATE_PULSE_LENGTH . . . . .	77
4.1.2.329 DAVIS_CONFIG_EXTINPUT_GENERATE_PULSE_POLARITY . . . . .	77
4.1.2.330 DAVIS_CONFIG_EXTINPUT_GENERATE_USE_CUSTOM_SIGNAL . . . . .	77
4.1.2.331 DAVIS_CONFIG_EXTINPUT_HAS_EXTRA_DETECTORS . . . . .	77
4.1.2.332 DAVIS_CONFIG_EXTINPUT_HAS_GENERATOR . . . . .	77
4.1.2.333 DAVIS_CONFIG_EXTINPUT_RUN_DETECTOR . . . . .	77
4.1.2.334 DAVIS_CONFIG_EXTINPUT_RUN_DETECTOR1 . . . . .	78
4.1.2.335 DAVIS_CONFIG_EXTINPUT_RUN_DETECTOR2 . . . . .	78
4.1.2.336 DAVIS_CONFIG_EXTINPUT_RUN_GENERATOR . . . . .	78
4.1.2.337 DAVIS_CONFIG_IMU . . . . .	78
4.1.2.338 DAVIS_CONFIG_IMU_ACCEL_FULL_SCALE . . . . .	78
4.1.2.339 DAVIS_CONFIG_IMU_ACCEL_STANDBY . . . . .	78
4.1.2.340 DAVIS_CONFIG_IMU_DIGITAL_LOW_PASS_FILTER . . . . .	78
4.1.2.341 DAVIS_CONFIG_IMU_GYRO_FULL_SCALE . . . . .	79
4.1.2.342 DAVIS_CONFIG_IMU_GYRO_STANDBY . . . . .	79
4.1.2.343 DAVIS_CONFIG_IMU_LP_CYCLE . . . . .	79
4.1.2.344 DAVIS_CONFIG_IMU_LP_WAKEUP . . . . .	79
4.1.2.345 DAVIS_CONFIG_IMU_RUN . . . . .	79
4.1.2.346 DAVIS_CONFIG_IMU_SAMPLE_RATE_DIVIDER . . . . .	79
4.1.2.347 DAVIS_CONFIG_IMU_TEMP_STANDBY . . . . .	79
4.1.2.348 DAVIS_CONFIG_MUX . . . . .	79
4.1.2.349 DAVIS_CONFIG_MUX_DROP_APS_ON_TRANSFER_STALL . . . . .	80
4.1.2.350 DAVIS_CONFIG_MUX_DROP_DVS_ON_TRANSFER_STALL . . . . .	80
4.1.2.351 DAVIS_CONFIG_MUX_DROP_EXTINPUT_ON_TRANSFER_STALL . . . . .	80
4.1.2.352 DAVIS_CONFIG_MUX_DROP_IMU_ON_TRANSFER_STALL . . . . .	80
4.1.2.353 DAVIS_CONFIG_MUX_FORCE_CHIP_BIAS_ENABLE . . . . .	80
4.1.2.354 DAVIS_CONFIG_MUX_RUN . . . . .	80
4.1.2.355 DAVIS_CONFIG_MUX_TIMESTAMP_RESET . . . . .	80
4.1.2.356 DAVIS_CONFIG_MUX_TIMESTAMP_RUN . . . . .	80

4.1.2.357 DAVIS_CONFIG_SYSINFO . . . . .	81
4.1.2.358 DAVIS_CONFIG_SYSINFO_ADC_CLOCK . . . . .	81
4.1.2.359 DAVIS_CONFIG_SYSINFO_CHIP_IDENTIFIER . . . . .	81
4.1.2.360 DAVIS_CONFIG_SYSINFO_DEVICE_IS_MASTER . . . . .	81
4.1.2.361 DAVIS_CONFIG_SYSINFO_LOGIC_CLOCK . . . . .	81
4.1.2.362 DAVIS_CONFIG_SYSINFO_LOGIC_VERSION . . . . .	81
4.1.2.363 DAVIS_CONFIG_USB . . . . .	81
4.1.2.364 DAVIS_CONFIG_USB_EARLY_PACKET_DELAY . . . . .	82
4.1.2.365 DAVIS_CONFIG_USB_RUN . . . . .	82
4.1.2.366 DAVISRGB_CONFIG_APS_GSFDRESET . . . . .	82
4.1.2.367 DAVISRGB_CONFIG_APS_GSPDRESET . . . . .	82
4.1.2.368 DAVISRGB_CONFIG_APS_GSRESETFALL . . . . .	82
4.1.2.369 DAVISRGB_CONFIG_APS_GSTXFALL . . . . .	82
4.1.2.370 DAVISRGB_CONFIG_APS_RSFDSETTLE . . . . .	82
4.1.2.371 DAVISRGB_CONFIG_APS_TRANSFER . . . . .	82
4.1.2.372 DAVISRGB_CONFIG_BIAS_ADCCOMPBP . . . . .	83
4.1.2.373 DAVISRGB_CONFIG_BIAS_ADCREFHIGH . . . . .	83
4.1.2.374 DAVISRGB_CONFIG_BIAS_ADCREFLOW . . . . .	83
4.1.2.375 DAVISRGB_CONFIG_BIAS_ADCTESTVOLTAGE . . . . .	83
4.1.2.376 DAVISRGB_CONFIG_BIAS_AEPDBN . . . . .	84
4.1.2.377 DAVISRGB_CONFIG_BIAS_AEPUXBP . . . . .	84
4.1.2.378 DAVISRGB_CONFIG_BIAS_AEPUYBP . . . . .	84
4.1.2.379 DAVISRGB_CONFIG_BIAS_APSCAS . . . . .	84
4.1.2.380 DAVISRGB_CONFIG_BIAS_APSROSFBN . . . . .	85
4.1.2.381 DAVISRGB_CONFIG_BIAS_ARRAYBIASBUFFERBN . . . . .	85
4.1.2.382 DAVISRGB_CONFIG_BIAS_ARRAYLOGICBUFFERBN . . . . .	85
4.1.2.383 DAVISRGB_CONFIG_BIAS_BIASBUFFER . . . . .	85
4.1.2.384 DAVISRGB_CONFIG_BIAS_DACBUFBP . . . . .	86
4.1.2.385 DAVISRGB_CONFIG_BIAS_DIFFBN . . . . .	86
4.1.2.386 DAVISRGB_CONFIG_BIAS_FALLTIMEBN . . . . .	86



4.1.2.387 DAVISRGB_CONFIG_BIAS_GND07 . . . . .	86
4.1.2.388 DAVISRGB_CONFIG_BIAS_IFREFRBN . . . . .	87
4.1.2.389 DAVISRGB_CONFIG_BIAS_IFTHRBN . . . . .	87
4.1.2.390 DAVISRGB_CONFIG_BIAS_LCOLTIMEOUTBN . . . . .	87
4.1.2.391 DAVISRGB_CONFIG_BIAS_LOCALBUFBN . . . . .	87
4.1.2.392 DAVISRGB_CONFIG_BIAS_OFFBN . . . . .	88
4.1.2.393 DAVISRGB_CONFIG_BIAS_ONBN . . . . .	88
4.1.2.394 DAVISRGB_CONFIG_BIAS_OVG1LO . . . . .	88
4.1.2.395 DAVISRGB_CONFIG_BIAS_OVG2LO . . . . .	88
4.1.2.396 DAVISRGB_CONFIG_BIAS_PADFOLLBN . . . . .	89
4.1.2.397 DAVISRGB_CONFIG_BIAS_PIXINVBN . . . . .	89
4.1.2.398 DAVISRGB_CONFIG_BIAS_PRBP . . . . .	89
4.1.2.399 DAVISRGB_CONFIG_BIAS_PRSFBP . . . . .	89
4.1.2.400 DAVISRGB_CONFIG_BIAS_READOUTBUFBP . . . . .	90
4.1.2.401 DAVISRGB_CONFIG_BIAS_REFRBP . . . . .	90
4.1.2.402 DAVISRGB_CONFIG_BIAS_RISETIMEBP . . . . .	90
4.1.2.403 DAVISRGB_CONFIG_BIAS_SSN . . . . .	90
4.1.2.404 DAVISRGB_CONFIG_BIAS_SSP . . . . .	91
4.1.2.405 DAVISRGB_CONFIG_BIAS_TX2OVG2HI . . . . .	91
4.1.2.406 DAVISRGB_CONFIG_CHIP_ADJUSTOVG1LO . . . . .	91
4.1.2.407 DAVISRGB_CONFIG_CHIP_ADJUSTOVG2LO . . . . .	91
4.1.2.408 DAVISRGB_CONFIG_CHIP_ADJUSTTX2OVG2HI . . . . .	91
4.1.2.409 DAVISRGB_CONFIG_CHIP_AERNAROW . . . . .	91
4.1.2.410 DAVISRGB_CONFIG_CHIP_ANALOGMUX0 . . . . .	92
4.1.2.411 DAVISRGB_CONFIG_CHIP_ANALOGMUX1 . . . . .	92
4.1.2.412 DAVISRGB_CONFIG_CHIP_ANALOGMUX2 . . . . .	92
4.1.2.413 DAVISRGB_CONFIG_CHIP_BIASMUX0 . . . . .	92
4.1.2.414 DAVISRGB_CONFIG_CHIP_DIGITALMUX0 . . . . .	92
4.1.2.415 DAVISRGB_CONFIG_CHIP_DIGITALMUX1 . . . . .	92
4.1.2.416 DAVISRGB_CONFIG_CHIP_DIGITALMUX2 . . . . .	92

4.1.2.417	DAVISRGB_CONFIG_CHIP_DIGITALMUX3 . . . . .	92
4.1.2.418	DAVISRGB_CONFIG_CHIP_RESETCALIBNEURON . . . . .	93
4.1.2.419	DAVISRGB_CONFIG_CHIP_RESETTESTPIXEL . . . . .	93
4.1.2.420	DAVISRGB_CONFIG_CHIP_SELECTGRAYCOUNTER . . . . .	93
4.1.2.421	DAVISRGB_CONFIG_CHIP_TESTADC . . . . .	93
4.1.2.422	DAVISRGB_CONFIG_CHIP_TYPENCALIBNEURON . . . . .	93
4.1.2.423	DAVISRGB_CONFIG_CHIP_USEAOUT . . . . .	93
4.1.2.424	IS_DAVIS128 . . . . .	93
4.1.2.425	IS_DAVIS208 . . . . .	93
4.1.2.426	IS_DAVIS240 . . . . .	94
4.1.2.427	IS_DAVIS240A . . . . .	94
4.1.2.428	IS_DAVIS240B . . . . .	94
4.1.2.429	IS_DAVIS240C . . . . .	94
4.1.2.430	IS_DAVIS346 . . . . .	94
4.1.2.431	IS_DAVIS346A . . . . .	94
4.1.2.432	IS_DAVIS346B . . . . .	94
4.1.2.433	IS_DAVIS346C . . . . .	94
4.1.2.434	IS_DAVIS640 . . . . .	94
4.1.2.435	IS_DAVISRGB . . . . .	95
4.1.3	Enumeration Type Documentation . . . . .	95
4.1.3.1	caer_bias_shiftedsourcesource_operating_mode . . . . .	95
4.1.3.2	caer_bias_shiftedsourcesource_voltage_level . . . . .	95
4.1.4	Function Documentation . . . . .	95
4.1.4.1	caerBiasCoarseFineGenerate(struct caer_bias_coarsefine coarseFineBias) . . .	95
4.1.4.2	caerBiasCoarseFineParse(uint16_t coarseFineBias) . . . . .	95
4.1.4.3	caerBiasShiftedSourceGenerate(struct caer_bias_shiftedsourcesource shiftedSource↵ Bias) . . . . .	96
4.1.4.4	caerBiasShiftedSourceParse(uint16_t shiftedSourceBias) . . . . .	96
4.1.4.5	caerBiasVDACGenerate(struct caer_bias_vdac vdacBias) . . . . .	96
4.1.4.6	caerBiasVDACParse(uint16_t vdacBias) . . . . .	97
4.1.4.7	caerDavisInfoGet(caerDeviceHandle handle) . . . . .	97

4.2	devices/dvs128.h File Reference	97
4.2.1	Detailed Description	98
4.2.2	Macro Definition Documentation	98
4.2.2.1	CAER_DEVICE_DVS128	98
4.2.2.2	DVS128_CONFIG_BIAS	98
4.2.2.3	DVS128_CONFIG_BIAS_CAS	98
4.2.2.4	DVS128_CONFIG_BIAS_DIFF	99
4.2.2.5	DVS128_CONFIG_BIAS_DIFFOFF	99
4.2.2.6	DVS128_CONFIG_BIAS_DIFFON	99
4.2.2.7	DVS128_CONFIG_BIAS_FOLL	99
4.2.2.8	DVS128_CONFIG_BIAS_INJGND	99
4.2.2.9	DVS128_CONFIG_BIAS_PR	99
4.2.2.10	DVS128_CONFIG_BIAS_PUX	99
4.2.2.11	DVS128_CONFIG_BIAS_PUY	99
4.2.2.12	DVS128_CONFIG_BIAS_REFR	99
4.2.2.13	DVS128_CONFIG_BIAS_REQ	100
4.2.2.14	DVS128_CONFIG_BIAS_REQPD	100
4.2.2.15	DVS128_CONFIG_DVS	100
4.2.2.16	DVS128_CONFIG_DVS_ARRAY_RESET	100
4.2.2.17	DVS128_CONFIG_DVS_RUN	100
4.2.2.18	DVS128_CONFIG_DVS_TIMESTAMP_RESET	100
4.2.2.19	DVS128_CONFIG_DVS_TS_MASTER	100
4.2.3	Function Documentation	100
4.2.3.1	caerDVS128InfoGet(caerDeviceHandle handle)	100
4.3	devices/dynapse.h File Reference	101
4.3.1	Detailed Description	102
4.3.2	Macro Definition Documentation	102
4.3.2.1	CAER_DEVICE_DYNAPSE	102
4.3.2.2	DYNAPSE_CHIP_DYNAPSE	102
4.3.2.3	DYNAPSE_CONFIG_AER	102

4.3.2.4	DYNAPSE_CONFIG_AER_ACK_DELAY . . . . .	102
4.3.2.5	DYNAPSE_CONFIG_AER_ACK_EXTENSION . . . . .	102
4.3.2.6	DYNAPSE_CONFIG_AER_EXTERNAL_AER_CONTROL . . . . .	102
4.3.2.7	DYNAPSE_CONFIG_AER_RUN . . . . .	102
4.3.2.8	DYNAPSE_CONFIG_AER_WAIT_ON_TRANSFER_STALL . . . . .	103
4.3.2.9	DYNAPSE_CONFIG_CHIP . . . . .	103
4.3.2.10	DYNAPSE_CONFIG_CHIP_CONTENT . . . . .	103
4.3.2.11	DYNAPSE_CONFIG_CHIP_ID . . . . .	103
4.3.2.12	DYNAPSE_CONFIG_CHIP_REQ_DELAY . . . . .	103
4.3.2.13	DYNAPSE_CONFIG_CHIP_REQ_EXTENSION . . . . .	103
4.3.2.14	DYNAPSE_CONFIG_CHIP_RUN . . . . .	103
4.3.2.15	DYNAPSE_CONFIG_MUX . . . . .	103
4.3.2.16	DYNAPSE_CONFIG_MUX_DROP_AER_ON_TRANSFER_STALL . . . . .	104
4.3.2.17	DYNAPSE_CONFIG_MUX_FORCE_CHIP_BIAS_ENABLE . . . . .	104
4.3.2.18	DYNAPSE_CONFIG_MUX_RUN . . . . .	104
4.3.2.19	DYNAPSE_CONFIG_MUX_TIMESTAMP_RESET . . . . .	104
4.3.2.20	DYNAPSE_CONFIG_MUX_TIMESTAMP_RUN . . . . .	104
4.3.2.21	DYNAPSE_CONFIG_SYSINFO . . . . .	104
4.3.2.22	DYNAPSE_CONFIG_SYSINFO_CHIP_IDENTIFIER . . . . .	104
4.3.2.23	DYNAPSE_CONFIG_SYSINFO_DEVICE_IS_MASTER . . . . .	104
4.3.2.24	DYNAPSE_CONFIG_SYSINFO_LOGIC_CLOCK . . . . .	105
4.3.2.25	DYNAPSE_CONFIG_SYSINFO_LOGIC_VERSION . . . . .	105
4.3.2.26	DYNAPSE_CONFIG_USB . . . . .	105
4.3.2.27	DYNAPSE_CONFIG_USB_EARLY_PACKET_DELAY . . . . .	105
4.3.2.28	DYNAPSE_CONFIG_USB_RUN . . . . .	105
4.3.3	Function Documentation . . . . .	105
4.3.3.1	caerDynapseInfoGet(caerDeviceHandle handle) . . . . .	105
4.4	devices/usb.h File Reference . . . . .	106
4.4.1	Detailed Description . . . . .	106
4.4.2	Macro Definition Documentation . . . . .	106

4.4.2.1	CAER_HOST_CONFIG_DATAEXCHANGE . . . . .	106
4.4.2.2	CAER_HOST_CONFIG_DATAEXCHANGE_BLOCKING . . . . .	107
4.4.2.3	CAER_HOST_CONFIG_DATAEXCHANGE_BUFFER_SIZE . . . . .	107
4.4.2.4	CAER_HOST_CONFIG_DATAEXCHANGE_START_PRODUCERS . . . . .	107
4.4.2.5	CAER_HOST_CONFIG_DATAEXCHANGE_STOP_PRODUCERS . . . . .	107
4.4.2.6	CAER_HOST_CONFIG_PACKETS . . . . .	107
4.4.2.7	CAER_HOST_CONFIG_PACKETS_MAX_CONTAINER_INTERVAL . . . . .	107
4.4.2.8	CAER_HOST_CONFIG_PACKETS_MAX_CONTAINER_PACKET_SIZE . . . . .	107
4.4.2.9	CAER_HOST_CONFIG_USB . . . . .	108
4.4.2.10	CAER_HOST_CONFIG_USB_BUFFER_NUMBER . . . . .	108
4.4.2.11	CAER_HOST_CONFIG_USB_BUFFER_SIZE . . . . .	108
4.4.3	Typedef Documentation . . . . .	108
4.4.3.1	caerDeviceHandle . . . . .	108
4.4.4	Function Documentation . . . . .	108
4.4.4.1	caerDeviceClose(caerDeviceHandle *handle) . . . . .	108
4.4.4.2	caerDeviceConfigGet(caerDeviceHandle handle, int8_t modAddr, uint8_t paramAddr, uint32_t *param) . . . . .	108
4.4.4.3	caerDeviceConfigSet(caerDeviceHandle handle, int8_t modAddr, uint8_t paramAddr, uint32_t param) . . . . .	109
4.4.4.4	caerDeviceDataGet(caerDeviceHandle handle) . . . . .	109
4.4.4.5	caerDeviceDataStart(caerDeviceHandle handle, void(*dataNotifyIncrease)(void *ptr), void(*dataNotifyDecrease)(void *ptr), void *dataNotifyUserPtr, void(*dataShutdownNotify)(void *ptr), void *dataShutdownUserPtr) . . . . .	110
4.4.4.6	caerDeviceDataStop(caerDeviceHandle handle) . . . . .	110
4.4.4.7	caerDeviceOpen(uint16_t deviceId, uint16_t deviceType, uint8_t busNumberRestrict, uint8_t devAddressRestrict, const char *serialNumberRestrict) . . . . .	110
4.4.4.8	caerDeviceSendDefaultConfig(caerDeviceHandle handle) . . . . .	111
4.5	events/common.h File Reference . . . . .	111
4.5.1	Detailed Description . . . . .	113
4.5.2	Macro Definition Documentation . . . . .	113
4.5.2.1	CAER_EVENT_PACKET_HEADER_SIZE . . . . .	113
4.5.2.2	CAER_ITERATOR_ALL_END . . . . .	113
4.5.2.3	CAER_ITERATOR_ALL_START . . . . .	113

4.5.2.4	<a href="#">CAER_ITERATOR_VALID_END</a>	113
4.5.2.5	<a href="#">CAER_ITERATOR_VALID_START</a>	113
4.5.2.6	<a href="#">TS_OVERFLOW_SHIFT</a>	114
4.5.2.7	<a href="#">VALID_MARK_MASK</a>	114
4.5.2.8	<a href="#">VALID_MARK_SHIFT</a>	114
4.5.3	<a href="#">Typedef Documentation</a>	114
4.5.3.1	<a href="#">caerEventPacketHeader</a>	114
4.5.4	<a href="#">Enumeration Type Documentation</a>	114
4.5.4.1	<a href="#">caer_default_event_types</a>	114
4.5.5	<a href="#">Function Documentation</a>	115
4.5.5.1	<a href="#">caerCleanEventPacket(void *eventPacket)</a>	115
4.5.5.2	<a href="#">caerCopyEventPacket(void *eventPacket)</a>	115
4.5.5.3	<a href="#">caerCopyEventPacketOnlyEvents(void *eventPacket)</a>	115
4.5.5.4	<a href="#">caerCopyEventPacketOnlyValidEvents(void *eventPacket)</a>	115
4.5.5.5	<a href="#">caerEventPacketHeaderGetEventCapacity(caerEventPacketHeader header)</a>	116
4.5.5.6	<a href="#">caerEventPacketHeaderGetEventNumber(caerEventPacketHeader header)</a>	116
4.5.5.7	<a href="#">caerEventPacketHeaderGetEventSize(caerEventPacketHeader header)</a>	116
4.5.5.8	<a href="#">caerEventPacketHeaderGetEventSource(caerEventPacketHeader header)</a>	116
4.5.5.9	<a href="#">caerEventPacketHeaderGetEventTSOffset(caerEventPacketHeader header)</a>	117
4.5.5.10	<a href="#">caerEventPacketHeaderGetEventTSOverflow(caerEventPacketHeader header)</a>	117
4.5.5.11	<a href="#">caerEventPacketHeaderGetEventType(caerEventPacketHeader header)</a>	117
4.5.5.12	<a href="#">caerEventPacketHeaderGetEventValid(caerEventPacketHeader header)</a>	118
4.5.5.13	<a href="#">caerEventPacketHeaderSetEventCapacity(caerEventPacketHeader header, int32_t eventsCapacity)</a>	118
4.5.5.14	<a href="#">caerEventPacketHeaderSetEventNumber(caerEventPacketHeader header, int32_t eventsNumber)</a>	118
4.5.5.15	<a href="#">caerEventPacketHeaderSetEventSize(caerEventPacketHeader header, int32_t eventSize)</a>	119
4.5.5.16	<a href="#">caerEventPacketHeaderSetEventSource(caerEventPacketHeader header, int16_t eventSource)</a>	119
4.5.5.17	<a href="#">caerEventPacketHeaderSetEventTSOffset(caerEventPacketHeader header, int32_t eventTSOffset)</a>	119

4.5.5.18	<code>caerEventPacketHeaderSetEventTSOverflow(caerEventPacketHeader header, int32_t eventTSOverflow)</code> . . . . .	119
4.5.5.19	<code>caerEventPacketHeaderSetEventType(caerEventPacketHeader header, int16_t eventType)</code> . . . . .	120
4.5.5.20	<code>caerEventPacketHeaderSetEventValid(caerEventPacketHeader header, int32_t eventsValid)</code> . . . . .	120
4.5.5.21	<code>caerGenericEventGetEvent(caerEventPacketHeader headerPtr, int32_t n)</code> . . . . .	120
4.5.5.22	<code>caerGenericEventGetTimestamp(void *eventPtr, caerEventPacketHeader headerPtr)</code> . . . . .	120
4.5.5.23	<code>caerGenericEventGetTimestamp64(void *eventPtr, caerEventPacketHeader headerPtr)</code> . . . . .	121
4.5.5.24	<code>caerGenericEventIsValid(void *eventPtr)</code> . . . . .	121
4.5.5.25	<code>caerGenericEventPacketAppend(caerEventPacketHeader packet, caerEventPacketHeader appendPacket)</code> . . . . .	121
4.5.5.26	<code>caerGenericEventPacketGrow(caerEventPacketHeader packet, int32_t newEventCapacity)</code> . . . . .	122
4.5.5.27	<code>PACKED_STRUCT(struct caer_event_packet_header{int16_t eventType;int16_t eventSource;int32_t eventSize;int32_t eventTSOffset;int32_t eventTSOverflow;int32_t eventCapacity;int32_t eventNumber;int32_t eventValid;})</code> . . . . .	122
4.6	<code>events/config.h</code> File Reference . . . . .	122
4.6.1	Detailed Description . . . . .	123
4.6.2	Macro Definition Documentation . . . . .	123
4.6.2.1	<code>CAER_CONFIGURATION_ITERATOR_ALL_END</code> . . . . .	123
4.6.2.2	<code>CAER_CONFIGURATION_ITERATOR_ALL_START</code> . . . . .	123
4.6.2.3	<code>CAER_CONFIGURATION_ITERATOR_VALID_END</code> . . . . .	124
4.6.2.4	<code>CAER_CONFIGURATION_ITERATOR_VALID_START</code> . . . . .	124
4.6.2.5	<code>MODULE_ADDR_MASK</code> . . . . .	124
4.6.2.6	<code>MODULE_ADDR_SHIFT</code> . . . . .	124
4.6.3	Typedef Documentation . . . . .	124
4.6.3.1	<code>caerConfigurationEvent</code> . . . . .	124
4.6.3.2	<code>caerConfigurationEventPacket</code> . . . . .	124
4.6.4	Function Documentation . . . . .	124
4.6.4.1	<code>caerConfigurationEventGetModuleAddress(caerConfigurationEvent event)</code> . . . . .	124
4.6.4.2	<code>caerConfigurationEventGetParameter(caerConfigurationEvent event)</code> . . . . .	125

4.6.4.3	<code>caerConfigurationEventGetParameterAddress(caerConfigurationEvent event)</code> . .	125
4.6.4.4	<code>caerConfigurationEventGetTimestamp(caerConfigurationEvent event)</code> . . . . .	125
4.6.4.5	<code>caerConfigurationEventGetTimestamp64(caerConfigurationEvent event, caer↔ ConfigurationEventPacket packet)</code> . . . . .	126
4.6.4.6	<code>caerConfigurationEventInvalidate(caerConfigurationEvent event, caerConfiguration↔ EventPacket packet)</code> . . . . .	126
4.6.4.7	<code>caerConfigurationEventsIsValid(caerConfigurationEvent event)</code> . . . . .	126
4.6.4.8	<code>caerConfigurationEventPacketAllocate(int32_t eventCapacity, int16_t event↔ Source, int32_t tsOverflow)</code> . . . . .	126
4.6.4.9	<code>caerConfigurationEventPacketGetEvent(caerConfigurationEventPacket packet, int32_t n)</code> . . . . .	127
4.6.4.10	<code>caerConfigurationEventSetModuleAddress(caerConfigurationEvent event, uint8_t moduleAddress)</code> . . . . .	127
4.6.4.11	<code>caerConfigurationEventSetParameter(caerConfigurationEvent event, uint32_t pa- rameter)</code> . . . . .	127
4.6.4.12	<code>caerConfigurationEventSetParameterAddress(caerConfigurationEvent event, uint8_t parameterAddress)</code> . . . . .	128
4.6.4.13	<code>caerConfigurationEventSetTimestamp(caerConfigurationEvent event, int32_t timestamp)</code> . . . . .	128
4.6.4.14	<code>caerConfigurationEventValidate(caerConfigurationEvent event, caerConfiguration↔ EventPacket packet)</code> . . . . .	128
4.6.4.15	<code>PACKED_STRUCT(struct caer_configuration_event{uint8_t moduleAddress;uint8↔ _t parameterAddress;uint32_t parameter;int32_t timestamp;})</code> . . . . .	128
4.6.4.16	<code>PACKED_STRUCT(struct caer_configuration_event_packet{struct caer_event↔ _packet_header packetHeader;struct caer_configuration_event events[];})</code> . . . .	128
4.7	events/ear.h File Reference . . . . .	129
4.7.1	Detailed Description . . . . .	130
4.7.2	Macro Definition Documentation . . . . .	130
4.7.2.1	<code>CAER_EAR_ITERATOR_ALL_END</code> . . . . .	130
4.7.2.2	<code>CAER_EAR_ITERATOR_ALL_START</code> . . . . .	130
4.7.2.3	<code>CAER_EAR_ITERATOR_VALID_END</code> . . . . .	130
4.7.2.4	<code>CAER_EAR_ITERATOR_VALID_START</code> . . . . .	130
4.7.2.5	<code>CHANNEL_MASK</code> . . . . .	131
4.7.2.6	<code>CHANNEL_SHIFT</code> . . . . .	131
4.7.2.7	<code>EAR_MASK</code> . . . . .	131



4.7.2.8	EAR_SHIFT	131
4.7.2.9	FILTER_MASK	131
4.7.2.10	FILTER_SHIFT	131
4.7.2.11	NEURON_MASK	131
4.7.2.12	NEURON_SHIFT	131
4.7.3	Typedef Documentation	132
4.7.3.1	caerEarEvent	132
4.7.3.2	caerEarEventPacket	132
4.7.4	Function Documentation	132
4.7.4.1	caerEarEventGetChannel(caerEarEvent event)	132
4.7.4.2	caerEarEventGetEar(caerEarEvent event)	132
4.7.4.3	caerEarEventGetTimestamp(caerEarEvent event)	132
4.7.4.4	caerEarEventGetTimestamp64(caerEarEvent event, caerEarEventPacket packet)	133
4.7.4.5	caerEarEventInvalidate(caerEarEvent event, caerEarEventPacket packet)	133
4.7.4.6	caerEarEventIsValid(caerEarEvent event)	133
4.7.4.7	caerEarEventPacketAllocate(int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow)	134
4.7.4.8	caerEarEventPacketGetEvent(caerEarEventPacket packet, int32_t n)	134
4.7.4.9	caerEarEventSetChannel(caerEarEvent event, uint16_t channel)	134
4.7.4.10	caerEarEventSetEar(caerEarEvent event, uint8_t ear)	134
4.7.4.11	caerEarEventSetTimestamp(caerEarEvent event, int32_t timestamp)	135
4.7.4.12	caerEarEventValidate(caerEarEvent event, caerEarEventPacket packet)	135
4.7.4.13	PACKED_STRUCT(struct caer_ear_event{uint32_t data;int32_t timestamp;})	135
4.7.4.14	PACKED_STRUCT(struct caer_ear_event_packet{struct caer_event_packet header packetHeader;struct caer_ear_event events[];})	135
4.8	events/frame.h File Reference	135
4.8.1	Detailed Description	137
4.8.2	Macro Definition Documentation	137
4.8.2.1	CAER_FRAME_ITERATOR_ALL_END	137
4.8.2.2	CAER_FRAME_ITERATOR_ALL_START	138
4.8.2.3	CAER_FRAME_ITERATOR_VALID_END	138

4.8.2.4	CAER_FRAME_ITERATOR_VALID_START . . . . .	138
4.8.2.5	CAER_FRAME_REVERSE_ITERATOR_ALL_END . . . . .	138
4.8.2.6	CAER_FRAME_REVERSE_ITERATOR_ALL_START . . . . .	138
4.8.2.7	CAER_FRAME_REVERSE_ITERATOR_VALID_END . . . . .	139
4.8.2.8	CAER_FRAME_REVERSE_ITERATOR_VALID_START . . . . .	139
4.8.2.9	COLOR_CHANNELS_MASK . . . . .	139
4.8.2.10	COLOR_CHANNELS_SHIFT . . . . .	139
4.8.2.11	COLOR_FILTER_MASK . . . . .	139
4.8.2.12	COLOR_FILTER_SHIFT . . . . .	139
4.8.2.13	ROI_IDENTIFIER_MASK . . . . .	140
4.8.2.14	ROI_IDENTIFIER_SHIFT . . . . .	140
4.8.3	Typedef Documentation . . . . .	140
4.8.3.1	caerFrameEvent . . . . .	140
4.8.3.2	caerFrameEventPacket . . . . .	140
4.8.4	Enumeration Type Documentation . . . . .	140
4.8.4.1	caer_frame_event_color_channels . . . . .	140
4.8.4.2	caer_frame_event_color_filter . . . . .	140
4.8.5	Function Documentation . . . . .	141
4.8.5.1	caerFrameEventGetChannelNumber(caerFrameEvent event) . . . . .	141
4.8.5.2	caerFrameEventGetColorFilter(caerFrameEvent event) . . . . .	141
4.8.5.3	caerFrameEventGetExposureLength(caerFrameEvent event) . . . . .	141
4.8.5.4	caerFrameEventGetLengthX(caerFrameEvent event) . . . . .	141
4.8.5.5	caerFrameEventGetLengthY(caerFrameEvent event) . . . . .	142
4.8.5.6	caerFrameEventGetPixel(caerFrameEvent event, int32_t xAddress, int32_t y← Address) . . . . .	142
4.8.5.7	caerFrameEventGetPixelArrayUnsafe(caerFrameEvent event) . . . . .	142
4.8.5.8	caerFrameEventGetPixelForChannel(caerFrameEvent event, int32_t xAddress, int32_t yAddress, uint8_t channel) . . . . .	143
4.8.5.9	caerFrameEventGetPixelForChannelUnsafe(caerFrameEvent event, int32_t x← Address, int32_t yAddress, uint8_t channel) . . . . .	143
4.8.5.10	caerFrameEventGetPixelsMaxIndex(caerFrameEvent event) . . . . .	143
4.8.5.11	caerFrameEventGetPixelsSize(caerFrameEvent event) . . . . .	144

4.8.5.12	<code>caerFrameEventGetPixelUnsafe(caerFrameEvent event, int32_t xAddress, int32_t yAddress)</code>	144
4.8.5.13	<code>caerFrameEventGetPositionX(caerFrameEvent event)</code>	144
4.8.5.14	<code>caerFrameEventGetPositionY(caerFrameEvent event)</code>	145
4.8.5.15	<code>caerFrameEventGetROIIdentifier(caerFrameEvent event)</code>	145
4.8.5.16	<code>caerFrameEventGetTimestamp(caerFrameEvent event)</code>	145
4.8.5.17	<code>caerFrameEventGetTimestamp64(caerFrameEvent event, caerFrameEvent↔Packet packet)</code>	146
4.8.5.18	<code>caerFrameEventGetTSEndOfExposure(caerFrameEvent event)</code>	146
4.8.5.19	<code>caerFrameEventGetTSEndOfExposure64(caerFrameEvent event, caerFrame↔EventPacket packet)</code>	146
4.8.5.20	<code>caerFrameEventGetTSEndOfFrame(caerFrameEvent event)</code>	147
4.8.5.21	<code>caerFrameEventGetTSEndOfFrame64(caerFrameEvent event, caerFrame↔EventPacket packet)</code>	147
4.8.5.22	<code>caerFrameEventGetTSStartOfExposure(caerFrameEvent event)</code>	147
4.8.5.23	<code>caerFrameEventGetTSStartOfExposure64(caerFrameEvent event, caerFrame↔EventPacket packet)</code>	148
4.8.5.24	<code>caerFrameEventGetTSStartOfFrame(caerFrameEvent event)</code>	148
4.8.5.25	<code>caerFrameEventGetTSStartOfFrame64(caerFrameEvent event, caerFrame↔EventPacket packet)</code>	148
4.8.5.26	<code>caerFrameEventInvalidate(caerFrameEvent event, caerFrameEventPacket packet)</code>	149
4.8.5.27	<code>caerFrameEventIsValid(caerFrameEvent event)</code>	149
4.8.5.28	<code>caerFrameEventPacketAllocate(int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow, int32_t maxLengthX, int32_t maxLengthY, int16_t max↔ChannelNumber)</code>	149
4.8.5.29	<code>caerFrameEventPacketGetEvent(caerFrameEventPacket packet, int32_t n)</code>	150
4.8.5.30	<code>caerFrameEventPacketGetPixelsMaxIndex(caerFrameEventPacket packet)</code>	150
4.8.5.31	<code>caerFrameEventPacketGetPixelsSize(caerFrameEventPacket packet)</code>	150
4.8.5.32	<code>caerFrameEventSetColorFilter(caerFrameEvent event, enum caer_frame_↔event_color_filter colorFilter)</code>	150
4.8.5.33	<code>caerFrameEventSetLengthXLengthYChannelNumber(caerFrameEvent event, int32_t lengthX, int32_t lengthY, enum caer_frame_event_color_channels channelNumber, caerFrameEventPacket packet)</code>	151
4.8.5.34	<code>caerFrameEventSetPixel(caerFrameEvent event, int32_t xAddress, int32_t y↔Address, uint16_t pixelValue)</code>	151

4.8.5.35	<code>caerFrameEventSetPixelForChannel(caerFrameEvent event, int32_t xAddress, int32_t yAddress, uint8_t channel, uint16_t pixelValue)</code> . . . . .	151
4.8.5.36	<code>caerFrameEventSetPixelForChannelUnsafe(caerFrameEvent event, int32_t x↵Address, int32_t yAddress, uint8_t channel, uint16_t pixelValue)</code> . . . . .	152
4.8.5.37	<code>caerFrameEventSetPixelUnsafe(caerFrameEvent event, int32_t xAddress, int32_t yAddress, uint16_t pixelValue)</code> . . . . .	152
4.8.5.38	<code>caerFrameEventSetPositionX(caerFrameEvent event, int32_t positionX)</code> . . . . .	152
4.8.5.39	<code>caerFrameEventSetPositionY(caerFrameEvent event, int32_t positionY)</code> . . . . .	152
4.8.5.40	<code>caerFrameEventSetROIIdentifier(caerFrameEvent event, uint8_t roiIdentifier)</code> . . . . .	153
4.8.5.41	<code>caerFrameEventSetTSEndOfExposure(caerFrameEvent event, int32_t end↵Exposure)</code> . . . . .	153
4.8.5.42	<code>caerFrameEventSetTSEndOfFrame(caerFrameEvent event, int32_t endFrame)</code> . . . . .	153
4.8.5.43	<code>caerFrameEventSetTSSStartOfExposure(caerFrameEvent event, int32_t start↵Exposure)</code> . . . . .	153
4.8.5.44	<code>caerFrameEventSetTSSStartOfFrame(caerFrameEvent event, int32_t startFrame)</code> . . . . .	153
4.8.5.45	<code>caerFrameEventValidate(caerFrameEvent event, caerFrameEventPacket packet)</code> . . . . .	154
4.8.5.46	<code>PACKED_STRUCT(struct caer_frame_event{uint32_t info;int32_t ts_startframe;int32_t↵ts_endframe;int32_t ts_startexposure;int32_t ts_endexposure;int32_t length↵X;int32_t lengthY;int32_t positionX;int32_t positionY;uint16_t pixels[];})</code> . . . . .	154
4.8.5.47	<code>PACKED_STRUCT(struct caer_frame_event_packet{struct caer_event_packet↵_header packetHeader;})</code> . . . . .	154
4.9	<code>events/imu6.h</code> File Reference . . . . .	154
4.9.1	Detailed Description . . . . .	155
4.9.2	Macro Definition Documentation . . . . .	155
4.9.2.1	<code>CAER_IMU6_ITERATOR_ALL_END</code> . . . . .	155
4.9.2.2	<code>CAER_IMU6_ITERATOR_ALL_START</code> . . . . .	156
4.9.2.3	<code>CAER_IMU6_ITERATOR_VALID_END</code> . . . . .	156
4.9.2.4	<code>CAER_IMU6_ITERATOR_VALID_START</code> . . . . .	156
4.9.3	Typedef Documentation . . . . .	156
4.9.3.1	<code>caerIMU6Event</code> . . . . .	156
4.9.3.2	<code>caerIMU6EventPacket</code> . . . . .	156
4.9.4	Function Documentation . . . . .	156
4.9.4.1	<code>caerIMU6EventGetAccelX(caerIMU6Event event)</code> . . . . .	156
4.9.4.2	<code>caerIMU6EventGetAccelY(caerIMU6Event event)</code> . . . . .	157

4.9.4.3	<code>caerIMU6EventGetAccelZ(caerIMU6Event event)</code>	157
4.9.4.4	<code>caerIMU6EventGetGyroX(caerIMU6Event event)</code>	157
4.9.4.5	<code>caerIMU6EventGetGyroY(caerIMU6Event event)</code>	158
4.9.4.6	<code>caerIMU6EventGetGyroZ(caerIMU6Event event)</code>	158
4.9.4.7	<code>caerIMU6EventGetTemp(caerIMU6Event event)</code>	158
4.9.4.8	<code>caerIMU6EventGetTimestamp(caerIMU6Event event)</code>	158
4.9.4.9	<code>caerIMU6EventGetTimestamp64(caerIMU6Event event, caerIMU6EventPacket packet)</code>	159
4.9.4.10	<code>caerIMU6EventInvalidate(caerIMU6Event event, caerIMU6EventPacket packet)</code>	159
4.9.4.11	<code>caerIMU6EventIsValid(caerIMU6Event event)</code>	159
4.9.4.12	<code>caerIMU6EventPacketAllocate(int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow)</code>	159
4.9.4.13	<code>caerIMU6EventPacketGetEvent(caerIMU6EventPacket packet, int32_t n)</code>	160
4.9.4.14	<code>caerIMU6EventSetAccelX(caerIMU6Event event, float accelX)</code>	160
4.9.4.15	<code>caerIMU6EventSetAccelY(caerIMU6Event event, float accelY)</code>	160
4.9.4.16	<code>caerIMU6EventSetAccelZ(caerIMU6Event event, float accelZ)</code>	161
4.9.4.17	<code>caerIMU6EventSetGyroX(caerIMU6Event event, float gyroX)</code>	161
4.9.4.18	<code>caerIMU6EventSetGyroY(caerIMU6Event event, float gyroY)</code>	161
4.9.4.19	<code>caerIMU6EventSetGyroZ(caerIMU6Event event, float gyroZ)</code>	161
4.9.4.20	<code>caerIMU6EventSetTemp(caerIMU6Event event, float temp)</code>	161
4.9.4.21	<code>caerIMU6EventSetTimestamp(caerIMU6Event event, int32_t timestamp)</code>	162
4.9.4.22	<code>caerIMU6EventValidate(caerIMU6Event event, caerIMU6EventPacket packet)</code>	162
4.9.4.23	<code>PACKED_STRUCT(struct caer_imu6_event{uint32_t info;int32_t timestamp;float accel_x;float accel_y;float accel_z;float gyro_x;float gyro_y;float gyro_z;float temp;})</code>	162
4.9.4.24	<code>PACKED_STRUCT(struct caer_imu6_event_packet{struct caer_event_packet←_header packetHeader;struct caer_imu6_event events[];})</code>	162
4.10	events/imu9.h File Reference	162
4.10.1	Detailed Description	163
4.10.2	Macro Definition Documentation	163
4.10.2.1	CAER_IMU9_ITERATOR_ALL_END	163
4.10.2.2	CAER_IMU9_ITERATOR_ALL_START	164
4.10.2.3	CAER_IMU9_ITERATOR_VALID_END	164

4.10.2.4	CAER_IMU9_ITERATOR_VALID_START	164
4.10.3	Typedef Documentation	164
4.10.3.1	caerIMU9Event	164
4.10.3.2	caerIMU9EventPacket	164
4.10.4	Function Documentation	164
4.10.4.1	caerIMU9EventGetAccelX(caerIMU9Event event)	164
4.10.4.2	caerIMU9EventGetAccelY(caerIMU9Event event)	165
4.10.4.3	caerIMU9EventGetAccelZ(caerIMU9Event event)	165
4.10.4.4	caerIMU9EventGetCompX(caerIMU9Event event)	165
4.10.4.5	caerIMU9EventGetCompY(caerIMU9Event event)	166
4.10.4.6	caerIMU9EventGetCompZ(caerIMU9Event event)	166
4.10.4.7	caerIMU9EventGetGyroX(caerIMU9Event event)	166
4.10.4.8	caerIMU9EventGetGyroY(caerIMU9Event event)	166
4.10.4.9	caerIMU9EventGetGyroZ(caerIMU9Event event)	167
4.10.4.10	caerIMU9EventGetTemp(caerIMU9Event event)	167
4.10.4.11	caerIMU9EventGetTimestamp(caerIMU9Event event)	167
4.10.4.12	caerIMU9EventGetTimestamp64(caerIMU9Event event, caerIMU9EventPacket packet)	168
4.10.4.13	caerIMU9EventInvalidate(caerIMU9Event event, caerIMU9EventPacket packet)	168
4.10.4.14	caerIMU9EventIsValid(caerIMU9Event event)	168
4.10.4.15	caerIMU9EventPacketAllocate(int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow)	168
4.10.4.16	caerIMU9EventPacketGetEvent(caerIMU9EventPacket packet, int32_t n)	169
4.10.4.17	caerIMU9EventSetAccelX(caerIMU9Event event, float accelX)	169
4.10.4.18	caerIMU9EventSetAccelY(caerIMU9Event event, float accelY)	169
4.10.4.19	caerIMU9EventSetAccelZ(caerIMU9Event event, float accelZ)	169
4.10.4.20	caerIMU9EventSetCompX(caerIMU9Event event, float compX)	170
4.10.4.21	caerIMU9EventSetCompY(caerIMU9Event event, float compY)	170
4.10.4.22	caerIMU9EventSetCompZ(caerIMU9Event event, float compZ)	170
4.10.4.23	caerIMU9EventSetGyroX(caerIMU9Event event, float gyroX)	170
4.10.4.24	caerIMU9EventSetGyroY(caerIMU9Event event, float gyroY)	170

4.10.4.25 caerIMU9EventSetGyroZ(caerIMU9Event event, float gyroZ) . . . . .	171
4.10.4.26 caerIMU9EventSetTemp(caerIMU9Event event, float temp) . . . . .	171
4.10.4.27 caerIMU9EventSetTimestamp(caerIMU9Event event, int32_t timestamp) . . . . .	171
4.10.4.28 caerIMU9EventValidate(caerIMU9Event event, caerIMU9EventPacket packet) . . . . .	171
4.10.4.29 PACKED_STRUCT(struct caer_imu9_event{uint32_t info;int32_t timestamp;float accel_x;float accel_y;float accel_z;float gyro_x;float gyro_y;float gyro_z;float temp;float comp_x;float comp_y;float comp_z;}) . . . . .	171
4.10.4.30 PACKED_STRUCT(struct caer_imu9_event_packet{struct caer_event_packet↔_header packetHeader;struct caer_imu9_event events[];}) . . . . .	172
4.11 events/packetContainer.h File Reference . . . . .	172
4.11.1 Detailed Description . . . . .	173
4.11.2 Macro Definition Documentation . . . . .	173
4.11.2.1 CAER_EVENT_PACKET_CONTAINER_ITERATOR_END . . . . .	173
4.11.2.2 CAER_EVENT_PACKET_CONTAINER_ITERATOR_START . . . . .	173
4.11.3 Typedef Documentation . . . . .	174
4.11.3.1 caerEventPacketContainer . . . . .	174
4.11.4 Function Documentation . . . . .	174
4.11.4.1 caerEventPacketContainerAllocate(int32_t eventPacketsNumber) . . . . .	174
4.11.4.2 caerEventPacketContainerCopyAllEvents(caerEventPacketContainer container) . . . . .	174
4.11.4.3 caerEventPacketContainerCopyValidEvents(caerEventPacketContainer container) . . . . .	174
4.11.4.4 caerEventPacketContainerFindEventPacketByType(caerEventPacketContainer container, int16_t typeId) . . . . .	175
4.11.4.5 caerEventPacketContainerFree(caerEventPacketContainer container) . . . . .	175
4.11.4.6 caerEventPacketContainerGetEventPacket(caerEventPacketContainer container, int32_t n) . . . . .	175
4.11.4.7 caerEventPacketContainerGetEventPacketsNumber(caerEventPacketContainer container) . . . . .	175
4.11.4.8 caerEventPacketContainerGetEventsNumber(caerEventPacketContainer container) . . . . .	176
4.11.4.9 caerEventPacketContainerGetEventsValidNumber(caerEventPacketContainer container) . . . . .	176
4.11.4.10 caerEventPacketContainerGetHighestEventTimestamp(caerEventPacket↔Container container) . . . . .	176
4.11.4.11 caerEventPacketContainerGetLowestEventTimestamp(caerEventPacket↔Container container) . . . . .	177

4.11.4.12 caerEventPacketContainerSetEventPacket(caerEventPacketContainer container, int32_t n, caerEventPacketHeader packetHeader) . . . . .	177
4.11.4.13 caerEventPacketContainerSetEventPacketsNumber(caerEventPacketContainer container, int32_t eventPacketsNumber) . . . . .	177
4.11.4.14 PACKED_STRUCT(struct caer_event_packet_container{int64_t lowestEventTimestamp;int64_t highestEventTimestamp;int32_t eventsNumber;int32_t eventsValidNumber;int32_t eventPacketsNumber;caerEventPacketHeader eventPackets[];}) . . . . .	177
4.12 events/point1d.h File Reference . . . . .	178
4.12.1 Detailed Description . . . . .	178
4.12.2 Macro Definition Documentation . . . . .	179
4.12.2.1 CAER_POINT1D_ITERATOR_ALL_END . . . . .	179
4.12.2.2 CAER_POINT1D_ITERATOR_ALL_START . . . . .	179
4.12.2.3 CAER_POINT1D_ITERATOR_VALID_END . . . . .	179
4.12.2.4 CAER_POINT1D_ITERATOR_VALID_START . . . . .	179
4.12.2.5 POINT1D_SCALE_MASK . . . . .	179
4.12.2.6 POINT1D_SCALE_SHIFT . . . . .	180
4.12.2.7 POINT1D_TYPE_MASK . . . . .	180
4.12.2.8 POINT1D_TYPE_SHIFT . . . . .	180
4.12.3 Typedef Documentation . . . . .	180
4.12.3.1 caerPoint1DEvent . . . . .	180
4.12.3.2 caerPoint1DEventPacket . . . . .	180
4.12.4 Function Documentation . . . . .	180
4.12.4.1 caerPoint1DEventGetScale(caerPoint1DEvent event) . . . . .	180
4.12.4.2 caerPoint1DEventGetTimestamp(caerPoint1DEvent event) . . . . .	181
4.12.4.3 caerPoint1DEventGetTimestamp64(caerPoint1DEvent event, caerPoint1DEventPacket packet) . . . . .	181
4.12.4.4 caerPoint1DEventGetType(caerPoint1DEvent event) . . . . .	181
4.12.4.5 caerPoint1DEventGetX(caerPoint1DEvent event) . . . . .	181
4.12.4.6 caerPoint1DEventInvalidate(caerPoint1DEvent event, caerPoint1DEventPacket packet) . . . . .	182
4.12.4.7 caerPoint1DEventIsValid(caerPoint1DEvent event) . . . . .	182
4.12.4.8 caerPoint1DEventPacketAllocate(int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow) . . . . .	182



4.12.4.9	<code>caerPoint1DEventPacketGetEvent(caerPoint1DEventPacket packet, int32_t n)</code>	183
4.12.4.10	<code>caerPoint1DEventSetScale(caerPoint1DEvent event, int8_t scale)</code>	183
4.12.4.11	<code>caerPoint1DEventSetTimestamp(caerPoint1DEvent event, int32_t timestamp)</code>	183
4.12.4.12	<code>caerPoint1DEventSetType(caerPoint1DEvent event, uint8_t type)</code>	183
4.12.4.13	<code>caerPoint1DEventSetX(caerPoint1DEvent event, float x)</code>	184
4.12.4.14	<code>caerPoint1DEventValidate(caerPoint1DEvent event, caerPoint1DEventPacket packet)</code>	184
4.12.4.15	<code>PACKED_STRUCT(struct caer_point1d_event{uint32_t info;float x;int32_t timestamp;})</code>	184
4.12.4.16	<code>PACKED_STRUCT(struct caer_point1d_event_packet{struct caer_event↵ packet_header packetHeader;struct caer_point1d_event events[];})</code>	184
4.13	events/point2d.h File Reference	184
4.13.1	Detailed Description	185
4.13.2	Macro Definition Documentation	185
4.13.2.1	<code>CAER_POINT2D_ITERATOR_ALL_END</code>	185
4.13.2.2	<code>CAER_POINT2D_ITERATOR_ALL_START</code>	185
4.13.2.3	<code>CAER_POINT2D_ITERATOR_VALID_END</code>	186
4.13.2.4	<code>CAER_POINT2D_ITERATOR_VALID_START</code>	186
4.13.2.5	<code>POINT2D_SCALE_MASK</code>	186
4.13.2.6	<code>POINT2D_SCALE_SHIFT</code>	186
4.13.2.7	<code>POINT2D_TYPE_MASK</code>	186
4.13.2.8	<code>POINT2D_TYPE_SHIFT</code>	186
4.13.3	Typedef Documentation	186
4.13.3.1	<code>caerPoint2DEvent</code>	186
4.13.3.2	<code>caerPoint2DEventPacket</code>	187
4.13.4	Function Documentation	187
4.13.4.1	<code>caerPoint2DEventGetScale(caerPoint2DEvent event)</code>	187
4.13.4.2	<code>caerPoint2DEventGetTimestamp(caerPoint2DEvent event)</code>	187
4.13.4.3	<code>caerPoint2DEventGetTimestamp64(caerPoint2DEvent event, caerPoint2D↵ EventPacket packet)</code>	187
4.13.4.4	<code>caerPoint2DEventGetType(caerPoint2DEvent event)</code>	188
4.13.4.5	<code>caerPoint2DEventGetX(caerPoint2DEvent event)</code>	188

4.13.4.6	<code>caerPoint2DEventGetY(caerPoint2DEvent event)</code>	188
4.13.4.7	<code>caerPoint2DEventInvalidate(caerPoint2DEvent event, caerPoint2DEventPacket packet)</code>	188
4.13.4.8	<code>caerPoint2DEventIsValid(caerPoint2DEvent event)</code>	189
4.13.4.9	<code>caerPoint2DEventPacketAllocate(int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow)</code>	189
4.13.4.10	<code>caerPoint2DEventPacketGetEvent(caerPoint2DEventPacket packet, int32_t n)</code>	189
4.13.4.11	<code>caerPoint2DEventSetScale(caerPoint2DEvent event, int8_t scale)</code>	190
4.13.4.12	<code>caerPoint2DEventSetTimestamp(caerPoint2DEvent event, int32_t timestamp)</code>	190
4.13.4.13	<code>caerPoint2DEventSetType(caerPoint2DEvent event, uint8_t type)</code>	190
4.13.4.14	<code>caerPoint2DEventSetX(caerPoint2DEvent event, float x)</code>	190
4.13.4.15	<code>caerPoint2DEventSetY(caerPoint2DEvent event, float y)</code>	190
4.13.4.16	<code>caerPoint2DEventValidate(caerPoint2DEvent event, caerPoint2DEventPacket packet)</code>	191
4.13.4.17	<code>PACKED_STRUCT(struct caer_point2d_event{uint32_t info;float x;float y;int32_t timestamp;})</code>	191
4.13.4.18	<code>PACKED_STRUCT(struct caer_point2d_event_packet{struct caer_event_packet_header packetHeader;struct caer_point2d_event events[];})</code>	191
4.14	events/point3d.h File Reference	191
4.14.1	Detailed Description	192
4.14.2	Macro Definition Documentation	192
4.14.2.1	<code>CAER_POINT3D_ITERATOR_ALL_END</code>	192
4.14.2.2	<code>CAER_POINT3D_ITERATOR_ALL_START</code>	193
4.14.2.3	<code>CAER_POINT3D_ITERATOR_VALID_END</code>	193
4.14.2.4	<code>CAER_POINT3D_ITERATOR_VALID_START</code>	193
4.14.2.5	<code>POINT3D_SCALE_MASK</code>	193
4.14.2.6	<code>POINT3D_SCALE_SHIFT</code>	193
4.14.2.7	<code>POINT3D_TYPE_MASK</code>	194
4.14.2.8	<code>POINT3D_TYPE_SHIFT</code>	194
4.14.3	Typedef Documentation	194
4.14.3.1	<code>caerPoint3DEvent</code>	194
4.14.3.2	<code>caerPoint3DEventPacket</code>	194
4.14.4	Function Documentation	194

4.14.4.1	<a href="#">caerPoint3DEventGetScale(caerPoint3DEvent event)</a>	194
4.14.4.2	<a href="#">caerPoint3DEventGetTimestamp(caerPoint3DEvent event)</a>	194
4.14.4.3	<a href="#">caerPoint3DEventGetTimestamp64(caerPoint3DEvent event, caerPoint3DEventPacket packet)</a>	195
4.14.4.4	<a href="#">caerPoint3DEventGetType(caerPoint3DEvent event)</a>	195
4.14.4.5	<a href="#">caerPoint3DEventGetX(caerPoint3DEvent event)</a>	195
4.14.4.6	<a href="#">caerPoint3DEventGetY(caerPoint3DEvent event)</a>	196
4.14.4.7	<a href="#">caerPoint3DEventGetZ(caerPoint3DEvent event)</a>	196
4.14.4.8	<a href="#">caerPoint3DEventInvalidate(caerPoint3DEvent event, caerPoint3DEventPacket packet)</a>	196
4.14.4.9	<a href="#">caerPoint3DEventIsValid(caerPoint3DEvent event)</a>	196
4.14.4.10	<a href="#">caerPoint3DEventPacketAllocate(int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow)</a>	197
4.14.4.11	<a href="#">caerPoint3DEventPacketGetEvent(caerPoint3DEventPacket packet, int32_t n)</a>	197
4.14.4.12	<a href="#">caerPoint3DEventSetScale(caerPoint3DEvent event, int8_t scale)</a>	197
4.14.4.13	<a href="#">caerPoint3DEventSetTimestamp(caerPoint3DEvent event, int32_t timestamp)</a>	198
4.14.4.14	<a href="#">caerPoint3DEventSetType(caerPoint3DEvent event, uint8_t type)</a>	198
4.14.4.15	<a href="#">caerPoint3DEventSetX(caerPoint3DEvent event, float x)</a>	198
4.14.4.16	<a href="#">caerPoint3DEventSetY(caerPoint3DEvent event, float y)</a>	198
4.14.4.17	<a href="#">caerPoint3DEventSetZ(caerPoint3DEvent event, float z)</a>	198
4.14.4.18	<a href="#">caerPoint3DEventValidate(caerPoint3DEvent event, caerPoint3DEventPacket packet)</a>	199
4.14.4.19	<a href="#">PACKED_STRUCT(struct caer_point3d_event{uint32_t info;float x;float y;float z;int32_t timestamp;})</a>	199
4.14.4.20	<a href="#">PACKED_STRUCT(struct caer_point3d_event_packet{struct caer_event_packet_header packetHeader;struct caer_point3d_event events[];})</a>	199
4.15	<a href="#">events/point4d.h File Reference</a>	199
4.15.1	<a href="#">Detailed Description</a>	200
4.15.2	<a href="#">Macro Definition Documentation</a>	200
4.15.2.1	<a href="#">CAER_POINT4D_ITERATOR_ALL_END</a>	200
4.15.2.2	<a href="#">CAER_POINT4D_ITERATOR_ALL_START</a>	200
4.15.2.3	<a href="#">CAER_POINT4D_ITERATOR_VALID_END</a>	201
4.15.2.4	<a href="#">CAER_POINT4D_ITERATOR_VALID_START</a>	201

4.15.2.5	POINT4D_SCALE_MASK . . . . .	201
4.15.2.6	POINT4D_SCALE_SHIFT . . . . .	201
4.15.2.7	POINT4D_TYPE_MASK . . . . .	201
4.15.2.8	POINT4D_TYPE_SHIFT . . . . .	201
4.15.3	Typedef Documentation . . . . .	201
4.15.3.1	caerPoint4DEvent . . . . .	201
4.15.3.2	caerPoint4DEventPacket . . . . .	202
4.15.4	Function Documentation . . . . .	202
4.15.4.1	caerPoint4DEventGetScale(caerPoint4DEvent event) . . . . .	202
4.15.4.2	caerPoint4DEventGetTimestamp(caerPoint4DEvent event) . . . . .	202
4.15.4.3	caerPoint4DEventGetTimestamp64(caerPoint4DEvent event, caerPoint4DEventPacket packet) . . . . .	202
4.15.4.4	caerPoint4DEventGetType(caerPoint4DEvent event) . . . . .	203
4.15.4.5	caerPoint4DEventGetW(caerPoint4DEvent event) . . . . .	203
4.15.4.6	caerPoint4DEventGetX(caerPoint4DEvent event) . . . . .	203
4.15.4.7	caerPoint4DEventGetY(caerPoint4DEvent event) . . . . .	203
4.15.4.8	caerPoint4DEventGetZ(caerPoint4DEvent event) . . . . .	204
4.15.4.9	caerPoint4DEventInvalidate(caerPoint4DEvent event, caerPoint4DEventPacket packet) . . . . .	204
4.15.4.10	caerPoint4DEventIsValid(caerPoint4DEvent event) . . . . .	204
4.15.4.11	caerPoint4DEventPacketAllocate(int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow) . . . . .	205
4.15.4.12	caerPoint4DEventPacketGetEvent(caerPoint4DEventPacket packet, int32_t n) . . . . .	205
4.15.4.13	caerPoint4DEventSetScale(caerPoint4DEvent event, int8_t scale) . . . . .	205
4.15.4.14	caerPoint4DEventSetTimestamp(caerPoint4DEvent event, int32_t timestamp) . . . . .	205
4.15.4.15	caerPoint4DEventSetType(caerPoint4DEvent event, uint8_t type) . . . . .	206
4.15.4.16	caerPoint4DEventSetW(caerPoint4DEvent event, float w) . . . . .	206
4.15.4.17	caerPoint4DEventSetX(caerPoint4DEvent event, float x) . . . . .	206
4.15.4.18	caerPoint4DEventSetY(caerPoint4DEvent event, float y) . . . . .	206
4.15.4.19	caerPoint4DEventSetZ(caerPoint4DEvent event, float z) . . . . .	207
4.15.4.20	caerPoint4DEventValidate(caerPoint4DEvent event, caerPoint4DEventPacket packet) . . . . .	207

4.15.4.21	PACKED_STRUCT(struct caer_point4d_event{uint32_t info;float x;float y;float z;float w;int32_t timestamp;})	207
4.15.4.22	PACKED_STRUCT(struct caer_point4d_event_packet{struct caer_event↵ packet_header packetHeader;struct caer_point4d_event events[];})	207
4.16	events/polarity.h File Reference	207
4.16.1	Detailed Description	208
4.16.2	Macro Definition Documentation	208
4.16.2.1	CAER_POLARITY_ITERATOR_ALL_END	208
4.16.2.2	CAER_POLARITY_ITERATOR_ALL_START	209
4.16.2.3	CAER_POLARITY_ITERATOR_VALID_END	209
4.16.2.4	CAER_POLARITY_ITERATOR_VALID_START	209
4.16.2.5	CAER_POLARITY_REVERSE_ITERATOR_ALL_END	209
4.16.2.6	CAER_POLARITY_REVERSE_ITERATOR_ALL_START	209
4.16.2.7	CAER_POLARITY_REVERSE_ITERATOR_VALID_END	210
4.16.2.8	CAER_POLARITY_REVERSE_ITERATOR_VALID_START	210
4.16.2.9	POLARITY_MASK	210
4.16.2.10	POLARITY_SHIFT	210
4.16.2.11	X_ADDR_MASK	210
4.16.2.12	X_ADDR_SHIFT	210
4.16.2.13	Y_ADDR_MASK	210
4.16.2.14	Y_ADDR_SHIFT	211
4.16.3	Typedef Documentation	211
4.16.3.1	caerPolarityEvent	211
4.16.3.2	caerPolarityEventPacket	211
4.16.4	Function Documentation	211
4.16.4.1	caerPolarityEventGetPolarity(caerPolarityEvent event)	211
4.16.4.2	caerPolarityEventGetTimestamp(caerPolarityEvent event)	211
4.16.4.3	caerPolarityEventGetTimestamp64(caerPolarityEvent event, caerPolarityEvent↵ Packet packet)	212
4.16.4.4	caerPolarityEventGetX(caerPolarityEvent event)	212
4.16.4.5	caerPolarityEventGetY(caerPolarityEvent event)	212

4.16.4.6	<code>caerPolarityEventInvalidate(caerPolarityEvent event, caerPolarityEventPacket packet)</code>	212
4.16.4.7	<code>caerPolarityEventIsValid(caerPolarityEvent event)</code>	213
4.16.4.8	<code>caerPolarityEventPacketAllocate(int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow)</code>	213
4.16.4.9	<code>caerPolarityEventPacketGetEvent(caerPolarityEventPacket packet, int32_t n)</code>	213
4.16.4.10	<code>caerPolarityEventSetPolarity(caerPolarityEvent event, bool polarity)</code>	214
4.16.4.11	<code>caerPolarityEventSetTimestamp(caerPolarityEvent event, int32_t timestamp)</code>	214
4.16.4.12	<code>caerPolarityEventSetX(caerPolarityEvent event, uint16_t xAddress)</code>	214
4.16.4.13	<code>caerPolarityEventSetY(caerPolarityEvent event, uint16_t yAddress)</code>	214
4.16.4.14	<code>caerPolarityEventValidate(caerPolarityEvent event, caerPolarityEventPacket packet)</code>	215
4.16.4.15	<code>PACKED_STRUCT(struct caer_polarity_event{uint32_t data;int32_t timestamp;})</code>	215
4.16.4.16	<code>PACKED_STRUCT(struct caer_polarity_event_packet{struct caer_event_← packet_header packetHeader;struct caer_polarity_event events[];})</code>	215
4.17	<code>events/sample.h</code> File Reference	215
4.17.1	Detailed Description	216
4.17.2	Macro Definition Documentation	216
4.17.2.1	<code>CAER_SAMPLE_ITERATOR_ALL_END</code>	216
4.17.2.2	<code>CAER_SAMPLE_ITERATOR_ALL_START</code>	216
4.17.2.3	<code>CAER_SAMPLE_ITERATOR_VALID_END</code>	216
4.17.2.4	<code>CAER_SAMPLE_ITERATOR_VALID_START</code>	217
4.17.2.5	<code>SAMPLE_MASK</code>	217
4.17.2.6	<code>SAMPLE_SHIFT</code>	217
4.17.2.7	<code>SAMPLE_TYPE_MASK</code>	217
4.17.2.8	<code>SAMPLE_TYPE_SHIFT</code>	217
4.17.3	Typedef Documentation	217
4.17.3.1	<code>caerSampleEvent</code>	217
4.17.3.2	<code>caerSampleEventPacket</code>	217
4.17.4	Function Documentation	217
4.17.4.1	<code>caerSampleEventGetSample(caerSampleEvent event)</code>	217
4.17.4.2	<code>caerSampleEventGetTimestamp(caerSampleEvent event)</code>	218

4.17.4.3	<a href="#">caerSampleEventGetTimestamp64(caerSampleEvent event, caerSampleEventPacket packet)</a>	218
4.17.4.4	<a href="#">caerSampleEventGetType(caerSampleEvent event)</a>	218
4.17.4.5	<a href="#">caerSampleEventInvalidate(caerSampleEvent event, caerSampleEventPacket packet)</a>	219
4.17.4.6	<a href="#">caerSampleEventsValid(caerSampleEvent event)</a>	219
4.17.4.7	<a href="#">caerSampleEventPacketAllocate(int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow)</a>	219
4.17.4.8	<a href="#">caerSampleEventPacketGetEvent(caerSampleEventPacket packet, int32_t n)</a>	219
4.17.4.9	<a href="#">caerSampleEventSetSample(caerSampleEvent event, uint32_t sample)</a>	220
4.17.4.10	<a href="#">caerSampleEventSetTimestamp(caerSampleEvent event, int32_t timestamp)</a>	220
4.17.4.11	<a href="#">caerSampleEventSetType(caerSampleEvent event, uint8_t type)</a>	220
4.17.4.12	<a href="#">caerSampleEventValidate(caerSampleEvent event, caerSampleEventPacket packet)</a>	220
4.17.4.13	<a href="#">PACKED_STRUCT(struct caer_sample_event{uint32_t data;int32_t timestamp;})</a>	221
4.17.4.14	<a href="#">PACKED_STRUCT(struct caer_sample_event_packet{struct caer_event_packet_header packetHeader;struct caer_sample_event events[];})</a>	221
4.18	<a href="#">events/special.h File Reference</a>	221
4.18.1	<a href="#">Detailed Description</a>	222
4.18.2	<a href="#">Macro Definition Documentation</a>	222
4.18.2.1	<a href="#">CAER_SPECIAL_ITERATOR_ALL_END</a>	222
4.18.2.2	<a href="#">CAER_SPECIAL_ITERATOR_ALL_START</a>	223
4.18.2.3	<a href="#">CAER_SPECIAL_ITERATOR_VALID_END</a>	223
4.18.2.4	<a href="#">CAER_SPECIAL_ITERATOR_VALID_START</a>	223
4.18.2.5	<a href="#">DATA_MASK</a>	223
4.18.2.6	<a href="#">DATA_SHIFT</a>	223
4.18.2.7	<a href="#">TYPE_MASK</a>	223
4.18.2.8	<a href="#">TYPE_SHIFT</a>	224
4.18.3	<a href="#">Typedef Documentation</a>	224
4.18.3.1	<a href="#">caerSpecialEvent</a>	224
4.18.3.2	<a href="#">caerSpecialEventPacket</a>	224
4.18.4	<a href="#">Enumeration Type Documentation</a>	224
4.18.4.1	<a href="#">caer_special_event_types</a>	224

4.18.5	Function Documentation	224
4.18.5.1	caerSpecialEventGetData(caerSpecialEvent event)	224
4.18.5.2	caerSpecialEventGetTimestamp(caerSpecialEvent event)	225
4.18.5.3	caerSpecialEventGetTimestamp64(caerSpecialEvent event, caerSpecialEvent↔ Packet packet)	225
4.18.5.4	caerSpecialEventGetType(caerSpecialEvent event)	225
4.18.5.5	caerSpecialEventInvalidate(caerSpecialEvent event, caerSpecialEventPacket packet)	226
4.18.5.6	caerSpecialEventsIsValid(caerSpecialEvent event)	226
4.18.5.7	caerSpecialEventPacketAllocate(int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow)	226
4.18.5.8	caerSpecialEventPacketFindEventByType(caerSpecialEventPacket packet, uint8_t type)	226
4.18.5.9	caerSpecialEventPacketFindValidEventByType(caerSpecialEventPacket packet, uint8_t type)	227
4.18.5.10	caerSpecialEventPacketGetEvent(caerSpecialEventPacket packet, int32_t n)	227
4.18.5.11	caerSpecialEventSetData(caerSpecialEvent event, uint32_t data)	227
4.18.5.12	caerSpecialEventSetTimestamp(caerSpecialEvent event, int32_t timestamp)	228
4.18.5.13	caerSpecialEventSetType(caerSpecialEvent event, uint8_t type)	228
4.18.5.14	caerSpecialEventValidate(caerSpecialEvent event, caerSpecialEventPacket packet)	228
4.18.5.15	PACKED_STRUCT(struct caer_special_event{uint32_t data;int32_t timestamp;})	228
4.18.5.16	PACKED_STRUCT(struct caer_special_event_packet{struct caer_event↔ packet_header packetHeader;struct caer_special_event events[];})	229
4.19	events/spike.h File Reference	229
4.19.1	Detailed Description	230
4.19.2	Macro Definition Documentation	230
4.19.2.1	CAER_SPIKE_ITERATOR_ALL_END	230
4.19.2.2	CAER_SPIKE_ITERATOR_ALL_START	230
4.19.2.3	CAER_SPIKE_ITERATOR_VALID_END	230
4.19.2.4	CAER_SPIKE_ITERATOR_VALID_START	230
4.19.2.5	SPIKE_CHIP_ID_MASK	231
4.19.2.6	SPIKE_CHIP_ID_SHIFT	231
4.19.2.7	SPIKE_NEURON_ID_MASK	231



4.19.2.8 SPIKE_NEURON_ID_SHIFT . . . . .	231
4.19.2.9 SPIKE_SOURCE_CORE_ID_MASK . . . . .	231
4.19.2.10 SPIKE_SOURCE_CORE_ID_SHIFT . . . . .	231
4.19.3 Typedef Documentation . . . . .	231
4.19.3.1 caerSpikeEvent . . . . .	231
4.19.3.2 caerSpikeEventPacket . . . . .	231
4.19.4 Function Documentation . . . . .	231
4.19.4.1 caerSpikeEventGetChipID(caerSpikeEvent event) . . . . .	231
4.19.4.2 caerSpikeEventGetNeuronID(caerSpikeEvent event) . . . . .	232
4.19.4.3 caerSpikeEventGetSourceCoreID(caerSpikeEvent event) . . . . .	232
4.19.4.4 caerSpikeEventGetTimestamp(caerSpikeEvent event) . . . . .	232
4.19.4.5 caerSpikeEventGetTimestamp64(caerSpikeEvent event, caerSpikeEventPacket packet) . . . . .	233
4.19.4.6 caerSpikeEventInvalidate(caerSpikeEvent event, caerSpikeEventPacket packet) . . . . .	233
4.19.4.7 caerSpikeEventIsValid(caerSpikeEvent event) . . . . .	233
4.19.4.8 caerSpikeEventPacketAllocate(int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow) . . . . .	233
4.19.4.9 caerSpikeEventPacketGetEvent(caerSpikeEventPacket packet, int32_t n) . . . . .	234
4.19.4.10 caerSpikeEventSetChipID(caerSpikeEvent event, uint8_t chipID) . . . . .	234
4.19.4.11 caerSpikeEventSetNeuronID(caerSpikeEvent event, uint8_t neuronID) . . . . .	234
4.19.4.12 caerSpikeEventSetSourceCoreID(caerSpikeEvent event, uint8_t sourceCoreID) . . . . .	235
4.19.4.13 caerSpikeEventSetTimestamp(caerSpikeEvent event, int32_t timestamp) . . . . .	235
4.19.4.14 caerSpikeEventValidate(caerSpikeEvent event, caerSpikeEventPacket packet) . . . . .	235
4.19.4.15 PACKED_STRUCT(struct caer_spike_event{uint32_t data;int32_t timestamp;}) . . . . .	235
4.19.4.16 PACKED_STRUCT(struct caer_spike_event_packet{struct caer_event_packet←_header packetHeader;struct caer_spike_event events[];}) . . . . .	235
4.20 frame_utils.h File Reference . . . . .	236
4.20.1 Detailed Description . . . . .	236
4.21 frame_utils_opencv.h File Reference . . . . .	236
4.21.1 Detailed Description . . . . .	236
4.22 libcaer.h File Reference . . . . .	237
4.22.1 Detailed Description . . . . .	238

4.22.2	Macro Definition Documentation	238
4.22.2.1	CLEAR_NUMBITS16	238
4.22.2.2	CLEAR_NUMBITS32	238
4.22.2.3	CLEAR_NUMBITS8	238
4.22.2.4	GET_NUMBITS16	238
4.22.2.5	GET_NUMBITS32	238
4.22.2.6	GET_NUMBITS8	238
4.22.2.7	I16T	238
4.22.2.8	I32T	238
4.22.2.9	I64T	239
4.22.2.10	I8T	239
4.22.2.11	LIBCAER_NAME_STRING	239
4.22.2.12	LIBCAER_VERSION	239
4.22.2.13	LIBCAER_VERSION_STRING	239
4.22.2.14	MASK_NUMBITS32	239
4.22.2.15	MASK_NUMBITS64	239
4.22.2.16	SET_NUMBITS16	239
4.22.2.17	SET_NUMBITS32	239
4.22.2.18	SET_NUMBITS8	239
4.22.2.19	SWAP_VAR	240
4.22.2.20	U16T	240
4.22.2.21	U32T	240
4.22.2.22	U64T	240
4.22.2.23	U8T	240
4.22.3	Function Documentation	240
4.22.3.1	caerByteArrayToInteger(uint8_t *byteArray, uint8_t byteArrayLength)	240
4.22.3.2	caerIntegerToByteArray(uint32_t integer, uint8_t *byteArray, uint8_t byteArrayLength)	240
4.22.3.3	caerStrEquals(const char *s1, const char *s2)	241
4.22.3.4	caerStrEqualsUpTo(const char *s1, const char *s2, size_t len)	241
4.23	log.h File Reference	241

4.23.1 Detailed Description . . . . .	242
4.23.2 Macro Definition Documentation . . . . .	242
4.23.2.1 CAER_LOG_ALERT . . . . .	242
4.23.2.2 CAER_LOG_CRITICAL . . . . .	242
4.23.2.3 CAER_LOG_DEBUG . . . . .	242
4.23.2.4 CAER_LOG_EMERGENCY . . . . .	242
4.23.2.5 CAER_LOG_ERROR . . . . .	243
4.23.2.6 CAER_LOG_INFO . . . . .	243
4.23.2.7 CAER_LOG_NOTICE . . . . .	243
4.23.2.8 CAER_LOG_WARNING . . . . .	243
4.23.3 Function Documentation . . . . .	243
4.23.3.1 caerLog(uint8_t logLevel, const char *subSystem, const char *format,...) ATT↵ RIBUTE_FORMAT . . . . .	243
4.23.3.2 caerLogFileDescriptorsSet(int fd1, int fd2) . . . . .	244
4.23.3.3 caerLogLevelGet(void) . . . . .	244
4.23.3.4 caerLogLevelSet(uint8_t logLevel) . . . . .	244
4.24 portable_endian.h File Reference . . . . .	244
4.24.1 Detailed Description . . . . .	244
<b>Index</b>	<b>245</b>



# Chapter 1

## Data Structure Index

### 1.1 Data Structures

Here are the data structures with brief descriptions:

<a href="#">caer_bias_coarsefine</a>	5
<a href="#">caer_bias_shiftedsources</a>	5
<a href="#">caer_bias_vdac</a>	6
<a href="#">caer_davis_info</a>	6
<a href="#">caer_dvs128_info</a>	8
<a href="#">caer_dynapse_info</a>	8



## Chapter 2

# File Index

### 2.1 File List

Here is a list of all documented files with brief descriptions:

<a href="#">frame_utils.h</a>	236
<a href="#">frame_utils_opencv.h</a>	236
<a href="#">libcaer.h</a>	237
<b>libcaer_in.h</b>	<b>??</b>
<a href="#">log.h</a>	241
<a href="#">portable_endian.h</a>	244
<a href="#">devices/davis.h</a>	11
<a href="#">devices/dvs128.h</a>	97
<a href="#">devices/dynapse.h</a>	101
<a href="#">devices/usb.h</a>	106
<a href="#">events/common.h</a>	111
<a href="#">events/config.h</a>	122
<a href="#">events/ear.h</a>	129
<a href="#">events/frame.h</a>	135
<a href="#">events/imu6.h</a>	154
<a href="#">events/imu9.h</a>	162
<a href="#">events/packetContainer.h</a>	172
<a href="#">events/point1d.h</a>	178
<a href="#">events/point2d.h</a>	184
<a href="#">events/point3d.h</a>	191
<a href="#">events/point4d.h</a>	199
<a href="#">events/polarity.h</a>	207
<a href="#">events/sample.h</a>	215
<a href="#">events/special.h</a>	221
<a href="#">events/spike.h</a>	229





## Chapter 3

# Data Structure Documentation

### 3.1 caer\_bias\_coarsefine Struct Reference

```
#include <davis.h>
```

#### Data Fields

- uint8\_t [coarseValue](#)  
*Coarse current, from 0 to 7, creates big variations in output current.*
- uint8\_t [fineValue](#)  
*Fine current, from 0 to 255, creates small variations in output current.*
- bool [enabled](#)  
*Whether this bias is enabled or not.*
- bool [sexN](#)  
*Bias sex: true for 'N' type, false for 'P' type.*
- bool [typeNormal](#)  
*Bias type: true for 'Normal', false for 'Cascode'.*
- bool [currentLevelNormal](#)  
*Bias current level: true for 'Normal', false for 'Low'.*

#### 3.1.1 Detailed Description

On-chip coarse-fine bias current configuration. See '<http://inilabs.com/support/biasing/>' for more details.

The documentation for this struct was generated from the following file:

- devices/[davis.h](#)

### 3.2 caer\_bias\_shiftedsources Struct Reference

```
#include <davis.h>
```

## Data Fields

- `uint8_t refValue`  
*Shifted-source bias level, from 0 to 63.*
- `uint8_t regValue`  
*Shifted-source bias current for buffer amplifier, from 0 to 63.*
- `enum caer_bias_shiftedsource_operating_mode operatingMode`  
*Shifted-source operating mode (see 'enum caer\_bias\_shiftedsource\_operating\_mode').*
- `enum caer_bias_shiftedsource_voltage_level voltageLevel`  
*Shifted-source voltage level (see 'enum caer\_bias\_shiftedsource\_voltage\_level').*

### 3.2.1 Detailed Description

On-chip shifted-source bias current configuration. See '<http://inilabs.com/support/biasing/>' for more details.

The documentation for this struct was generated from the following file:

- `devices/davis.h`

## 3.3 caer\_bias\_vdac Struct Reference

```
#include <davis.h>
```

## Data Fields

- `uint8_t voltageValue`  
*Voltage, between 0 and 63, as a fraction of 1/64th of VDD=3.3V.*
- `uint8_t currentValue`  
*Current, between 0 and 7, that drives the voltage.*

### 3.3.1 Detailed Description

On-chip voltage digital-to-analog converter configuration. See '<http://inilabs.com/support/biasing/>' for more details.

The documentation for this struct was generated from the following file:

- `devices/davis.h`

## 3.4 caer\_davis\_info Struct Reference

```
#include <davis.h>
```

## Data Fields

- `int16_t deviceId`  
*Unique device identifier. Also 'source' for events.*
- `char deviceSerialNumber [8+1]`  
*Device serial number.*
- `uint8_t deviceUSBBusNumber`  
*Device USB bus number.*
- `uint8_t deviceUSBDeviceAddress`  
*Device USB device address.*
- `char * deviceString`  
*Device information string, for logging purposes.*
- `int16_t logicVersion`  
*Logic (FPGA/CPLD) version.*
- `bool devicesMaster`  
*Whether the device is a time-stamp master or slave.*
- `int16_t logicClock`  
*Clock in MHz for main logic (FPGA/CPLD).*
- `int16_t adcClock`  
*Clock in MHz for ADC/APS logic (FPGA/CPLD).*
- `int16_t chipID`  
*Chip identifier/type.*
- `int16_t dvsSizeX`  
*DVS X axis resolution.*
- `int16_t dvsSizeY`  
*DVS Y axis resolution.*
- `bool dvsHasPixelFilter`  
*Feature test: DVS pixel-level filtering.*
- `bool dvsHasBackgroundActivityFilter`  
*Feature test: DVS Background Activity filter.*
- `bool dvsHasTestEventGenerator`  
*Feature test: fake event generator (testing/debug).*
- `int16_t apsSizeX`  
*APS X axis resolution.*
- `int16_t apsSizeY`  
*APS Y axis resolution.*
- `enum caer_frame_event_color_filter apsColorFilter`  
*APS color filter type.*
- `bool apsHasGlobalShutter`  
*Feature test: APS supports Global Shutter.*
- `bool apsHasQuadROI`  
*Feature test: APS supports Quadruple Region-of-Interest readout.*
- `bool apsHasExternalADC`  
*Feature test: APS supports External ADC for getting the image.*
- `bool apsHasInternalADC`  
*Feature test: APS supports Internal (on-chip) ADC for getting the image.*
- `bool extInputHasGenerator`  
*Feature test: External Input module supports Signal-Generation.*
- `bool extInputHasExtraDetectors`  
*Feature test: External Input module supports extra detectors (1 & 2).*

### 3.4.1 Detailed Description

DAVIS device-related information.

The documentation for this struct was generated from the following file:

- [devices/davis.h](#)

## 3.5 caer\_dvs128\_info Struct Reference

```
#include <dvs128.h>
```

### Data Fields

- `int16_t` [deviceId](#)  
*Unique device identifier. Also 'source' for events.*
- `char` [deviceSerialNumber](#) [8+1]  
*Device serial number.*
- `uint8_t` [deviceUSBBusNumber](#)  
*Device USB bus number.*
- `uint8_t` [deviceUSBDeviceAddress](#)  
*Device USB device address.*
- `char *` [deviceString](#)  
*Device information string, for logging purposes.*
- `int16_t` [logicVersion](#)  
*Logic (FPGA/CPLD) version.*
- `bool` [deviceIsMaster](#)  
*Whether the device is a time-stamp master or slave.*
- `int16_t` [dvsSizeX](#)  
*DVS X axis resolution.*
- `int16_t` [dvsSizeY](#)  
*DVS Y axis resolution.*

### 3.5.1 Detailed Description

DVS128 device-related information.

The documentation for this struct was generated from the following file:

- [devices/dvs128.h](#)

## 3.6 caer\_dynapse\_info Struct Reference

```
#include <dynapse.h>
```

## Data Fields

- `int16_t` [deviceId](#)  
*Unique device identifier. Also 'source' for events.*
- `char` [deviceSerialNumber](#) [8+1]  
*Device serial number.*
- `uint8_t` [deviceUSBBusNumber](#)  
*Device USB bus number.*
- `uint8_t` [deviceUSBDeviceAddress](#)  
*Device USB device address.*
- `char *` [deviceString](#)  
*Device information string, for logging purposes.*
- `int16_t` [logicVersion](#)  
*Logic (FPGA/CPLD) version.*
- `bool` [devicesMaster](#)  
*Whether the device is a time-stamp master or slave.*
- `int16_t` [logicClock](#)  
*Clock in MHz for main logic (FPGA/CPLD).*
- `int16_t` [chipID](#)  
*Chip identifier/type.*

### 3.6.1 Detailed Description

Dynap-se device-related information.

The documentation for this struct was generated from the following file:

- `devices/dynapse.h`



## Chapter 4

# File Documentation

### 4.1 devices/davis.h File Reference

```
#include "usb.h"
#include "../events/polarity.h"
#include "../events/special.h"
#include "../events/frame.h"
#include "../events/imu6.h"
```

#### Data Structures

- struct [caer\\_davis\\_info](#)
- struct [caer\\_bias\\_vdac](#)
- struct [caer\\_bias\\_coarsefine](#)
- struct [caer\\_bias\\_shiftedsources](#)

#### Macros

- #define [CAER\\_DEVICE\\_DAVIS\\_FX2](#) 1
- #define [CAER\\_DEVICE\\_DAVIS\\_FX3](#) 2
- #define [DAVIS\\_CHIP\\_DAVIS240A](#) 0
- #define [DAVIS\\_CHIP\\_DAVIS240B](#) 1
- #define [DAVIS\\_CHIP\\_DAVIS240C](#) 2
- #define [DAVIS\\_CHIP\\_DAVIS128](#) 3
- #define [DAVIS\\_CHIP\\_DAVIS346A](#) 4
- #define [DAVIS\\_CHIP\\_DAVIS346B](#) 5
- #define [DAVIS\\_CHIP\\_DAVIS640](#) 6
- #define [DAVIS\\_CHIP\\_DAVISRGB](#) 7
- #define [DAVIS\\_CHIP\\_DAVIS208](#) 8
- #define [DAVIS\\_CHIP\\_DAVIS346C](#) 9
- #define [DAVIS\\_CONFIG\\_MUX](#) 0
- #define [DAVIS\\_CONFIG\\_DVS](#) 1
- #define [DAVIS\\_CONFIG\\_APS](#) 2
- #define [DAVIS\\_CONFIG\\_IMU](#) 3
- #define [DAVIS\\_CONFIG\\_EXTINPUT](#) 4

- #define DAVIS\_CONFIG\_BIAS 5
- #define DAVIS\_CONFIG\_CHIP 5
- #define DAVIS\_CONFIG\_SYSINFO 6
- #define DAVIS\_CONFIG\_USB 9
- #define DAVIS\_CONFIG\_MUX\_RUN 0
- #define DAVIS\_CONFIG\_MUX\_TIMESTAMP\_RUN 1
- #define DAVIS\_CONFIG\_MUX\_TIMESTAMP\_RESET 2
- #define DAVIS\_CONFIG\_MUX\_FORCE\_CHIP\_BIAS\_ENABLE 3
- #define DAVIS\_CONFIG\_MUX\_DROP\_DVS\_ON\_TRANSFER\_STALL 4
- #define DAVIS\_CONFIG\_MUX\_DROP\_APS\_ON\_TRANSFER\_STALL 5
- #define DAVIS\_CONFIG\_MUX\_DROP\_IMU\_ON\_TRANSFER\_STALL 6
- #define DAVIS\_CONFIG\_MUX\_DROP\_EXTINPUT\_ON\_TRANSFER\_STALL 7
- #define DAVIS\_CONFIG\_DVS\_SIZE\_COLUMNS 0
- #define DAVIS\_CONFIG\_DVS\_SIZE\_ROWS 1
- #define DAVIS\_CONFIG\_DVS\_ORIENTATION\_INFO 2
- #define DAVIS\_CONFIG\_DVS\_RUN 3
- #define DAVIS\_CONFIG\_DVS\_ACK\_DELAY\_ROW 4
- #define DAVIS\_CONFIG\_DVS\_ACK\_DELAY\_COLUMN 5
- #define DAVIS\_CONFIG\_DVS\_ACK\_EXTENSION\_ROW 6
- #define DAVIS\_CONFIG\_DVS\_ACK\_EXTENSION\_COLUMN 7
- #define DAVIS\_CONFIG\_DVS\_WAIT\_ON\_TRANSFER\_STALL 8
- #define DAVIS\_CONFIG\_DVS\_FILTER\_ROW\_ONLY\_EVENTS 9
- #define DAVIS\_CONFIG\_DVS\_EXTERNAL\_AER\_CONTROL 10
- #define DAVIS\_CONFIG\_DVS\_HAS\_PIXEL\_FILTER 11
- #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_0\_ROW 12
- #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_0\_COLUMN 13
- #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_1\_ROW 14
- #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_1\_COLUMN 15
- #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_2\_ROW 16
- #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_2\_COLUMN 17
- #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_3\_ROW 18
- #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_3\_COLUMN 19
- #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_4\_ROW 20
- #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_4\_COLUMN 21
- #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_5\_ROW 22
- #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_5\_COLUMN 23
- #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_6\_ROW 24
- #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_6\_COLUMN 25
- #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_7\_ROW 26
- #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_7\_COLUMN 27
- #define DAVIS\_CONFIG\_DVS\_HAS\_BACKGROUND\_ACTIVITY\_FILTER 28
- #define DAVIS\_CONFIG\_DVS\_FILTER\_BACKGROUND\_ACTIVITY 29
- #define DAVIS\_CONFIG\_DVS\_FILTER\_BACKGROUND\_ACTIVITY\_DELTAT 30
- #define DAVIS\_CONFIG\_DVS\_HAS\_TEST\_EVENT\_GENERATOR 31
- #define DAVIS\_CONFIG\_DVS\_TEST\_EVENT\_GENERATOR\_ENABLE 32
- #define DAVIS\_CONFIG\_APS\_SIZE\_COLUMNS 0
- #define DAVIS\_CONFIG\_APS\_SIZE\_ROWS 1
- #define DAVIS\_CONFIG\_APS\_ORIENTATION\_INFO 2
- #define DAVIS\_CONFIG\_APS\_COLOR\_FILTER 3
- #define DAVIS\_CONFIG\_APS\_RUN 4
- #define DAVIS\_CONFIG\_APS\_RESET\_READ 5
- #define DAVIS\_CONFIG\_APS\_WAIT\_ON\_TRANSFER\_STALL 6
- #define DAVIS\_CONFIG\_APS\_HAS\_GLOBAL\_SHUTTER 7
- #define DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER 8
- #define DAVIS\_CONFIG\_APS\_START\_COLUMN\_0 9



- #define [DAVIS\\_CONFIG\\_APS\\_START\\_ROW\\_0](#) 10
- #define [DAVIS\\_CONFIG\\_APS\\_END\\_COLUMN\\_0](#) 11
- #define [DAVIS\\_CONFIG\\_APS\\_END\\_ROW\\_0](#) 12
- #define [DAVIS\\_CONFIG\\_APS\\_EXPOSURE](#) 13
- #define [DAVIS\\_CONFIG\\_APS\\_FRAME\\_DELAY](#) 14
- #define [DAVIS\\_CONFIG\\_APS\\_RESET\\_SETTLE](#) 15
- #define [DAVIS\\_CONFIG\\_APS\\_COLUMN\\_SETTLE](#) 16
- #define [DAVIS\\_CONFIG\\_APS\\_ROW\\_SETTLE](#) 17
- #define [DAVIS\\_CONFIG\\_APS\\_NULL\\_SETTLE](#) 18
- #define [DAVIS\\_CONFIG\\_APS\\_HAS\\_QUAD\\_ROI](#) 19
- #define [DAVIS\\_CONFIG\\_APS\\_START\\_COLUMN\\_1](#) 20
- #define [DAVIS\\_CONFIG\\_APS\\_START\\_ROW\\_1](#) 21
- #define [DAVIS\\_CONFIG\\_APS\\_END\\_COLUMN\\_1](#) 22
- #define [DAVIS\\_CONFIG\\_APS\\_END\\_ROW\\_1](#) 23
- #define [DAVIS\\_CONFIG\\_APS\\_START\\_COLUMN\\_2](#) 24
- #define [DAVIS\\_CONFIG\\_APS\\_START\\_ROW\\_2](#) 25
- #define [DAVIS\\_CONFIG\\_APS\\_END\\_COLUMN\\_2](#) 26
- #define [DAVIS\\_CONFIG\\_APS\\_END\\_ROW\\_2](#) 27
- #define [DAVIS\\_CONFIG\\_APS\\_START\\_COLUMN\\_3](#) 28
- #define [DAVIS\\_CONFIG\\_APS\\_START\\_ROW\\_3](#) 29
- #define [DAVIS\\_CONFIG\\_APS\\_END\\_COLUMN\\_3](#) 30
- #define [DAVIS\\_CONFIG\\_APS\\_END\\_ROW\\_3](#) 31
- #define [DAVIS\\_CONFIG\\_APS\\_HAS\\_EXTERNAL\\_ADC](#) 32
- #define [DAVIS\\_CONFIG\\_APS\\_HAS\\_INTERNAL\\_ADC](#) 33
- #define [DAVIS\\_CONFIG\\_APS\\_USE\\_INTERNAL\\_ADC](#) 34
- #define [DAVIS\\_CONFIG\\_APS\\_SAMPLE\\_ENABLE](#) 35
- #define [DAVIS\\_CONFIG\\_APS\\_SAMPLE\\_SETTLE](#) 36
- #define [DAVIS\\_CONFIG\\_APS\\_RAMP\\_RESET](#) 37
- #define [DAVIS\\_CONFIG\\_APS\\_RAMP\\_SHORT\\_RESET](#) 38
- #define [DAVIS\\_CONFIG\\_APS\\_ADC\\_TEST\\_MODE](#) 39
- #define [DAVISRGB\\_CONFIG\\_APS\\_TRANSFER](#) 50
- #define [DAVISRGB\\_CONFIG\\_APS\\_RSFDSETTLE](#) 51
- #define [DAVISRGB\\_CONFIG\\_APS\\_GSPDRESET](#) 52
- #define [DAVISRGB\\_CONFIG\\_APS\\_GSRESETFALL](#) 53
- #define [DAVISRGB\\_CONFIG\\_APS\\_GSTXFALL](#) 54
- #define [DAVISRGB\\_CONFIG\\_APS\\_GSFDRESET](#) 55
- #define [DAVIS\\_CONFIG\\_APS\\_SNAPSHOT](#) 80
- #define [DAVIS\\_CONFIG\\_IMU\\_RUN](#) 0
- #define [DAVIS\\_CONFIG\\_IMU\\_TEMP\\_STANDBY](#) 1
- #define [DAVIS\\_CONFIG\\_IMU\\_ACCEL\\_STANDBY](#) 2
- #define [DAVIS\\_CONFIG\\_IMU\\_GYRO\\_STANDBY](#) 3
- #define [DAVIS\\_CONFIG\\_IMU\\_LP\\_CYCLE](#) 4
- #define [DAVIS\\_CONFIG\\_IMU\\_LP\\_WAKEUP](#) 5
- #define [DAVIS\\_CONFIG\\_IMU\\_SAMPLE\\_RATE\\_DIVIDER](#) 6
- #define [DAVIS\\_CONFIG\\_IMU\\_DIGITAL\\_LOW\\_PASS\\_FILTER](#) 7
- #define [DAVIS\\_CONFIG\\_IMU\\_ACCEL\\_FULL\\_SCALE](#) 8
- #define [DAVIS\\_CONFIG\\_IMU\\_GYRO\\_FULL\\_SCALE](#) 9
- #define [DAVIS\\_CONFIG\\_EXTINPUT\\_RUN\\_DETECTOR](#) 0
- #define [DAVIS\\_CONFIG\\_EXTINPUT\\_DETECT\\_RISING\\_EDGES](#) 1
- #define [DAVIS\\_CONFIG\\_EXTINPUT\\_DETECT\\_FALLING\\_EDGES](#) 2
- #define [DAVIS\\_CONFIG\\_EXTINPUT\\_DETECT\\_PULSES](#) 3
- #define [DAVIS\\_CONFIG\\_EXTINPUT\\_DETECT\\_PULSE\\_POLARITY](#) 4
- #define [DAVIS\\_CONFIG\\_EXTINPUT\\_DETECT\\_PULSE\\_LENGTH](#) 5
- #define [DAVIS\\_CONFIG\\_EXTINPUT\\_HAS\\_GENERATOR](#) 6
- #define [DAVIS\\_CONFIG\\_EXTINPUT\\_RUN\\_GENERATOR](#) 7

- #define DAVIS\_CONFIG\_EXTINPUT\_GENERATE\_USE\_CUSTOM\_SIGNAL 8
- #define DAVIS\_CONFIG\_EXTINPUT\_GENERATE\_PULSE\_POLARITY 9
- #define DAVIS\_CONFIG\_EXTINPUT\_GENERATE\_PULSE\_INTERVAL 10
- #define DAVIS\_CONFIG\_EXTINPUT\_GENERATE\_PULSE\_LENGTH 11
- #define DAVIS\_CONFIG\_EXTINPUT\_GENERATE\_INJECT\_ON\_RISING\_EDGE 12
- #define DAVIS\_CONFIG\_EXTINPUT\_GENERATE\_INJECT\_ON\_FALLING\_EDGE 13
- #define DAVIS\_CONFIG\_EXTINPUT\_HAS\_EXTRA\_DETECTORS 14
- #define DAVIS\_CONFIG\_EXTINPUT\_RUN\_DETECTOR1 15
- #define DAVIS\_CONFIG\_EXTINPUT\_DETECT\_RISING\_EDGES1 16
- #define DAVIS\_CONFIG\_EXTINPUT\_DETECT\_FALLING\_EDGES1 17
- #define DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSES1 18
- #define DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_POLARITY1 19
- #define DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_LENGTH1 20
- #define DAVIS\_CONFIG\_EXTINPUT\_RUN\_DETECTOR2 21
- #define DAVIS\_CONFIG\_EXTINPUT\_DETECT\_RISING\_EDGES2 22
- #define DAVIS\_CONFIG\_EXTINPUT\_DETECT\_FALLING\_EDGES2 23
- #define DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSES2 24
- #define DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_POLARITY2 25
- #define DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_LENGTH2 26
- #define DAVIS\_CONFIG\_SYSINFO\_LOGIC\_VERSION 0
- #define DAVIS\_CONFIG\_SYSINFO\_CHIP\_IDENTIFIER 1
- #define DAVIS\_CONFIG\_SYSINFO\_DEVICE\_IS\_MASTER 2
- #define DAVIS\_CONFIG\_SYSINFO\_LOGIC\_CLOCK 3
- #define DAVIS\_CONFIG\_SYSINFO\_ADC\_CLOCK 4
- #define DAVIS\_CONFIG\_USB\_RUN 0
- #define DAVIS\_CONFIG\_USB\_EARLY\_PACKET\_DELAY 1

  

- #define IS\_DAVIS128(chipID) ((chipID) == DAVIS\_CHIP\_DAVIS128)
- #define IS\_DAVIS208(chipID) ((chipID) == DAVIS\_CHIP\_DAVIS208)
- #define IS\_DAVIS240A(chipID) ((chipID) == DAVIS\_CHIP\_DAVIS240A)
- #define IS\_DAVIS240B(chipID) ((chipID) == DAVIS\_CHIP\_DAVIS240B)
- #define IS\_DAVIS240C(chipID) ((chipID) == DAVIS\_CHIP\_DAVIS240C)
- #define IS\_DAVIS240(chipID) (IS\_DAVIS240A(chipID) || IS\_DAVIS240B(chipID) || IS\_DAVIS240C(chipID))
- #define IS\_DAVIS346A(chipID) ((chipID) == DAVIS\_CHIP\_DAVIS346A)
- #define IS\_DAVIS346B(chipID) ((chipID) == DAVIS\_CHIP\_DAVIS346B)
- #define IS\_DAVIS346C(chipID) ((chipID) == DAVIS\_CHIP\_DAVIS346C)
- #define IS\_DAVIS346(chipID) (IS\_DAVIS346A(chipID) || IS\_DAVIS346B(chipID) || IS\_DAVIS346C(chipID))
- #define IS\_DAVIS640(chipID) ((chipID) == DAVIS\_CHIP\_DAVIS640)
- #define IS\_DAVISRGB(chipID) ((chipID) == DAVIS\_CHIP\_DAVISRGB)

  

- #define DAVIS128\_CONFIG\_BIAS\_APSEVERFLOWLEVEL 0
- #define DAVIS128\_CONFIG\_BIAS\_APSCAS 1
- #define DAVIS128\_CONFIG\_BIAS\_ADCCREFHIGH 2
- #define DAVIS128\_CONFIG\_BIAS\_ADCCREFLOW 3
- #define DAVIS128\_CONFIG\_BIAS\_LOCALBUFBN 8
- #define DAVIS128\_CONFIG\_BIAS\_PADFOLLBN 9
- #define DAVIS128\_CONFIG\_BIAS\_DIFFBN 10
- #define DAVIS128\_CONFIG\_BIAS\_ONBN 11
- #define DAVIS128\_CONFIG\_BIAS\_OFFBN 12
- #define DAVIS128\_CONFIG\_BIAS\_PIXINBN 13
- #define DAVIS128\_CONFIG\_BIAS\_PRBP 14
- #define DAVIS128\_CONFIG\_BIAS\_PRFBP 15
- #define DAVIS128\_CONFIG\_BIAS\_REFRBP 16

- #define [DAVIS128\\_CONFIG\\_BIAS\\_READOUTBUFBP](#) 17
  - #define [DAVIS128\\_CONFIG\\_BIAS\\_APSROSFBP](#) 18
  - #define [DAVIS128\\_CONFIG\\_BIAS\\_ADCCOMPBP](#) 19
  - #define [DAVIS128\\_CONFIG\\_BIAS\\_COLSELLOWBN](#) 20
  - #define [DAVIS128\\_CONFIG\\_BIAS\\_DACBUFBP](#) 21
  - #define [DAVIS128\\_CONFIG\\_BIAS\\_LCOLTIMEOUTBN](#) 22
  - #define [DAVIS128\\_CONFIG\\_BIAS\\_AEPDBN](#) 23
  - #define [DAVIS128\\_CONFIG\\_BIAS\\_AEPUXBP](#) 24
  - #define [DAVIS128\\_CONFIG\\_BIAS\\_AEPUYBP](#) 25
  - #define [DAVIS128\\_CONFIG\\_BIAS\\_IFREFRBN](#) 26
  - #define [DAVIS128\\_CONFIG\\_BIAS\\_IFTHRBN](#) 27
  - #define [DAVIS128\\_CONFIG\\_BIAS\\_BIASBUFFER](#) 34
  - #define [DAVIS128\\_CONFIG\\_BIAS\\_SSP](#) 35
  - #define [DAVIS128\\_CONFIG\\_BIAS\\_SSN](#) 36
- 
- #define [DAVIS128\\_CONFIG\\_CHIP\\_DIGITALMUX0](#) 128
  - #define [DAVIS128\\_CONFIG\\_CHIP\\_DIGITALMUX1](#) 129
  - #define [DAVIS128\\_CONFIG\\_CHIP\\_DIGITALMUX2](#) 130
  - #define [DAVIS128\\_CONFIG\\_CHIP\\_DIGITALMUX3](#) 131
  - #define [DAVIS128\\_CONFIG\\_CHIP\\_ANALOGMUX0](#) 132
  - #define [DAVIS128\\_CONFIG\\_CHIP\\_ANALOGMUX1](#) 133
  - #define [DAVIS128\\_CONFIG\\_CHIP\\_ANALOGMUX2](#) 134
  - #define [DAVIS128\\_CONFIG\\_CHIP\\_BIASMUX0](#) 135
  - #define [DAVIS128\\_CONFIG\\_CHIP\\_RESETCALIBNEURON](#) 136
  - #define [DAVIS128\\_CONFIG\\_CHIP\\_TYPENCALIBNEURON](#) 137
  - #define [DAVIS128\\_CONFIG\\_CHIP\\_RESETTESTPIXEL](#) 138
  - #define [DAVIS128\\_CONFIG\\_CHIP\\_AERNAROW](#) 140
  - #define [DAVIS128\\_CONFIG\\_CHIP\\_USEAOUT](#) 141
  - #define [DAVIS128\\_CONFIG\\_CHIP\\_GLOBAL\\_SHUTTER](#) 142
  - #define [DAVIS128\\_CONFIG\\_CHIP\\_SELECTGRAYCOUNTER](#) 143
- 
- #define [DAVIS208\\_CONFIG\\_BIAS\\_APSEVERFLOWLEVEL](#) 0
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_APSCAS](#) 1
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_ADCREFHIGH](#) 2
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_ADCREFLOW](#) 3
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_RESETHIGHPASS](#) 6
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_REFSS](#) 7
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_LOCALBUFBN](#) 8
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_PADFOLLBN](#) 9
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_DIFFBN](#) 10
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_ONBN](#) 11
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_OFFBN](#) 12
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_PIXINVBN](#) 13
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_PRBP](#) 14
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_PRSFBP](#) 15
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_REFRBP](#) 16
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_READOUTBUFBP](#) 17
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_APSROSFBP](#) 18
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_ADCCOMPBP](#) 19
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_COLSELLOWBN](#) 20
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_DACBUFBP](#) 21
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_LCOLTIMEOUTBN](#) 22
  - #define [DAVIS208\\_CONFIG\\_BIAS\\_AEPDBN](#) 23

- #define [DAVIS208\\_CONFIG\\_BIAS\\_AEPUXBP](#) 24
- #define [DAVIS208\\_CONFIG\\_BIAS\\_AEPUYBP](#) 25
- #define [DAVIS208\\_CONFIG\\_BIAS\\_IFREFRBN](#) 26
- #define [DAVIS208\\_CONFIG\\_BIAS\\_IFTHRBN](#) 27
- #define [DAVIS208\\_CONFIG\\_BIAS\\_REGBIASBP](#) 28
- #define [DAVIS208\\_CONFIG\\_BIAS\\_REFSSBN](#) 30
- #define [DAVIS208\\_CONFIG\\_BIAS\\_BIASBUFFER](#) 34
- #define [DAVIS208\\_CONFIG\\_BIAS\\_SSP](#) 35
- #define [DAVIS208\\_CONFIG\\_BIAS\\_SSN](#) 36
  
- #define [DAVIS208\\_CONFIG\\_CHIP\\_DIGITALMUX0](#) 128
- #define [DAVIS208\\_CONFIG\\_CHIP\\_DIGITALMUX1](#) 129
- #define [DAVIS208\\_CONFIG\\_CHIP\\_DIGITALMUX2](#) 130
- #define [DAVIS208\\_CONFIG\\_CHIP\\_DIGITALMUX3](#) 131
- #define [DAVIS208\\_CONFIG\\_CHIP\\_ANALOGMUX0](#) 132
- #define [DAVIS208\\_CONFIG\\_CHIP\\_ANALOGMUX1](#) 133
- #define [DAVIS208\\_CONFIG\\_CHIP\\_ANALOGMUX2](#) 134
- #define [DAVIS208\\_CONFIG\\_CHIP\\_BIASMUX0](#) 135
- #define [DAVIS208\\_CONFIG\\_CHIP\\_RESETCALIBNEURON](#) 136
- #define [DAVIS208\\_CONFIG\\_CHIP\\_TYPENCALIBNEURON](#) 137
- #define [DAVIS208\\_CONFIG\\_CHIP\\_RESETTESTPIXEL](#) 138
- #define [DAVIS208\\_CONFIG\\_CHIP\\_AERNAROW](#) 140
- #define [DAVIS208\\_CONFIG\\_CHIP\\_USEAOUT](#) 141
- #define [DAVIS208\\_CONFIG\\_CHIP\\_GLOBAL\\_SHUTTER](#) 142
- #define [DAVIS208\\_CONFIG\\_CHIP\\_SELECTGRAYCOUNTER](#) 143
- #define [DAVIS208\\_CONFIG\\_CHIP\\_SELECTPREAMPVG](#) 145
- #define [DAVIS208\\_CONFIG\\_CHIP\\_SELECTBIASREFSS](#) 146
- #define [DAVIS208\\_CONFIG\\_CHIP\\_SELECTSENSE](#) 147
- #define [DAVIS208\\_CONFIG\\_CHIP\\_SELECTPOSFB](#) 148
- #define [DAVIS208\\_CONFIG\\_CHIP\\_SELECTHIGHPASS](#) 149
  
- #define [DAVIS240\\_CONFIG\\_BIAS\\_DIFFBN](#) 0
- #define [DAVIS240\\_CONFIG\\_BIAS\\_ONBN](#) 1
- #define [DAVIS240\\_CONFIG\\_BIAS\\_OFFBN](#) 2
- #define [DAVIS240\\_CONFIG\\_BIAS\\_APSCASEPC](#) 3
- #define [DAVIS240\\_CONFIG\\_BIAS\\_DIFFCASBNC](#) 4
- #define [DAVIS240\\_CONFIG\\_BIAS\\_APSROSFBN](#) 5
- #define [DAVIS240\\_CONFIG\\_BIAS\\_LOCALBUFBN](#) 6
- #define [DAVIS240\\_CONFIG\\_BIAS\\_PIXINVBN](#) 7
- #define [DAVIS240\\_CONFIG\\_BIAS\\_PRBP](#) 8
- #define [DAVIS240\\_CONFIG\\_BIAS\\_PRSFBP](#) 9
- #define [DAVIS240\\_CONFIG\\_BIAS\\_REFRBP](#) 10
- #define [DAVIS240\\_CONFIG\\_BIAS\\_AEPDBN](#) 11
- #define [DAVIS240\\_CONFIG\\_BIAS\\_LCOLTIMEOUTBN](#) 12
- #define [DAVIS240\\_CONFIG\\_BIAS\\_AEPUXBP](#) 13
- #define [DAVIS240\\_CONFIG\\_BIAS\\_AEPUYBP](#) 14
- #define [DAVIS240\\_CONFIG\\_BIAS\\_IFTHRBN](#) 15
- #define [DAVIS240\\_CONFIG\\_BIAS\\_IFREFRBN](#) 16
- #define [DAVIS240\\_CONFIG\\_BIAS\\_PADFOLLBN](#) 17
- #define [DAVIS240\\_CONFIG\\_BIAS\\_APSEVERFLOWLEVELBN](#) 18
- #define [DAVIS240\\_CONFIG\\_BIAS\\_BIASBUFFER](#) 19
- #define [DAVIS240\\_CONFIG\\_BIAS\\_SSP](#) 20
- #define [DAVIS240\\_CONFIG\\_BIAS\\_SSN](#) 21

- #define [DAVIS240\\_CONFIG\\_CHIP\\_DIGITALMUX0](#) 128
- #define [DAVIS240\\_CONFIG\\_CHIP\\_DIGITALMUX1](#) 129
- #define [DAVIS240\\_CONFIG\\_CHIP\\_DIGITALMUX2](#) 130
- #define [DAVIS240\\_CONFIG\\_CHIP\\_DIGITALMUX3](#) 131
- #define [DAVIS240\\_CONFIG\\_CHIP\\_ANALOGMUX0](#) 132
- #define [DAVIS240\\_CONFIG\\_CHIP\\_ANALOGMUX1](#) 133
- #define [DAVIS240\\_CONFIG\\_CHIP\\_ANALOGMUX2](#) 134
- #define [DAVIS240\\_CONFIG\\_CHIP\\_BIASMUX0](#) 135
- #define [DAVIS240\\_CONFIG\\_CHIP\\_RESETCALIBNEURON](#) 136
- #define [DAVIS240\\_CONFIG\\_CHIP\\_TYPENCALIBNEURON](#) 137
- #define [DAVIS240\\_CONFIG\\_CHIP\\_RESETTESTPIXEL](#) 138
- #define [DAVIS240\\_CONFIG\\_CHIP\\_SPECIALPIXELCONTROL](#) 139
- #define [DAVIS240\\_CONFIG\\_CHIP\\_AERNAROW](#) 140
- #define [DAVIS240\\_CONFIG\\_CHIP\\_USEAOUT](#) 141
- #define [DAVIS240\\_CONFIG\\_CHIP\\_GLOBAL\\_SHUTTER](#) 142

- #define [DAVIS346\\_CONFIG\\_BIAS\\_APSOEVERFLOWLEVEL](#) 0
- #define [DAVIS346\\_CONFIG\\_BIAS\\_APSCAS](#) 1
- #define [DAVIS346\\_CONFIG\\_BIAS\\_ADCREFHIGH](#) 2
- #define [DAVIS346\\_CONFIG\\_BIAS\\_ADCREFLOW](#) 3
- #define [DAVIS346\\_CONFIG\\_BIAS\\_ADCTESTVOLTAGE](#) 4
- #define [DAVIS346\\_CONFIG\\_BIAS\\_LOCALBUFBN](#) 8
- #define [DAVIS346\\_CONFIG\\_BIAS\\_PADFOLLBN](#) 9
- #define [DAVIS346\\_CONFIG\\_BIAS\\_DIFFBN](#) 10
- #define [DAVIS346\\_CONFIG\\_BIAS\\_ONBN](#) 11
- #define [DAVIS346\\_CONFIG\\_BIAS\\_OFFBN](#) 12
- #define [DAVIS346\\_CONFIG\\_BIAS\\_PIXINBN](#) 13
- #define [DAVIS346\\_CONFIG\\_BIAS\\_PRBP](#) 14
- #define [DAVIS346\\_CONFIG\\_BIAS\\_PRSEFBP](#) 15
- #define [DAVIS346\\_CONFIG\\_BIAS\\_REFRBP](#) 16
- #define [DAVIS346\\_CONFIG\\_BIAS\\_READOUTBUFBP](#) 17
- #define [DAVIS346\\_CONFIG\\_BIAS\\_APSROSEFBN](#) 18
- #define [DAVIS346\\_CONFIG\\_BIAS\\_ADCCOMPBP](#) 19
- #define [DAVIS346\\_CONFIG\\_BIAS\\_COLSELLOWBN](#) 20
- #define [DAVIS346\\_CONFIG\\_BIAS\\_DACBUFBP](#) 21
- #define [DAVIS346\\_CONFIG\\_BIAS\\_LCOLTIMEOUTBN](#) 22
- #define [DAVIS346\\_CONFIG\\_BIAS\\_AEPDBN](#) 23
- #define [DAVIS346\\_CONFIG\\_BIAS\\_AEPUXBP](#) 24
- #define [DAVIS346\\_CONFIG\\_BIAS\\_AEPUYBP](#) 25
- #define [DAVIS346\\_CONFIG\\_BIAS\\_IFREFRBN](#) 26
- #define [DAVIS346\\_CONFIG\\_BIAS\\_IFTHRBN](#) 27
- #define [DAVIS346\\_CONFIG\\_BIAS\\_BIASBUFFER](#) 34
- #define [DAVIS346\\_CONFIG\\_BIAS\\_SSP](#) 35
- #define [DAVIS346\\_CONFIG\\_BIAS\\_SSN](#) 36

- #define [DAVIS346\\_CONFIG\\_CHIP\\_DIGITALMUX0](#) 128
- #define [DAVIS346\\_CONFIG\\_CHIP\\_DIGITALMUX1](#) 129
- #define [DAVIS346\\_CONFIG\\_CHIP\\_DIGITALMUX2](#) 130
- #define [DAVIS346\\_CONFIG\\_CHIP\\_DIGITALMUX3](#) 131
- #define [DAVIS346\\_CONFIG\\_CHIP\\_ANALOGMUX0](#) 132
- #define [DAVIS346\\_CONFIG\\_CHIP\\_ANALOGMUX1](#) 133
- #define [DAVIS346\\_CONFIG\\_CHIP\\_ANALOGMUX2](#) 134
- #define [DAVIS346\\_CONFIG\\_CHIP\\_BIASMUX0](#) 135

- `#define DAVIS346_CONFIG_CHIP_RESETCALIBNEURON` 136
- `#define DAVIS346_CONFIG_CHIP_TYPENCALIBNEURON` 137
- `#define DAVIS346_CONFIG_CHIP_RESETTESTPIXEL` 138
- `#define DAVIS346_CONFIG_CHIP_AERNAROW` 140
- `#define DAVIS346_CONFIG_CHIP_USEAOUT` 141
- `#define DAVIS346_CONFIG_CHIP_GLOBAL_SHUTTER` 142
- `#define DAVIS346_CONFIG_CHIP_SELECTGRAYCOUNTER` 143
- `#define DAVIS346_CONFIG_CHIP_TESTADC` 144

- `#define DAVIS640_CONFIG_BIAS_APSOEVERFLOWLEVEL` 0
- `#define DAVIS640_CONFIG_BIAS_APSCAS` 1
- `#define DAVIS640_CONFIG_BIAS_ADCREFHIGH` 2
- `#define DAVIS640_CONFIG_BIAS_ADCREFLOW` 3
- `#define DAVIS640_CONFIG_BIAS_ADCTESTVOLTAGE` 4
- `#define DAVIS640_CONFIG_BIAS_LOCALBUFBN` 8
- `#define DAVIS640_CONFIG_BIAS_PADFOLLBN` 9
- `#define DAVIS640_CONFIG_BIAS_DIFFBN` 10
- `#define DAVIS640_CONFIG_BIAS_ONBN` 11
- `#define DAVIS640_CONFIG_BIAS_OFFBN` 12
- `#define DAVIS640_CONFIG_BIAS_PIXINBN` 13
- `#define DAVIS640_CONFIG_BIAS_PRBP` 14
- `#define DAVIS640_CONFIG_BIAS_PRSBP` 15
- `#define DAVIS640_CONFIG_BIAS_REFRBP` 16
- `#define DAVIS640_CONFIG_BIAS_READOUTBUFBP` 17
- `#define DAVIS640_CONFIG_BIAS_APSROSFBP` 18
- `#define DAVIS640_CONFIG_BIAS_ADCCOMPBP` 19
- `#define DAVIS640_CONFIG_BIAS_COLSELLOWBN` 20
- `#define DAVIS640_CONFIG_BIAS_DACBUFBP` 21
- `#define DAVIS640_CONFIG_BIAS_LCOLTIMEOUTBN` 22
- `#define DAVIS640_CONFIG_BIAS_AEPDBN` 23
- `#define DAVIS640_CONFIG_BIAS_AEPUXBP` 24
- `#define DAVIS640_CONFIG_BIAS_AEPUYBP` 25
- `#define DAVIS640_CONFIG_BIAS_IFREFRBN` 26
- `#define DAVIS640_CONFIG_BIAS_IFTHRBN` 27
- `#define DAVIS640_CONFIG_BIAS_BIASBUFFER` 34
- `#define DAVIS640_CONFIG_BIAS_SSP` 35
- `#define DAVIS640_CONFIG_BIAS_SSN` 36

- `#define DAVIS640_CONFIG_CHIP_DIGITALMUX0` 128
- `#define DAVIS640_CONFIG_CHIP_DIGITALMUX1` 129
- `#define DAVIS640_CONFIG_CHIP_DIGITALMUX2` 130
- `#define DAVIS640_CONFIG_CHIP_DIGITALMUX3` 131
- `#define DAVIS640_CONFIG_CHIP_ANALOGMUX0` 132
- `#define DAVIS640_CONFIG_CHIP_ANALOGMUX1` 133
- `#define DAVIS640_CONFIG_CHIP_ANALOGMUX2` 134
- `#define DAVIS640_CONFIG_CHIP_BIASMUX0` 135
- `#define DAVIS640_CONFIG_CHIP_RESETCALIBNEURON` 136
- `#define DAVIS640_CONFIG_CHIP_TYPENCALIBNEURON` 137
- `#define DAVIS640_CONFIG_CHIP_RESETTESTPIXEL` 138
- `#define DAVIS640_CONFIG_CHIP_AERNAROW` 140
- `#define DAVIS640_CONFIG_CHIP_USEAOUT` 141
- `#define DAVIS640_CONFIG_CHIP_GLOBAL_SHUTTER` 142
- `#define DAVIS640_CONFIG_CHIP_SELECTGRAYCOUNTER` 143



- #define [DAVIS640\\_CONFIG\\_CHIP\\_TESTADC](#) 144
  
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_APSCAS](#) 0
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_OVG1LO](#) 1
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_OVG2LO](#) 2
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_TX2OVG2HI](#) 3
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_GND07](#) 4
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_ADCTESTVOLTAGE](#) 5
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_ADCREFHIGH](#) 6
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_ADCREFLOW](#) 7
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_IFREFRBN](#) 8
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_IFTHRBN](#) 9
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_LOCALBUFBN](#) 10
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_PADFOLLBN](#) 11
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_PIXINBN](#) 13
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_DIFFBN](#) 14
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_ONBN](#) 15
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_OFFBN](#) 16
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_PRBP](#) 17
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_PRSBP](#) 18
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_REFRBP](#) 19
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_ARRAYBIASBUFFERBN](#) 20
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_ARRAYLOGICBUFFERBN](#) 22
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_FALLTIMEBN](#) 23
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_RISETIMEBP](#) 24
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_READOUTBUFBP](#) 25
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_APSROSFBP](#) 26
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_ADCCOMPBP](#) 27
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_DACBUFBP](#) 28
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_LCOLTIMEOUTBN](#) 30
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_AEPDBN](#) 31
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_AEPUXBP](#) 32
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_AEPUYBP](#) 33
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_BIASBUFFER](#) 34
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_SSP](#) 35
- #define [DAVISRGB\\_CONFIG\\_BIAS\\_SSN](#) 36
  
- #define [DAVISRGB\\_CONFIG\\_CHIP\\_DIGITALMUX0](#) 128
- #define [DAVISRGB\\_CONFIG\\_CHIP\\_DIGITALMUX1](#) 129
- #define [DAVISRGB\\_CONFIG\\_CHIP\\_DIGITALMUX2](#) 130
- #define [DAVISRGB\\_CONFIG\\_CHIP\\_DIGITALMUX3](#) 131
- #define [DAVISRGB\\_CONFIG\\_CHIP\\_ANALOGMUX0](#) 132
- #define [DAVISRGB\\_CONFIG\\_CHIP\\_ANALOGMUX1](#) 133
- #define [DAVISRGB\\_CONFIG\\_CHIP\\_ANALOGMUX2](#) 134
- #define [DAVISRGB\\_CONFIG\\_CHIP\\_BIASMUX0](#) 135
- #define [DAVISRGB\\_CONFIG\\_CHIP\\_RESETCALIBNEURON](#) 136
- #define [DAVISRGB\\_CONFIG\\_CHIP\\_TYPCALIBNEURON](#) 137
- #define [DAVISRGB\\_CONFIG\\_CHIP\\_RESETTESTPIXEL](#) 138
- #define [DAVISRGB\\_CONFIG\\_CHIP\\_AERNAROW](#) 140
- #define [DAVISRGB\\_CONFIG\\_CHIP\\_USEAOUT](#) 141
- #define [DAVISRGB\\_CONFIG\\_CHIP\\_SELECTGRAYCOUNTER](#) 143
- #define [DAVISRGB\\_CONFIG\\_CHIP\\_TESTADC](#) 144
- #define [DAVISRGB\\_CONFIG\\_CHIP\\_ADJUSTOVG1LO](#) 145
- #define [DAVISRGB\\_CONFIG\\_CHIP\\_ADJUSTOVG2LO](#) 146
- #define [DAVISRGB\\_CONFIG\\_CHIP\\_ADJUSTTX2OVG2HI](#) 147

## Enumerations

- enum `caer_bias_shiftedsource_operating_mode` { `SHIFTED_SOURCE` = 0, `HI_Z` = 1, `TIED_TO_RAIL` = 2 }
- enum `caer_bias_shiftedsource_voltage_level` { `SPLIT_GATE` = 0, `SINGLE_DIODE` = 1, `DOUBLE_DIODE` = 2 }

## Functions

- struct `caer_davis_info` `caerDavisInfoGet` (`caerDeviceHandle` handle)
- uint16\_t `caerBiasVDACGenerate` (struct `caer_bias_vdac` vdacBias)
- struct `caer_bias_vdac` `caerBiasVDACParse` (uint16\_t vdacBias)
- uint16\_t `caerBiasCoarseFineGenerate` (struct `caer_bias_coarsefine` coarseFineBias)
- struct `caer_bias_coarsefine` `caerBiasCoarseFineParse` (uint16\_t coarseFineBias)
- uint16\_t `caerBiasShiftedSourceGenerate` (struct `caer_bias_shiftedsource` shiftedSourceBias)
- struct `caer_bias_shiftedsource` `caerBiasShiftedSourceParse` (uint16\_t shiftedSourceBias)

### 4.1.1 Detailed Description

DAVIS specific configuration defines and information structures.

### 4.1.2 Macro Definition Documentation

#### 4.1.2.1 #define CAER\_DEVICE\_DAVIS\_FX2 1

Device type definition for iniLabs DAVIS FX2-based boards, like DAVIS240a/b/c.

#### 4.1.2.2 #define CAER\_DEVICE\_DAVIS\_FX3 2

Device type definition for iniLabs DAVIS FX3-based boards, like DAVIS640.

#### 4.1.2.3 #define DAVIS128\_CONFIG\_BIAS\_ADCCOMPBP 19

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.



#### 4.1.2.4 #define DAVIS128\_CONFIG\_BIAS\_ADCREFHIGH 2

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.5 #define DAVIS128\_CONFIG\_BIAS\_ADCREFLOW 3

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.6 #define DAVIS128\_CONFIG\_BIAS\_AEPDBN 23

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.7 #define DAVIS128\_CONFIG\_BIAS\_AEPUXBP 24

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.8 `#define DAVIS128_CONFIG_BIAS_AEPUYBP 25`

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.9 `#define DAVIS128_CONFIG_BIAS_APSCAS 1`

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.10 `#define DAVIS128_CONFIG_BIAS_APSOVERFLOWLEVEL 0`

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.11 `#define DAVIS128_CONFIG_BIAS_APSROSFBN 18`

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.12 `#define DAVIS128_CONFIG_BIAS_BIASBUFFER 34`

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.13 `#define DAVIS128_CONFIG_BIAS_COLSELLOWBN 20`

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.14 `#define DAVIS128_CONFIG_BIAS_DACBUFBP 21`

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.15 `#define DAVIS128_CONFIG_BIAS_DIFFBN 10`

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.16 `#define DAVIS128_CONFIG_BIAS_IFREFRBN 26`

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.17 `#define DAVIS128_CONFIG_BIAS_IFTHRBN 27`

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.18 `#define DAVIS128_CONFIG_BIAS_LCOLTIMEOUTBN 22`

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.19 `#define DAVIS128_CONFIG_BIAS_LOCALBUFBN 8`

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.20 #define DAVIS128\_CONFIG\_BIAS\_OFFBN 12

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.21 #define DAVIS128\_CONFIG\_BIAS\_ONBN 11

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.22 #define DAVIS128\_CONFIG\_BIAS\_PADFOLLBN 9

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.23 #define DAVIS128\_CONFIG\_BIAS\_PIXINBN 13

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.24 `#define DAVIS128_CONFIG_BIAS_PRBP 14`

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.25 `#define DAVIS128_CONFIG_BIAS_PRSFBP 15`

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.26 `#define DAVIS128_CONFIG_BIAS_READOUTBUFBP 17`

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.27 `#define DAVIS128_CONFIG_BIAS_REFRBP 16`

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.28 #define DAVIS128\_CONFIG\_BIAS\_SSN 36

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.29 #define DAVIS128\_CONFIG\_BIAS\_SSP 35

Parameter address for module DAVIS128\_CONFIG\_BIAS: DAVIS128 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.30 #define DAVIS128\_CONFIG\_CHIP\_AERNAROW 140

Parameter address for module DAVIS128\_CONFIG\_CHIP: DAVIS128 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.31 #define DAVIS128\_CONFIG\_CHIP\_ANALOGMUX0 132

Parameter address for module DAVIS128\_CONFIG\_CHIP: DAVIS128 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.32 #define DAVIS128\_CONFIG\_CHIP\_ANALOGMUX1 133

Parameter address for module DAVIS128\_CONFIG\_CHIP: DAVIS128 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.33 #define DAVIS128\_CONFIG\_CHIP\_ANALOGMUX2 134

Parameter address for module DAVIS128\_CONFIG\_CHIP: DAVIS128 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.34 `#define DAVIS128_CONFIG_CHIP_BIASMUX0` 135

Parameter address for module DAVIS128\_CONFIG\_CHIP: DAVIS128 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.35 `#define DAVIS128_CONFIG_CHIP_DIGITALMUX0` 128

Parameter address for module DAVIS128\_CONFIG\_CHIP: DAVIS128 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.36 `#define DAVIS128_CONFIG_CHIP_DIGITALMUX1` 129

Parameter address for module DAVIS128\_CONFIG\_CHIP: DAVIS128 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.37 `#define DAVIS128_CONFIG_CHIP_DIGITALMUX2` 130

Parameter address for module DAVIS128\_CONFIG\_CHIP: DAVIS128 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.38 `#define DAVIS128_CONFIG_CHIP_DIGITALMUX3` 131

Parameter address for module DAVIS128\_CONFIG\_CHIP: DAVIS128 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.39 `#define DAVIS128_CONFIG_CHIP_GLOBAL_SHUTTER` 142

Parameter address for module DAVIS128\_CONFIG\_CHIP: DAVIS128 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.40 `#define DAVIS128_CONFIG_CHIP_RESETCALIBNEURON` 136

Parameter address for module DAVIS128\_CONFIG\_CHIP: DAVIS128 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.41 `#define DAVIS128_CONFIG_CHIP_RESETTESTPIXEL` 138

Parameter address for module DAVIS128\_CONFIG\_CHIP: DAVIS128 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.



#### 4.1.2.42 `#define DAVIS128_CONFIG_CHIP_SELECTGRAYCOUNTER 143`

Parameter address for module DAVIS128\_CONFIG\_CHIP: DAVIS128 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.43 `#define DAVIS128_CONFIG_CHIP_TYPENCALIBNEURON 137`

Parameter address for module DAVIS128\_CONFIG\_CHIP: DAVIS128 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.44 `#define DAVIS128_CONFIG_CHIP_USEAOUT 141`

Parameter address for module DAVIS128\_CONFIG\_CHIP: DAVIS128 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.45 `#define DAVIS208_CONFIG_BIAS_ADCCOMPBP 19`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.46 `#define DAVIS208_CONFIG_BIAS_ADCREFHIGH 2`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.47 `#define DAVIS208_CONFIG_BIAS_ADCREFLOW 3`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.48 `#define DAVIS208_CONFIG_BIAS_AEPDBN 23`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.49 `#define DAVIS208_CONFIG_BIAS_AEPUXBP 24`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.50 `#define DAVIS208_CONFIG_BIAS_AEPUYBP 25`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.51 `#define DAVIS208_CONFIG_BIAS_APSCAS 1`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.52 `#define DAVIS208_CONFIG_BIAS_APSOVERFLOWLEVEL 0`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.53 `#define DAVIS208_CONFIG_BIAS_APSROSFBN 18`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.54 `#define DAVIS208_CONFIG_BIAS_BIASBUFFER 34`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.55 `#define DAVIS208_CONFIG_BIAS_COLSELOWBN 20`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.56 `#define DAVIS208_CONFIG_BIAS_DACBUFBP 21`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.57 `#define DAVIS208_CONFIG_BIAS_DIFFBN 10`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.58 `#define DAVIS208_CONFIG_BIAS_IFREFRBN 26`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.59 `#define DAVIS208_CONFIG_BIAS_IFTHRBN 27`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.60 `#define DAVIS208_CONFIG_BIAS_LCOLTIMEOUTBN 22`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.61 `#define DAVIS208_CONFIG_BIAS_LOCALBUFBN 8`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.62 `#define DAVIS208_CONFIG_BIAS_OFFBN 12`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.63 `#define DAVIS208_CONFIG_BIAS_ONBN 11`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.64 `#define DAVIS208_CONFIG_BIAS_PADFOLLBN 9`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.65 `#define DAVIS208_CONFIG_BIAS_PIXINVB 13`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.66 `#define DAVIS208_CONFIG_BIAS_PRBP 14`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.67 `#define DAVIS208_CONFIG_BIAS_PRFBP 15`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.68 #define DAVIS208\_CONFIG\_BIAS\_READOUTBUFBP 17

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.69 #define DAVIS208\_CONFIG\_BIAS\_REFRBP 16

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.70 #define DAVIS208\_CONFIG\_BIAS\_REFSS 7

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.71 #define DAVIS208\_CONFIG\_BIAS\_REFSSBN 30

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.72 `#define DAVIS208_CONFIG_BIAS_REGBIASBP 28`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [`caerBiasVDACGenerate\(\)`](#) for VDAC (voltage) biases.
- [`caerBiasCoarseFineGenerate\(\)`](#) for coarse-fine (current) biases.
- [`caerBiasShiftedSourceGenerate\(\)`](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.73 `#define DAVIS208_CONFIG_BIAS_RESETHIGHPASS 6`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [`caerBiasVDACGenerate\(\)`](#) for VDAC (voltage) biases.
- [`caerBiasCoarseFineGenerate\(\)`](#) for coarse-fine (current) biases.
- [`caerBiasShiftedSourceGenerate\(\)`](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.74 `#define DAVIS208_CONFIG_BIAS_SSN 36`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [`caerBiasVDACGenerate\(\)`](#) for VDAC (voltage) biases.
- [`caerBiasCoarseFineGenerate\(\)`](#) for coarse-fine (current) biases.
- [`caerBiasShiftedSourceGenerate\(\)`](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.75 `#define DAVIS208_CONFIG_BIAS_SSP 35`

Parameter address for module DAVIS208\_CONFIG\_BIAS: DAVIS208 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [`caerBiasVDACGenerate\(\)`](#) for VDAC (voltage) biases.
- [`caerBiasCoarseFineGenerate\(\)`](#) for coarse-fine (current) biases.
- [`caerBiasShiftedSourceGenerate\(\)`](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.



**4.1.2.76 #define DAVIS208\_CONFIG\_CHIP\_AERNAROW 140**

Parameter address for module DAVIS208\_CONFIG\_CHIP: DAVIS208 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.77 #define DAVIS208\_CONFIG\_CHIP\_ANALOGMUX0 132**

Parameter address for module DAVIS208\_CONFIG\_CHIP: DAVIS208 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.78 #define DAVIS208\_CONFIG\_CHIP\_ANALOGMUX1 133**

Parameter address for module DAVIS208\_CONFIG\_CHIP: DAVIS208 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.79 #define DAVIS208\_CONFIG\_CHIP\_ANALOGMUX2 134**

Parameter address for module DAVIS208\_CONFIG\_CHIP: DAVIS208 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.80 #define DAVIS208\_CONFIG\_CHIP\_BIASMUX0 135**

Parameter address for module DAVIS208\_CONFIG\_CHIP: DAVIS208 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.81 #define DAVIS208\_CONFIG\_CHIP\_DIGITALMUX0 128**

Parameter address for module DAVIS208\_CONFIG\_CHIP: DAVIS208 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.82 #define DAVIS208\_CONFIG\_CHIP\_DIGITALMUX1 129**

Parameter address for module DAVIS208\_CONFIG\_CHIP: DAVIS208 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.83 #define DAVIS208\_CONFIG\_CHIP\_DIGITALMUX2 130**

Parameter address for module DAVIS208\_CONFIG\_CHIP: DAVIS208 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.84 #define DAVIS208\_CONFIG\_CHIP\_DIGITALMUX3 131**

Parameter address for module DAVIS208\_CONFIG\_CHIP: DAVIS208 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.85 #define DAVIS208\_CONFIG\_CHIP\_GLOBAL\_SHUTTER 142**

Parameter address for module DAVIS208\_CONFIG\_CHIP: DAVIS208 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.86 #define DAVIS208\_CONFIG\_CHIP\_RESETCALIBNEURON 136**

Parameter address for module DAVIS208\_CONFIG\_CHIP: DAVIS208 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.87 #define DAVIS208\_CONFIG\_CHIP\_RESETESTPIXEL 138**

Parameter address for module DAVIS208\_CONFIG\_CHIP: DAVIS208 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.88 #define DAVIS208\_CONFIG\_CHIP\_SELECTBIASREFSS 146**

Parameter address for module DAVIS208\_CONFIG\_CHIP: DAVIS208 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.89 #define DAVIS208\_CONFIG\_CHIP\_SELECTGRAYCOUNTER 143**

Parameter address for module DAVIS208\_CONFIG\_CHIP: DAVIS208 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.90 #define DAVIS208\_CONFIG\_CHIP\_SELECTHIGHPASS 149**

Parameter address for module DAVIS208\_CONFIG\_CHIP: DAVIS208 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.91 #define DAVIS208\_CONFIG\_CHIP\_SELECTPOSB 148**

Parameter address for module DAVIS208\_CONFIG\_CHIP: DAVIS208 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.92 `#define DAVIS208_CONFIG_CHIP_SELECTPREAMPAVG 145`

Parameter address for module DAVIS208\_CONFIG\_CHIP: DAVIS208 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.93 `#define DAVIS208_CONFIG_CHIP_SELECTSENSE 147`

Parameter address for module DAVIS208\_CONFIG\_CHIP: DAVIS208 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.94 `#define DAVIS208_CONFIG_CHIP_TYPENCALIBNEURON 137`

Parameter address for module DAVIS208\_CONFIG\_CHIP: DAVIS208 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.95 `#define DAVIS208_CONFIG_CHIP_USEAOUT 141`

Parameter address for module DAVIS208\_CONFIG\_CHIP: DAVIS208 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.96 `#define DAVIS240_CONFIG_BIAS_AEPDBN 11`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.97 `#define DAVIS240_CONFIG_BIAS_AEPUXBP 13`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.98 `#define DAVIS240_CONFIG_BIAS_AEPUYBP 14`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.99 `#define DAVIS240_CONFIG_BIAS_APSCASEPC 3`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.100 `#define DAVIS240_CONFIG_BIAS_APSOEVERFLOWLEVELBN 18`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.101 `#define DAVIS240_CONFIG_BIAS_APSROSFBN 5`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.102 `#define DAVIS240_CONFIG_BIAS_BIASBUFFER 19`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.103 `#define DAVIS240_CONFIG_BIAS_DIFFBN 0`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.104 `#define DAVIS240_CONFIG_BIAS_DIFFCASN 4`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.105 `#define DAVIS240_CONFIG_BIAS_IFREFRBN 16`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.106 `#define DAVIS240_CONFIG_BIAS_IFTHRBN 15`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.107 `#define DAVIS240_CONFIG_BIAS_LCOLTIMEOUTBN 12`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.108 `#define DAVIS240_CONFIG_BIAS_LOCALBUFBN 6`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.109 `#define DAVIS240_CONFIG_BIAS_OFFBN 2`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.110 `#define DAVIS240_CONFIG_BIAS_ONBN 1`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.111 `#define DAVIS240_CONFIG_BIAS_PADFOLLBN 17`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.112 `#define DAVIS240_CONFIG_BIAS_PIXINBN 7`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.113 `#define DAVIS240_CONFIG_BIAS_PRBP 8`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.114 `#define DAVIS240_CONFIG_BIAS_PRSFBP 9`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.115 `#define DAVIS240_CONFIG_BIAS_REFRBP 10`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.116 `#define DAVIS240_CONFIG_BIAS_SSN 21`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.117 `#define DAVIS240_CONFIG_BIAS_SSP 20`

Parameter address for module DAVIS240\_CONFIG\_BIAS: DAVIS240chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.118 `#define DAVIS240_CONFIG_CHIP_AERNAROW 140`

Parameter address for module DAVIS240\_CONFIG\_CHIP: DAVIS240 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead. On DAVIS240B cameras, DAVIS240\_CONFIG\_CHIP\_SPECIALPIXELCONTROL can be used to enable the test pixel array.

#### 4.1.2.119 `#define DAVIS240_CONFIG_CHIP_ANALOGMUX0 132`

Parameter address for module DAVIS240\_CONFIG\_CHIP: DAVIS240 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead. On DAVIS240B cameras, DAVIS240\_CONFIG\_CHIP\_SPECIALPIXELCONTROL can be used to enable the test pixel array.

#### 4.1.2.120 `#define DAVIS240_CONFIG_CHIP_ANALOGMUX1 133`

Parameter address for module DAVIS240\_CONFIG\_CHIP: DAVIS240 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead. On DAVIS240B cameras, DAVIS240\_CONFIG\_CHIP\_SPECIALPIXELCONTROL can be used to enable the test pixel array.

#### 4.1.2.121 `#define DAVIS240_CONFIG_CHIP_ANALOGMUX2 134`

Parameter address for module DAVIS240\_CONFIG\_CHIP: DAVIS240 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead. On DAVIS240B cameras, DAVIS240\_CONFIG\_CHIP\_SPECIALPIXELCONTROL can be used to enable the test pixel array.

#### 4.1.2.122 `#define DAVIS240_CONFIG_CHIP_BIASMUX0 135`

Parameter address for module DAVIS240\_CONFIG\_CHIP: DAVIS240 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead. On DAVIS240B cameras, DAVIS240\_CONFIG\_CHIP\_SPECIALPIXELCONTROL can be used to enable the test pixel array.

#### 4.1.2.123 `#define DAVIS240_CONFIG_CHIP_DIGITALMUX0 128`

Parameter address for module DAVIS240\_CONFIG\_CHIP: DAVIS240 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead. On DAVIS240B cameras, DAVIS240\_CONFIG\_CHIP\_SPECIALPIXELCONTROL can be used to enable the test pixel array.

#### 4.1.2.124 `#define DAVIS240_CONFIG_CHIP_DIGITALMUX1 129`

Parameter address for module DAVIS240\_CONFIG\_CHIP: DAVIS240 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead. On DAVIS240B cameras, DAVIS240\_CONFIG\_CHIP\_SPECIALPIXELCONTROL can be used to enable the test pixel array.



**4.1.2.125 #define DAVIS240\_CONFIG\_CHIP\_DIGITALMUX2 130**

Parameter address for module DAVIS240\_CONFIG\_CHIP: DAVIS240 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead. On DAVIS240B cameras, DAVIS240\_CONFIG\_CHIP\_SPECIALPIXELCONTROL can be used to enable the test pixel array.

**4.1.2.126 #define DAVIS240\_CONFIG\_CHIP\_DIGITALMUX3 131**

Parameter address for module DAVIS240\_CONFIG\_CHIP: DAVIS240 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead. On DAVIS240B cameras, DAVIS240\_CONFIG\_CHIP\_SPECIALPIXELCONTROL can be used to enable the test pixel array.

**4.1.2.127 #define DAVIS240\_CONFIG\_CHIP\_GLOBAL\_SHUTTER 142**

Parameter address for module DAVIS240\_CONFIG\_CHIP: DAVIS240 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead. On DAVIS240B cameras, DAVIS240\_CONFIG\_CHIP\_SPECIALPIXELCONTROL can be used to enable the test pixel array.

**4.1.2.128 #define DAVIS240\_CONFIG\_CHIP\_RESETCALIBNEURON 136**

Parameter address for module DAVIS240\_CONFIG\_CHIP: DAVIS240 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead. On DAVIS240B cameras, DAVIS240\_CONFIG\_CHIP\_SPECIALPIXELCONTROL can be used to enable the test pixel array.

**4.1.2.129 #define DAVIS240\_CONFIG\_CHIP\_RESETTESTPIXEL 138**

Parameter address for module DAVIS240\_CONFIG\_CHIP: DAVIS240 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead. On DAVIS240B cameras, DAVIS240\_CONFIG\_CHIP\_SPECIALPIXELCONTROL can be used to enable the test pixel array.

**4.1.2.130 #define DAVIS240\_CONFIG\_CHIP\_SPECIALPIXELCONTROL 139**

Parameter address for module DAVIS240\_CONFIG\_CHIP: DAVIS240 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead. On DAVIS240B cameras, DAVIS240\_CONFIG\_CHIP\_SPECIALPIXELCONTROL can be used to enable the test pixel array.

**4.1.2.131 #define DAVIS240\_CONFIG\_CHIP\_TYPENCALIBNEURON 137**

Parameter address for module DAVIS240\_CONFIG\_CHIP: DAVIS240 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead. On DAVIS240B cameras, DAVIS240\_CONFIG\_CHIP\_SPECIALPIXELCONTROL can be used to enable the test pixel array.

#### 4.1.2.132 `#define DAVIS240_CONFIG_CHIP_USEAOUT 141`

Parameter address for module DAVIS240\_CONFIG\_CHIP: DAVIS240 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead. On DAVIS240B cameras, DAVIS240\_CONFIG\_CHIP\_SPECIALPIXELCONTROL can be used to enable the test pixel array.

#### 4.1.2.133 `#define DAVIS346_CONFIG_BIAS_ADCCOMPBP 19`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.134 `#define DAVIS346_CONFIG_BIAS_ADCREFHIGH 2`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.135 `#define DAVIS346_CONFIG_BIAS_ADCREFLOW 3`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.136 `#define DAVIS346_CONFIG_BIAS_ADCTESTVOLTAGE 4`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.137 `#define DAVIS346_CONFIG_BIAS_AEPDBN 23`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.138 `#define DAVIS346_CONFIG_BIAS_AEPUXBP 24`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.139 `#define DAVIS346_CONFIG_BIAS_AEPUYBP 25`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.140 `#define DAVIS346_CONFIG_BIAS_APSCAS 1`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.141 `#define DAVIS346_CONFIG_BIAS_APSOVERFLOWLEVEL 0`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.142 `#define DAVIS346_CONFIG_BIAS_APSROSFBN 18`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.143 `#define DAVIS346_CONFIG_BIAS_BIASBUFFER 34`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.144 `#define DAVIS346_CONFIG_BIAS_COLSELOWBN 20`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.145 `#define DAVIS346_CONFIG_BIAS_DACBUFBP 21`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.146 `#define DAVIS346_CONFIG_BIAS_DIFFBN 10`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.147 `#define DAVIS346_CONFIG_BIAS_IFREFRBN 26`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.148 `#define DAVIS346_CONFIG_BIAS_IFTHRBN 27`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.149 `#define DAVIS346_CONFIG_BIAS_LCOLTIMEOUTBN 22`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.150 `#define DAVIS346_CONFIG_BIAS_LOCALBUFBN 8`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.151 `#define DAVIS346_CONFIG_BIAS_OFFBN 12`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.152 `#define DAVIS346_CONFIG_BIAS_ONBN 11`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.153 `#define DAVIS346_CONFIG_BIAS_PADFOLLBN 9`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.154 `#define DAVIS346_CONFIG_BIAS_PIXINBN 13`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.155 `#define DAVIS346_CONFIG_BIAS_PRBP 14`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.156 `#define DAVIS346_CONFIG_BIAS_PRSFBP 15`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.157 `#define DAVIS346_CONFIG_BIAS_READOUTBUFBP 17`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.158 `#define DAVIS346_CONFIG_BIAS_REFRBP 16`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.159 `#define DAVIS346_CONFIG_BIAS_SSN 36`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.160 `#define DAVIS346_CONFIG_BIAS_SSP 35`

Parameter address for module DAVIS346\_CONFIG\_BIAS: DAVIS346 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.



**4.1.2.161 #define DAVIS346\_CONFIG\_CHIP\_AERNAROW 140**

Parameter address for module DAVIS346\_CONFIG\_CHIP: DAVIS346 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.162 #define DAVIS346\_CONFIG\_CHIP\_ANALOGMUX0 132**

Parameter address for module DAVIS346\_CONFIG\_CHIP: DAVIS346 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.163 #define DAVIS346\_CONFIG\_CHIP\_ANALOGMUX1 133**

Parameter address for module DAVIS346\_CONFIG\_CHIP: DAVIS346 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.164 #define DAVIS346\_CONFIG\_CHIP\_ANALOGMUX2 134**

Parameter address for module DAVIS346\_CONFIG\_CHIP: DAVIS346 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.165 #define DAVIS346\_CONFIG\_CHIP\_BIASMUX0 135**

Parameter address for module DAVIS346\_CONFIG\_CHIP: DAVIS346 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.166 #define DAVIS346\_CONFIG\_CHIP\_DIGITALMUX0 128**

Parameter address for module DAVIS346\_CONFIG\_CHIP: DAVIS346 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.167 #define DAVIS346\_CONFIG\_CHIP\_DIGITALMUX1 129**

Parameter address for module DAVIS346\_CONFIG\_CHIP: DAVIS346 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.168 #define DAVIS346\_CONFIG\_CHIP\_DIGITALMUX2 130**

Parameter address for module DAVIS346\_CONFIG\_CHIP: DAVIS346 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.169 #define DAVIS346\_CONFIG\_CHIP\_DIGITALMUX3 131**

Parameter address for module DAVIS346\_CONFIG\_CHIP: DAVIS346 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.170 #define DAVIS346\_CONFIG\_CHIP\_GLOBAL\_SHUTTER 142**

Parameter address for module DAVIS346\_CONFIG\_CHIP: DAVIS346 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.171 #define DAVIS346\_CONFIG\_CHIP\_RESETCALIBNEURON 136**

Parameter address for module DAVIS346\_CONFIG\_CHIP: DAVIS346 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.172 #define DAVIS346\_CONFIG\_CHIP\_RESETTESTPIXEL 138**

Parameter address for module DAVIS346\_CONFIG\_CHIP: DAVIS346 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.173 #define DAVIS346\_CONFIG\_CHIP\_SELECTGRAYCOUNTER 143**

Parameter address for module DAVIS346\_CONFIG\_CHIP: DAVIS346 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.174 #define DAVIS346\_CONFIG\_CHIP\_TESTADC 144**

Parameter address for module DAVIS346\_CONFIG\_CHIP: DAVIS346 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.175 #define DAVIS346\_CONFIG\_CHIP\_TYPENCALIBNEURON 137**

Parameter address for module DAVIS346\_CONFIG\_CHIP: DAVIS346 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.176 #define DAVIS346\_CONFIG\_CHIP\_USEAOUT 141**

Parameter address for module DAVIS346\_CONFIG\_CHIP: DAVIS346 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.177 #define DAVIS640\_CONFIG\_BIAS\_ADCCOMPBP 19**

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.178 #define DAVIS640\_CONFIG\_BIAS\_ADCREFHIGH 2**

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.179 #define DAVIS640\_CONFIG\_BIAS\_ADCREFLOW 3**

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.180 #define DAVIS640\_CONFIG\_BIAS\_ADCTESTVOLTAGE 4**

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.181 `#define DAVIS640_CONFIG_BIAS_AEPDBN 23`

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.182 `#define DAVIS640_CONFIG_BIAS_AEPUXBP 24`

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.183 `#define DAVIS640_CONFIG_BIAS_AEPUYBP 25`

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.184 `#define DAVIS640_CONFIG_BIAS_APSCAS 1`

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.185 #define DAVIS640\_CONFIG\_BIAS\_APSOVERFLOWLEVEL 0**

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.186 #define DAVIS640\_CONFIG\_BIAS\_APSROSFBN 18**

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.187 #define DAVIS640\_CONFIG\_BIAS\_BIASBUFFER 34**

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.188 #define DAVIS640\_CONFIG\_BIAS\_COLSELLOWBN 20**

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.189 `#define DAVIS640_CONFIG_BIAS_DACBUFBP 21`

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.190 `#define DAVIS640_CONFIG_BIAS_DIFFBN 10`

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.191 `#define DAVIS640_CONFIG_BIAS_IFREFRBN 26`

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.192 `#define DAVIS640_CONFIG_BIAS_IFTHRBN 27`

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.193 #define DAVIS640\_CONFIG\_BIAS\_LCOLTIMEOUTBN 22**

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.194 #define DAVIS640\_CONFIG\_BIAS\_LOCALBUFBN 8**

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.195 #define DAVIS640\_CONFIG\_BIAS\_OFFBN 12**

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.196 #define DAVIS640\_CONFIG\_BIAS\_ONBN 11**

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.197 `#define DAVIS640_CONFIG_BIAS_PADFOLLBN 9`

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.198 `#define DAVIS640_CONFIG_BIAS_PIXINBN 13`

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.199 `#define DAVIS640_CONFIG_BIAS_PRBP 14`

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.200 `#define DAVIS640_CONFIG_BIAS_PRSFBP 15`

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.



#### 4.1.2.201 `#define DAVIS640_CONFIG_BIAS_READOUTBUFBP 17`

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.202 `#define DAVIS640_CONFIG_BIAS_REFRBP 16`

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.203 `#define DAVIS640_CONFIG_BIAS_SSN 36`

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.204 `#define DAVIS640_CONFIG_BIAS_SSP 35`

Parameter address for module DAVIS640\_CONFIG\_BIAS: DAVIS640 chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.205 #define DAVIS640\_CONFIG\_CHIP\_AERNAROW 140**

Parameter address for module DAVIS640\_CONFIG\_CHIP: DAVIS640 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.206 #define DAVIS640\_CONFIG\_CHIP\_ANALOGMUX0 132**

Parameter address for module DAVIS640\_CONFIG\_CHIP: DAVIS640 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.207 #define DAVIS640\_CONFIG\_CHIP\_ANALOGMUX1 133**

Parameter address for module DAVIS640\_CONFIG\_CHIP: DAVIS640 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.208 #define DAVIS640\_CONFIG\_CHIP\_ANALOGMUX2 134**

Parameter address for module DAVIS640\_CONFIG\_CHIP: DAVIS640 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.209 #define DAVIS640\_CONFIG\_CHIP\_BIASMUX0 135**

Parameter address for module DAVIS640\_CONFIG\_CHIP: DAVIS640 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.210 #define DAVIS640\_CONFIG\_CHIP\_DIGITALMUX0 128**

Parameter address for module DAVIS640\_CONFIG\_CHIP: DAVIS640 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.211 #define DAVIS640\_CONFIG\_CHIP\_DIGITALMUX1 129**

Parameter address for module DAVIS640\_CONFIG\_CHIP: DAVIS640 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.212 #define DAVIS640\_CONFIG\_CHIP\_DIGITALMUX2 130**

Parameter address for module DAVIS640\_CONFIG\_CHIP: DAVIS640 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.213 #define DAVIS640\_CONFIG\_CHIP\_DIGITALMUX3 131**

Parameter address for module DAVIS640\_CONFIG\_CHIP: DAVIS640 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.214 #define DAVIS640\_CONFIG\_CHIP\_GLOBAL\_SHUTTER 142**

Parameter address for module DAVIS640\_CONFIG\_CHIP: DAVIS640 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.215 #define DAVIS640\_CONFIG\_CHIP\_RESETCALIBNEURON 136**

Parameter address for module DAVIS640\_CONFIG\_CHIP: DAVIS640 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.216 #define DAVIS640\_CONFIG\_CHIP\_RESETTESTPIXEL 138**

Parameter address for module DAVIS640\_CONFIG\_CHIP: DAVIS640 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.217 #define DAVIS640\_CONFIG\_CHIP\_SELECTGRAYCOUNTER 143**

Parameter address for module DAVIS640\_CONFIG\_CHIP: DAVIS640 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.218 #define DAVIS640\_CONFIG\_CHIP\_TESTADC 144**

Parameter address for module DAVIS640\_CONFIG\_CHIP: DAVIS640 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.219 #define DAVIS640\_CONFIG\_CHIP\_TYPENCALIBNEURON 137**

Parameter address for module DAVIS640\_CONFIG\_CHIP: DAVIS640 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.220 #define DAVIS640\_CONFIG\_CHIP\_USEAOUT 141**

Parameter address for module DAVIS640\_CONFIG\_CHIP: DAVIS640 chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

4.1.2.221 `#define DAVIS_CHIP_DAVIS128 3`

DAVIS128 chip identifier. 128x128, color possible, internal ADC.

4.1.2.222 `#define DAVIS_CHIP_DAVIS208 8`

DAVIS208 chip identifier. 208x192, special sensitive test pixels, color possible, internal ADC.

4.1.2.223 `#define DAVIS_CHIP_DAVIS240A 0`

DAVIS240A chip identifier. 240x180, no color, no global shutter.

4.1.2.224 `#define DAVIS_CHIP_DAVIS240B 1`

DAVIS240B chip identifier. 240x180, no color, 50 test columns left-side.

4.1.2.225 `#define DAVIS_CHIP_DAVIS240C 2`

DAVIS240C chip identifier. 240x180, no color.

4.1.2.226 `#define DAVIS_CHIP_DAVIS346A 4`

DAVIS346A chip identifier. 346x260, color possible, internal ADC.

4.1.2.227 `#define DAVIS_CHIP_DAVIS346B 5`

DAVIS346B chip identifier. 346x260, color possible, internal ADC.

4.1.2.228 `#define DAVIS_CHIP_DAVIS346C 9`

DAVIS346C chip identifier. 346x260, BSI, color possible, internal ADC.

4.1.2.229 `#define DAVIS_CHIP_DAVIS640 6`

DAVIS640 chip identifier. 640x480, color possible, internal ADC.

4.1.2.230 `#define DAVIS_CHIP_DAVISRGB 7`

DAVISRGB chip identifier. 640x480 APS, 320x240 DVS, color possible, internal ADC.

**4.1.2.231 #define DAVIS\_CONFIG\_APS 2**

Module address: device-side APS (Frame) configuration. The APS (Active-Pixel-Sensor) is responsible for getting the normal, synchronous frame from the camera chip. It supports various options for very precise timing control, as well as Region of Interest imaging.

**4.1.2.232 #define DAVIS\_CONFIG\_APS\_ADC\_TEST\_MODE 39**

Parameter address for module DAVIS\_CONFIG\_APS: put all APS pixels into reset, while keeping everything else running. This is only useful for testing and characterizing the internal ADC, to minimize noise.

**4.1.2.233 #define DAVIS\_CONFIG\_APS\_COLOR\_FILTER 3**

Parameter address for module DAVIS\_CONFIG\_APS: read-only parameter, contains information on the type of color filter present on the device. This is reserved for internal use and should not be used by anything other than libcaer. Please see the 'struct [caer\\_davis\\_info](#)' documentation to get proper color filter information.

**4.1.2.234 #define DAVIS\_CONFIG\_APS\_COLUMN\_SETTLE 16**

Parameter address for module DAVIS\_CONFIG\_APS: column settle time in ADCClock cycles.

**4.1.2.235 #define DAVIS\_CONFIG\_APS\_END\_COLUMN\_0 11**

Parameter address for module DAVIS\_CONFIG\_APS: end position on the X axis for Region of Interest 0. Must be between 0 and APS\_SIZE\_X-1, and be greater or equal to DAVIS\_CONFIG\_APS\_START\_COLUMN\_0.

**4.1.2.236 #define DAVIS\_CONFIG\_APS\_END\_COLUMN\_1 22**

Parameter address for module DAVIS\_CONFIG\_APS: end position on the X axis for Region of Interest 1. Must be between 0 and APS\_SIZE\_X-1, and be greater or equal to DAVIS\_CONFIG\_APS\_START\_COLUMN\_1.

**4.1.2.237 #define DAVIS\_CONFIG\_APS\_END\_COLUMN\_2 26**

Parameter address for module DAVIS\_CONFIG\_APS: end position on the X axis for Region of Interest 2. Must be between 0 and APS\_SIZE\_X-1, and be greater or equal to DAVIS\_CONFIG\_APS\_START\_COLUMN\_2.

**4.1.2.238 #define DAVIS\_CONFIG\_APS\_END\_COLUMN\_3 30**

Parameter address for module DAVIS\_CONFIG\_APS: end position on the X axis for Region of Interest 3. Must be between 0 and APS\_SIZE\_X-1, and be greater or equal to DAVIS\_CONFIG\_APS\_START\_COLUMN\_3.

**4.1.2.239 #define DAVIS\_CONFIG\_APS\_END\_ROW\_0 12**

Parameter address for module DAVIS\_CONFIG\_APS: end position on the Y axis for Region of Interest 0. Must be between 0 and APS\_SIZE\_Y-1, and be greater or equal to DAVIS\_CONFIG\_APS\_START\_ROW\_0.

**4.1.2.240 #define DAVIS\_CONFIG\_APS\_END\_ROW\_1 23**

Parameter address for module DAVIS\_CONFIG\_APS: end position on the Y axis for Region of Interest 1. Must be between 0 and APS\_SIZE\_Y-1, and be greater or equal to DAVIS\_CONFIG\_APS\_START\_ROW\_1.

**4.1.2.241 #define DAVIS\_CONFIG\_APS\_END\_ROW\_2 27**

Parameter address for module DAVIS\_CONFIG\_APS: end position on the Y axis for Region of Interest 2. Must be between 0 and APS\_SIZE\_Y-1, and be greater or equal to DAVIS\_CONFIG\_APS\_START\_ROW\_2.

**4.1.2.242 #define DAVIS\_CONFIG\_APS\_END\_ROW\_3 31**

Parameter address for module DAVIS\_CONFIG\_APS: end position on the Y axis for Region of Interest 3. Must be between 0 and APS\_SIZE\_Y-1, and be greater or equal to DAVIS\_CONFIG\_APS\_START\_ROW\_3.

**4.1.2.243 #define DAVIS\_CONFIG\_APS\_EXPOSURE 13**

Parameter address for module DAVIS\_CONFIG\_APS: frame exposure time in microseconds, up to about one second maximum. Very precise for Global Shutter, slightly less exact for Rolling Shutter due to column-based timing constraints.

**4.1.2.244 #define DAVIS\_CONFIG\_APS\_FRAME\_DELAY 14**

Parameter address for module DAVIS\_CONFIG\_APS: delay between consecutive frames in microseconds, up to about one second maximum. This can be used to achieve slower frame-rates, down to about 1 Hertz.

**4.1.2.245 #define DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER 8**

Parameter address for module DAVIS\_CONFIG\_APS: enable Global Shutter mode instead of Rolling Shutter. The Global Shutter eliminates motion artifacts, but is noisier than the Rolling Shutter (worse quality).

**4.1.2.246 #define DAVIS\_CONFIG\_APS\_HAS\_EXTERNAL\_ADC 32**

Parameter address for module DAVIS\_CONFIG\_APS: read-only parameter, information about the presence of an external ADC to read the pixel values. This is reserved for internal use and should not be used by anything other than libcaer. Please see the 'struct [caer\\_davis\\_info](#)' documentation to get this information.

**4.1.2.247 #define DAVIS\_CONFIG\_APS\_HAS\_GLOBAL\_SHUTTER 7**

Parameter address for module DAVIS\_CONFIG\_APS: read-only parameter, information about the presence of the global shutter feature. This is reserved for internal use and should not be used by anything other than libcaer. Please see the 'struct [caer\\_davis\\_info](#)' documentation to get this information.

**4.1.2.248 #define DAVIS\_CONFIG\_APS\_HAS\_INTERNAL\_ADC 33**

Parameter address for module DAVIS\_CONFIG\_APS: read-only parameter, information about the presence of an internal, on-chip ADC to read the pixel values. This is reserved for internal use and should not be used by anything other than libcaer. Please see the 'struct [caer\\_davis\\_info](#)' documentation to get this information.

**4.1.2.249 #define DAVIS\_CONFIG\_APS\_HAS\_QUAD\_ROI 19**

Parameter address for module DAVIS\_CONFIG\_APS: read-only parameter, information about the presence of the Quadruple Region of Interest feature. This is reserved for internal use and should not be used by anything other than libcaer. Please see the 'struct [caer\\_davis\\_info](#)' documentation to get this information.

**4.1.2.250 #define DAVIS\_CONFIG\_APS\_NULL\_SETTLE 18**

Parameter address for module DAVIS\_CONFIG\_APS: null (between states) settle time in ADCClock cycles.

**4.1.2.251 #define DAVIS\_CONFIG\_APS\_ORIENTATION\_INFO 2**

Parameter address for module DAVIS\_CONFIG\_APS: read-only parameter, contains information on the orientation of the X/Y axes, whether they should be inverted or not on the host when parsing incoming pixels, as well as if the X or Y axes need to be flipped when reading the pixels. Bit 2: apsInvertXY Bit 1: apsFlipX Bit 0: apsFlipY This is reserved for internal use and should not be used by anything other than libcaer. Please see the 'struct [caer\\_davis\\_info](#)' documentation to get proper size information that already considers the rotation and orientation settings.

**4.1.2.252 #define DAVIS\_CONFIG\_APS\_RAMP\_RESET 37**

Parameter address for module DAVIS\_CONFIG\_APS: ramp reset time in ADCClock cycles.

**4.1.2.253 #define DAVIS\_CONFIG\_APS\_RAMP\_SHORT\_RESET 38**

Parameter address for module DAVIS\_CONFIG\_APS: only perform a short ramp (half length) during reset reads, given that the voltage should always be close to the top of the range. This increases the frame-rate, but may have impacts on image quality, especially in very bright regions.

**4.1.2.254 #define DAVIS\_CONFIG\_APS\_RESET\_READ 5**

Parameter address for module DAVIS\_CONFIG\_APS: enable the reset read phase in addition to the signal read, to allow for correlated double sampling schemes. This heavily improves image quality and should always be turned on. In special cases, especially when the camera is perfectly stationary, this can be turned off for longer periods of time to achieve a higher frame-rate and significantly faster frame capture.

**4.1.2.255 #define DAVIS\_CONFIG\_APS\_RESET\_SETTLE 15**

Parameter address for module DAVIS\_CONFIG\_APS: column reset settle time in ADCClock cycles.

**4.1.2.256 #define DAVIS\_CONFIG\_APS\_ROW\_SETTLE 17**

Parameter address for module DAVIS\_CONFIG\_APS: row settle time in ADCClock cycles.

**4.1.2.257 #define DAVIS\_CONFIG\_APS\_RUN 4**

Parameter address for module DAVIS\_CONFIG\_APS: enable the APS module and take intensity images of the scene. While this parameter is enabled, frames will be taken continuously. To slow down the frame-rate, see DAVIS\_CONFIG\_APS\_FRAME\_DELAY. To only take snapshots, see DAVIS\_CONFIG\_APS\_SNAPSHOT.

**4.1.2.258 #define DAVIS\_CONFIG\_APS\_SAMPLE\_ENABLE 35**

Parameter address for module DAVIS\_CONFIG\_APS: enable sampling of pixel voltage by the internal ADC circuitry. Must always be enabled to get proper frame values.

**4.1.2.259 #define DAVIS\_CONFIG\_APS\_SAMPLE\_SETTLE 36**

Parameter address for module DAVIS\_CONFIG\_APS: sample settle time in ADCClock cycles.

**4.1.2.260 #define DAVIS\_CONFIG\_APS\_SIZE\_COLUMNS 0**

Parameter address for module DAVIS\_CONFIG\_APS: read-only parameter, contains the X axis resolution of the APS frames returned by the camera. This is reserved for internal use and should not be used by anything other than libcaer. Please see the 'struct [caer\\_davis\\_info](#)' documentation to get proper size information that already considers the rotation and orientation settings.

**4.1.2.261 #define DAVIS\_CONFIG\_APS\_SIZE\_ROWS 1**

Parameter address for module DAVIS\_CONFIG\_APS: read-only parameter, contains the Y axis resolution of the APS frames returned by the camera. This is reserved for internal use and should not be used by anything other than libcaer. Please see the 'struct [caer\\_davis\\_info](#)' documentation to get proper size information that already considers the rotation and orientation settings.

**4.1.2.262 #define DAVIS\_CONFIG\_APS\_SNAPSHOT 80**

Parameter address for module DAVIS\_CONFIG\_APS: takes a snapshot (one frame), like a photo-camera. More efficient implementation than just toggling the DAVIS\_CONFIG\_APS\_RUN parameter. The APS module should not be running prior to calling this, as it only makes sense if frames are not being generated at the time. Also, DAVIS\_CONFIG\_APS\_FRAME\_DELAY should be set to zero if only doing snapshots, to ensure a quicker readiness for the next one, since the delay is always observed after taking a frame.

**4.1.2.263 #define DAVIS\_CONFIG\_APS\_START\_COLUMN\_0 9**

Parameter address for module DAVIS\_CONFIG\_APS: start position on the X axis for Region of Interest 0. Must be between 0 and APS\_SIZE\_X-1, and be smaller or equal to DAVIS\_CONFIG\_APS\_END\_COLUMN\_0 for the ROI region to be enabled. Setting it to APS\_SIZE\_X itself deactivates this ROI region completely.



**4.1.2.264 #define DAVIS\_CONFIG\_APS\_START\_COLUMN\_1 20**

Parameter address for module DAVIS\_CONFIG\_APS: start position on the X axis for Region of Interest 1. Must be between 0 and APS\_SIZE\_X-1, and be smaller or equal to DAVIS\_CONFIG\_APS\_END\_COLUMN\_1 for the ROI region to be enabled. Setting it to APS\_SIZE\_X itself deactivates this ROI region completely.

**4.1.2.265 #define DAVIS\_CONFIG\_APS\_START\_COLUMN\_2 24**

Parameter address for module DAVIS\_CONFIG\_APS: start position on the X axis for Region of Interest 2. Must be between 0 and APS\_SIZE\_X-1, and be smaller or equal to DAVIS\_CONFIG\_APS\_END\_COLUMN\_2 for the ROI region to be enabled. Setting it to APS\_SIZE\_X itself deactivates this ROI region completely.

**4.1.2.266 #define DAVIS\_CONFIG\_APS\_START\_COLUMN\_3 28**

Parameter address for module DAVIS\_CONFIG\_APS: start position on the X axis for Region of Interest 3. Must be between 0 and APS\_SIZE\_X-1, and be smaller or equal to DAVIS\_CONFIG\_APS\_END\_COLUMN\_3 for the ROI region to be enabled. Setting it to APS\_SIZE\_X itself deactivates this ROI region completely.

**4.1.2.267 #define DAVIS\_CONFIG\_APS\_START\_ROW\_0 10**

Parameter address for module DAVIS\_CONFIG\_APS: start position on the Y axis for Region of Interest 0. Must be between 0 and APS\_SIZE\_Y-1, and be smaller or equal to DAVIS\_CONFIG\_APS\_END\_ROW\_0.

**4.1.2.268 #define DAVIS\_CONFIG\_APS\_START\_ROW\_1 21**

Parameter address for module DAVIS\_CONFIG\_APS: start position on the Y axis for Region of Interest 1. Must be between 0 and APS\_SIZE\_Y-1, and be smaller or equal to DAVIS\_CONFIG\_APS\_END\_ROW\_1.

**4.1.2.269 #define DAVIS\_CONFIG\_APS\_START\_ROW\_2 25**

Parameter address for module DAVIS\_CONFIG\_APS: start position on the Y axis for Region of Interest 2. Must be between 0 and APS\_SIZE\_Y-1, and be smaller or equal to DAVIS\_CONFIG\_APS\_END\_ROW\_2.

**4.1.2.270 #define DAVIS\_CONFIG\_APS\_START\_ROW\_3 29**

Parameter address for module DAVIS\_CONFIG\_APS: start position on the Y axis for Region of Interest 3. Must be between 0 and APS\_SIZE\_Y-1, and be smaller or equal to DAVIS\_CONFIG\_APS\_END\_ROW\_3.

**4.1.2.271 #define DAVIS\_CONFIG\_APS\_USE\_INTERNAL\_ADC 34**

Parameter address for module DAVIS\_CONFIG\_APS: use the internal, on-chip ADC instead of the external one. This enables a much faster and more power-efficient readout for the frames, and should as such always be preferred.

**4.1.2.272 #define DAVIS\_CONFIG\_APS\_WAIT\_ON\_TRANSFER\_STALL 6**

Parameter address for module DAVIS\_CONFIG\_APS: if the output FIFO for this module is full, stall the APS state machine and wait until it's free again, instead of just dropping the pixels as they are being read out. This guarantees a complete frame readout, at the possible cost of slight timing differences between pixels. If disabled, incomplete frames may be transmitted and will then be dropped on the host, resulting in lower frame-rates, especially during high DVS traffic.

**4.1.2.273 #define DAVIS\_CONFIG\_BIAS 5**

Module address: device-side chip bias configuration. Shared with DAVIS\_CONFIG\_CHIP. This state machine is responsible for configuring the chip's bias generator.

**4.1.2.274 #define DAVIS\_CONFIG\_CHIP 5**

Module address: device-side chip control configuration. Shared with DAVIS\_CONFIG\_BIAS. This state machine is responsible for configuring the chip's internal control shift registers, to set special options.

**4.1.2.275 #define DAVIS\_CONFIG\_DVS 1**

Module address: device-side DVS configuration. The DVS state machine handshakes with the chip's AER bus and gets the polarity events from it. It supports various configurable delays, as well as advanced filtering capabilities on the polarity events.

**4.1.2.276 #define DAVIS\_CONFIG\_DVS\_ACK\_DELAY\_COLUMN 5**

Parameter address for module DAVIS\_CONFIG\_DVS: delay capturing the data and acknowledging it on the AER bus for the column events (serial AER protocol) by this many LogicClock cycles.

**4.1.2.277 #define DAVIS\_CONFIG\_DVS\_ACK\_DELAY\_ROW 4**

Parameter address for module DAVIS\_CONFIG\_DVS: delay capturing the data and acknowledging it on the AER bus for the row events (serial AER protocol) by this many LogicClock cycles.

**4.1.2.278 #define DAVIS\_CONFIG\_DVS\_ACK\_EXTENSION\_COLUMN 7**

Parameter address for module DAVIS\_CONFIG\_DVS: extend the length of the acknowledge on the AER bus for the column events (serial AER protocol) by this many LogicClock cycles.

**4.1.2.279 #define DAVIS\_CONFIG\_DVS\_ACK\_EXTENSION\_ROW 6**

Parameter address for module DAVIS\_CONFIG\_DVS: extend the length of the acknowledge on the AER bus for the row events (serial AER protocol) by this many LogicClock cycles.

**4.1.2.280 #define DAVIS\_CONFIG\_DVS\_EXTERNAL\_AER\_CONTROL 10**

Parameter address for module DAVIS\_CONFIG\_DVS: enable external AER control. This ensures the chip and the DVS pixel array are running, but doesn't do the handshake and leaves the ACK pin in high-impedance, to allow for an external system to take over the AER communication with the chip. DAVIS\_CONFIG\_DVS\_RUN has to be turned off for this to work.

**4.1.2.281 #define DAVIS\_CONFIG\_DVS\_FILTER\_BACKGROUND\_ACTIVITY 29**

Parameter address for module DAVIS\_CONFIG\_DVS: enable the background-activity filter, which tries to remove events caused by transistor leakage, by rejecting uncorrelated events.

**4.1.2.282 #define DAVIS\_CONFIG\_DVS\_FILTER\_BACKGROUND\_ACTIVITY\_DELTAT 30**

Parameter address for module DAVIS\_CONFIG\_DVS: specify the time difference constant for the background-activity filter in microseconds. Events that do correlated within this time-frame are let through, while others are filtered out.

**4.1.2.283 #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_0\_COLUMN 13**

Parameter address for module DAVIS\_CONFIG\_DVS: the pixel filter completely suppresses up to eight pixels in the DVS array, filtering out all events produced by them. This is the pixel 0, X axis setting.

**4.1.2.284 #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_0\_ROW 12**

Parameter address for module DAVIS\_CONFIG\_DVS: the pixel filter completely suppresses up to eight pixels in the DVS array, filtering out all events produced by them. This is the pixel 0, Y axis setting.

**4.1.2.285 #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_1\_COLUMN 15**

Parameter address for module DAVIS\_CONFIG\_DVS: the pixel filter completely suppresses up to eight pixels in the DVS array, filtering out all events produced by them. This is the pixel 1, X axis setting.

**4.1.2.286 #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_1\_ROW 14**

Parameter address for module DAVIS\_CONFIG\_DVS: the pixel filter completely suppresses up to eight pixels in the DVS array, filtering out all events produced by them. This is the pixel 1, Y axis setting.

**4.1.2.287 #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_2\_COLUMN 17**

Parameter address for module DAVIS\_CONFIG\_DVS: the pixel filter completely suppresses up to eight pixels in the DVS array, filtering out all events produced by them. This is the pixel 2, X axis setting.

**4.1.2.288 #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_2\_ROW 16**

Parameter address for module DAVIS\_CONFIG\_DVS: the pixel filter completely suppresses up to eight pixels in the DVS array, filtering out all events produced by them. This is the pixel 2, Y axis setting.

**4.1.2.289 #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_3\_COLUMN 19**

Parameter address for module DAVIS\_CONFIG\_DVS: the pixel filter completely suppresses up to eight pixels in the DVS array, filtering out all events produced by them. This is the pixel 3, X axis setting.

**4.1.2.290 #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_3\_ROW 18**

Parameter address for module DAVIS\_CONFIG\_DVS: the pixel filter completely suppresses up to eight pixels in the DVS array, filtering out all events produced by them. This is the pixel 3, Y axis setting.

**4.1.2.291 #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_4\_COLUMN 21**

Parameter address for module DAVIS\_CONFIG\_DVS: the pixel filter completely suppresses up to eight pixels in the DVS array, filtering out all events produced by them. This is the pixel 4, X axis setting.

**4.1.2.292 #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_4\_ROW 20**

Parameter address for module DAVIS\_CONFIG\_DVS: the pixel filter completely suppresses up to eight pixels in the DVS array, filtering out all events produced by them. This is the pixel 4, Y axis setting.

**4.1.2.293 #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_5\_COLUMN 23**

Parameter address for module DAVIS\_CONFIG\_DVS: the pixel filter completely suppresses up to eight pixels in the DVS array, filtering out all events produced by them. This is the pixel 5, X axis setting.

**4.1.2.294 #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_5\_ROW 22**

Parameter address for module DAVIS\_CONFIG\_DVS: the pixel filter completely suppresses up to eight pixels in the DVS array, filtering out all events produced by them. This is the pixel 5, Y axis setting.

**4.1.2.295 #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_6\_COLUMN 25**

Parameter address for module DAVIS\_CONFIG\_DVS: the pixel filter completely suppresses up to eight pixels in the DVS array, filtering out all events produced by them. This is the pixel 6, X axis setting.

**4.1.2.296 #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_6\_ROW 24**

Parameter address for module DAVIS\_CONFIG\_DVS: the pixel filter completely suppresses up to eight pixels in the DVS array, filtering out all events produced by them. This is the pixel 6, Y axis setting.

**4.1.2.297 #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_7\_COLUMN 27**

Parameter address for module DAVIS\_CONFIG\_DVS: the pixel filter completely suppresses up to eight pixels in the DVS array, filtering out all events produced by them. This is the pixel 7, X axis setting.

**4.1.2.298 #define DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_7\_ROW 26**

Parameter address for module DAVIS\_CONFIG\_DVS: the pixel filter completely suppresses up to eight pixels in the DVS array, filtering out all events produced by them. This is the pixel 7, Y axis setting.

**4.1.2.299 #define DAVIS\_CONFIG\_DVS\_FILTER\_ROW\_ONLY\_EVENTS 9**

Parameter address for module DAVIS\_CONFIG\_DVS: enable row-only event filter, to eliminate spurious row events with no following columns events. This can happen on DAVIS240 chips, or following the various pixel and background-activity filtering stages, which drop column events to achieve their effect. This should always be enabled!

**4.1.2.300 #define DAVIS\_CONFIG\_DVS\_HAS\_BACKGROUND\_ACTIVITY\_FILTER 28**

Parameter address for module DAVIS\_CONFIG\_DVS: read-only parameter, information about the presence of the background-activity filter feature. This is reserved for internal use and should not be used by anything other than libcaer. Please see the 'struct [caer\\_davis\\_info](#)' documentation to get this information.

**4.1.2.301 #define DAVIS\_CONFIG\_DVS\_HAS\_PIXEL\_FILTER 11**

Parameter address for module DAVIS\_CONFIG\_DVS: read-only parameter, information about the presence of the pixel filter feature. This is reserved for internal use and should not be used by anything other than libcaer. Please see the 'struct [caer\\_davis\\_info](#)' documentation to get this information.

**4.1.2.302 #define DAVIS\_CONFIG\_DVS\_HAS\_TEST\_EVENT\_GENERATOR 31**

Parameter address for module DAVIS\_CONFIG\_DVS: read-only parameter, information about the presence of the test event generator feature. This is reserved for internal use and should not be used by anything other than libcaer. Please see the 'struct [caer\\_davis\\_info](#)' documentation to get this information.

**4.1.2.303 #define DAVIS\_CONFIG\_DVS\_ORIENTATION\_INFO 2**

Parameter address for module DAVIS\_CONFIG\_DVS: read-only parameter, contains information on the orientation of the X/Y axes, whether they should be inverted or not on the host when parsing incoming events. Bit 0: dvsInvert↔XY This is reserved for internal use and should not be used by anything other than libcaer. Please see the 'struct [caer\\_davis\\_info](#)' documentation to get proper size information that already considers the rotation and orientation settings.

**4.1.2.304 #define DAVIS\_CONFIG\_DVS\_RUN 3**

Parameter address for module DAVIS\_CONFIG\_DVS: run the DVS state machine and get polarity events from the chip by handshaking with its AER bus.

#### 4.1.2.305 `#define DAVIS_CONFIG_DVS_SIZE_COLUMNS 0`

Parameter address for module `DAVIS_CONFIG_DVS`: read-only parameter, contains the X axis resolution of the DVS events returned by the camera. This is reserved for internal use and should not be used by anything other than `libcaer`. Please see the 'struct `caer_davis_info`' documentation to get proper size information that already considers the rotation and orientation settings.

#### 4.1.2.306 `#define DAVIS_CONFIG_DVS_SIZE_ROWS 1`

Parameter address for module `DAVIS_CONFIG_DVS`: read-only parameter, contains the Y axis resolution of the DVS events returned by the camera. This is reserved for internal use and should not be used by anything other than `libcaer`. Please see the 'struct `caer_davis_info`' documentation to get proper size information that already considers the rotation and orientation settings.

#### 4.1.2.307 `#define DAVIS_CONFIG_DVS_TEST_EVENT_GENERATOR_ENABLE 32`

Parameter address for module `DAVIS_CONFIG_DVS`: enable the test event generator for debugging purposes. This generates fake events that appear to originate from all rows sequentially, and for each row going through all its columns, first with an ON polarity and then with an OFF polarity. Both `DAVIS_CONFIG_DVS_RUN` and `DAVIS_CONFIG_DVS_EXTERNAL_AER_CONTROL` have to be turned off for this to work.

#### 4.1.2.308 `#define DAVIS_CONFIG_DVS_WAIT_ON_TRANSFER_STALL 8`

Parameter address for module `DAVIS_CONFIG_DVS`: if the output FIFO for this module is full, stall the AER handshake with the chip and wait until it's free again, instead of just continuing the handshake and dropping the resulting events.

#### 4.1.2.309 `#define DAVIS_CONFIG_EXTINPUT 4`

Module address: device-side External Input (signal detector/generator) configuration. The External Input module is used to detect external signals on the external input jack and inject an event into the event stream when this happens. It can detect pulses of a specific length or rising and falling edges. On some systems, a signal generator module is also present, which can generate PWM-like pulsed signals with configurable timing.

#### 4.1.2.310 `#define DAVIS_CONFIG_EXTINPUT_DETECT_FALLING_EDGES 2`

Parameter address for module `DAVIS_CONFIG_EXTINPUT`: send a special `EXTERNAL_INPUT_FALLING_EDGE` event when a falling edge is detected (transition from high voltage to low).

#### 4.1.2.311 `#define DAVIS_CONFIG_EXTINPUT_DETECT_FALLING_EDGES1 17`

Parameter address for module `DAVIS_CONFIG_EXTINPUT`: send a special `EXTERNAL_INPUT1_FALLING_EDGE` event when a falling edge is detected (transition from high voltage to low).

#### 4.1.2.312 `#define DAVIS_CONFIG_EXTINPUT_DETECT_FALLING_EDGES2 23`

Parameter address for module DAVIS\_CONFIG\_EXTINPUT: send a special EXTERNAL\_INPUT2\_FALLING\_EDGE event when a falling edge is detected (transition from high voltage to low).

#### 4.1.2.313 `#define DAVIS_CONFIG_EXTINPUT_DETECT_PULSE_LENGTH 5`

Parameter address for module DAVIS\_CONFIG\_EXTINPUT: the minimal length that a pulse must have to trigger the sending of a special event. This is measured in cycles at LogicClock frequency (see 'struct [caer\\_davis\\_info](#)' for details on how to get the frequency).

#### 4.1.2.314 `#define DAVIS_CONFIG_EXTINPUT_DETECT_PULSE_LENGTH1 20`

Parameter address for module DAVIS\_CONFIG\_EXTINPUT: the minimal length that a pulse must have to trigger the sending of a special event. This is measured in cycles at LogicClock frequency (see 'struct [caer\\_davis\\_info](#)' for details on how to get the frequency).

#### 4.1.2.315 `#define DAVIS_CONFIG_EXTINPUT_DETECT_PULSE_LENGTH2 26`

Parameter address for module DAVIS\_CONFIG\_EXTINPUT: the minimal length that a pulse must have to trigger the sending of a special event. This is measured in cycles at LogicClock frequency (see 'struct [caer\\_davis\\_info](#)' for details on how to get the frequency).

#### 4.1.2.316 `#define DAVIS_CONFIG_EXTINPUT_DETECT_PULSE_POLARITY 4`

Parameter address for module DAVIS\_CONFIG\_EXTINPUT: the polarity the pulse must exhibit to be detected as such. '1' means active high; a pulse will start when the signal goes from low to high and will continue to be seen as the same pulse as long as it stays high. '0' means active low; a pulse will start when the signal goes from high to low and will continue to be seen as the same pulse as long as it stays low.

#### 4.1.2.317 `#define DAVIS_CONFIG_EXTINPUT_DETECT_PULSE_POLARITY1 19`

Parameter address for module DAVIS\_CONFIG\_EXTINPUT: the polarity the pulse must exhibit to be detected as such. '1' means active high; a pulse will start when the signal goes from low to high and will continue to be seen as the same pulse as long as it stays high. '0' means active low; a pulse will start when the signal goes from high to low and will continue to be seen as the same pulse as long as it stays low.

#### 4.1.2.318 `#define DAVIS_CONFIG_EXTINPUT_DETECT_PULSE_POLARITY2 25`

Parameter address for module DAVIS\_CONFIG\_EXTINPUT: the polarity the pulse must exhibit to be detected as such. '1' means active high; a pulse will start when the signal goes from low to high and will continue to be seen as the same pulse as long as it stays high. '0' means active low; a pulse will start when the signal goes from high to low and will continue to be seen as the same pulse as long as it stays low.

**4.1.2.319 #define DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSES 3**

Parameter address for module DAVIS\_CONFIG\_EXTINPUT: send a special EXTERNAL\_INPUT\_PULSE event when a pulse, of a specified, configurable polarity and length, is detected. See DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_POLARITY and DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_LENGTH for more details.

**4.1.2.320 #define DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSES1 18**

Parameter address for module DAVIS\_CONFIG\_EXTINPUT: send a special EXTERNAL\_INPUT1\_PULSE event when a pulse, of a specified, configurable polarity and length, is detected. See DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_POLARITY1 and DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_LENGTH1 for more details.

**4.1.2.321 #define DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSES2 24**

Parameter address for module DAVIS\_CONFIG\_EXTINPUT: send a special EXTERNAL\_INPUT2\_PULSE event when a pulse, of a specified, configurable polarity and length, is detected. See DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_POLARITY2 and DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_LENGTH2 for more details.

**4.1.2.322 #define DAVIS\_CONFIG\_EXTINPUT\_DETECT\_RISING\_EDGES 1**

Parameter address for module DAVIS\_CONFIG\_EXTINPUT: send a special EXTERNAL\_INPUT\_RISING\_EDGE event when a rising edge is detected (transition from low voltage to high).

**4.1.2.323 #define DAVIS\_CONFIG\_EXTINPUT\_DETECT\_RISING\_EDGES1 16**

Parameter address for module DAVIS\_CONFIG\_EXTINPUT: send a special EXTERNAL\_INPUT1\_RISING\_EDGE event when a rising edge is detected (transition from low voltage to high).

**4.1.2.324 #define DAVIS\_CONFIG\_EXTINPUT\_DETECT\_RISING\_EDGES2 22**

Parameter address for module DAVIS\_CONFIG\_EXTINPUT: send a special EXTERNAL\_INPUT2\_RISING\_EDGE event when a rising edge is detected (transition from low voltage to high).

**4.1.2.325 #define DAVIS\_CONFIG\_EXTINPUT\_GENERATE\_INJECT\_ON\_FALLING\_EDGE 13**

Parameter address for module DAVIS\_CONFIG\_EXTINPUT: enables event injection when a falling edge occurs in the generated signal; a special event EXTERNAL\_GENERATOR\_FALLING\_EDGE is emitted into the event stream.

**4.1.2.326 #define DAVIS\_CONFIG\_EXTINPUT\_GENERATE\_INJECT\_ON\_RISING\_EDGE 12**

Parameter address for module DAVIS\_CONFIG\_EXTINPUT: enables event injection when a rising edge occurs in the generated signal; a special event EXTERNAL\_GENERATOR\_RISING\_EDGE is emitted into the event stream.



#### 4.1.2.327 `#define DAVIS_CONFIG_EXTINPUT_GENERATE_PULSE_INTERVAL 10`

Parameter address for module `DAVIS_CONFIG_EXTINPUT`: the interval between the start of two consecutive pulses, expressed in cycles at LogicClock frequency (see 'struct [caer\\_davis\\_info](#)' for details on how to get the frequency). This must be bigger or equal to `DAVIS_CONFIG_EXTINPUT_GENERATE_PULSE_LENGTH`. To generate a signal with 50% duty cycle, this would have to be exactly double of `DAVIS_CONFIG_EXTINPUT_GENERATE_PULSE_LENGTH`.

#### 4.1.2.328 `#define DAVIS_CONFIG_EXTINPUT_GENERATE_PULSE_LENGTH 11`

Parameter address for module `DAVIS_CONFIG_EXTINPUT`: the length a pulse stays active, expressed in cycles at LogicClock frequency (see 'struct [caer\\_davis\\_info](#)' for details on how to get the frequency). This must be smaller or equal to `DAVIS_CONFIG_EXTINPUT_GENERATE_PULSE_INTERVAL`. To generate a signal with 50% duty cycle, this would have to be exactly half of `DAVIS_CONFIG_EXTINPUT_GENERATE_PULSE_INTERVAL`.

#### 4.1.2.329 `#define DAVIS_CONFIG_EXTINPUT_GENERATE_PULSE_POLARITY 9`

Parameter address for module `DAVIS_CONFIG_EXTINPUT`: polarity of the PWM-like signal to be generated. '1' means active high, '0' means active low.

#### 4.1.2.330 `#define DAVIS_CONFIG_EXTINPUT_GENERATE_USE_CUSTOM_SIGNAL 8`

Parameter address for module `DAVIS_CONFIG_EXTINPUT`: instead of generating a PWM-like signal by using the configured parameters, use a signal on the FPGA/CPLD that's passed as an input to the External Input module. By default this is disabled and tied to ground, but it can be useful for customized logic designs.

#### 4.1.2.331 `#define DAVIS_CONFIG_EXTINPUT_HAS_EXTRA_DETECTORS 14`

Parameter address for module `DAVIS_CONFIG_EXTINPUT`: read-only parameter, information about the presence of the extra detectors feature. This is reserved for internal use and should not be used by anything other than `libcaer`. Please see the 'struct [caer\\_davis\\_info](#)' documentation to get this information.

#### 4.1.2.332 `#define DAVIS_CONFIG_EXTINPUT_HAS_GENERATOR 6`

Parameter address for module `DAVIS_CONFIG_EXTINPUT`: read-only parameter, information about the presence of the signal generator feature. This is reserved for internal use and should not be used by anything other than `libcaer`. Please see the 'struct [caer\\_davis\\_info](#)' documentation to get this information.

#### 4.1.2.333 `#define DAVIS_CONFIG_EXTINPUT_RUN_DETECTOR 0`

Parameter address for module `DAVIS_CONFIG_EXTINPUT`: enable the signal detector module. It generates events when it sees certain types of signals, such as edges or pulses of a defined length, on the IN JACK signal. This can be useful to inject events into the event stream in response to external stimuli or controls, such as turning on a LED lamp.

**4.1.2.334 #define DAVIS\_CONFIG\_EXTINPUT\_RUN\_DETECTOR1 15**

Parameter address for module DAVIS\_CONFIG\_EXTINPUT: enable the signal detector module. It generates events when it sees certain types of signals, such as edges or pulses of a defined length, on the B1P20 input pin. This can be useful to inject events into the event stream in response to external stimuli or controls, such as turning on a LED lamp.

**4.1.2.335 #define DAVIS\_CONFIG\_EXTINPUT\_RUN\_DETECTOR2 21**

Parameter address for module DAVIS\_CONFIG\_EXTINPUT: enable the signal detector module. It generates events when it sees certain types of signals, such as edges or pulses of a defined length, on the B1P21 input pin. This can be useful to inject events into the event stream in response to external stimuli or controls, such as turning on a LED lamp.

**4.1.2.336 #define DAVIS\_CONFIG\_EXTINPUT\_RUN\_GENERATOR 7**

Parameter address for module DAVIS\_CONFIG\_EXTINPUT: enable the signal generator module. It generates a PWM-like signal based on configurable parameters and outputs it on the OUT JACK signal.

**4.1.2.337 #define DAVIS\_CONFIG\_IMU 3**

Module address: device-side IMU (Inertial Measurement Unit) configuration. The IMU module connects to the external IMU chip and sends data on the device's movement in space. It can configure various options on the external chip, such as accelerometer range or gyroscope refresh rate.

**4.1.2.338 #define DAVIS\_CONFIG\_IMU\_ACCEL\_FULL\_SCALE 8**

Parameter address for module DAVIS\_CONFIG\_IMU: select the full scale range of the accelerometer outputs. Valid values are: 0 - +- 2 g 1 - +- 4 g 2 - +- 8 g 3 - +- 16 g

**4.1.2.339 #define DAVIS\_CONFIG\_IMU\_ACCEL\_STANDBY 2**

Parameter address for module DAVIS\_CONFIG\_IMU: put the accelerometer sensor in standby, disabling it.

**4.1.2.340 #define DAVIS\_CONFIG\_IMU\_DIGITAL\_LOW\_PASS\_FILTER 7**

Parameter address for module DAVIS\_CONFIG\_IMU: this configures the digital low-pass filter for both the accelerometer and the gyroscope. Valid values are from 0 to 7 and have the following meaning: 0 - Accel: BW=260Hz, Delay=0ms, FS=1kHz - Gyro: BW=256Hz, Delay=0.98ms, FS=8kHz 1 - Accel: BW=184Hz, Delay=2.0ms, FS=1kHz - Gyro: BW=188Hz, Delay=1.9ms, FS=1kHz 2 - Accel: BW=94Hz, Delay=3.0ms, FS=1kHz - Gyro: BW=98Hz, Delay=2.8ms, FS=1kHz 3 - Accel: BW=44Hz, Delay=4.9ms, FS=1kHz - Gyro: BW=42Hz, Delay=4.8ms, FS=1kHz 4 - Accel: BW=21Hz, Delay=8.5ms, FS=1kHz - Gyro: BW=20Hz, Delay=8.3ms, FS=1kHz 5 - Accel: BW=10Hz, Delay=13.8ms, FS=1kHz - Gyro: BW=10Hz, Delay=13.4ms, FS=1kHz 6 - Accel: BW=5Hz, Delay=19.0ms, FS=1kHz - Gyro: BW=5Hz, Delay=18.6ms, FS=1kHz 7 - Accel: RESERVED, FS=1kHz - Gyro: RESERVED, FS=8kHz

**4.1.2.341 #define DAVIS\_CONFIG\_IMU\_GYRO\_FULL\_SCALE 9**

Parameter address for module DAVIS\_CONFIG\_IMU: select the full scale range of the gyroscope outputs. Valid values are: 0 - +- 250 °/s 1 - +- 500 °/s 2 - +- 1000 °/s 3 - +- 2000 °/s

**4.1.2.342 #define DAVIS\_CONFIG\_IMU\_GYRO\_STANDBY 3**

Parameter address for module DAVIS\_CONFIG\_IMU: put the gyroscope sensor in standby, disabling it.

**4.1.2.343 #define DAVIS\_CONFIG\_IMU\_LP\_CYCLE 4**

Parameter address for module DAVIS\_CONFIG\_IMU: put the IMU into Cycle Mode. In Cycle Mode, the device cycles between sleep mode and waking up to take a single sample of data from the accelerometer at a rate determined by DAVIS\_CONFIG\_IMU\_LP\_WAKEUP.

**4.1.2.344 #define DAVIS\_CONFIG\_IMU\_LP\_WAKEUP 5**

Parameter address for module DAVIS\_CONFIG\_IMU: rate at which the IMU takes an accelerometer sample while in Cycle Mode (see DAVIS\_CONFIG\_IMU\_LP\_CYCLE). Valid values are: 0 - 1.25 Hz wake-up frequency 1 - 5 Hz wake-up frequency 2 - 20 Hz wake-up frequency 3 - 40 Hz wake-up frequency

**4.1.2.345 #define DAVIS\_CONFIG\_IMU\_RUN 0**

Parameter address for module DAVIS\_CONFIG\_IMU: run the IMU state machine to get information about the movement and position of the device. This takes the IMU chip out of sleep.

**4.1.2.346 #define DAVIS\_CONFIG\_IMU\_SAMPLE\_RATE\_DIVIDER 6**

Parameter address for module DAVIS\_CONFIG\_IMU: this specifies the divider from the Gyroscope Output Rate used to generate the Sample Rate for the IMU. Valid values are from 0 to 255. The Sample Rate is generated like this:  $\text{Sample Rate} = \text{Gyroscope Output Rate} / (1 + \text{DAVIS\_CONFIG\_IMU\_SAMPLE\_RATE\_DIVIDER})$  where Gyroscope Output Rate = 8 kHz when DAVIS\_CONFIG\_IMU\_DIGITAL\_LOW\_PASS\_FILTER is disabled (set to 0 or 7), and 1 kHz when enabled. Note: the accelerometer output rate is 1 kHz. This means that for a Sample Rate greater than 1 kHz, the same accelerometer sample may be output multiple times.

**4.1.2.347 #define DAVIS\_CONFIG\_IMU\_TEMP\_STANDBY 1**

Parameter address for module DAVIS\_CONFIG\_IMU: put the temperature sensor in standby, disabling it.

**4.1.2.348 #define DAVIS\_CONFIG\_MUX 0**

Module address: device-side Multiplexer configuration. The Multiplexer is responsible for mixing, timestamping and outputting (via USB) the various event types generated by the device. It is also responsible for timestamp generation and synchronization.

**4.1.2.349 #define DAVIS\_CONFIG\_MUX\_DROP\_APS\_ON\_TRANSFER\_STALL 5**

Parameter address for module DAVIS\_CONFIG\_MUX: drop APS events if the USB output FIFO is full, instead of having them pile up at the input FIFOs. This normally should not be enabled to guarantee complete, coherent frame events, though small timing differences may cause a reduction in observed image quality.

**4.1.2.350 #define DAVIS\_CONFIG\_MUX\_DROP\_DVS\_ON\_TRANSFER\_STALL 4**

Parameter address for module DAVIS\_CONFIG\_MUX: drop DVS events if the USB output FIFO is full, instead of having them pile up at the input FIFOs.

**4.1.2.351 #define DAVIS\_CONFIG\_MUX\_DROP\_EXTINPUT\_ON\_TRANSFER\_STALL 7**

Parameter address for module DAVIS\_CONFIG\_MUX: drop External Input events if the USB output FIFO is full, instead of having them pile up at the input FIFOs.

**4.1.2.352 #define DAVIS\_CONFIG\_MUX\_DROP\_IMU\_ON\_TRANSFER\_STALL 6**

Parameter address for module DAVIS\_CONFIG\_MUX: drop IMU events if the USB output FIFO is full, instead of having them pile up at the input FIFOs. This normally should not be enabled to guarantee complete, coherent IMU events, and not get incomplete or wrong IMU information.

**4.1.2.353 #define DAVIS\_CONFIG\_MUX\_FORCE\_CHIP\_BIAS\_ENABLE 3**

Parameter address for module DAVIS\_CONFIG\_MUX: under normal circumstances, the chip's bias generator is only powered up when either the DVS or the APS state machines are running, to save power. This flag forces the bias generator to be powered up all the time, which may be useful when one wants to shut-down both APS and DVS temporarily, but still have a quick and well-defined resume behavior.

**4.1.2.354 #define DAVIS\_CONFIG\_MUX\_RUN 0**

Parameter address for module DAVIS\_CONFIG\_MUX: run the Multiplexer state machine, which is responsible for mixing the various event types at the device level, timestamping them and outputting them via USB or other connectors.

**4.1.2.355 #define DAVIS\_CONFIG\_MUX\_TIMESTAMP\_RESET 2**

Parameter address for module DAVIS\_CONFIG\_MUX: reset the Timestamp Generator to zero. This also sends a reset pulse to all connected slave devices, resetting their timestamp too.

**4.1.2.356 #define DAVIS\_CONFIG\_MUX\_TIMESTAMP\_RUN 1**

Parameter address for module DAVIS\_CONFIG\_MUX: run the Timestamp Generator inside the Multiplexer state machine, which will provide microsecond accurate timestamps to the events passing through.

#### 4.1.2.357 `#define DAVIS_CONFIG_SYSINFO 6`

Module address: device-side system information. The system information module provides various details on the device, such as currently installed logic revision or clock speeds. All its parameters are read-only. This is reserved for internal use and should not be used by anything other than libcaer. Please see the 'struct [caer\\_davis\\_info](#)' documentation for more details on what information is available.

#### 4.1.2.358 `#define DAVIS_CONFIG_SYSINFO_ADC_CLOCK 4`

Parameter address for module DAVIS\_CONFIG\_SYSINFO: read-only parameter, the frequency in MHz at which the FPGA/CPLD logic related to APS frame grabbing is running. This is reserved for internal use and should not be used by anything other than libcaer. Please see the 'struct [caer\\_davis\\_info](#)' documentation to get this information.

#### 4.1.2.359 `#define DAVIS_CONFIG_SYSINFO_CHIP_IDENTIFIER 1`

Parameter address for module DAVIS\_CONFIG\_SYSINFO: read-only parameter, an integer used to identify the different types of sensor chips used on the device. This is reserved for internal use and should not be used by anything other than libcaer. Please see the 'struct [caer\\_davis\\_info](#)' documentation to get this information.

#### 4.1.2.360 `#define DAVIS_CONFIG_SYSINFO_DEVICE_IS_MASTER 2`

Parameter address for module DAVIS\_CONFIG\_SYSINFO: read-only parameter, whether the device is currently a timestamp master or slave when synchronizing multiple devices together. This is reserved for internal use and should not be used by anything other than libcaer. Please see the 'struct [caer\\_davis\\_info](#)' documentation to get this information.

#### 4.1.2.361 `#define DAVIS_CONFIG_SYSINFO_LOGIC_CLOCK 3`

Parameter address for module DAVIS\_CONFIG\_SYSINFO: read-only parameter, the frequency in MHz at which the main FPGA/CPLD logic is running. This is reserved for internal use and should not be used by anything other than libcaer. Please see the 'struct [caer\\_davis\\_info](#)' documentation to get this information.

#### 4.1.2.362 `#define DAVIS_CONFIG_SYSINFO_LOGIC_VERSION 0`

Parameter address for module DAVIS\_CONFIG\_SYSINFO: read-only parameter, the version of the logic currently running on the device's FPGA/CPLD. It usually represents a specific SVN revision, at which the logic code was synthesized. This is reserved for internal use and should not be used by anything other than libcaer. Please see the 'struct [caer\\_davis\\_info](#)' documentation to get this information.

#### 4.1.2.363 `#define DAVIS_CONFIG_USB 9`

Module address: device-side USB output configuration. The USB output module forwards the data from the device and the FPGA/CPLD to the USB chip, usually a Cypress FX2 or FX3.

**4.1.2.364 #define DAVIS\_CONFIG\_USB\_EARLY\_PACKET\_DELAY 1**

Parameter address for module DAVIS\_CONFIG\_USB: the time delay after which a packet of data is committed to USB, even if it is not full yet (short USB packet). The value is in 125µs time-slices, corresponding to how USB schedules its operations (a value of 4 for example would mean waiting at most 0.5ms until sending a short USB packet to the host).

**4.1.2.365 #define DAVIS\_CONFIG\_USB\_RUN 0**

Parameter address for module DAVIS\_CONFIG\_USB: enable the USB FIFO module, which transfers the data from the FPGA/CPLD to the USB chip, to be then sent to the host. Turning this off will suppress any USB data communication!

**4.1.2.366 #define DAVISRGB\_CONFIG\_APS\_GSFDRESET 55**

Parameter address for module DAVIS\_CONFIG\_APS (only for DAVIS RGB chip): Global Shutter FD reset time in ADCClock cycles.

**4.1.2.367 #define DAVISRGB\_CONFIG\_APS\_GSPDRESET 52**

Parameter address for module DAVIS\_CONFIG\_APS (only for DAVIS RGB chip): Global Shutter PD reset time in ADCClock cycles.

**4.1.2.368 #define DAVISRGB\_CONFIG\_APS\_GSRESETFALL 53**

Parameter address for module DAVIS\_CONFIG\_APS (only for DAVIS RGB chip): Global Shutter Reset Fall time in ADCClock cycles.

**4.1.2.369 #define DAVISRGB\_CONFIG\_APS\_GSTXFALL 54**

Parameter address for module DAVIS\_CONFIG\_APS (only for DAVIS RGB chip): Global Shutter Transfer Fall time in ADCClock cycles.

**4.1.2.370 #define DAVISRGB\_CONFIG\_APS\_RSFDSETTLE 51**

Parameter address for module DAVIS\_CONFIG\_APS (only for DAVIS RGB chip): Rolling Shutter FD settle time in ADCClock cycles.

**4.1.2.371 #define DAVISRGB\_CONFIG\_APS\_TRANSFER 50**

Parameter address for module DAVIS\_CONFIG\_APS (only for DAVIS RGB chip): charge transfer time in ADCClock cycles.

#### 4.1.2.372 #define DAVISRGB\_CONFIG\_BIAS\_ADCCOMPBP 27

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.373 #define DAVISRGB\_CONFIG\_BIAS\_ADCREFHIGH 6

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.374 #define DAVISRGB\_CONFIG\_BIAS\_ADCREFLOW 7

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.375 #define DAVISRGB\_CONFIG\_BIAS\_ADCTESTVOLTAGE 5

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.376 `#define DAVISRGB_CONFIG_BIAS_AEPDBN 31`

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [`caerBiasVDACGenerate\(\)`](#) for VDAC (voltage) biases.
- [`caerBiasCoarseFineGenerate\(\)`](#) for coarse-fine (current) biases.
- [`caerBiasShiftedSourceGenerate\(\)`](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.377 `#define DAVISRGB_CONFIG_BIAS_AEPUXBP 32`

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [`caerBiasVDACGenerate\(\)`](#) for VDAC (voltage) biases.
- [`caerBiasCoarseFineGenerate\(\)`](#) for coarse-fine (current) biases.
- [`caerBiasShiftedSourceGenerate\(\)`](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.378 `#define DAVISRGB_CONFIG_BIAS_AEPUYBP 33`

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [`caerBiasVDACGenerate\(\)`](#) for VDAC (voltage) biases.
- [`caerBiasCoarseFineGenerate\(\)`](#) for coarse-fine (current) biases.
- [`caerBiasShiftedSourceGenerate\(\)`](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.379 `#define DAVISRGB_CONFIG_BIAS_APSCAS 0`

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [`caerBiasVDACGenerate\(\)`](#) for VDAC (voltage) biases.
- [`caerBiasCoarseFineGenerate\(\)`](#) for coarse-fine (current) biases.
- [`caerBiasShiftedSourceGenerate\(\)`](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.



**4.1.2.380 #define DAVISRGB\_CONFIG\_BIAS\_APSROSFBN 26**

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.381 #define DAVISRGB\_CONFIG\_BIAS\_ARRAYBIASBUFFERBN 20**

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.382 #define DAVISRGB\_CONFIG\_BIAS\_ARRAYLOGICBUFFERBN 22**

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.383 #define DAVISRGB\_CONFIG\_BIAS\_BIASBUFFER 34**

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.384 `#define DAVISRGB_CONFIG_BIAS_DACBUFBP 28`

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [`caerBiasVDACGenerate\(\)`](#) for VDAC (voltage) biases.
- [`caerBiasCoarseFineGenerate\(\)`](#) for coarse-fine (current) biases.
- [`caerBiasShiftedSourceGenerate\(\)`](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.385 `#define DAVISRGB_CONFIG_BIAS_DIFFBN 14`

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [`caerBiasVDACGenerate\(\)`](#) for VDAC (voltage) biases.
- [`caerBiasCoarseFineGenerate\(\)`](#) for coarse-fine (current) biases.
- [`caerBiasShiftedSourceGenerate\(\)`](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.386 `#define DAVISRGB_CONFIG_BIAS_FALLTIMEBN 23`

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [`caerBiasVDACGenerate\(\)`](#) for VDAC (voltage) biases.
- [`caerBiasCoarseFineGenerate\(\)`](#) for coarse-fine (current) biases.
- [`caerBiasShiftedSourceGenerate\(\)`](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.387 `#define DAVISRGB_CONFIG_BIAS_GND07 4`

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [`caerBiasVDACGenerate\(\)`](#) for VDAC (voltage) biases.
- [`caerBiasCoarseFineGenerate\(\)`](#) for coarse-fine (current) biases.
- [`caerBiasShiftedSourceGenerate\(\)`](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.388 #define DAVISRGB\_CONFIG\_BIAS\_IFREFRBN 8**

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.389 #define DAVISRGB\_CONFIG\_BIAS\_IFTHRBN 9**

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.390 #define DAVISRGB\_CONFIG\_BIAS\_LCOLTIMEOUTBN 30**

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.391 #define DAVISRGB\_CONFIG\_BIAS\_LOCALBUFBN 10**

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.392 `#define DAVISRGB_CONFIG_BIAS_OFFBN 16`

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.393 `#define DAVISRGB_CONFIG_BIAS_ONBN 15`

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.394 `#define DAVISRGB_CONFIG_BIAS_OVG1LO 1`

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.395 `#define DAVISRGB_CONFIG_BIAS_OVG2LO 2`

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- `caerBiasVDACGenerate()` for VDAC (voltage) biases.
- `caerBiasCoarseFineGenerate()` for coarse-fine (current) biases.
- `caerBiasShiftedSourceGenerate()` for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.396 #define DAVISRGB\_CONFIG\_BIAS\_PADFOLLBN 11**

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.397 #define DAVISRGB\_CONFIG\_BIAS\_PIXINBN 13**

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.398 #define DAVISRGB\_CONFIG\_BIAS\_PRBP 17**

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

**4.1.2.399 #define DAVISRGB\_CONFIG\_BIAS\_PRFBP 18**

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.400 `#define DAVISRGB_CONFIG_BIAS_READOUTBUFBP 25`

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [`caerBiasVDACGenerate\(\)`](#) for VDAC (voltage) biases.
- [`caerBiasCoarseFineGenerate\(\)`](#) for coarse-fine (current) biases.
- [`caerBiasShiftedSourceGenerate\(\)`](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.401 `#define DAVISRGB_CONFIG_BIAS_REFRBP 19`

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [`caerBiasVDACGenerate\(\)`](#) for VDAC (voltage) biases.
- [`caerBiasCoarseFineGenerate\(\)`](#) for coarse-fine (current) biases.
- [`caerBiasShiftedSourceGenerate\(\)`](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.402 `#define DAVISRGB_CONFIG_BIAS_RISETIMEBP 24`

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [`caerBiasVDACGenerate\(\)`](#) for VDAC (voltage) biases.
- [`caerBiasCoarseFineGenerate\(\)`](#) for coarse-fine (current) biases.
- [`caerBiasShiftedSourceGenerate\(\)`](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.403 `#define DAVISRGB_CONFIG_BIAS_SSN 36`

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [`caerBiasVDACGenerate\(\)`](#) for VDAC (voltage) biases.
- [`caerBiasCoarseFineGenerate\(\)`](#) for coarse-fine (current) biases.
- [`caerBiasShiftedSourceGenerate\(\)`](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.404 #define DAVISRGB\_CONFIG\_BIAS\_SSP 35

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.405 #define DAVISRGB\_CONFIG\_BIAS\_TX2OVG2HI 3

Parameter address for module DAVISRGB\_CONFIG\_BIAS: DAVISRGB chip biases. Bias configuration values must be generated using the proper functions, which are:

- [caerBiasVDACGenerate\(\)](#) for VDAC (voltage) biases.
- [caerBiasCoarseFineGenerate\(\)](#) for coarse-fine (current) biases.
- [caerBiasShiftedSourceGenerate\(\)](#) for shifted-source biases. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.1.2.406 #define DAVISRGB\_CONFIG\_CHIP\_ADJUSTOVG1LO 145

Parameter address for module DAVISRGB\_CONFIG\_CHIP: DAVISRGB chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.407 #define DAVISRGB\_CONFIG\_CHIP\_ADJUSTOVG2LO 146

Parameter address for module DAVISRGB\_CONFIG\_CHIP: DAVISRGB chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.408 #define DAVISRGB\_CONFIG\_CHIP\_ADJUSTTX2OVG2HI 147

Parameter address for module DAVISRGB\_CONFIG\_CHIP: DAVISRGB chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

#### 4.1.2.409 #define DAVISRGB\_CONFIG\_CHIP\_AERNAROW 140

Parameter address for module DAVISRGB\_CONFIG\_CHIP: DAVISRGB chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.410 #define DAVISRGB\_CONFIG\_CHIP\_ANALOGMUX0 132**

Parameter address for module DAVISRGB\_CONFIG\_CHIP: DAVISRGB chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.411 #define DAVISRGB\_CONFIG\_CHIP\_ANALOGMUX1 133**

Parameter address for module DAVISRGB\_CONFIG\_CHIP: DAVISRGB chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.412 #define DAVISRGB\_CONFIG\_CHIP\_ANALOGMUX2 134**

Parameter address for module DAVISRGB\_CONFIG\_CHIP: DAVISRGB chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.413 #define DAVISRGB\_CONFIG\_CHIP\_BIASMUX0 135**

Parameter address for module DAVISRGB\_CONFIG\_CHIP: DAVISRGB chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.414 #define DAVISRGB\_CONFIG\_CHIP\_DIGITALMUX0 128**

Parameter address for module DAVISRGB\_CONFIG\_CHIP: DAVISRGB chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.415 #define DAVISRGB\_CONFIG\_CHIP\_DIGITALMUX1 129**

Parameter address for module DAVISRGB\_CONFIG\_CHIP: DAVISRGB chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.416 #define DAVISRGB\_CONFIG\_CHIP\_DIGITALMUX2 130**

Parameter address for module DAVISRGB\_CONFIG\_CHIP: DAVISRGB chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.417 #define DAVISRGB\_CONFIG\_CHIP\_DIGITALMUX3 131**

Parameter address for module DAVISRGB\_CONFIG\_CHIP: DAVISRGB chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.



**4.1.2.418 #define DAVISRGB\_CONFIG\_CHIP\_RESETCALIBNEURON 136**

Parameter address for module DAVISRGB\_CONFIG\_CHIP: DAVISRGB chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.419 #define DAVISRGB\_CONFIG\_CHIP\_RESETESTPIXEL 138**

Parameter address for module DAVISRGB\_CONFIG\_CHIP: DAVISRGB chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.420 #define DAVISRGB\_CONFIG\_CHIP\_SELECTGRAYCOUNTER 143**

Parameter address for module DAVISRGB\_CONFIG\_CHIP: DAVISRGB chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.421 #define DAVISRGB\_CONFIG\_CHIP\_TESTADC 144**

Parameter address for module DAVISRGB\_CONFIG\_CHIP: DAVISRGB chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.422 #define DAVISRGB\_CONFIG\_CHIP\_TYPENCALIBNEURON 137**

Parameter address for module DAVISRGB\_CONFIG\_CHIP: DAVISRGB chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.423 #define DAVISRGB\_CONFIG\_CHIP\_USEAOUT 141**

Parameter address for module DAVISRGB\_CONFIG\_CHIP: DAVISRGB chip configuration. These are for expert control and should never be used or changed unless for advanced debugging purposes. To change the Global Shutter configuration, please use DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER instead.

**4.1.2.424 #define IS\_DAVIS128( chipID ) ((chipID) == DAVIS\_CHIP\_DAVIS128)**

Macros to check a chip identifier integer against the known chip types. Returns true if a chip identifier matches, false otherwise.

**4.1.2.425 #define IS\_DAVIS208( chipID ) ((chipID) == DAVIS\_CHIP\_DAVIS208)**

Macros to check a chip identifier integer against the known chip types. Returns true if a chip identifier matches, false otherwise.

4.1.2.426 `#define IS_DAVIS240( chipID ) (IS_DAVIS240A(chipID) || IS_DAVIS240B(chipID) || IS_DAVIS240C(chipID))`

Macros to check a chip identifier integer against the known chip types. Returns true if a chip identifier matches, false otherwise.

4.1.2.427 `#define IS_DAVIS240A( chipID ) ((chipID) == DAVIS_CHIP_DAVIS240A)`

Macros to check a chip identifier integer against the known chip types. Returns true if a chip identifier matches, false otherwise.

4.1.2.428 `#define IS_DAVIS240B( chipID ) ((chipID) == DAVIS_CHIP_DAVIS240B)`

Macros to check a chip identifier integer against the known chip types. Returns true if a chip identifier matches, false otherwise.

4.1.2.429 `#define IS_DAVIS240C( chipID ) ((chipID) == DAVIS_CHIP_DAVIS240C)`

Macros to check a chip identifier integer against the known chip types. Returns true if a chip identifier matches, false otherwise.

4.1.2.430 `#define IS_DAVIS346( chipID ) (IS_DAVIS346A(chipID) || IS_DAVIS346B(chipID) || IS_DAVIS346C(chipID))`

Macros to check a chip identifier integer against the known chip types. Returns true if a chip identifier matches, false otherwise.

4.1.2.431 `#define IS_DAVIS346A( chipID ) ((chipID) == DAVIS_CHIP_DAVIS346A)`

Macros to check a chip identifier integer against the known chip types. Returns true if a chip identifier matches, false otherwise.

4.1.2.432 `#define IS_DAVIS346B( chipID ) ((chipID) == DAVIS_CHIP_DAVIS346B)`

Macros to check a chip identifier integer against the known chip types. Returns true if a chip identifier matches, false otherwise.

4.1.2.433 `#define IS_DAVIS346C( chipID ) ((chipID) == DAVIS_CHIP_DAVIS346C)`

Macros to check a chip identifier integer against the known chip types. Returns true if a chip identifier matches, false otherwise.

4.1.2.434 `#define IS_DAVIS640( chipID ) ((chipID) == DAVIS_CHIP_DAVIS640)`

Macros to check a chip identifier integer against the known chip types. Returns true if a chip identifier matches, false otherwise.

4.1.2.435 `#define IS_DAVISRGB( chipID ) ((chipID) == DAVIS_CHIP_DAVISRGB)`

Macros to check a chip identifier integer against the known chip types. Returns true if a chip identifier matches, false otherwise.

### 4.1.3 Enumeration Type Documentation

#### 4.1.3.1 `enum caer_bias_shiftedsource_operating_mode`

Shifted-source bias operating mode.

Enumerator

**SHIFTED\_SOURCE** Standard mode.  
**HI\_Z** High impedance (driven from outside).  
**TIED\_TO\_RAIL** Tied to ground (SSN) or VDD (SSP).

#### 4.1.3.2 `enum caer_bias_shiftedsource_voltage_level`

Shifted-source bias voltage level.

Enumerator

**SPLIT\_GATE** Standard mode (200-400mV).  
**SINGLE\_DIODE** Higher shifted-source voltage (one cascode).  
**DOUBLE\_DIODE** Even higher shifted-source voltage (two cascodes).

### 4.1.4 Function Documentation

#### 4.1.4.1 `uint16_t caerBiasCoarseFineGenerate ( struct caer_bias_coarsefine coarseFineBias )`

Transform coarse-fine bias structure into internal integer representation, suited for sending directly to the device via [caerDeviceConfigSet\(\)](#).

Parameters

<i>coarseFineBias</i>	coarse-fine bias structure.
-----------------------	-----------------------------

Returns

internal integer representation for device configuration.

#### 4.1.4.2 `struct caer_bias_coarsefine caerBiasCoarseFineParse ( uint16_t coarseFineBias )`

Transform internal integer representation, as received by calls to [caerDeviceConfigGet\(\)](#), into a coarse-fine bias structure, for easier handling and understanding of the various parameters.

**Parameters**

<i>coarseFineBias</i>	internal integer representation from device.
-----------------------	--

**Returns**

coarse-fine bias structure.

**4.1.4.3 `uint16_t caerBiasShiftedSourceGenerate ( struct caer_bias_shiftedsourcesource shiftedSourceBias )`**

Transform shifted-source bias structure into internal integer representation, suited for sending directly to the device via [caerDeviceConfigSet\(\)](#).

**Parameters**

<i>shiftedSourceBias</i>	shifted-source bias structure.
--------------------------	--------------------------------

**Returns**

internal integer representation for device configuration.

**4.1.4.4 `struct caer_bias_shiftedsourcesource caerBiasShiftedSourceParse ( uint16_t shiftedSourceBias )`**

Transform internal integer representation, as received by calls to [caerDeviceConfigGet\(\)](#), into a shifted-source bias structure, for easier handling and understanding of the various parameters.

**Parameters**

<i>shiftedSourceBias</i>	internal integer representation from device.
--------------------------	--

**Returns**

shifted-source bias structure.

**4.1.4.5 `uint16_t caerBiasVDACGenerate ( struct caer_bias_vdac vdacBias )`**

Transform VDAC bias structure into internal integer representation, suited for sending directly to the device via [caerDeviceConfigSet\(\)](#).

**Parameters**

<i>vdacBias</i>	VDAC bias structure.
-----------------	----------------------

**Returns**

internal integer representation for device configuration.

**4.1.4.6 struct caer\_bias\_vdac caerBiasVDACParse ( uint16\_t *vdacBias* )**

Transform internal integer representation, as received by calls to [caerDeviceConfigGet\(\)](#), into a VDAC bias structure, for easier handling and understanding of the various parameters.

**Parameters**

<i>vdacBias</i>	internal integer representation from device.
-----------------	--

**Returns**

VDAC bias structure.

**4.1.4.7 struct caer\_davis\_info caerDavisInfoGet ( caerDeviceHandle *handle* )**

Return basic information on the device, such as its ID, its resolution, the logic version, and so on. See the 'struct [caer\\_davis\\_info](#)' documentation for more details.

**Parameters**

<i>handle</i>	a valid device handle.
---------------	------------------------

**Returns**

a copy of the device information structure if successful, an empty structure (all zeros) on failure.

## 4.2 devices/dvs128.h File Reference

```
#include "usb.h"
#include "../events/polarity.h"
#include "../events/special.h"
```

**Data Structures**

- struct [caer\\_dvs128\\_info](#)

## Macros

- `#define CAER_DEVICE_DVS128 0`
- `#define DVS128_CONFIG_DVS 0`
- `#define DVS128_CONFIG_BIAS 1`
- `#define DVS128_CONFIG_DVS_RUN 0`
- `#define DVS128_CONFIG_DVS_TIMESTAMP_RESET 1`
- `#define DVS128_CONFIG_DVS_ARRAY_RESET 2`
- `#define DVS128_CONFIG_DVS_TS_MASTER 3`
- `#define DVS128_CONFIG_BIAS_CAS 0`
- `#define DVS128_CONFIG_BIAS_INJGND 1`
- `#define DVS128_CONFIG_BIAS_REQPD 2`
- `#define DVS128_CONFIG_BIAS_PUX 3`
- `#define DVS128_CONFIG_BIAS_DIFFOFF 4`
- `#define DVS128_CONFIG_BIAS_REQ 5`
- `#define DVS128_CONFIG_BIAS_REFR 6`
- `#define DVS128_CONFIG_BIAS_PUY 7`
- `#define DVS128_CONFIG_BIAS_DIFFON 8`
- `#define DVS128_CONFIG_BIAS_DIFF 9`
- `#define DVS128_CONFIG_BIAS_FOLL 10`
- `#define DVS128_CONFIG_BIAS_PR 11`

## Functions

- struct `caer_dvs128_info` `caerDVS128InfoGet` (`caerDeviceHandle` handle)

### 4.2.1 Detailed Description

DVS128 specific configuration defines and information structures.

### 4.2.2 Macro Definition Documentation

#### 4.2.2.1 `#define CAER_DEVICE_DVS128 0`

Device type definition for iniLabs DVS128.

#### 4.2.2.2 `#define DVS128_CONFIG_BIAS 1`

Module address: device-side chip bias generator configuration.

#### 4.2.2.3 `#define DVS128_CONFIG_BIAS_CAS 0`

Parameter address for module DVS128\_CONFIG\_BIAS: First stage amplifier cascode bias. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.2.2.4 #define DVS128\_CONFIG\_BIAS\_DIFF 9

Parameter address for module DVS128\_CONFIG\_BIAS: Differential (second stage amplifier) bias. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.2.2.5 #define DVS128\_CONFIG\_BIAS\_DIFFOFF 4

Parameter address for module DVS128\_CONFIG\_BIAS: Off events threshold bias. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.2.2.6 #define DVS128\_CONFIG\_BIAS\_DIFFON 8

Parameter address for module DVS128\_CONFIG\_BIAS: On events threshold bias. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.2.2.7 #define DVS128\_CONFIG\_BIAS\_FOLL 10

Parameter address for module DVS128\_CONFIG\_BIAS: Source follower bias. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.2.2.8 #define DVS128\_CONFIG\_BIAS\_INJGND 1

Parameter address for module DVS128\_CONFIG\_BIAS: Injected ground bias. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.2.2.9 #define DVS128\_CONFIG\_BIAS\_PR 11

Parameter address for module DVS128\_CONFIG\_BIAS: Photoreceptor bias. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.2.2.10 #define DVS128\_CONFIG\_BIAS\_PUX 3

Parameter address for module DVS128\_CONFIG\_BIAS: Pull up on request from X arbiter (AER). See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.2.2.11 #define DVS128\_CONFIG\_BIAS\_PUY 7

Parameter address for module DVS128\_CONFIG\_BIAS: Pull up on request from Y arbiter (AER). See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.2.2.12 #define DVS128\_CONFIG\_BIAS\_REFR 6

Parameter address for module DVS128\_CONFIG\_BIAS: Refractory period bias. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.2.2.13 `#define DVS128_CONFIG_BIAS_REQ 5`

Parameter address for module DVS128\_CONFIG\_BIAS: Pull down for passive load inverters in digital AER pixel circuitry. See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.2.2.14 `#define DVS128_CONFIG_BIAS_REQPD 2`

Parameter address for module DVS128\_CONFIG\_BIAS: Pull down on chip request (AER). See '<http://inilabs.com/support/biasing/>' for more details.

#### 4.2.2.15 `#define DVS128_CONFIG_DVS 0`

Module address: device-side DVS configuration.

#### 4.2.2.16 `#define DVS128_CONFIG_DVS_ARRAY_RESET 2`

Parameter address for module DVS128\_CONFIG\_DVS: reset the whole DVS pixel array. This is a temporary configuration switch and will reset itself right away.

#### 4.2.2.17 `#define DVS128_CONFIG_DVS_RUN 0`

Parameter address for module DVS128\_CONFIG\_DVS: run the DVS chip and generate polarity event data.

#### 4.2.2.18 `#define DVS128_CONFIG_DVS_TIMESTAMP_RESET 1`

Parameter address for module DVS128\_CONFIG\_DVS: reset the time-stamp counter of the device. This is a temporary configuration switch and will reset itself right away.

#### 4.2.2.19 `#define DVS128_CONFIG_DVS_TS_MASTER 3`

Parameter address for module DVS128\_CONFIG\_DVS: control if this DVS is a timestamp master device. Default is enabled.

### 4.2.3 Function Documentation

#### 4.2.3.1 `struct caer_dvs128_info caerDVS128InfoGet ( caerDeviceHandle handle )`

Return basic information on the device, such as its ID, its resolution, the logic version, and so on. See the '[struct caer\\_dvs128\\_info](#)' documentation for more details.

##### Parameters

<i>handle</i>	a valid device handle.
---------------	------------------------



**Returns**

a copy of the device information structure if successful, an empty structure (all zeros) on failure.

**4.3 devices/dynapse.h File Reference**

```
#include "usb.h"
#include "../events/spike.h"
```

**Data Structures**

- struct [caer\\_dynapse\\_info](#)

**Macros**

- #define [CAER\\_DEVICE\\_DYNAPSE](#) 3
- #define [DYNAPSE\\_CHIP\\_DYNAPSE](#) 64
- #define [DYNAPSE\\_CONFIG\\_MUX](#) 0
- #define [DYNAPSE\\_CONFIG\\_AER](#) 1
- #define [DYNAPSE\\_CONFIG\\_CHIP](#) 5
- #define [DYNAPSE\\_CONFIG\\_SYSINFO](#) 6
- #define [DYNAPSE\\_CONFIG\\_USB](#) 9
- #define [DYNAPSE\\_CONFIG\\_MUX\\_RUN](#) 0
- #define [DYNAPSE\\_CONFIG\\_MUX\\_TIMESTAMP\\_RUN](#) 1
- #define [DYNAPSE\\_CONFIG\\_MUX\\_TIMESTAMP\\_RESET](#) 2
- #define [DYNAPSE\\_CONFIG\\_MUX\\_FORCE\\_CHIP\\_BIAS\\_ENABLE](#) 3
- #define [DYNAPSE\\_CONFIG\\_MUX\\_DROP\\_AER\\_ON\\_TRANSFER\\_STALL](#) 4
- #define [DYNAPSE\\_CONFIG\\_AER\\_RUN](#) 3
- #define [DYNAPSE\\_CONFIG\\_AER\\_ACK\\_DELAY](#) 4
- #define [DYNAPSE\\_CONFIG\\_AER\\_ACK\\_EXTENSION](#) 6
- #define [DYNAPSE\\_CONFIG\\_AER\\_WAIT\\_ON\\_TRANSFER\\_STALL](#) 8
- #define [DYNAPSE\\_CONFIG\\_AER\\_EXTERNAL\\_AER\\_CONTROL](#) 10
- #define [DYNAPSE\\_CONFIG\\_CHIP\\_RUN](#) 0
- #define [DYNAPSE\\_CONFIG\\_CHIP\\_ID](#) 1
- #define [DYNAPSE\\_CONFIG\\_CHIP\\_CONTENT](#) 2
- #define [DYNAPSE\\_CONFIG\\_CHIP\\_REQ\\_DELAY](#) 3
- #define [DYNAPSE\\_CONFIG\\_CHIP\\_REQ\\_EXTENSION](#) 4
- #define [DYNAPSE\\_CONFIG\\_SYSINFO\\_LOGIC\\_VERSION](#) 0
- #define [DYNAPSE\\_CONFIG\\_SYSINFO\\_CHIP\\_IDENTIFIER](#) 1
- #define [DYNAPSE\\_CONFIG\\_SYSINFO\\_DEVICE\\_IS\\_MASTER](#) 2
- #define [DYNAPSE\\_CONFIG\\_SYSINFO\\_LOGIC\\_CLOCK](#) 3
- #define [DYNAPSE\\_CONFIG\\_USB\\_RUN](#) 0
- #define [DYNAPSE\\_CONFIG\\_USB\\_EARLY\\_PACKET\\_DELAY](#) 1

**Functions**

- struct [caer\\_dynapse\\_info](#) [caerDynapseInfoGet](#) ([caerDeviceHandle](#) handle)

### 4.3.1 Detailed Description

Dynap-se specific configuration defines and information structures.

### 4.3.2 Macro Definition Documentation

#### 4.3.2.1 `#define CAER_DEVICE_DYNAPSE 3`

Device type definition for iniLabs Dynap-se FX2-based boards.

#### 4.3.2.2 `#define DYNAPSE_CHIP_DYNAPSE 64`

Dynap-se chip identifier.

#### 4.3.2.3 `#define DYNAPSE_CONFIG_AER 1`

Module address: device-side AER configuration (from chip). The AER state machine handshakes with the chip's AER bus and gets the spike events from it. It supports various configurable delays.

#### 4.3.2.4 `#define DYNAPSE_CONFIG_AER_ACK_DELAY 4`

Parameter address for module DYNAPSE\_CONFIG\_AER: delay capturing the data and acknowledging it on the AER bus for the events by this many LogicClock cycles.

#### 4.3.2.5 `#define DYNAPSE_CONFIG_AER_ACK_EXTENSION 6`

Parameter address for module DYNAPSE\_CONFIG\_AER: extend the length of the acknowledge on the AER bus for the events by this many LogicClock cycles.

#### 4.3.2.6 `#define DYNAPSE_CONFIG_AER_EXTERNAL_AER_CONTROL 10`

Parameter address for module DYNAPSE\_CONFIG\_AER: enable external AER control. This ensures the chip and the neuron array are running, but doesn't do the handshake and leaves the ACK pin in high-impedance, to allow for an external system to take over the AER communication with the chip. DYNAPSE\_CONFIG\_AER\_RUN has to be turned off for this to work.

#### 4.3.2.7 `#define DYNAPSE_CONFIG_AER_RUN 3`

Parameter address for module DYNAPSE\_CONFIG\_AER: run the AER state machine and get spike events from the chip by handshaking with its AER bus.

#### 4.3.2.8 `#define DYNAPSE_CONFIG_AER_WAIT_ON_TRANSFER_STALL 8`

Parameter address for module `DYNAPSE_CONFIG_AER`: if the output FIFO for this module is full, stall the AER handshake with the chip and wait until it's free again, instead of just continuing the handshake and dropping the resulting events.

#### 4.3.2.9 `#define DYNAPSE_CONFIG_CHIP 5`

Module address: device-side chip control configuration. This state machine is responsible for configuring the chip's internal control registers, to set special options and biases.

#### 4.3.2.10 `#define DYNAPSE_CONFIG_CHIP_CONTENT 2`

Parameter address for module `DYNAPSE_CONFIG_CHIP`: set the configuration content to send to the chip. Every time this changes, the chip ID is appended and the configuration is sent out to the chip.

#### 4.3.2.11 `#define DYNAPSE_CONFIG_CHIP_ID 1`

Parameter address for module `DYNAPSE_CONFIG_CHIP`: set the chip ID to which configuration content is being sent.

#### 4.3.2.12 `#define DYNAPSE_CONFIG_CHIP_REQ_DELAY 3`

Parameter address for module `DYNAPSE_CONFIG_CHIP`: delay doing the request after putting out the data by this many LogicClock cycles.

#### 4.3.2.13 `#define DYNAPSE_CONFIG_CHIP_REQ_EXTENSION 4`

Parameter address for module `DYNAPSE_CONFIG_CHIP`: extend the request after receiving the ACK by this many LogicClock cycles.

#### 4.3.2.14 `#define DYNAPSE_CONFIG_CHIP_RUN 0`

Parameter address for module `DYNAPSE_CONFIG_CHIP`: enable the configuration AER state machine to send bias and control configuration to the chip.

#### 4.3.2.15 `#define DYNAPSE_CONFIG_MUX 0`

Module address: device-side Multiplexer configuration. The Multiplexer is responsible for mixing, timestamping and outputting (via USB) the various event types generated by the device. It is also responsible for timestamp generation.

#### 4.3.2.16 `#define DYNAPSE_CONFIG_MUX_DROP_AER_ON_TRANSFER_STALL 4`

Parameter address for module `DYNAPSE_CONFIG_MUX`: drop AER events if the USB output FIFO is full, instead of having them pile up at the input FIFOs.

#### 4.3.2.17 `#define DYNAPSE_CONFIG_MUX_FORCE_CHIP_BIAS_ENABLE 3`

Parameter address for module `DYNAPSE_CONFIG_MUX`: under normal circumstances, the chip's bias generator is only powered up when either the AER or the configuration state machines are running, to save power. This flag forces the bias generator to be powered up all the time.

#### 4.3.2.18 `#define DYNAPSE_CONFIG_MUX_RUN 0`

Parameter address for module `DYNAPSE_CONFIG_MUX`: run the Multiplexer state machine, which is responsible for mixing the various event types at the device level, timestamping them and outputting them via USB or other connectors.

#### 4.3.2.19 `#define DYNAPSE_CONFIG_MUX_TIMESTAMP_RESET 2`

Parameter address for module `DYNAPSE_CONFIG_MUX`: reset the Timestamp Generator to zero. This also sends a reset pulse to all connected slave devices, resetting their timestamp too.

#### 4.3.2.20 `#define DYNAPSE_CONFIG_MUX_TIMESTAMP_RUN 1`

Parameter address for module `DYNAPSE_CONFIG_MUX`: run the Timestamp Generator inside the Multiplexer state machine, which will provide microsecond accurate timestamps to the events passing through.

#### 4.3.2.21 `#define DYNAPSE_CONFIG_SYSINFO 6`

Module address: device-side system information. The system information module provides various details on the device, such as currently installed logic revision or clock speeds. All its parameters are read-only. This is reserved for internal use and should not be used by anything other than `libcaer`. Please see the 'struct [caer\\_dynapse\\_info](#)' documentation for more details on what information is available.

#### 4.3.2.22 `#define DYNAPSE_CONFIG_SYSINFO_CHIP_IDENTIFIER 1`

Parameter address for module `DYNAPSE_CONFIG_SYSINFO`: read-only parameter, an integer used to identify the different types of sensor chips used on the device. This is reserved for internal use and should not be used by anything other than `libcaer`. Please see the 'struct [caer\\_dynapse\\_info](#)' documentation to get this information.

#### 4.3.2.23 `#define DYNAPSE_CONFIG_SYSINFO_DEVICE_IS_MASTER 2`

Parameter address for module `DYNAPSE_CONFIG_SYSINFO`: read-only parameter, whether the device is currently a timestamp master or slave when synchronizing multiple devices together. This is reserved for internal use and should not be used by anything other than `libcaer`. Please see the 'struct [caer\\_dynapse\\_info](#)' documentation to get this information.

4.3.2.24 `#define DYNAPSE_CONFIG_SYSINFO_LOGIC_CLOCK 3`

Parameter address for module DYNAPSE\_CONFIG\_SYSINFO: read-only parameter, the frequency in MHz at which the main FPGA/CPLD logic is running. This is reserved for internal use and should not be used by anything other than libcaer. Please see the 'struct [caer\\_dynapse\\_info](#)' documentation to get this information.

4.3.2.25 `#define DYNAPSE_CONFIG_SYSINFO_LOGIC_VERSION 0`

Parameter address for module DYNAPSE\_CONFIG\_SYSINFO: read-only parameter, the version of the logic currently running on the device's FPGA/CPLD. It usually represents a specific SVN revision, at which the logic code was synthesized. This is reserved for internal use and should not be used by anything other than libcaer. Please see the 'struct [caer\\_dynapse\\_info](#)' documentation to get this information.

4.3.2.26 `#define DYNAPSE_CONFIG_USB 9`

Module address: device-side USB output configuration. The USB output module forwards the data from the device and the FPGA/CPLD to the USB chip, usually a Cypress FX2 or FX3.

4.3.2.27 `#define DYNAPSE_CONFIG_USB_EARLY_PACKET_DELAY 1`

Parameter address for module DYNAPSE\_CONFIG\_USB: the time delay after which a packet of data is committed to USB, even if it is not full yet (short USB packet). The value is in 125µs time-slices, corresponding to how USB schedules its operations (a value of 4 for example would mean waiting at most 0.5ms until sending a short USB packet to the host).

4.3.2.28 `#define DYNAPSE_CONFIG_USB_RUN 0`

Parameter address for module DYNAPSE\_CONFIG\_USB: enable the USB FIFO module, which transfers the data from the FPGA/CPLD to the USB chip, to be then sent to the host. Turning this off will suppress any USB data communication!

### 4.3.3 Function Documentation

4.3.3.1 `struct caer_dynapse_info caerDynapseInfoGet ( caerDeviceHandle handle )`

Return basic information on the device, such as its ID, the logic version, and so on. See the 'struct [caer\\_dynapse\\_info](#)' documentation for more details.

#### Parameters

<i>handle</i>	a valid device handle.
---------------	------------------------

#### Returns

a copy of the device information structure if successful, an empty structure (all zeros) on failure.

## 4.4 devices/usb.h File Reference

```
#include "../libcaer.h"
#include "../events/packetContainer.h"
```

### Macros

- `#define CAER_HOST_CONFIG_USB -1`
- `#define CAER_HOST_CONFIG_DATAEXCHANGE -2`
- `#define CAER_HOST_CONFIG_PACKETS -3`
- `#define CAER_HOST_CONFIG_USB_BUFFER_NUMBER 0`
- `#define CAER_HOST_CONFIG_USB_BUFFER_SIZE 1`
- `#define CAER_HOST_CONFIG_DATAEXCHANGE_BUFFER_SIZE 0`
- `#define CAER_HOST_CONFIG_DATAEXCHANGE_BLOCKING 1`
- `#define CAER_HOST_CONFIG_DATAEXCHANGE_START_PRODUCERS 2`
- `#define CAER_HOST_CONFIG_DATAEXCHANGE_STOP_PRODUCERS 3`
- `#define CAER_HOST_CONFIG_PACKETS_MAX_CONTAINER_PACKET_SIZE 0`
- `#define CAER_HOST_CONFIG_PACKETS_MAX_CONTAINER_INTERVAL 1`

### Typedefs

- `typedef struct caer_device_handle * caerDeviceHandle`

### Functions

- `caerDeviceHandle caerDeviceOpen` (uint16\_t deviceID, uint16\_t deviceType, uint8\_t busNumberRestrict, uint8\_t devAddressRestrict, const char \*serialNumberRestrict)
- `bool caerDeviceClose` (caerDeviceHandle \*handle)
- `bool caerDeviceSendDefaultConfig` (caerDeviceHandle handle)
- `bool caerDeviceConfigSet` (caerDeviceHandle handle, int8\_t modAddr, uint8\_t paramAddr, uint32\_t param)
- `bool caerDeviceConfigGet` (caerDeviceHandle handle, int8\_t modAddr, uint8\_t paramAddr, uint32\_t \*param)
- `bool caerDeviceDataStart` (caerDeviceHandle handle, void(\*dataNotifyIncrease)(void \*ptr), void(\*dataNotifyDecrease)(void \*ptr), void \*dataNotifyUserPtr, void(\*dataShutdownNotify)(void \*ptr), void \*dataShutdownUserPtr)
- `bool caerDeviceDataStop` (caerDeviceHandle handle)
- `caerEventPacketContainer caerDeviceDataGet` (caerDeviceHandle handle)

#### 4.4.1 Detailed Description

Common functions to access, configure and exchange data with supported USB devices. Also contains defines for host/USB related configuration options.

#### 4.4.2 Macro Definition Documentation

##### 4.4.2.1 `#define CAER_HOST_CONFIG_DATAEXCHANGE -2`

Module address: host-side data exchange (ring-buffer) configuration.

#### 4.4.2.2 #define CAER\_HOST\_CONFIG\_DATAEXCHANGE\_BLOCKING 1

Parameter address for module CAER\_HOST\_CONFIG\_DATAEXCHANGE: when calling [caerDeviceDataGet\(\)](#), the function can either be blocking, meaning it waits until it has a valid EventPacketContainer to return, or not, meaning it returns right away. This behavior can be set with this flag. Please see the [caerDeviceDataGet\(\)](#) documentation for more information on its return values.

#### 4.4.2.3 #define CAER\_HOST\_CONFIG\_DATAEXCHANGE\_BUFFER\_SIZE 0

Parameter address for module CAER\_HOST\_CONFIG\_DATAEXCHANGE: set size of elements that can be held by the thread-safe FIFO buffer between the USB data transfer thread and the main thread. The default values are usually fine, only change them if you're running into lots of dropped/missing packets; you can turn on the INFO log level to see when this is the case.

#### 4.4.2.4 #define CAER\_HOST\_CONFIG\_DATAEXCHANGE\_START\_PRODUCERS 2

Parameter address for module CAER\_HOST\_CONFIG\_DATAEXCHANGE: whether to start all the data producer modules on the device (DVS, APS, Mux, ...) automatically when starting the USB data transfer thread with [caer↔DeviceDataStart\(\)](#) or not. If disabled, be aware you will have to start the right modules manually, which can be useful if you need precise control over which ones are running at any time.

#### 4.4.2.5 #define CAER\_HOST\_CONFIG\_DATAEXCHANGE\_STOP\_PRODUCERS 3

Parameter address for module CAER\_HOST\_CONFIG\_DATAEXCHANGE: whether to stop all the data producer modules on the device (DVS, APS, Mux, ...) automatically when stopping the USB data transfer thread with [caer↔DeviceDataStop\(\)](#) or not. If disabled, be aware you will have to stop the right modules manually, to halt the data flow, which can be useful if you need precise control over which ones are running at any time.

#### 4.4.2.6 #define CAER\_HOST\_CONFIG\_PACKETS -3

Module address: host-side event packets generation configuration.

#### 4.4.2.7 #define CAER\_HOST\_CONFIG\_PACKETS\_MAX\_CONTAINER\_INTERVAL 1

Parameter address for module CAER\_HOST\_CONFIG\_PACKETS: set the time interval between subsequent packet containers. The value is in microseconds, and is checked across all types of events contained in the Event↔PacketContainer.

#### 4.4.2.8 #define CAER\_HOST\_CONFIG\_PACKETS\_MAX\_CONTAINER\_PACKET\_SIZE 0

Parameter address for module CAER\_HOST\_CONFIG\_PACKETS: set the maximum number of events any of a packet container's packets may hold before it's made available to the user. This is checked for each number of events held in each typed EventPacket that is a part of the EventPacketContainer.

#### 4.4.2.9 `#define CAER_HOST_CONFIG_USB -1`

Module address: host-side USB configuration.

#### 4.4.2.10 `#define CAER_HOST_CONFIG_USB_BUFFER_NUMBER 0`

Parameter address for module CAER\_HOST\_CONFIG\_USB: set number of buffers used by libusb for asynchronous data transfers with the USB device. The default values are usually fine, only change them if you're running into I/O limits.

#### 4.4.2.11 `#define CAER_HOST_CONFIG_USB_BUFFER_SIZE 1`

Parameter address for module CAER\_HOST\_CONFIG\_USB: set size of each buffer used by libusb for asynchronous data transfers with the USB device. The default values are usually fine, only change them if you're running into I/O limits.

### 4.4.3 Typedef Documentation

#### 4.4.3.1 `typedef struct caer_device_handle* caerDeviceHandle`

Reference to an open device on which to operate.

### 4.4.4 Function Documentation

#### 4.4.4.1 `bool caerDeviceClose ( caerDeviceHandle * handle )`

Close a previously opened USB device and invalidate its handle.

##### Parameters

<i>handle</i>	pointer to a valid device handle. Will set handle to NULL if closing is successful, to prevent further usage of this handle for other operations.
---------------	---

##### Returns

true if closing was successful, false on errors.

#### 4.4.4.2 `bool caerDeviceConfigGet ( caerDeviceHandle handle, int8_t modAddr, uint8_t paramAddr, uint32_t * param )`

Get the value of a configuration parameter.

##### Parameters

<i>handle</i>	a valid device handle.
---------------	------------------------



## Parameters

<i>modAddr</i>	a module address, used to specify which configuration module one wants to query. Negative addresses are used for host-side configuration, while positive addresses (including zero) are used for device-side configuration.
<i>paramAddr</i>	a parameter address, to select a specific parameter to query from this particular configuration module. Only positive numbers (including zero) are allowed.
<i>param</i>	a pointer to an integer, in which to store the configuration parameter's current value. The integer will always be either set to zero (on failure), or to the current value (on success).

## Returns

true if sending the configuration was successful, false on errors.

#### 4.4.4.3 bool caerDeviceConfigSet ( caerDeviceHandle handle, int8\_t modAddr, uint8\_t paramAddr, uint32\_t param )

Set a configuration parameter to a given value.

## Parameters

<i>handle</i>	a valid device handle.
<i>modAddr</i>	a module address, used to specify which configuration module one wants to update. Negative addresses are used for host-side configuration, while positive addresses (including zero) are used for device-side configuration.
<i>paramAddr</i>	a parameter address, to select a specific parameter to update from this particular configuration module. Only positive numbers (including zero) are allowed.
<i>param</i>	a configuration parameter's new value.

## Returns

true if sending the configuration was successful, false on errors.

#### 4.4.4.4 caerEventPacketContainer caerDeviceDataGet ( caerDeviceHandle handle )

Get an event packet container, which contains events of various types generated by the device, from the USB data transfer thread for further processing. The returned data structures are allocated in memory and will need to be freed. The [caerEventPacketContainerFree\(\)](#) function can be used to correctly free the full container memory. For single caerEventPackets, just use free(). This function can be made blocking with the CAER\_HOST\_CONFIG\_D↔ATAEXCHANGE\_BLOCKING configuration parameter. By default it is non-blocking.

## Parameters

<i>handle</i>	a valid device handle.
---------------	------------------------

## Returns

a valid event packet container. NULL will be returned on errors, or when there is no container available in non-blocking mode. Always check for this!

**4.4.4.5** `bool caerDeviceDataStart ( caerDeviceHandle handle, void(*)(void *ptr) dataNotifyIncrease, void(*)(void *ptr) dataNotifyDecrease, void * dataNotifyUserPtr, void(*)(void *ptr) dataShutdownNotify, void * dataShutdownUserPtr )`

Start getting data from the device, setting up the USB data transfer thread and starting the data producers (see CAER\_HOST\_CONFIG\_DATAEXCHANGE\_START\_PRODUCERS). Supports notification of new data and shutdown events via user-defined call-backs.

#### Parameters

<i>handle</i>	a valid device handle.
<i>dataNotifyIncrease</i>	function pointer, called every time a new piece of data available and has been put in the FIFO buffer for consumption. dataNotifyUserPtr will be passed as parameter to the function.
<i>dataNotifyDecrease</i>	function pointer, called every time a new piece of data has been consumed from the FIFO buffer inside <a href="#">caerDeviceDataGet()</a> . dataNotifyUserPtr will be passed as parameter to the function.
<i>dataNotifyUserPtr</i>	pointer that will be passed to the dataNotifyIncrease and dataNotifyDecrease functions. Can be NULL.
<i>dataShutdownNotify</i>	function pointer, called on shut-down of the USB data transfer thread. This can be used to detect exceptional shut-downs that do not come from calling <a href="#">caerDeviceDataStop()</a> , such as when the device is disconnected or all USB transfers fail.
<i>dataShutdownUserPtr</i>	pointer that will be passed to the dataShutdownNotify function. Can be NULL.

#### Returns

true if starting the data transfer was successful, false on errors.

**4.4.4.6** `bool caerDeviceDataStop ( caerDeviceHandle handle )`

Stop getting data from the device, shutting down the USB data transfer thread and stopping the data producers (see CAER\_HOST\_CONFIG\_DATAEXCHANGE\_STOP\_PRODUCERS). This normal shut-down will also generate a notification (see [caerDeviceDataStart\(\)](#)).

#### Parameters

<i>handle</i>	a valid device handle.
---------------	------------------------

#### Returns

true if stopping the data transfer was successful, false on errors.

**4.4.4.7** `caerDeviceHandle caerDeviceOpen ( uint16_t deviceID, uint16_t deviceType, uint8_t busNumberRestrict, uint8_t devAddressRestrict, const char * serialNumberRestrict )`

Open a specified USB device, assign an ID to it and return a handle for further usage. Various means can be employed to limit the selection of the device.

## Parameters

<i>deviceId</i>	a unique ID to identify the device from others. Will be used as the source for EventPackets being generate from its data.
<i>deviceType</i>	type of the device to open. Currently supported are: CAER_DEVICE_DVS128, CAER_DEVICE_DAVIS_FX2, CAER_DEVICE_DAVIS_FX3
<i>busNumberRestrict</i>	restrict the search for viable devices to only this USB bus number.
<i>devAddressRestrict</i>	restrict the search for viable devices to only this USB device address.
<i>serialNumberRestrict</i>	restrict the search for viable devices to only devices which do possess the given Serial Number in their USB SerialNumber descriptor.

## Returns

a valid device handle that can be used with the other libcaer functions, or NULL on error. Always check for this!

## 4.4.4.8 bool caerDeviceSendDefaultConfig ( caerDeviceHandle handle )

Send a set of good default configuration settings to the device. This avoids users having to set every configuration option each time, especially when wanting to get going quickly or just needing to change a few settings to get to the desired operating mode.

## Parameters

<i>handle</i>	a valid device handle.
---------------	------------------------

## Returns

true if sending the configuration was successful, false on errors.

## 4.5 events/common.h File Reference

```
#include "../libcaer.h"
```

## Macros

- `#define TS_OVERFLOW_SHIFT 31`
- `#define CAER_EVENT_PACKET_HEADER_SIZE 28`
- `#define CAER_ITERATOR_ALL_START(PACKET_HEADER, EVENT_TYPE)`
- `#define CAER_ITERATOR_ALL_END }`
- `#define CAER_ITERATOR_VALID_START(PACKET_HEADER, EVENT_TYPE)`
- `#define CAER_ITERATOR_VALID_END }`
  
- `#define VALID_MARK_SHIFT 0`
- `#define VALID_MARK_MASK 0x00000001`

## Typedefs

- typedef struct caer\_event\_packet\_header \* caerEventPacketHeader

## Enumerations

- enum caer\_default\_event\_types {  
     SPECIAL\_EVENT = 0, POLARITY\_EVENT = 1, FRAME\_EVENT = 2, IMU6\_EVENT = 3,  
     IMU9\_EVENT = 4, SAMPLE\_EVENT = 5, EAR\_EVENT = 6, CONFIG\_EVENT = 7,  
     POINT1D\_EVENT = 8, POINT2D\_EVENT = 9, POINT3D\_EVENT = 10, POINT4D\_EVENT = 11,  
     SPIKE\_EVENT = 12 }

## Functions

- **PACKED\_STRUCT** (struct caer\_event\_packet\_header{int16\_t eventType;int16\_t eventSource;int32\_t eventSize;int32\_t eventTSOffset;int32\_t eventTSOverflow;int32\_t eventCapacity;int32\_t eventNumber;int32\_t eventValid;})
- static int16\_t caerEventPacketHeaderGetEventType (caerEventPacketHeader header)
- static void caerEventPacketHeaderSetEventType (caerEventPacketHeader header, int16\_t eventType)
- static int16\_t caerEventPacketHeaderGetEventSource (caerEventPacketHeader header)
- static void caerEventPacketHeaderSetEventSource (caerEventPacketHeader header, int16\_t eventSource)
- static int32\_t caerEventPacketHeaderGetEventSize (caerEventPacketHeader header)
- static void caerEventPacketHeaderSetEventSize (caerEventPacketHeader header, int32\_t eventSize)
- static int32\_t caerEventPacketHeaderGetEventTSOffset (caerEventPacketHeader header)
- static void caerEventPacketHeaderSetEventTSOffset (caerEventPacketHeader header, int32\_t eventTSOffset)
- static int32\_t caerEventPacketHeaderGetEventTSOverflow (caerEventPacketHeader header)
- static void caerEventPacketHeaderSetEventTSOverflow (caerEventPacketHeader header, int32\_t eventTSOverflow)
- static int32\_t caerEventPacketHeaderGetEventCapacity (caerEventPacketHeader header)
- static void caerEventPacketHeaderSetEventCapacity (caerEventPacketHeader header, int32\_t eventsCapacity)
- static int32\_t caerEventPacketHeaderGetEventNumber (caerEventPacketHeader header)
- static void caerEventPacketHeaderSetEventNumber (caerEventPacketHeader header, int32\_t eventsNumber)
- static int32\_t caerEventPacketHeaderGetEventValid (caerEventPacketHeader header)
- static void caerEventPacketHeaderSetEventValid (caerEventPacketHeader header, int32\_t eventsValid)
- static caerEventPacketHeader caerGenericEventPacketGrow (caerEventPacketHeader packet, int32\_t newEventCapacity)
- static caerEventPacketHeader caerGenericEventPacketAppend (caerEventPacketHeader packet, caerEventPacketHeader appendPacket)
- static void \* caerGenericEventGetEvent (caerEventPacketHeader headerPtr, int32\_t n)
- static int32\_t caerGenericEventGetTimestamp (void \*eventPtr, caerEventPacketHeader headerPtr)
- static int64\_t caerGenericEventGetTimestamp64 (void \*eventPtr, caerEventPacketHeader headerPtr)
- static bool caerGenericEventsValid (void \*eventPtr)
- static void \* caerCopyEventPacket (void \*eventPacket)
- static void \* caerCopyEventPacketOnlyEvents (void \*eventPacket)
- static void \* caerCopyEventPacketOnlyValidEvents (void \*eventPacket)
- **caerEventPacketHeaderSetEventCapacity** (eventPacketCopy, eventValid)
- **caerEventPacketHeaderSetEventNumber** (eventPacketCopy, eventValid)
- **return** (eventPacketCopy)
- static void caerCleanEventPacket (void \*eventPacket)
- **memset** (((uint8\_t \*) header)+offset, 0,(size\_t)((eventCapacity-eventValid)\*eventSize))
- **caerEventPacketHeaderSetEventNumber** (header, eventValid)

### 4.5.1 Detailed Description

Common EventPacket header format definition and handling functions. Every EventPacket, of any type, has as a first member a common header, which describes various properties of the contained events. This allows easy parsing of events. See the 'struct caer\_event\_packet\_header' documentation for more details.

### 4.5.2 Macro Definition Documentation

#### 4.5.2.1 #define CAER\_EVENT\_PACKET\_HEADER\_SIZE 28

Size of the EventPacket header. This is constant across all supported systems.

#### 4.5.2.2 #define CAER\_ITERATOR\_ALL\_END }

Generic iterator close statement.

#### 4.5.2.3 #define CAER\_ITERATOR\_ALL\_START( PACKET\_HEADER, EVENT\_TYPE )

**Value:**

```
for (int32_t caerIteratorCounter = 0; \
    caerIteratorCounter < caerEventPacketHeaderGetEventNumber(
    PACKET_HEADER); \
    caerIteratorCounter++) { \
    EVENT_TYPE caerIteratorElement = (EVENT_TYPE) caerGenericEventGetEvent(
    PACKET_HEADER, caerIteratorCounter);
```

Generic iterator over all events in a packet. Returns the current index in the 'caerIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerIteratorElement' variable of type EVENT\_TYPE.

PACKET\_HEADER: a valid EventPacket header pointer. Cannot be NULL. EVENT\_TYPE: the event pointer type for this EventPacket (ie. caerPolarityEvent or caerFrameEvent).

#### 4.5.2.4 #define CAER\_ITERATOR\_VALID\_END }

Generic iterator close statement.

#### 4.5.2.5 #define CAER\_ITERATOR\_VALID\_START( PACKET\_HEADER, EVENT\_TYPE )

**Value:**

```
for (int32_t caerIteratorCounter = 0; \
    caerIteratorCounter < caerEventPacketHeaderGetEventNumber(
    PACKET_HEADER); \
    caerIteratorCounter++) { \
    EVENT_TYPE caerIteratorElement = (EVENT_TYPE) caerGenericEventGetEvent(
    PACKET_HEADER, caerIteratorCounter); \
    if (!caerGenericEventIsValid(caerIteratorElement)) { continue; }
```

Generic iterator over only the valid events in a packet. Returns the current index in the 'caerIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerIteratorElement' variable of type EVENT\_TYPE.

PACKET\_HEADER: a valid EventPacket header pointer. Cannot be NULL. EVENT\_TYPE: the event pointer type for this EventPacket (ie. caerPolarityEvent or caerFrameEvent).

#### 4.5.2.6 #define TS\_OVERFLOW\_SHIFT 31

64bit timestamp support: since timestamps wrap around after some time, being only 31 bit (32 bit signed int), another timestamp at the packet level provides another 31 bit (32 bit signed int), to enable the generation of a 62 bit (64 bit signed int) microsecond timestamp which is guaranteed to never wrap around (in the next 146'138 years at least). The TSOVerflow needs to be shifted by 31 thus when constructing such a timestamp.

#### 4.5.2.7 #define VALID\_MARK\_MASK 0x00000001

Generic validity mark: this bit is used to mark whether an event is still valid or not, and can be used to efficiently filter out events from a packet. The caerXXXEventValidate() and caerXXXEventInvalidate() functions should be used to toggle this! 0 in the 0th bit of the first byte means invalid, 1 means valid. This way zeroing-out an event packet sets all its events to invalid. Care must be taken to put the field containing the validity mark always as the first member of an event.

#### 4.5.2.8 #define VALID\_MARK\_SHIFT 0

Generic validity mark: this bit is used to mark whether an event is still valid or not, and can be used to efficiently filter out events from a packet. The caerXXXEventValidate() and caerXXXEventInvalidate() functions should be used to toggle this! 0 in the 0th bit of the first byte means invalid, 1 means valid. This way zeroing-out an event packet sets all its events to invalid. Care must be taken to put the field containing the validity mark always as the first member of an event.

### 4.5.3 Typedef Documentation

#### 4.5.3.1 typedef struct caer\_event\_packet\_header\* caerEventPacketHeader

Type for pointer to EventPacket header data structure.

### 4.5.4 Enumeration Type Documentation

#### 4.5.4.1 enum caer\_default\_event\_types

List of supported event types. Each event type has its own integer representation. All event types below 100 are reserved for use by libcaer and cAER. DO NOT USE THEM FOR YOUR OWN EVENT TYPES!

Enumerator

**SPECIAL\_EVENT** Special events.  
**POLARITY\_EVENT** Polarity (change, DVS) events.  
**FRAME\_EVENT** Frame (intensity, APS) events.  
**IMU6\_EVENT** 6 axes IMU events.  
**IMU9\_EVENT** 9 axes IMU events.  
**SAMPLE\_EVENT** ADC sample events.  
**EAR\_EVENT** Ear (cochlea) events.  
**CONFIG\_EVENT** Device configuration events.  
**POINT1D\_EVENT** 1D measurement events.  
**POINT2D\_EVENT** 2D measurement events.  
**POINT3D\_EVENT** 3D measurement events.  
**POINT4D\_EVENT** 4D measurement events.  
**SPIKE\_EVENT** Spike events.

### 4.5.5 Function Documentation

#### 4.5.5.1 static void caerCleanEventPacket ( void \* *eventPacket* ) [inline],[static]

Cleanup a packet by removing all invalid events, so that the total number of events is the number of valid events. The packet's capacity doesn't change.

##### Parameters

<i>eventPacket</i>	an event packet to clean.
--------------------	---------------------------

#### 4.5.5.2 static void\* caerCopyEventPacket ( void \* *eventPacket* ) [inline],[static]

Make a full copy of an event packet (up to eventCapacity).

##### Parameters

<i>eventPacket</i>	an event packet to copy.
--------------------	--------------------------

##### Returns

a full copy of an event packet.

#### 4.5.5.3 static void\* caerCopyEventPacketOnlyEvents ( void \* *eventPacket* ) [inline],[static]

Make a copy of an event packet, sized down to only include the currently present events (eventNumber, valid+invalid), and not including the possible extra unused events (up to eventCapacity).

##### Parameters

<i>eventPacket</i>	an event packet to copy.
--------------------	--------------------------

##### Returns

a sized down copy of an event packet.

#### 4.5.5.4 static void\* caerCopyEventPacketOnlyValidEvents ( void \* *eventPacket* ) [inline],[static]

Make a copy of an event packet, sized down to only include the currently valid events (eventValid), and discarding everything else.

##### Parameters

<i>eventPacket</i>	an event packet to copy.
--------------------	--------------------------

**Returns**

a copy of an event packet, containing only valid events.

**4.5.5.5** `static int32_t caerEventPacketHeaderGetEventCapacity ( caerEventPacketHeader header ) [inline], [static]`

Get the maximum number of events this packet can store.

**Parameters**

<i>header</i>	a valid EventPacket header pointer. Cannot be NULL.
---------------	---

**Returns**

the number of events this packet can hold.

**4.5.5.6** `static int32_t caerEventPacketHeaderGetEventNumber ( caerEventPacketHeader header ) [inline], [static]`

Get the number of events currently stored in this packet, considering both valid and invalid events.

**Parameters**

<i>header</i>	a valid EventPacket header pointer. Cannot be NULL.
---------------	---

**Returns**

the number of events in this packet.

**4.5.5.7** `static int32_t caerEventPacketHeaderGetEventSize ( caerEventPacketHeader header ) [inline], [static]`

Get the size of a single event, in bytes. All events inside an event packet always have the same size.

**Parameters**

<i>header</i>	a valid EventPacket header pointer. Cannot be NULL.
---------------	---

**Returns**

the event size in bytes.

**4.5.5.8** `static int16_t caerEventPacketHeaderGetEventSource ( caerEventPacketHeader header ) [inline], [static]`

Get the numerical event source ID, representing the event source that generated all the events present in this packet.



## Parameters

<i>header</i>	a valid EventPacket header pointer. Cannot be NULL.
---------------	---

## Returns

the numerical event source ID.

**4.5.5.9** `static int32_t caerEventPacketHeaderGetEventTSOffset ( caerEventPacketHeader header ) [inline],  
[static]`

Get the offset, in bytes, to where the field with the main 32 bit timestamp is stored. This is useful for generic access to the timestamp field, given that different event types might have it at different offsets or might even have multiple timestamps, in which case this offset references the 'main' timestamp, the most representative one.

## Parameters

<i>header</i>	a valid EventPacket header pointer. Cannot be NULL.
---------------	---

## Returns

the event timestamp offset in bytes.

**4.5.5.10** `static int32_t caerEventPacketHeaderGetEventTSOverflow ( caerEventPacketHeader header ) [inline],  
[static]`

Get the 32 bit timestamp overflow counter (in microseconds). This is per-packet and is used to generate a 64 bit timestamp that never wraps around. Since timestamps wrap around after some time, being only 31 bit (32 bit signed int), another timestamp at the packet level provides another 31 bit (32 bit signed int), to enable the generation of a 62 bit (64 bit signed int) microsecond timestamp which is guaranteed to never wrap around (in the next 146'138 years at least).

## Parameters

<i>header</i>	a valid EventPacket header pointer. Cannot be NULL.
---------------	---

## Returns

the packet-level timestamp overflow counter, in microseconds.

**4.5.5.11** `static int16_t caerEventPacketHeaderGetEventType ( caerEventPacketHeader header ) [inline],  
[static]`

Return the numerical event type ID, representing the event type this EventPacket is containing.

**Parameters**

<i>header</i>	a valid EventPacket header pointer. Cannot be NULL.
---------------	---

**Returns**

the numerical event type (see 'enum caer\_default\_event\_types').

**4.5.5.12** `static int32_t caerEventPacketHeaderGetEventValid ( caerEventPacketHeader header ) [inline], [static]`

Get the number of valid events in this packet, disregarding invalid ones (where the invalid mark is set).

**Parameters**

<i>header</i>	a valid EventPacket header pointer. Cannot be NULL.
---------------	---

**Returns**

the number of valid events in this packet.

**4.5.5.13** `static void caerEventPacketHeaderSetEventCapacity ( caerEventPacketHeader header, int32_t eventsCapacity ) [inline], [static]`

Set the maximum number of events this packet can store. This is determined at packet allocation time and should not be changed during the life-time of the packet.

**Parameters**

<i>header</i>	a valid EventPacket header pointer. Cannot be NULL.
<i>eventsCapacity</i>	the number of events this packet can hold.

**4.5.5.14** `static void caerEventPacketHeaderSetEventNumber ( caerEventPacketHeader header, int32_t eventsNumber ) [inline], [static]`

Set the number of events currently stored in this packet, considering both valid and invalid events.

**Parameters**

<i>header</i>	a valid EventPacket header pointer. Cannot be NULL.
<i>eventsNumber</i>	the number of events in this packet.

**4.5.5.15** `static void caerEventPacketHeaderSetEventSize ( caerEventPacketHeader header, int32_t eventSize )`  
[inline],[static]

Set the size of a single event, in bytes. All events inside an event packet always have the same size.

#### Parameters

<i>header</i>	a valid EventPacket header pointer. Cannot be NULL.
<i>eventSize</i>	the event size in bytes.

**4.5.5.16** `static void caerEventPacketHeaderSetEventSource ( caerEventPacketHeader header, int16_t eventSource )`  
[inline],[static]

Set the numerical event source ID, representing the event source that generated all the events present in this packet. This ID should be unique at least within a process, if not within the whole system, to guarantee correct identification of who generated an event later on.

#### Parameters

<i>header</i>	a valid EventPacket header pointer. Cannot be NULL.
<i>eventSource</i>	the numerical event source ID.

**4.5.5.17** `static void caerEventPacketHeaderSetEventTSOffset ( caerEventPacketHeader header, int32_t eventTSOffset )`  
[inline],[static]

Set the offset, in bytes, to where the field with the main 32 bit timestamp is stored. This is useful for generic access to the timestamp field, given that different event types might have it at different offsets or might even have multiple timestamps, in which case this offset references the 'main' timestamp, the most representative one.

#### Parameters

<i>header</i>	a valid EventPacket header pointer. Cannot be NULL.
<i>eventTSOffset</i>	the event timestamp offset in bytes.

**4.5.5.18** `static void caerEventPacketHeaderSetEventTSOverflow ( caerEventPacketHeader header, int32_t eventTSOverflow )` [inline],[static]

Set the 32 bit timestamp overflow counter (in microseconds). This is per-packet and is used to generate a 64 bit timestamp that never wraps around. Since timestamps wrap around after some time, being only 31 bit (32 bit signed int), another timestamp at the packet level provides another 31 bit (32 bit signed int), to enable the generation of a 62 bit (64 bit signed int) microsecond timestamp which is guaranteed to never wrap around (in the next 146'138 years at least).

#### Parameters

<i>header</i>	a valid EventPacket header pointer. Cannot be NULL.
<i>eventTSOverflow</i>	the packet-level timestamp overflow counter, in microseconds.

**4.5.5.19** `static void caerEventPacketHeaderSetEventType ( caerEventPacketHeader header, int16_t eventType )`  
`[inline],[static]`

Set the numerical event type ID, representing the event type this EventPacket will contain. All event types below 100 are reserved for use by libcaer and cAER. DO NOT USE THEM FOR YOUR OWN EVENT TYPES!

#### Parameters

<i>header</i>	a valid EventPacket header pointer. Cannot be NULL.
<i>eventType</i>	the numerical event type (see 'enum caer_default_event_types').

**4.5.5.20** `static void caerEventPacketHeaderSetEventValid ( caerEventPacketHeader header, int32_t eventsValid )`  
`[inline],[static]`

Set the number of valid events in this packet, disregarding invalid ones (where the invalid mark is set).

#### Parameters

<i>header</i>	a valid EventPacket header pointer. Cannot be NULL.
<i>eventsValid</i>	the number of valid events in this packet.

**4.5.5.21** `static void* caerGenericEventGetEvent ( caerEventPacketHeader headerPtr, int32_t n )` `[inline],[static]`

Get a generic pointer to an event, without having to know what event type the packet is containing.

#### Parameters

<i>headerPtr</i>	a valid EventPacket header pointer. Cannot be NULL.
<i>n</i>	the index of the returned event. Must be within [0,eventCapacity[ bounds.

#### Returns

a generic pointer to the requested event. NULL on error.

**4.5.5.22** `static int32_t caerGenericEventGetTimestamp ( void * eventPtr, caerEventPacketHeader headerPtr )`  
`[inline],[static]`

Get the main 32 bit timestamp for a generic event, without having to know what event type the packet is containing.

#### Parameters

<i>eventPtr</i>	a generic pointer to an event. Cannot be NULL.
<i>headerPtr</i>	a valid EventPacket header pointer. Cannot be NULL.

**Returns**

the main 32 bit timestamp of this event.

**4.5.5.23** `static int64_t caerGenericEventGetTimestamp64 ( void * eventPtr, caerEventPacketHeader headerPtr )`  
[inline],[static]

Get the main 64 bit timestamp for a generic event, without having to know what event type the packet is containing. This takes the per-packet timestamp into account too, generating a timestamp that doesn't suffer from overflow problems.

**Parameters**

<i>eventPtr</i>	a generic pointer to an event. Cannot be NULL.
<i>headerPtr</i>	a valid EventPacket header pointer. Cannot be NULL.

**Returns**

the main 64 bit timestamp of this event.

**4.5.5.24** `static bool caerGenericEventIsValid ( void * eventPtr )` [inline],[static]

Check if the given generic event is valid or not.

**Parameters**

<i>eventPtr</i>	a generic pointer to an event. Cannot be NULL.
-----------------	--

**Returns**

true if the event is valid, false otherwise.

**4.5.5.25** `static caerEventPacketHeader caerGenericEventPacketAppend ( caerEventPacketHeader packet, caerEventPacketHeader appendPacket )` [inline],[static]

Appends an event packet to another. This is a simple append operation, no timestamp reordering is done. Please ensure time is monotonically increasing over the two packets! Use free() to reclaim this memory afterwards.

**Parameters**

<i>packet</i>	the main events packet.
<i>appendPacket</i>	the events packet to append on the main one.

**Returns**

a valid event packet handle or NULL on error. On success, the old packet handle is to be considered invalid and not to be used anymore. On failure, the old packet handle is not touched in any way. The appendPacket handle is never touched in any way.

**4.5.5.26** `static caerEventPacketHeader caerGenericEventPacketGrow ( caerEventPacketHeader packet, int32_t newEventCapacity )` `[inline],[static]`

Grows an event packet. Use free() to reclaim this memory afterwards.

**Parameters**

<i>packet</i>	the current events packet.
<i>eventCapacity</i>	the new maximum number of events this packet will hold.

**Returns**

a valid event packet handle or NULL on error. On success, the old packet handle is to be considered invalid and not to be used anymore. On failure, the old packet handle is not touched in any way.

**4.5.5.27** `PACKED_STRUCT ( struct caer_event_packet_header{int16_t eventType;int16_t eventSource;int32_t eventSize;int32_t eventTSOffset;int32_t eventTSOverflow;int32_t eventCapacity;int32_t eventNumber;int32_t eventValid;} )`

EventPacket header data structure definition. The size, also defined in CAER\_EVENT\_PACKET\_HEADER\_SIZE, must always be constant. The header is common to all types of event packets and is always the very first member of an event packet data structure. Signed integers are used for compatibility with languages that do not have unsigned ones, such as Java.

## 4.6 events/config.h File Reference

```
#include "common.h"
```

**Macros**

- `#define CAER_CONFIGURATION_ITERATOR_ALL_START`(CONFIGURATION\_PACKET)
- `#define CAER_CONFIGURATION_ITERATOR_ALL_END` }
- `#define CAER_CONFIGURATION_ITERATOR_VALID_START`(CONFIGURATION\_PACKET)
- `#define CAER_CONFIGURATION_ITERATOR_VALID_END` }
- `#define MODULE_ADDR_SHIFT` 1
- `#define MODULE_ADDR_MASK` 0x0000007F

**Typedefs**

- `typedef struct caer_configuration_event * caerConfigurationEvent`
- `typedef struct caer_configuration_event_packet * caerConfigurationEventPacket`

## Functions

- **PACKED\_STRUCT** (struct caer\_configuration\_event{uint8\_t moduleAddress;uint8\_t parameterAddress;uint32\_t parameter;int32\_t timestamp;})
- **PACKED\_STRUCT** (struct caer\_configuration\_event\_packet{struct caer\_event\_packet\_header packetHeader;struct caer\_configuration\_event events[ ];})
- caerConfigurationEventPacket caerConfigurationEventPacketAllocate (int32\_t eventCapacity, int16\_t eventSource, int32\_t tsOverflow)
- static caerConfigurationEvent caerConfigurationEventPacketGetEvent (caerConfigurationEventPacket packet, int32\_t n)
- static int32\_t caerConfigurationEventGetTimestamp (caerConfigurationEvent event)
- static int64\_t caerConfigurationEventGetTimestamp64 (caerConfigurationEvent event, caerConfigurationEventPacket packet)
- static void caerConfigurationEventSetTimestamp (caerConfigurationEvent event, int32\_t timestamp)
- static bool caerConfigurationEventIsValid (caerConfigurationEvent event)
- static void caerConfigurationEventValidate (caerConfigurationEvent event, caerConfigurationEventPacket packet)
- static void caerConfigurationEventInvalidate (caerConfigurationEvent event, caerConfigurationEventPacket packet)
- static uint8\_t caerConfigurationEventGetModuleAddress (caerConfigurationEvent event)
- static void caerConfigurationEventSetModuleAddress (caerConfigurationEvent event, uint8\_t moduleAddress)
- static uint8\_t caerConfigurationEventGetParameterAddress (caerConfigurationEvent event)
- static void caerConfigurationEventSetParameterAddress (caerConfigurationEvent event, uint8\_t parameterAddress)
- static uint32\_t caerConfigurationEventGetParameter (caerConfigurationEvent event)
- static void caerConfigurationEventSetParameter (caerConfigurationEvent event, uint32\_t parameter)

### 4.6.1 Detailed Description

Configuration Events format definition and handling functions. This event contains information about the current configuration of the device. By having configuration as a standardized event format, it becomes host-software agnostic, and it also becomes part of the event stream, enabling easy tracking of changes through time, by putting them into the event stream at the moment they happen. While the resolution of the timestamps for these events is in microseconds for compatibility with all other event types, the precision is in the order of ~1-20 milliseconds, given that these events are generated and injected on the host-side.

### 4.6.2 Macro Definition Documentation

#### 4.6.2.1 #define CAER\_CONFIGURATION\_ITERATOR\_ALL\_END }

Iterator close statement.

#### 4.6.2.2 #define CAER\_CONFIGURATION\_ITERATOR\_ALL\_START( CONFIGURATION\_PACKET )

**Value:**

```
for (int32_t caerConfigurationIteratorCounter = 0; \
    caerConfigurationIteratorCounter < caerEventPacketHeaderGetEventNumber \
    (&(CONFIGURATION_PACKET)->packetHeader); \
    caerConfigurationIteratorCounter++) { \
    caerConfigurationEvent caerConfigurationIteratorElement = \
    caerConfigurationEventPacketGetEvent (CONFIGURATION_PACKET, \
    caerConfigurationIteratorCounter);
```

Iterator over all configuration events in a packet. Returns the current index in the 'caerConfigurationIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerConfigurationIteratorElement' variable of type caerConfigurationEvent.

CONFIGURATION\_PACKET: a valid ConfigurationEventPacket pointer. Cannot be NULL.

#### 4.6.2.3 `#define CAER_CONFIGURATION_ITERATOR_VALID_END` }

Iterator close statement.

#### 4.6.2.4 `#define CAER_CONFIGURATION_ITERATOR_VALID_START( CONFIGURATION_PACKET )`

**Value:**

```
for (int32_t caerConfigurationIteratorCounter = 0; \
    caerConfigurationIteratorCounter < caerEventPacketHeaderGetEventNumber
    (&(CONFIGURATION_PACKET)->packetHeader); \
    caerConfigurationIteratorCounter++) { \
    caerConfigurationEvent caerConfigurationIteratorElement =
    caerConfigurationEventPacketGetEvent(CONFIGURATION_PACKET,
    caerConfigurationIteratorCounter); \
    if (!caerConfigurationEventIsValid(caerConfigurationIteratorElement))
    { continue; }
```

Iterator over only the valid configuration events in a packet. Returns the current index in the 'caerConfigurationIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerConfigurationIteratorElement' variable of type caerConfigurationEvent.

CONFIGURATION\_PACKET: a valid ConfigurationEventPacket pointer. Cannot be NULL.

#### 4.6.2.5 `#define MODULE_ADDR_MASK 0x0000007F`

Shift and mask values for the module address. Module address is only 7 bits, since the eighth bit is used device-side to differentiate reads from writes. Here we can just re-use it for the validity mark.

#### 4.6.2.6 `#define MODULE_ADDR_SHIFT 1`

Shift and mask values for the module address. Module address is only 7 bits, since the eighth bit is used device-side to differentiate reads from writes. Here we can just re-use it for the validity mark.

### 4.6.3 Typedef Documentation

#### 4.6.3.1 `typedef struct caer_configuration_event* caerConfigurationEvent`

Type for pointer to configuration event data structure.

#### 4.6.3.2 `typedef struct caer_configuration_event_packet* caerConfigurationEventPacket`

Type for pointer to configuration event packet data structure.

### 4.6.4 Function Documentation

#### 4.6.4.1 `static uint8_t caerConfigurationEventGetModuleAddress ( caerConfigurationEvent event ) [inline], [static]`

Get the configuration event's module address.



## Parameters

<i>event</i>	a valid ConfigurationEvent pointer. Cannot be NULL.
--------------	---

## Returns

configuration module address.

**4.6.4.2** `static uint32_t caerConfigurationEventGetParameter ( caerConfigurationEvent event ) [inline], [static]`

Get the configuration event's parameter.

## Parameters

<i>event</i>	a valid ConfigurationEvent pointer. Cannot be NULL.
--------------	---

## Returns

configuration parameter.

**4.6.4.3** `static uint8_t caerConfigurationEventGetParameterAddress ( caerConfigurationEvent event ) [inline], [static]`

Get the configuration event's parameter address.

## Parameters

<i>event</i>	a valid ConfigurationEvent pointer. Cannot be NULL.
--------------	---

## Returns

configuration parameter address.

**4.6.4.4** `static int32_t caerConfigurationEventGetTimestamp ( caerConfigurationEvent event ) [inline], [static]`

Get the 32bit event timestamp, in microseconds. Be aware that this wraps around! You can either ignore this fact, or handle the special 'TIMESTAMP\_WRAP' event that is generated when this happens, or use the 64bit timestamp which never wraps around. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

## Parameters

<i>event</i>	a valid ConfigurationEvent pointer. Cannot be NULL.
--------------	---

**Returns**

this event's 32bit microsecond timestamp.

**4.6.4.5** `static int64_t caerConfigurationEventGetTimestamp64 ( caerConfigurationEvent event, caerConfigurationEventPacket packet )` `[inline], [static]`

Get the 64bit event timestamp, in microseconds. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

**Parameters**

<i>event</i>	a valid ConfigurationEvent pointer. Cannot be NULL.
<i>packet</i>	the ConfigurationEventPacket pointer for the packet containing this event. Cannot be NULL.

**Returns**

this event's 64bit microsecond timestamp.

**4.6.4.6** `static void caerConfigurationEventInvalidate ( caerConfigurationEvent event, caerConfigurationEventPacket packet )` `[inline], [static]`

Invalidate the current event by setting its valid bit to false and decreasing the number of valid events held in the packet. Only works with events that are already valid!

**Parameters**

<i>event</i>	a valid ConfigurationEvent pointer. Cannot be NULL.
<i>packet</i>	the ConfigurationEventPacket pointer for the packet containing this event. Cannot be NULL.

**4.6.4.7** `static bool caerConfigurationEventsValid ( caerConfigurationEvent event )` `[inline], [static]`

Check if this configuration event is valid.

**Parameters**

<i>event</i>	a valid ConfigurationEvent pointer. Cannot be NULL.
--------------	---

**Returns**

true if valid, false if not.

**4.6.4.8** `caerConfigurationEventPacket caerConfigurationEventPacketAllocate ( int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow )`

Allocate a new configuration events packet. Use free() to reclaim this memory.

## Parameters

<i>eventCapacity</i>	the maximum number of events this packet will hold.
<i>eventSource</i>	the unique ID representing the source/generator of this packet.
<i>tsOverflow</i>	the current timestamp overflow counter value for this packet.

## Returns

a valid ConfigurationEventPacket handle or NULL on error.

**4.6.4.9** `static caerConfigurationEvent caerConfigurationEventPacketGetEvent ( caerConfigurationEventPacket packet, int32_t n ) [inline],[static]`

Get the configuration event at the given index from the event packet.

## Parameters

<i>packet</i>	a valid ConfigurationEventPacket pointer. Cannot be NULL.
<i>n</i>	the index of the returned event. Must be within [0,eventCapacity[ bounds.

## Returns

the requested configuration event. NULL on error.

**4.6.4.10** `static void caerConfigurationEventSetModuleAddress ( caerConfigurationEvent event, uint8_t moduleAddress ) [inline],[static]`

Set the configuration event's module address.

## Parameters

<i>event</i>	a valid ConfigurationEvent pointer. Cannot be NULL.
<i>moduleAddress</i>	configuration module address.

**4.6.4.11** `static void caerConfigurationEventSetParameter ( caerConfigurationEvent event, uint32_t parameter ) [inline],[static]`

Set the configuration event's parameter.

## Parameters

<i>event</i>	a valid ConfigurationEvent pointer. Cannot be NULL.
<i>parameter</i>	configuration parameter.

**4.6.4.12** `static void caerConfigurationEventSetParameterAddress ( caerConfigurationEvent event, uint8_t parameterAddress ) [inline], [static]`

Set the configuration event's parameter address.

#### Parameters

<i>event</i>	a valid ConfigurationEvent pointer. Cannot be NULL.
<i>parameterAddress</i>	configuration parameter address.

**4.6.4.13** `static void caerConfigurationEventSetTimestamp ( caerConfigurationEvent event, int32_t timestamp ) [inline], [static]`

Set the 32bit event timestamp, the value has to be in microseconds.

#### Parameters

<i>event</i>	a valid ConfigurationEvent pointer. Cannot be NULL.
<i>timestamp</i>	a positive 32bit microsecond timestamp.

**4.6.4.14** `static void caerConfigurationEventValidate ( caerConfigurationEvent event, caerConfigurationEventPacket packet ) [inline], [static]`

Validate the current event by setting its valid bit to true and increasing the event packet's event count and valid event count. Only works on events that are invalid. DO NOT CALL THIS AFTER HAVING PREVIOUSLY ALREADY INVALIDATED THIS EVENT, the total count will be incorrect.

#### Parameters

<i>event</i>	a valid ConfigurationEvent pointer. Cannot be NULL.
<i>packet</i>	the ConfigurationEventPacket pointer for the packet containing this event. Cannot be NULL.

**4.6.4.15** `PACKED_STRUCT ( struct caer_configuration_event{uint8_t moduleAddress;uint8_t parameterAddress;uint32_t parameter;int32_t timestamp;} )`

Configuration event data structure definition. This contains the actual configuration module address, the parameter address and the actual parameter content, as well as the 32 bit event timestamp. Signed integers are used for fields that are to be interpreted directly, for compatibility with languages that do not have unsigned integer types, such as Java.

**4.6.4.16** `PACKED_STRUCT ( struct caer_configuration_event_packet{struct caer_event_packet_header packetHeader;struct caer_configuration_event events[];} )`

Configuration event packet data structure definition. EventPackets are always made up of the common packet header, followed by 'eventCapacity' events. Everything has to be in one contiguous memory block.

## 4.7 events/ear.h File Reference

```
#include "common.h"
```

### Macros

- `#define CAER_EAR_ITERATOR_ALL_START(EAR_PACKET)`
- `#define CAER_EAR_ITERATOR_ALL_END }`
- `#define CAER_EAR_ITERATOR_VALID_START(EAR_PACKET)`
- `#define CAER_EAR_ITERATOR_VALID_END }`
- `#define EAR_SHIFT 1`
- `#define EAR_MASK 0x0000000F`
- `#define CHANNEL_SHIFT 5`
- `#define CHANNEL_MASK 0x000007FF`
- `#define NEURON_SHIFT 16`
- `#define NEURON_MASK 0x000000FF`
- `#define FILTER_SHIFT 24`
- `#define FILTER_MASK 0x000000FF`

### Typedefs

- `typedef struct caer_ear_event * caerEarEvent`
- `typedef struct caer_ear_event_packet * caerEarEventPacket`

### Functions

- `PACKED_STRUCT` (struct caer\_ear\_event{uint32\_t data;int32\_t timestamp;})
- `PACKED_STRUCT` (struct caer\_ear\_event\_packet{struct caer\_event\_packet\_header packetHeader;struct caer\_ear\_event events[ ];})
- `caerEarEventPacket caerEarEventPacketAllocate` (int32\_t eventCapacity, int16\_t eventSource, int32\_t ts↵ Overflow)
- static `caerEarEvent caerEarEventPacketGetEvent` (caerEarEventPacket packet, int32\_t n)
- static int32\_t `caerEarEventGetTimestamp` (caerEarEvent event)
- static int64\_t `caerEarEventGetTimestamp64` (caerEarEvent event, caerEarEventPacket packet)
- static void `caerEarEventSetTimestamp` (caerEarEvent event, int32\_t timestamp)
- static bool `caerEarEventIsValid` (caerEarEvent event)
- static void `caerEarEventValidate` (caerEarEvent event, caerEarEventPacket packet)
- static void `caerEarEventInvalidate` (caerEarEvent event, caerEarEventPacket packet)
- static uint8\_t `caerEarEventGetEar` (caerEarEvent event)
- static void `caerEarEventSetEar` (caerEarEvent event, uint8\_t ear)
- static uint16\_t `caerEarEventGetChannel` (caerEarEvent event)
- static void `caerEarEventSetChannel` (caerEarEvent event, uint16\_t channel)
- static uint8\_t `caerEarEventGetNeuron` (caerEarEvent event)
- static void `caerEarEventSetNeuron` (caerEarEvent event, uint8\_t neuron)
- static uint8\_t `caerEarEventGetFilter` (caerEarEvent event)
- static void `caerEarEventSetFilter` (caerEarEvent event, uint8\_t filter)

### 4.7.1 Detailed Description

Ear (Cochlea) Events format definition and handling functions. This encodes events from a silicon cochlea chip, containing information about which ear (microphone) generated the event, as well as which channel was involved and additional information on filters and neurons.

### 4.7.2 Macro Definition Documentation

#### 4.7.2.1 #define CAER\_EAR\_ITERATOR\_ALL\_END }

Iterator close statement.

#### 4.7.2.2 #define CAER\_EAR\_ITERATOR\_ALL\_START( EAR\_PACKET )

**Value:**

```
for (int32_t caerEarIteratorCounter = 0; \
     caerEarIteratorCounter < caerEventPacketHeaderGetEventNumber (&(
EAR_PACKET)->packetHeader); \
     caerEarIteratorCounter++) { \
    caerEarEvent caerEarIteratorElement =
    caerEarEventPacketGetEvent (EAR_PACKET, caerEarIteratorCounter);
```

Iterator over all ear events in a packet. Returns the current index in the 'caerEarIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerEarIteratorElement' variable of type caerEarEvent.

EAR\_PACKET: a valid EarEventPacket pointer. Cannot be NULL.

#### 4.7.2.3 #define CAER\_EAR\_ITERATOR\_VALID\_END }

Iterator close statement.

#### 4.7.2.4 #define CAER\_EAR\_ITERATOR\_VALID\_START( EAR\_PACKET )

**Value:**

```
for (int32_t caerEarIteratorCounter = 0; \
     caerEarIteratorCounter < caerEventPacketHeaderGetEventNumber (&(
EAR_PACKET)->packetHeader); \
     caerEarIteratorCounter++) { \
    caerEarEvent caerEarIteratorElement =
    caerEarEventPacketGetEvent (EAR_PACKET, caerEarIteratorCounter); \
    if (!caerEarEventIsValid(caerEarIteratorElement)) { continue; }
```

Iterator over only the valid ear events in a packet. Returns the current index in the 'caerEarIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerEarIteratorElement' variable of type caerEarEvent.

EAR\_PACKET: a valid EarEventPacket pointer. Cannot be NULL.

#### 4.7.2.5 `#define CHANNEL_MASK 0x000007FF`

Shift and mask values for the ear event values coming from a cochlea: the ear position (up to 16), the channel number (up to 2048), the ganglion (up to 256) and the filter (up to 256). Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.7.2.6 `#define CHANNEL_SHIFT 5`

Shift and mask values for the ear event values coming from a cochlea: the ear position (up to 16), the channel number (up to 2048), the ganglion (up to 256) and the filter (up to 256). Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.7.2.7 `#define EAR_MASK 0x0000000F`

Shift and mask values for the ear event values coming from a cochlea: the ear position (up to 16), the channel number (up to 2048), the ganglion (up to 256) and the filter (up to 256). Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.7.2.8 `#define EAR_SHIFT 1`

Shift and mask values for the ear event values coming from a cochlea: the ear position (up to 16), the channel number (up to 2048), the ganglion (up to 256) and the filter (up to 256). Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.7.2.9 `#define FILTER_MASK 0x000000FF`

Shift and mask values for the ear event values coming from a cochlea: the ear position (up to 16), the channel number (up to 2048), the ganglion (up to 256) and the filter (up to 256). Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.7.2.10 `#define FILTER_SHIFT 24`

Shift and mask values for the ear event values coming from a cochlea: the ear position (up to 16), the channel number (up to 2048), the ganglion (up to 256) and the filter (up to 256). Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.7.2.11 `#define NEURON_MASK 0x000000FF`

Shift and mask values for the ear event values coming from a cochlea: the ear position (up to 16), the channel number (up to 2048), the ganglion (up to 256) and the filter (up to 256). Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.7.2.12 `#define NEURON_SHIFT 16`

Shift and mask values for the ear event values coming from a cochlea: the ear position (up to 16), the channel number (up to 2048), the ganglion (up to 256) and the filter (up to 256). Bit 0 is the valid mark, see '[common.h](#)' for more details.

### 4.7.3 Typedef Documentation

#### 4.7.3.1 typedef struct caer\_ear\_event\* caerEarEvent

Type for pointer to ear (cochlea) event data structure.

#### 4.7.3.2 typedef struct caer\_ear\_event\_packet\* caerEarEventPacket

Type for pointer to ear (cochlea) event packet data structure.

### 4.7.4 Function Documentation

#### 4.7.4.1 static uint16\_t caerEarEventGetChannel ( caerEarEvent event ) [inline],[static]

Get the channel (frequency band) ID. The channels count from 0 upward, where 0 is the highest frequency channel, while higher numbers are progressively lower frequency channels. This is derived from how the actual human ear works.

##### Parameters

<i>event</i>	a valid EarEvent pointer. Cannot be NULL.
--------------	---

##### Returns

the channel (frequency band) ID.

#### 4.7.4.2 static uint8\_t caerEarEventGetEar ( caerEarEvent event ) [inline],[static]

Get the numerical ID of the ear (microphone). Usually, 0 is left, 1 is right for 2 ear cochleas. For 4 ear cochleas, 0 is front left, 1 is front right, 2 is back left and 3 is back right.

##### Parameters

<i>event</i>	a valid EarEvent pointer. Cannot be NULL.
--------------	---

##### Returns

the ear (microphone) ID.

#### 4.7.4.3 static int32\_t caerEarEventGetTimestamp ( caerEarEvent event ) [inline],[static]

Get the 32bit event timestamp, in microseconds. Be aware that this wraps around! You can either ignore this fact, or handle the special 'TIMESTAMP\_WRAP' event that is generated when this happens, or use the 64bit timestamp which never wraps around. See ['caerEventPacketHeaderGetEventTSOverflow\(\)' documentation](#) for more details on the 64bit timestamp.



## Parameters

<i>event</i>	a valid EarEvent pointer. Cannot be NULL.
--------------	---

## Returns

this event's 32bit microsecond timestamp.

**4.7.4.4** `static int64_t caerEarEventGetTimestamp64 ( caerEarEvent event, caerEarEventPacket packet ) [inline], [static]`

Get the 64bit event timestamp, in microseconds. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

## Parameters

<i>event</i>	a valid EarEvent pointer. Cannot be NULL.
<i>packet</i>	the EarEventPacket pointer for the packet containing this event. Cannot be NULL.

## Returns

this event's 64bit microsecond timestamp.

**4.7.4.5** `static void caerEarEventInvalidate ( caerEarEvent event, caerEarEventPacket packet ) [inline], [static]`

Invalidate the current event by setting its valid bit to false and decreasing the number of valid events held in the packet. Only works with events that are already valid!

## Parameters

<i>event</i>	a valid EarEvent pointer. Cannot be NULL.
<i>packet</i>	the EarEventPacket pointer for the packet containing this event. Cannot be NULL.

**4.7.4.6** `static bool caerEarEventsValid ( caerEarEvent event ) [inline], [static]`

Check if this ear (cochlea) event is valid.

## Parameters

<i>event</i>	a valid EarEvent pointer. Cannot be NULL.
--------------	---

## Returns

true if valid, false if not.

#### 4.7.4.7 `caerEarEventPacket caerEarEventPacketAllocate ( int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow )`

Allocate a new ear (cochlea) events packet. Use `free()` to reclaim this memory.

##### Parameters

<i>eventCapacity</i>	the maximum number of events this packet will hold.
<i>eventSource</i>	the unique ID representing the source/generator of this packet.
<i>tsOverflow</i>	the current timestamp overflow counter value for this packet.

##### Returns

a valid `EarEventPacket` handle or `NULL` on error.

#### 4.7.4.8 `static caerEarEvent caerEarEventPacketGetEvent ( caerEarEventPacket packet, int32_t n ) [inline], [static]`

Get the ear (cochlea) event at the given index from the event packet.

##### Parameters

<i>packet</i>	a valid <code>EarEventPacket</code> pointer. Cannot be <code>NULL</code> .
<i>n</i>	the index of the returned event. Must be within <code>[0,eventCapacity[</code> bounds.

##### Returns

the requested ear (cochlea) event. `NULL` on error.

#### 4.7.4.9 `static void caerEarEventSetChannel ( caerEarEvent event, uint16_t channel ) [inline], [static]`

Set the channel (frequency band) ID. The channels count from 0 upward, where 0 is the highest frequency channel, while higher numbers are progressively lower frequency channels. This is derived from how the actual human ear works.

##### Parameters

<i>event</i>	a valid <code>EarEvent</code> pointer. Cannot be <code>NULL</code> .
<i>channel</i>	the channel (frequency band) ID.

#### 4.7.4.10 `static void caerEarEventSetEar ( caerEarEvent event, uint8_t ear ) [inline], [static]`

Set the numerical ID of the ear (microphone). Usually, 0 is left, 1 is right for 2 ear cochleas. For 4 ear cochleas, 0 is front left, 1 is front right, 2 is back left and 3 is back right.

## Parameters

<i>event</i>	a valid EarEvent pointer. Cannot be NULL.
<i>ear</i>	the ear (microphone) ID.

**4.7.4.11** `static void caerEarEventSetTimestamp ( caerEarEvent event, int32_t timestamp )` `[inline],[static]`

Set the 32bit event timestamp, the value has to be in microseconds.

## Parameters

<i>event</i>	a valid EarEvent pointer. Cannot be NULL.
<i>timestamp</i>	a positive 32bit microsecond timestamp.

**4.7.4.12** `static void caerEarEventValidate ( caerEarEvent event, caerEarEventPacket packet )` `[inline],[static]`

Validate the current event by setting its valid bit to true and increasing the event packet's event count and valid event count. Only works on events that are invalid. DO NOT CALL THIS AFTER HAVING PREVIOUSLY ALREADY INVALIDATED THIS EVENT, the total count will be incorrect.

## Parameters

<i>event</i>	a valid EarEvent pointer. Cannot be NULL.
<i>packet</i>	the EarEventPacket pointer for the packet containing this event. Cannot be NULL.

**4.7.4.13** `PACKED_STRUCT ( struct caer_ear_event{uint32_t data;int32_t timestamp;} )`

Ear (cochlea) event data structure definition. Contains information on events gotten from a cochlea chip: ears, channels, neurons and filters are stored. Signed integers are used for fields that are to be interpreted directly, for compatibility with languages that do not have unsigned integer types, such as Java.

**4.7.4.14** `PACKED_STRUCT ( struct caer_ear_event_packet{struct caer_event_packet_header packetHeader;struct caer_ear_event events[];} )`

Ear (cochlea) event packet data structure definition. EventPackets are always made up of the common packet header, followed by 'eventCapacity' events. Everything has to be in one contiguous memory block.

## 4.8 events/frame.h File Reference

```
#include "common.h"
```

## Macros

- `#define CAER_FRAME_ITERATOR_ALL_START(FRAME_PACKET)`
- `#define CAER_FRAME_ITERATOR_ALL_END }`
- `#define CAER_FRAME_ITERATOR_VALID_START(FRAME_PACKET)`
- `#define CAER_FRAME_ITERATOR_VALID_END }`
- `#define CAER_FRAME_REVERSE_ITERATOR_ALL_START(FRAME_PACKET)`
- `#define CAER_FRAME_REVERSE_ITERATOR_ALL_END }`
- `#define CAER_FRAME_REVERSE_ITERATOR_VALID_START(FRAME_PACKET)`
- `#define CAER_FRAME_REVERSE_ITERATOR_VALID_END }`
  
- `#define COLOR_CHANNELS_SHIFT 1`
- `#define COLOR_CHANNELS_MASK 0x00000007`
- `#define COLOR_FILTER_SHIFT 4`
- `#define COLOR_FILTER_MASK 0x0000000F`
- `#define ROI_IDENTIFIER_SHIFT 8`
- `#define ROI_IDENTIFIER_MASK 0x0000007F`

## Typedefs

- `typedef struct caer_frame_event * caerFrameEvent`
- `typedef struct caer_frame_event_packet * caerFrameEventPacket`

## Enumerations

- `enum caer_frame_event_color_channels { GRAYSCALE = 1, RGB = 3, RGBA = 4 }`
- `enum caer_frame_event_color_filter {  
MONO = 0, RGBG = 1, GRGB = 2, GBGR = 3,  
BGRG = 4, RGBW = 5, GRWB = 6, WBGR = 7,  
BWRG = 8 }`

## Functions

- `PACKED_STRUCT` (struct caer\_frame\_event{uint32\_t info;int32\_t ts\_startframe;int32\_t ts\_endframe;int32\_t ts\_startexposure;int32\_t ts\_endexposure;int32\_t lengthX;int32\_t lengthY;int32\_t positionX;int32\_t positionY;uint16\_t pixels[ ];})
- `PACKED_STRUCT` (struct caer\_frame\_event\_packet{struct caer\_event\_packet\_header packetHeader;})
- `caerFrameEventPacket caerFrameEventPacketAllocate` (int32\_t eventCapacity, int16\_t eventSource, int32\_t tsOverflow, int32\_t maxLengthX, int32\_t maxLengthY, int16\_t maxChannelNumber)
- `static caerFrameEvent caerFrameEventPacketGetEvent` (caerFrameEventPacket packet, int32\_t n)
- `static int32_t caerFrameEventGetTSSStartOfFrame` (caerFrameEvent event)
- `static int64_t caerFrameEventGetTSSStartOfFrame64` (caerFrameEvent event, caerFrameEventPacket packet)
- `static void caerFrameEventSetTSSStartOfFrame` (caerFrameEvent event, int32\_t startFrame)
- `static int32_t caerFrameEventGetTSEndOfFrame` (caerFrameEvent event)
- `static int64_t caerFrameEventGetTSEndOfFrame64` (caerFrameEvent event, caerFrameEventPacket packet)
- `static void caerFrameEventSetTSEndOfFrame` (caerFrameEvent event, int32\_t endFrame)
- `static int32_t caerFrameEventGetTSSStartOfExposure` (caerFrameEvent event)
- `static int64_t caerFrameEventGetTSSStartOfExposure64` (caerFrameEvent event, caerFrameEventPacket packet)
- `static void caerFrameEventSetTSSStartOfExposure` (caerFrameEvent event, int32\_t startExposure)
- `static int32_t caerFrameEventGetTSEndOfExposure` (caerFrameEvent event)

- static int64\_t caerFrameEventGetTSEndOfExposure64 (caerFrameEvent event, caerFrameEventPacket packet)
- static void caerFrameEventSetTSEndOfExposure (caerFrameEvent event, int32\_t endExposure)
- static int32\_t caerFrameEventGetExposureLength (caerFrameEvent event)
- static int32\_t caerFrameEventGetTimestamp (caerFrameEvent event)
- static int64\_t caerFrameEventGetTimestamp64 (caerFrameEvent event, caerFrameEventPacket packet)
- static bool caerFrameEventsValid (caerFrameEvent event)
- static void caerFrameEventValidate (caerFrameEvent event, caerFrameEventPacket packet)
- static void caerFrameEventInvalidate (caerFrameEvent event, caerFrameEventPacket packet)
- static size\_t caerFrameEventPacketGetPixelsSize (caerFrameEventPacket packet)
- static size\_t caerFrameEventPacketGetPixelsMaxIndex (caerFrameEventPacket packet)
- static uint8\_t caerFrameEventGetROIIdentifier (caerFrameEvent event)
- static void caerFrameEventSetROIIdentifier (caerFrameEvent event, uint8\_t roiIdentifier)
- static enum caer\_frame\_event\_color\_filter caerFrameEventGetColorFilter (caerFrameEvent event)
- static void caerFrameEventSetColorFilter (caerFrameEvent event, enum caer\_frame\_event\_color\_filter colorFilter)
- static int32\_t caerFrameEventGetLengthX (caerFrameEvent event)
- static int32\_t caerFrameEventGetLengthY (caerFrameEvent event)
- static enum caer\_frame\_event\_color\_channels caerFrameEventGetChannelNumber (caerFrameEvent event)
- static void caerFrameEventSetLengthXLengthYChannelNumber (caerFrameEvent event, int32\_t lengthX, int32\_t lengthY, enum caer\_frame\_event\_color\_channels channelNumber, caerFrameEventPacket packet)
- static size\_t caerFrameEventGetPixelsMaxIndex (caerFrameEvent event)
- static size\_t caerFrameEventGetPixelsSize (caerFrameEvent event)
- static int32\_t caerFrameEventGetPositionX (caerFrameEvent event)
- static void caerFrameEventSetPositionX (caerFrameEvent event, int32\_t positionX)
- static int32\_t caerFrameEventGetPositionY (caerFrameEvent event)
- static void caerFrameEventSetPositionY (caerFrameEvent event, int32\_t positionY)
- static uint16\_t caerFrameEventGetPixel (caerFrameEvent event, int32\_t xAddress, int32\_t yAddress)
- static void caerFrameEventSetPixel (caerFrameEvent event, int32\_t xAddress, int32\_t yAddress, uint16\_t pixelValue)
- static uint16\_t caerFrameEventGetPixelForChannel (caerFrameEvent event, int32\_t xAddress, int32\_t yAddress, uint8\_t channel)
- static void caerFrameEventSetPixelForChannel (caerFrameEvent event, int32\_t xAddress, int32\_t yAddress, uint8\_t channel, uint16\_t pixelValue)
- static uint16\_t caerFrameEventGetPixelUnsafe (caerFrameEvent event, int32\_t xAddress, int32\_t yAddress)
- static void caerFrameEventSetPixelUnsafe (caerFrameEvent event, int32\_t xAddress, int32\_t yAddress, uint16\_t pixelValue)
- static uint16\_t caerFrameEventGetPixelForChannelUnsafe (caerFrameEvent event, int32\_t xAddress, int32\_t yAddress, uint8\_t channel)
- static void caerFrameEventSetPixelForChannelUnsafe (caerFrameEvent event, int32\_t xAddress, int32\_t yAddress, uint8\_t channel, uint16\_t pixelValue)
- static uint16\_t \* caerFrameEventGetPixelArrayUnsafe (caerFrameEvent event)

### 4.8.1 Detailed Description

Frame Events format definition and handling functions. This event type encodes intensity frames, like you would get from a normal APS camera. It supports multiple channels for color, color filter information, as well as multiple Regions of Interest (ROI). The (0, 0) pixel is in the upper left corner of the screen, like in OpenCV/computer graphics. The pixel array is laid out row by row (increasing X axis), going from top to bottom (increasing Y axis).

### 4.8.2 Macro Definition Documentation

#### 4.8.2.1 #define CAER\_FRAME\_ITERATOR\_ALL\_END }

Iterator close statement.

#### 4.8.2.2 #define CAER\_FRAME\_ITERATOR\_ALL\_START( FRAME\_PACKET )

**Value:**

```
for (int32_t caerFrameIteratorCounter = 0; \
    caerFrameIteratorCounter < caerEventPacketHeaderGetEventNumber (&
(FRAME_PACKET)->packetHeader); \
    caerFrameIteratorCounter++) { \
    caerFrameEvent caerFrameIteratorElement =
    caerFrameEventPacketGetEvent (FRAME_PACKET, caerFrameIteratorCounter);
```

Iterator over all frame events in a packet. Returns the current index in the 'caerFrameIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerFrameIteratorElement' variable of type caerFrameEvent.

FRAME\_PACKET: a valid FrameEventPacket pointer. Cannot be NULL.

#### 4.8.2.3 #define CAER\_FRAME\_ITERATOR\_VALID\_END }

Iterator close statement.

#### 4.8.2.4 #define CAER\_FRAME\_ITERATOR\_VALID\_START( FRAME\_PACKET )

**Value:**

```
for (int32_t caerFrameIteratorCounter = 0; \
    caerFrameIteratorCounter < caerEventPacketHeaderGetEventNumber (&
(FRAME_PACKET)->packetHeader); \
    caerFrameIteratorCounter++) { \
    caerFrameEvent caerFrameIteratorElement =
    caerFrameEventPacketGetEvent (FRAME_PACKET, caerFrameIteratorCounter); \
    if (!caerFrameEventIsValid(caerFrameIteratorElement)) { continue; }
```

Iterator over only the valid frame events in a packet. Returns the current index in the 'caerFrameIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerFrameIteratorElement' variable of type caerFrameEvent.

FRAME\_PACKET: a valid FrameEventPacket pointer. Cannot be NULL.

#### 4.8.2.5 #define CAER\_FRAME\_REVERSE\_ITERATOR\_ALL\_END }

Reverse iterator close statement.

#### 4.8.2.6 #define CAER\_FRAME\_REVERSE\_ITERATOR\_ALL\_START( FRAME\_PACKET )

**Value:**

```
for (int32_t caerFrameIteratorCounter = caerEventPacketHeaderGetEventNumber
(&(FRAME_PACKET)->packetHeader) - 1; \
    caerFrameIteratorCounter >= 0; \
    caerFrameIteratorCounter--) { \
    caerFrameEvent caerFrameIteratorElement =
    caerFrameEventPacketGetEvent (FRAME_PACKET, caerFrameIteratorCounter);
```

Reverse iterator over all frame events in a packet. Returns the current index in the 'caerFrameIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerFrameIteratorElement' variable of type caerFrameEvent.

FRAME\_PACKET: a valid FrameEventPacket pointer. Cannot be NULL.

4.8.2.7 `#define CAER_FRAME_REVERSE_ITERATOR_VALID_END`

Reverse iterator close statement.

4.8.2.8 `#define CAER_FRAME_REVERSE_ITERATOR_VALID_START( FRAME_PACKET )`

**Value:**

```
for (int32_t caerFrameIteratorCounter = caerEventPacketHeaderGetEventNumber
    (&(FRAME_PACKET)->packetHeader) - 1; \
    caerFrameIteratorCounter >= 0; \
    caerFrameIteratorCounter--) { \
    caerFrameEvent caerFrameIteratorElement =
    caerFrameEventPacketGetEvent(FRAME_PACKET, caerFrameIteratorCounter); \
    if (!caerFrameEventIsValid(caerFrameIteratorElement)) { continue; }
```

Reverse iterator over only the valid frame events in a packet. Returns the current index in the 'caerFrameIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerFrameIteratorElement' variable of type caerFrameEvent.

FRAME\_PACKET: a valid FrameEventPacket pointer. Cannot be NULL.

4.8.2.9 `#define COLOR_CHANNELS_MASK 0x00000007`

Shift and mask values for the color channels number, the color filter arrangement and the ROI identifier contained in the 'info' field of the frame event. Multiple channels (RGB for example) are possible, see the 'enum caer\_frame\_event\_color\_channels'. To understand the original color filter arrangement to interpolate color images, see the 'enum caer\_frame\_event\_color\_filter'. Also, up to 128 different Regions of Interest (ROI) can be tracked. Bit 0 is the valid mark, see [common.h](#) for more details.

4.8.2.10 `#define COLOR_CHANNELS_SHIFT 1`

Shift and mask values for the color channels number, the color filter arrangement and the ROI identifier contained in the 'info' field of the frame event. Multiple channels (RGB for example) are possible, see the 'enum caer\_frame\_event\_color\_channels'. To understand the original color filter arrangement to interpolate color images, see the 'enum caer\_frame\_event\_color\_filter'. Also, up to 128 different Regions of Interest (ROI) can be tracked. Bit 0 is the valid mark, see [common.h](#) for more details.

4.8.2.11 `#define COLOR_FILTER_MASK 0x0000000F`

Shift and mask values for the color channels number, the color filter arrangement and the ROI identifier contained in the 'info' field of the frame event. Multiple channels (RGB for example) are possible, see the 'enum caer\_frame\_event\_color\_channels'. To understand the original color filter arrangement to interpolate color images, see the 'enum caer\_frame\_event\_color\_filter'. Also, up to 128 different Regions of Interest (ROI) can be tracked. Bit 0 is the valid mark, see [common.h](#) for more details.

4.8.2.12 `#define COLOR_FILTER_SHIFT 4`

Shift and mask values for the color channels number, the color filter arrangement and the ROI identifier contained in the 'info' field of the frame event. Multiple channels (RGB for example) are possible, see the 'enum caer\_frame\_event\_color\_channels'. To understand the original color filter arrangement to interpolate color images, see the 'enum caer\_frame\_event\_color\_filter'. Also, up to 128 different Regions of Interest (ROI) can be tracked. Bit 0 is the valid mark, see [common.h](#) for more details.

#### 4.8.2.13 `#define ROI_IDENTIFIER_MASK 0x0000007F`

Shift and mask values for the color channels number, the color filter arrangement and the ROI identifier contained in the 'info' field of the frame event. Multiple channels (RGB for example) are possible, see the 'enum caer\_frame\_event\_color\_channels'. To understand the original color filter arrangement to interpolate color images, see the 'enum caer\_frame\_event\_color\_filter'. Also, up to 128 different Regions of Interest (ROI) can be tracked. Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.8.2.14 `#define ROI_IDENTIFIER_SHIFT 8`

Shift and mask values for the color channels number, the color filter arrangement and the ROI identifier contained in the 'info' field of the frame event. Multiple channels (RGB for example) are possible, see the 'enum caer\_frame\_event\_color\_channels'. To understand the original color filter arrangement to interpolate color images, see the 'enum caer\_frame\_event\_color\_filter'. Also, up to 128 different Regions of Interest (ROI) can be tracked. Bit 0 is the valid mark, see '[common.h](#)' for more details.

### 4.8.3 Typedef Documentation

#### 4.8.3.1 `typedef struct caer_frame_event* caerFrameEvent`

Type for pointer to frame event data structure.

#### 4.8.3.2 `typedef struct caer_frame_event_packet* caerFrameEventPacket`

Type for pointer to frame event packet data structure.

### 4.8.4 Enumeration Type Documentation

#### 4.8.4.1 `enum caer_frame_event_color_channels`

List of all frame event color channel identifiers. Used to interpret the frame event color channel field.

Enumerator

**GRAYSCALE** Grayscale, one channel only.

**RGB** Red Green Blue, 3 color channels.

**RGBA** Red Green Blue Alpha, 3 color channels plus transparency.

#### 4.8.4.2 `enum caer_frame_event_color_filter`

List of all frame event color filter identifiers. Used to interpret the frame event color filter field.

Enumerator

**MONO** No color filter present, all light passes.

**RGBG** Standard Bayer color filter, 1 red 2 green 1 blue. Variation 1.

**GRGB** Standard Bayer color filter, 1 red 2 green 1 blue. Variation 2.

**GBGR** Standard Bayer color filter, 1 red 2 green 1 blue. Variation 3.

**BGRG** Standard Bayer color filter, 1 red 2 green 1 blue. Variation 4.

**RGBW** Modified Bayer color filter, with white (pass all light) instead of extra green. Variation 1.

**GRWB** Modified Bayer color filter, with white (pass all light) instead of extra green. Variation 2.

**WBGR** Modified Bayer color filter, with white (pass all light) instead of extra green. Variation 3.

**BWRG** Modified Bayer color filter, with white (pass all light) instead of extra green. Variation 4.



## 4.8.5 Function Documentation

**4.8.5.1** `static enum caer_frame_event_color_channels caerFrameEventGetChannelNumber ( caerFrameEvent event ) [inline],[static]`

Get the actual color channels number for the current frame. This can be used to store RGB frames for example.

### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
--------------	---

### Returns

frame color channels number.

**4.8.5.2** `static enum caer_frame_event_color_filter caerFrameEventGetColorFilter ( caerFrameEvent event ) [inline],[static]`

Get the identifier for the color filter used by the sensor. Useful for interpolating color images.

### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
--------------	---

### Returns

color filter identifier.

**4.8.5.3** `static int32_t caerFrameEventGetExposureLength ( caerFrameEvent event ) [inline],[static]`

The total length, in microseconds, of the frame exposure time.

### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
--------------	---

### Returns

the exposure time in microseconds.

**4.8.5.4** `static int32_t caerFrameEventGetLengthX ( caerFrameEvent event ) [inline],[static]`

Get the actual X axis length for the current frame.

**Parameters**

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
--------------	---

**Returns**

frame X axis length.

**4.8.5.5** `static int32_t caerFrameEventGetLengthY ( caerFrameEvent event )` `[inline],[static]`

Get the actual Y axis length for the current frame.

**Parameters**

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
--------------	---

**Returns**

frame Y axis length.

**4.8.5.6** `static uint16_t caerFrameEventGetPixel ( caerFrameEvent event, int32_t xAddress, int32_t yAddress )` `[inline],[static]`

Get the pixel value at the specified (X, Y) address. (X, Y) are checked against the actual possible values for this frame. Different channels are not taken into account! The (0, 0) pixel is in the upper left corner, like in OpenCV/computer graphics.

**Parameters**

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>xAddress</i>	X address value (checked).
<i>yAddress</i>	Y address value (checked).

**Returns**

pixel value (normalized to 16 bit depth).

**4.8.5.7** `static uint16_t* caerFrameEventGetPixelArrayUnsafe ( caerFrameEvent event )` `[inline],[static]`

Get a direct reference to the underlying pixels array. This can be used to both get and set values. No checks at all are performed at any point, nor any conversions, use this at your own risk! Remember that the 16 bit pixel values are in little-endian! The pixel array is laid out row by row (increasing X axis), going from top to bottom (increasing Y axis).

## Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
--------------	---

## Returns

the pixels array (16 bit integers are little-endian).

**4.8.5.8** `static uint16_t caerFrameEventGetPixelForChannel ( caerFrameEvent event, int32_t xAddress, int32_t yAddress, uint8_t channel ) [inline],[static]`

Get the pixel value at the specified (X, Y) address, taking into account the specified channel. (X, Y) and the channel number are checked against the actual possible values for this frame. The (0, 0) pixel is in the upper left corner, like in OpenCV/computer graphics.

## Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>xAddress</i>	X address value (checked).
<i>yAddress</i>	Y address value (checked).
<i>channel</i>	the channel number (checked).

## Returns

pixel value (normalized to 16 bit depth).

**4.8.5.9** `static uint16_t caerFrameEventGetPixelForChannelUnsafe ( caerFrameEvent event, int32_t xAddress, int32_t yAddress, uint8_t channel ) [inline],[static]`

Get the pixel value at the specified (X, Y) address, taking into account the specified channel. No checks on (X, Y) and the channel number are performed! The (0, 0) pixel is in the upper left corner, like in OpenCV/computer graphics.

## Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>xAddress</i>	X address value (unchecked).
<i>yAddress</i>	Y address value (unchecked).
<i>channel</i>	the channel number (unchecked).

## Returns

pixel value (normalized to 16 bit depth).

**4.8.5.10** `static size_t caerFrameEventGetPixelsMaxIndex ( caerFrameEvent event ) [inline],[static]`

Get the maximum valid index into the pixel array, at which you can still get valid pixels.

**Parameters**

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
--------------	---

**Returns**

maximum valid pixels array index.

**4.8.5.11** `static size_t caerFrameEventGetPixelsSize ( caerFrameEvent event )` `[inline],[static]`

Get the maximum size of the pixels array in bytes, in which you can still get valid pixels.

**Parameters**

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
--------------	---

**Returns**

maximum valid pixels array size in bytes.

**4.8.5.12** `static uint16_t caerFrameEventGetPixelUnsafe ( caerFrameEvent event, int32_t xAddress, int32_t yAddress )`  
`[inline],[static]`

Get the pixel value at the specified (X, Y) address. No checks on (X, Y) are performed! The (0, 0) pixel is in the upper left corner, like in OpenCV/computer graphics.

**Parameters**

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>xAddress</i>	X address value (unchecked).
<i>yAddress</i>	Y address value (unchecked).

**Returns**

pixel value (normalized to 16 bit depth).

**4.8.5.13** `static int32_t caerFrameEventGetPositionX ( caerFrameEvent event )` `[inline],[static]`

Get the X axis position offset. This is used to place partial frames, like the ones gotten from ROI readouts, in the visual space.

**Parameters**

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
--------------	---

**Returns**

X axis position offset.

**4.8.5.14** `static int32_t caerFrameEventGetPositionY ( caerFrameEvent event )` `[inline],[static]`

Get the Y axis position offset. This is used to place partial frames, like the ones gotten from ROI readouts, in the visual space.

**Parameters**

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
--------------	---

**Returns**

Y axis position offset.

**4.8.5.15** `static uint8_t caerFrameEventGetROIIdentifier ( caerFrameEvent event )` `[inline],[static]`

Get the numerical identifier for the Region of Interest (ROI) region, to distinguish between multiple of them.

**Parameters**

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
--------------	---

**Returns**

numerical ROI identifier.

**4.8.5.16** `static int32_t caerFrameEventGetTimestamp ( caerFrameEvent event )` `[inline],[static]`

Get the 32bit event timestamp, in microseconds. This is a median of the exposure timestamps. Be aware that this wraps around! You can either ignore this fact, or handle the special 'TIMESTAMP\_WRAP' event that is generated when this happens, or use the 64bit timestamp which never wraps around. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

**Parameters**

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
--------------	---

**Returns**

this event's 32bit microsecond timestamp.

**4.8.5.17** `static int64_t caerFrameEventGetTimestamp64 ( caerFrameEvent event, caerFrameEventPacket packet )`  
`[inline],[static]`

Get the 64bit event timestamp, in microseconds. This is a median of the exposure timestamps. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

#### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>packet</i>	the FrameEventPacket pointer for the packet containing this event. Cannot be NULL.

#### Returns

this event's 64bit microsecond timestamp.

**4.8.5.18** `static int32_t caerFrameEventGetTSEndOfExposure ( caerFrameEvent event )` `[inline],[static]`

Get the 32bit end of exposure timestamp, in microseconds. Be aware that this wraps around! You can either ignore this fact, or handle the special 'TIMESTAMP\_WRAP' event that is generated when this happens, or use the 64bit timestamp which never wraps around. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

#### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
--------------	---

#### Returns

this event's 32bit microsecond end of exposure timestamp.

**4.8.5.19** `static int64_t caerFrameEventGetTSEndOfExposure64 ( caerFrameEvent event, caerFrameEventPacket packet )`  
`[inline],[static]`

Get the 64bit end of exposure timestamp, in microseconds. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

#### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>packet</i>	the FrameEventPacket pointer for the packet containing this event. Cannot be NULL.

#### Returns

this event's 64bit microsecond end of exposure timestamp.

#### 4.8.5.20 `static int32_t caerFrameEventGetTSEndOfFrame ( caerFrameEvent event ) [inline],[static]`

Get the 32bit end of frame capture timestamp, in microseconds. Be aware that this wraps around! You can either ignore this fact, or handle the special 'TIMESTAMP\_WRAP' event that is generated when this happens, or use the 64bit timestamp which never wraps around. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

##### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
--------------	---

##### Returns

this event's 32bit microsecond end of frame timestamp.

#### 4.8.5.21 `static int64_t caerFrameEventGetTSEndOfFrame64 ( caerFrameEvent event, caerFrameEventPacket packet ) [inline],[static]`

Get the 64bit end of frame capture timestamp, in microseconds. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

##### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>packet</i>	the FrameEventPacket pointer for the packet containing this event. Cannot be NULL.

##### Returns

this event's 64bit microsecond end of frame timestamp.

#### 4.8.5.22 `static int32_t caerFrameEventGetTSStartOfExposure ( caerFrameEvent event ) [inline],[static]`

Get the 32bit start of exposure timestamp, in microseconds. Be aware that this wraps around! You can either ignore this fact, or handle the special 'TIMESTAMP\_WRAP' event that is generated when this happens, or use the 64bit timestamp which never wraps around. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

##### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
--------------	---

##### Returns

this event's 32bit microsecond start of exposure timestamp.

**4.8.5.23** `static int64_t caerFrameEventGetTSStartOfExposure64 ( caerFrameEvent event, caerFrameEventPacket packet ) [inline],[static]`

Get the 64bit start of exposure timestamp, in microseconds. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

#### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>packet</i>	the FrameEventPacket pointer for the packet containing this event. Cannot be NULL.

#### Returns

this event's 64bit microsecond start of exposure timestamp.

**4.8.5.24** `static int32_t caerFrameEventGetTSStartOfFrame ( caerFrameEvent event ) [inline],[static]`

Get the 32bit start of frame capture timestamp, in microseconds. Be aware that this wraps around! You can either ignore this fact, or handle the special 'TIMESTAMP\_WRAP' event that is generated when this happens, or use the 64bit timestamp which never wraps around. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

#### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
--------------	---

#### Returns

this event's 32bit microsecond start of frame timestamp.

**4.8.5.25** `static int64_t caerFrameEventGetTSStartOfFrame64 ( caerFrameEvent event, caerFrameEventPacket packet ) [inline],[static]`

Get the 64bit start of frame capture timestamp, in microseconds. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

#### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>packet</i>	the FrameEventPacket pointer for the packet containing this event. Cannot be NULL.

#### Returns

this event's 64bit microsecond start of frame timestamp.



**4.8.5.26** `static void caerFrameEventInvalidate ( caerFrameEvent event, caerFrameEventPacket packet )`  
`[inline], [static]`

Invalidate the current event by setting its valid bit to false and decreasing the number of valid events held in the packet. Only works with events that are already valid!

#### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>packet</i>	the FrameEventPacket pointer for the packet containing this event. Cannot be NULL.

**4.8.5.27** `static bool caerFrameEventsValid ( caerFrameEvent event )` `[inline], [static]`

Check if this frame event is valid.

#### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
--------------	---

#### Returns

true if valid, false if not.

**4.8.5.28** `caerFrameEventPacket caerFrameEventPacketAllocate ( int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow, int32_t maxLengthX, int32_t maxLengthY, int16_t maxChannelNumber )`

Allocate a new frame events packet. Use free() to reclaim this memory. The frame events allocate memory for a maximum sized pixels array, depending on the parameters passed to this function, so that every event occupies the same amount of memory (constant size). The actual frames inside of it might be smaller than that, for example when using ROI, and their actual size is stored inside the frame event and should always be queried from there. The unused part of a pixels array is guaranteed to be zeros.

#### Parameters

<i>eventCapacity</i>	the maximum number of events this packet will hold.
<i>eventSource</i>	the unique ID representing the source/generator of this packet.
<i>tsOverflow</i>	the current timestamp overflow counter value for this packet.
<i>maxLengthX</i>	the maximum expected X axis size for frames in this packet.
<i>maxLengthY</i>	the maximum expected Y axis size for frames in this packet.
<i>maxChannelNumber</i>	the maximum expected number of channels for frames in this packet.

#### Returns

a valid FrameEventPacket handle or NULL on error.

**4.8.5.29** `static caerFrameEvent caerFrameEventPacketGetEvent ( caerFrameEventPacket packet, int32_t n )`  
`[inline],[static]`

Get the frame event at the given index from the event packet.

#### Parameters

<i>packet</i>	a valid FrameEventPacket pointer. Cannot be NULL.
<i>n</i>	the index of the returned event. Must be within [0,eventCapacity[ bounds.

#### Returns

the requested frame event. NULL on error.

**4.8.5.30** `static size_t caerFrameEventPacketGetPixelsMaxIndex ( caerFrameEventPacket packet )` `[inline],[static]`

Get the maximum index into the pixels array, based upon how much memory was allocated to it by '[caerFrameEventPacketAllocate\(\)](#)'.

#### Parameters

<i>packet</i>	a valid FrameEventPacket pointer. Cannot be NULL.
---------------	---

#### Returns

maximum pixels array index.

**4.8.5.31** `static size_t caerFrameEventPacketGetPixelsSize ( caerFrameEventPacket packet )` `[inline],[static]`

Get the maximum size of the pixels array in bytes, based upon how much memory was allocated to it by '[caerFrameEventPacketAllocate\(\)](#)'.

#### Parameters

<i>packet</i>	a valid FrameEventPacket pointer. Cannot be NULL.
---------------	---

#### Returns

maximum pixels array size in bytes.

**4.8.5.32** `static void caerFrameEventSetColorFilter ( caerFrameEvent event, enum caer_frame_event_color_filter colorFilter )` `[inline],[static]`

Set the identifier for the color filter used by the sensor. Useful for interpolating color images.

## Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>colorFilter</i>	color filter identifier.

**4.8.5.33** `static void caerFrameEventSetLengthXLengthYChannelNumber ( caerFrameEvent event, int32_t lengthX, int32_t lengthY, enum caer_frame_event_color_channels channelNumber, caerFrameEventPacket packet )` `[inline], [static]`

Set the X and Y axes length and the color channels number for a frame, while taking into account the maximum amount of memory available for the pixel array, as allocated in '[caerFrameEventPacketAllocate\(\)](#)'.

## Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>lengthX</i>	the frame's X axis length.
<i>lengthY</i>	the frame's Y axis length.
<i>channelNumber</i>	the number of color channels for this frame.
<i>packet</i>	the FrameEventPacket pointer for the packet containing this event. Cannot be NULL.

**4.8.5.34** `static void caerFrameEventSetPixel ( caerFrameEvent event, int32_t xAddress, int32_t yAddress, uint16_t pixelValue )` `[inline], [static]`

Set the pixel value at the specified (X, Y) address. (X, Y) are checked against the actual possible values for this frame. Different channels are not taken into account! The (0, 0) pixel is in the upper left corner, like in OpenCV/computer graphics.

## Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>xAddress</i>	X address value (checked).
<i>yAddress</i>	Y address value (checked).
<i>pixelValue</i>	pixel value (normalized to 16 bit depth).

**4.8.5.35** `static void caerFrameEventSetPixelForChannel ( caerFrameEvent event, int32_t xAddress, int32_t yAddress, uint8_t channel, uint16_t pixelValue )` `[inline], [static]`

Set the pixel value at the specified (X, Y) address, taking into account the specified channel. (X, Y) and the channel number are checked against the actual possible values for this frame. The (0, 0) pixel is in the upper left corner, like in OpenCV/computer graphics.

## Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>xAddress</i>	X address value (checked).
<i>yAddress</i>	Y address value (checked).
<i>channel</i>	the channel number (checked).
<i>pixelValue</i>	pixel value (normalized to 16 bit depth).

**4.8.5.36** `static void caerFrameEventSetPixelForChannelUnsafe ( caerFrameEvent event, int32_t xAddress, int32_t yAddress, uint8_t channel, uint16_t pixelValue ) [inline],[static]`

Set the pixel value at the specified (X, Y) address, taking into account the specified channel. No checks on (X, Y) and the channel number are performed! The (0, 0) pixel is in the upper left corner, like in OpenCV/computer graphics.

#### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>xAddress</i>	X address value (unchecked).
<i>yAddress</i>	Y address value (unchecked).
<i>channel</i>	the channel number (unchecked).
<i>pixelValue</i>	pixel value (normalized to 16 bit depth).

**4.8.5.37** `static void caerFrameEventSetPixelUnsafe ( caerFrameEvent event, int32_t xAddress, int32_t yAddress, uint16_t pixelValue ) [inline],[static]`

Set the pixel value at the specified (X, Y) address. No checks on (X, Y) are performed! The (0, 0) pixel is in the upper left corner, like in OpenCV/computer graphics.

#### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>xAddress</i>	X address value (unchecked).
<i>yAddress</i>	Y address value (unchecked).
<i>pixelValue</i>	pixel value (normalized to 16 bit depth).

**4.8.5.38** `static void caerFrameEventSetPositionX ( caerFrameEvent event, int32_t positionX ) [inline],[static]`

Set the X axis position offset. This is used to place partial frames, like the ones gotten from ROI readouts, in the visual space.

#### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>positionX</i>	X axis position offset.

**4.8.5.39** `static void caerFrameEventSetPositionY ( caerFrameEvent event, int32_t positionY ) [inline],[static]`

Set the Y axis position offset. This is used to place partial frames, like the ones gotten from ROI readouts, in the visual space.

#### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>positionY</i>	Y axis position offset.

**4.8.5.40** `static void caerFrameEventSetROIIdentifier ( caerFrameEvent event, uint8_t roiIdentifier ) [inline], [static]`

Set the numerical identifier for the Region of Interest (ROI) region, to distinguish between multiple of them.

#### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>roiIdentifier</i>	numerical ROI identifier.

**4.8.5.41** `static void caerFrameEventSetTSEndOfExposure ( caerFrameEvent event, int32_t endExposure ) [inline], [static]`

Set the 32bit end of exposure timestamp, the value has to be in microseconds.

#### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>endExposure</i>	a positive 32bit microsecond timestamp.

**4.8.5.42** `static void caerFrameEventSetTSEndOfFrame ( caerFrameEvent event, int32_t endFrame ) [inline], [static]`

Set the 32bit end of frame capture timestamp, the value has to be in microseconds.

#### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>endFrame</i>	a positive 32bit microsecond timestamp.

**4.8.5.43** `static void caerFrameEventSetTSStartOfExposure ( caerFrameEvent event, int32_t startExposure ) [inline], [static]`

Set the 32bit start of exposure timestamp, the value has to be in microseconds.

#### Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>startExposure</i>	a positive 32bit microsecond timestamp.

**4.8.5.44** `static void caerFrameEventSetTSStartOfFrame ( caerFrameEvent event, int32_t startFrame ) [inline], [static]`

Set the 32bit start of frame capture timestamp, the value has to be in microseconds.

## Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>startFrame</i>	a positive 32bit microsecond timestamp.

**4.8.5.45** `static void caerFrameEventValidate ( caerFrameEvent event, caerFrameEventPacket packet )` `[inline]`, `[static]`

Validate the current event by setting its valid bit to true and increasing the event packet's event count and valid event count. Only works on events that are invalid. DO NOT CALL THIS AFTER HAVING PREVIOUSLY ALREADY INVALIDATED THIS EVENT, the total count will be incorrect.

## Parameters

<i>event</i>	a valid FrameEvent pointer. Cannot be NULL.
<i>packet</i>	the FrameEventPacket pointer for the packet containing this event. Cannot be NULL.

**4.8.5.46** `PACKED_STRUCT ( struct caer_frame_event{uint32_t info;int32_t ts_startframe;int32_t ts_endframe;int32_t ts_startexposure;int32_t ts_endexposure;int32_t lengthX;int32_t lengthY;int32_t positionX;int32_t positionY;uint16_t pixels[ ];} )`

Frame event data structure definition. This contains the actual information on the frame (ROI, color channels, color filter), several timestamps to signal start and end of capture and of exposure, as well as the actual pixels, in a 16 bit normalized format. The (0, 0) address is in the upper left corner, like in OpenCV/computer graphics. The pixel array is laid out row by row (increasing X axis), going from top to bottom (increasing Y axis). Signed integers are used for fields that are to be interpreted directly, for compatibility with languages that do not have unsigned integer types, such as Java.

**4.8.5.47** `PACKED_STRUCT ( struct caer_frame_event_packet{struct caer_event_packet_header packetHeader;} )`

Frame event packet data structure definition. EventPackets are always made up of the common packet header, followed by 'eventCapacity' events. Everything has to be in one contiguous memory block. Direct access to the events array is not possible for Frame events. To calculate position offsets, use the 'eventSize' field in the packet header.

## 4.9 events/imu6.h File Reference

```
#include "common.h"
```

### Macros

- `#define CAER_IMU6_ITERATOR_ALL_START(IMU6_PACKET)`
- `#define CAER_IMU6_ITERATOR_ALL_END }`
- `#define CAER_IMU6_ITERATOR_VALID_START(IMU6_PACKET)`
- `#define CAER_IMU6_ITERATOR_VALID_END }`

## Typedefs

- typedef struct caer\_imu6\_event \* [caerIMU6Event](#)
- typedef struct caer\_imu6\_event\_packet \* [caerIMU6EventPacket](#)

## Functions

- [PACKED\\_STRUCT](#) (struct caer\_imu6\_event{uint32\_t info;int32\_t timestamp;float accel\_x;float accel\_y;float accel\_z;float gyro\_x;float gyro\_y;float gyro\_z;float temp;})
- [PACKED\\_STRUCT](#) (struct caer\_imu6\_event\_packet{struct caer\_event\_packet\_header packetHeader;struct caer\_imu6\_event events[ ];})
- [caerIMU6EventPacket](#) [caerIMU6EventPacketAllocate](#) (int32\_t eventCapacity, int16\_t eventSource, int32\_t tsOverflow)
- static [caerIMU6Event](#) [caerIMU6EventPacketGetEvent](#) ([caerIMU6EventPacket](#) packet, int32\_t n)
- static int32\_t [caerIMU6EventGetTimestamp](#) ([caerIMU6Event](#) event)
- static int64\_t [caerIMU6EventGetTimestamp64](#) ([caerIMU6Event](#) event, [caerIMU6EventPacket](#) packet)
- static void [caerIMU6EventSetTimestamp](#) ([caerIMU6Event](#) event, int32\_t timestamp)
- static bool [caerIMU6EventIsValid](#) ([caerIMU6Event](#) event)
- static void [caerIMU6EventValidate](#) ([caerIMU6Event](#) event, [caerIMU6EventPacket](#) packet)
- static void [caerIMU6EventInvalidate](#) ([caerIMU6Event](#) event, [caerIMU6EventPacket](#) packet)
- static float [caerIMU6EventGetAccelX](#) ([caerIMU6Event](#) event)
- static void [caerIMU6EventSetAccelX](#) ([caerIMU6Event](#) event, float accelX)
- static float [caerIMU6EventGetAccelY](#) ([caerIMU6Event](#) event)
- static void [caerIMU6EventSetAccelY](#) ([caerIMU6Event](#) event, float accelY)
- static float [caerIMU6EventGetAccelZ](#) ([caerIMU6Event](#) event)
- static void [caerIMU6EventSetAccelZ](#) ([caerIMU6Event](#) event, float accelZ)
- static float [caerIMU6EventGetGyroX](#) ([caerIMU6Event](#) event)
- static void [caerIMU6EventSetGyroX](#) ([caerIMU6Event](#) event, float gyroX)
- static float [caerIMU6EventGetGyroY](#) ([caerIMU6Event](#) event)
- static void [caerIMU6EventSetGyroY](#) ([caerIMU6Event](#) event, float gyroY)
- static float [caerIMU6EventGetGyroZ](#) ([caerIMU6Event](#) event)
- static void [caerIMU6EventSetGyroZ](#) ([caerIMU6Event](#) event, float gyroZ)
- static float [caerIMU6EventGetTemp](#) ([caerIMU6Event](#) event)
- static void [caerIMU6EventSetTemp](#) ([caerIMU6Event](#) event, float temp)

### 4.9.1 Detailed Description

IMU6 (6 axes) Events format definition and handling functions. This contains data coming from the Inertial Measurement Unit chip, with the 3-axes accelerometer and 3-axes gyroscope. Temperature is also included.

### 4.9.2 Macro Definition Documentation

#### 4.9.2.1 #define CAER\_IMU6\_ITERATOR\_ALL\_END }

Iterator close statement.

#### 4.9.2.2 #define CAER\_IMU6\_ITERATOR\_ALL\_START( IMU6\_PACKET )

**Value:**

```
for (int32_t caerIMU6IteratorCounter = 0; \
    caerIMU6IteratorCounter < caerEventPacketHeaderGetEventNumber(&(
IMU6_PACKET)->packetHeader); \
    caerIMU6IteratorCounter++) { \
    caerIMU6Event caerIMU6IteratorElement =
    caerIMU6EventPacketGetEvent(IMU6_PACKET, caerIMU6IteratorCounter);
```

Iterator over all IMU6 events in a packet. Returns the current index in the 'caerIMU6IteratorCounter' variable of type 'int32\_t' and the current event in the 'caerIMU6IteratorElement' variable of type caerIMU6Event.

IMU6\_PACKET: a valid IMU6EventPacket pointer. Cannot be NULL.

#### 4.9.2.3 #define CAER\_IMU6\_ITERATOR\_VALID\_END }

Iterator close statement.

#### 4.9.2.4 #define CAER\_IMU6\_ITERATOR\_VALID\_START( IMU6\_PACKET )

**Value:**

```
for (int32_t caerIMU6IteratorCounter = 0; \
    caerIMU6IteratorCounter < caerEventPacketHeaderGetEventNumber(&(
IMU6_PACKET)->packetHeader); \
    caerIMU6IteratorCounter++) { \
    caerIMU6Event caerIMU6IteratorElement =
    caerIMU6EventPacketGetEvent(IMU6_PACKET, caerIMU6IteratorCounter); \
    if (!caerIMU6EventIsValid(caerIMU6IteratorElement)) { continue; }
```

Iterator over only the valid IMU6 events in a packet. Returns the current index in the 'caerIMU6IteratorCounter' variable of type 'int32\_t' and the current event in the 'caerIMU6IteratorElement' variable of type caerIMU6Event.

IMU6\_PACKET: a valid IMU6EventPacket pointer. Cannot be NULL.

### 4.9.3 Typedef Documentation

#### 4.9.3.1 typedef struct caer\_imu6\_event\* caerIMU6Event

Type for pointer to IMU 6-axes event data structure.

#### 4.9.3.2 typedef struct caer\_imu6\_event\_packet\* caerIMU6EventPacket

Type for pointer to IMU 6-axes event packet data structure.

### 4.9.4 Function Documentation

#### 4.9.4.1 static float caerIMU6EventGetAccelX ( caerIMU6Event event ) [inline], [static]

Get the X axis acceleration reading (from accelerometer). This is in g (1 g = 9.81 m/s<sup>2</sup>).



## Parameters

<i>event</i>	a valid IMU6Event pointer. Cannot be NULL.
--------------	--

## Returns

acceleration on the X axis.

**4.9.4.2** `static float caerIMU6EventGetAccelY ( caerIMU6Event event )` `[inline], [static]`

Get the Y axis acceleration reading (from accelerometer). This is in g (1 g = 9.81 m/s<sup>2</sup>).

## Parameters

<i>event</i>	a valid IMU6Event pointer. Cannot be NULL.
--------------	--

## Returns

acceleration on the Y axis.

**4.9.4.3** `static float caerIMU6EventGetAccelZ ( caerIMU6Event event )` `[inline], [static]`

Get the Z axis acceleration reading (from accelerometer). This is in g (1 g = 9.81 m/s<sup>2</sup>).

## Parameters

<i>event</i>	a valid IMU6Event pointer. Cannot be NULL.
--------------	--

## Returns

acceleration on the Z axis.

**4.9.4.4** `static float caerIMU6EventGetGyroX ( caerIMU6Event event )` `[inline], [static]`

Get the X axis (roll) angular velocity reading (from gyroscope). This is in °/s (deg/sec).

## Parameters

<i>event</i>	a valid IMU6Event pointer. Cannot be NULL.
--------------	--

## Returns

angular velocity on the X axis (roll).

4.9.4.5 `static float caerIMU6EventGetGyroY ( caerIMU6Event event ) [inline],[static]`

Get the Y axis (pitch) angular velocity reading (from gyroscope). This is in °/s (deg/sec).

Parameters

<i>event</i>	a valid IMU6Event pointer. Cannot be NULL.
--------------	--

Returns

angular velocity on the Y axis (pitch).

4.9.4.6 `static float caerIMU6EventGetGyroZ ( caerIMU6Event event ) [inline],[static]`

Get the Z axis (yaw) angular velocity reading (from gyroscope). This is in °/s (deg/sec).

Parameters

<i>event</i>	a valid IMU6Event pointer. Cannot be NULL.
--------------	--

Returns

angular velocity on the Z axis (yaw).

4.9.4.7 `static float caerIMU6EventGetTemp ( caerIMU6Event event ) [inline],[static]`

Get the temperature reading. This is in °C.

Parameters

<i>event</i>	a valid IMU6Event pointer. Cannot be NULL.
--------------	--

Returns

temperature in °C.

4.9.4.8 `static int32_t caerIMU6EventGetTimestamp ( caerIMU6Event event ) [inline],[static]`

Get the 32bit event timestamp, in microseconds. Be aware that this wraps around! You can either ignore this fact, or handle the special 'TIMESTAMP\_WRAP' event that is generated when this happens, or use the 64bit timestamp which never wraps around. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

Parameters

<i>event</i>	a valid IMU6Event pointer. Cannot be NULL.
--------------	--

**Returns**

this event's 32bit microsecond timestamp.

**4.9.4.9** `static int64_t caerIMU6EventGetTimestamp64 ( caerIMU6Event event, caerIMU6EventPacket packet )`  
`[inline],[static]`

Get the 64bit event timestamp, in microseconds. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

**Parameters**

<i>event</i>	a valid IMU6Event pointer. Cannot be NULL.
<i>packet</i>	the IMU6EventPacket pointer for the packet containing this event. Cannot be NULL.

**Returns**

this event's 64bit microsecond timestamp.

**4.9.4.10** `static void caerIMU6EventInvalidate ( caerIMU6Event event, caerIMU6EventPacket packet )` `[inline],[static]`

Invalidate the current event by setting its valid bit to false and decreasing the number of valid events held in the packet. Only works with events that are already valid!

**Parameters**

<i>event</i>	a valid IMU6Event pointer. Cannot be NULL.
<i>packet</i>	the IMU6EventPacket pointer for the packet containing this event. Cannot be NULL.

**4.9.4.11** `static bool caerIMU6EventIsValid ( caerIMU6Event event )` `[inline],[static]`

Check if this IMU 6-axes event is valid.

**Parameters**

<i>event</i>	a valid IMU6Event pointer. Cannot be NULL.
--------------	--

**Returns**

true if valid, false if not.

**4.9.4.12** `caerIMU6EventPacket caerIMU6EventPacketAllocate ( int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow )`

Allocate a new IMU 6-axes events packet. Use free() to reclaim this memory.

## Parameters

<i>eventCapacity</i>	the maximum number of events this packet will hold.
<i>eventSource</i>	the unique ID representing the source/generator of this packet.
<i>tsOverflow</i>	the current timestamp overflow counter value for this packet.

## Returns

a valid IMU6EventPacket handle or NULL on error.

**4.9.4.13** `static caerIMU6Event caerIMU6EventPacketGetEvent ( caerIMU6EventPacket packet, int32_t n )`  
`[inline],[static]`

Get the IMU 6-axes event at the given index from the event packet.

## Parameters

<i>packet</i>	a valid IMU6EventPacket pointer. Cannot be NULL.
<i>n</i>	the index of the returned event. Must be within [0,eventCapacity[ bounds.

## Returns

the requested IMU 6-axes event. NULL on error.

**4.9.4.14** `static void caerIMU6EventSetAccelX ( caerIMU6Event event, float accelX )` `[inline],[static]`

Set the X axis acceleration reading (from accelerometer). This is in g (1 g = 9.81 m/s<sup>2</sup>).

## Parameters

<i>event</i>	a valid IMU6Event pointer. Cannot be NULL.
<i>accelX</i>	acceleration on the X axis.

**4.9.4.15** `static void caerIMU6EventSetAccelY ( caerIMU6Event event, float accelY )` `[inline],[static]`

Set the Y axis acceleration reading (from accelerometer). This is in g (1 g = 9.81 m/s<sup>2</sup>).

## Parameters

<i>event</i>	a valid IMU6Event pointer. Cannot be NULL.
<i>accelY</i>	acceleration on the Y axis.

**4.9.4.16** `static void caerIMU6EventSetAccelZ ( caerIMU6Event event, float accelZ )` `[inline], [static]`

Set the Z axis acceleration reading (from accelerometer). This is in g (1 g = 9.81 m/s<sup>2</sup>).

Parameters

<i>event</i>	a valid IMU6Event pointer. Cannot be NULL.
<i>accelZ</i>	acceleration on the Z axis.

**4.9.4.17** `static void caerIMU6EventSetGyroX ( caerIMU6Event event, float gyroX )` `[inline], [static]`

Set the X axis (roll) angular velocity reading (from gyroscope). This is in °/s (deg/sec).

Parameters

<i>event</i>	a valid IMU6Event pointer. Cannot be NULL.
<i>gyroX</i>	angular velocity on the X axis (roll).

**4.9.4.18** `static void caerIMU6EventSetGyroY ( caerIMU6Event event, float gyroY )` `[inline], [static]`

Set the Y axis (pitch) angular velocity reading (from gyroscope). This is in °/s (deg/sec).

Parameters

<i>event</i>	a valid IMU6Event pointer. Cannot be NULL.
<i>gyroY</i>	angular velocity on the Y axis (pitch).

**4.9.4.19** `static void caerIMU6EventSetGyroZ ( caerIMU6Event event, float gyroZ )` `[inline], [static]`

Set the Z axis (yaw) angular velocity reading (from gyroscope). This is in °/s (deg/sec).

Parameters

<i>event</i>	a valid IMU6Event pointer. Cannot be NULL.
<i>gyroZ</i>	angular velocity on the Z axis (yaw).

**4.9.4.20** `static void caerIMU6EventSetTemp ( caerIMU6Event event, float temp )` `[inline], [static]`

Set the temperature reading. This is in °C.

Parameters

<i>event</i>	a valid IMU6Event pointer. Cannot be NULL.
<i>temp</i>	temperature in °C.

**4.9.4.21** `static void caerIMU6EventSetTimestamp ( caerIMU6Event event, int32_t timestamp )` `[inline]`, `[static]`

Set the 32bit event timestamp, the value has to be in microseconds.

#### Parameters

<i>event</i>	a valid IMU6Event pointer. Cannot be NULL.
<i>timestamp</i>	a positive 32bit microsecond timestamp.

**4.9.4.22** `static void caerIMU6EventValidate ( caerIMU6Event event, caerIMU6EventPacket packet )` `[inline]`, `[static]`

Validate the current event by setting its valid bit to true and increasing the event packet's event count and valid event count. Only works on events that are invalid. DO NOT CALL THIS AFTER HAVING PREVIOUSLY ALREADY INVALIDATED THIS EVENT, the total count will be incorrect.

#### Parameters

<i>event</i>	a valid IMU6Event pointer. Cannot be NULL.
<i>packet</i>	the IMU6EventPacket pointer for the packet containing this event. Cannot be NULL.

**4.9.4.23** `PACKED_STRUCT ( struct caer_imu6_event{uint32_t info;int32_t timestamp;float accel_x;float accel_y;float accel_z;float gyro_x;float gyro_y;float gyro_z;float temp;} )`

IMU 6-axes event data structure definition. This contains accelerometer and gyroscope headings, plus temperature. The X, Y and Z axes are referred to the camera plane. X increases to the right, Y going up and Z towards where the lens is pointing. Rotation for the gyroscope is counter-clockwise along the increasing axis, for all three axes. Floats are in IEEE 754-2008 binary32 format. Signed integers are used for fields that are to be interpreted directly, for compatibility with languages that do not have unsigned integer types, such as Java.

**4.9.4.24** `PACKED_STRUCT ( struct caer_imu6_event_packet{struct caer_event_packet_header packetHeader;struct caer_imu6_event events[];} )`

IMU 6-axes event packet data structure definition. EventPackets are always made up of the common packet header, followed by 'eventCapacity' events. Everything has to be in one contiguous memory block.

## 4.10 events/imu9.h File Reference

```
#include "common.h"
```

### Macros

- `#define CAER_IMU9_ITERATOR_ALL_START(IMU9_PACKET)`
- `#define CAER_IMU9_ITERATOR_ALL_END }`
- `#define CAER_IMU9_ITERATOR_VALID_START(IMU9_PACKET)`
- `#define CAER_IMU9_ITERATOR_VALID_END }`

## Typedefs

- typedef struct caer\_imu9\_event \* [caerIMU9Event](#)
- typedef struct caer\_imu9\_event\_packet \* [caerIMU9EventPacket](#)

## Functions

- [PACKED\\_STRUCT](#) (struct caer\_imu9\_event{uint32\_t info;int32\_t timestamp;float accel\_x;float accel\_y;float accel\_z;float gyro\_x;float gyro\_y;float gyro\_z;float temp;float comp\_x;float comp\_y;float comp\_z;})
- [PACKED\\_STRUCT](#) (struct caer\_imu9\_event\_packet{struct caer\_event\_packet\_header packetHeader;struct caer\_imu9\_event events[ ];})
- [caerIMU9EventPacket caerIMU9EventPacketAllocate](#) (int32\_t eventCapacity, int16\_t eventSource, int32\_t tsOverflow)
- static [caerIMU9Event caerIMU9EventPacketGetEvent](#) ([caerIMU9EventPacket](#) packet, int32\_t n)
- static int32\_t [caerIMU9EventGetTimestamp](#) ([caerIMU9Event](#) event)
- static int64\_t [caerIMU9EventGetTimestamp64](#) ([caerIMU9Event](#) event, [caerIMU9EventPacket](#) packet)
- static void [caerIMU9EventSetTimestamp](#) ([caerIMU9Event](#) event, int32\_t timestamp)
- static bool [caerIMU9EventIsValid](#) ([caerIMU9Event](#) event)
- static void [caerIMU9EventValidate](#) ([caerIMU9Event](#) event, [caerIMU9EventPacket](#) packet)
- static void [caerIMU9EventInvalidate](#) ([caerIMU9Event](#) event, [caerIMU9EventPacket](#) packet)
- static float [caerIMU9EventGetAccelX](#) ([caerIMU9Event](#) event)
- static void [caerIMU9EventSetAccelX](#) ([caerIMU9Event](#) event, float accelX)
- static float [caerIMU9EventGetAccelY](#) ([caerIMU9Event](#) event)
- static void [caerIMU9EventSetAccelY](#) ([caerIMU9Event](#) event, float accelY)
- static float [caerIMU9EventGetAccelZ](#) ([caerIMU9Event](#) event)
- static void [caerIMU9EventSetAccelZ](#) ([caerIMU9Event](#) event, float accelZ)
- static float [caerIMU9EventGetGyroX](#) ([caerIMU9Event](#) event)
- static void [caerIMU9EventSetGyroX](#) ([caerIMU9Event](#) event, float gyroX)
- static float [caerIMU9EventGetGyroY](#) ([caerIMU9Event](#) event)
- static void [caerIMU9EventSetGyroY](#) ([caerIMU9Event](#) event, float gyroY)
- static float [caerIMU9EventGetGyroZ](#) ([caerIMU9Event](#) event)
- static void [caerIMU9EventSetGyroZ](#) ([caerIMU9Event](#) event, float gyroZ)
- static float [caerIMU9EventGetTemp](#) ([caerIMU9Event](#) event)
- static void [caerIMU9EventSetTemp](#) ([caerIMU9Event](#) event, float temp)
- static float [caerIMU9EventGetCompX](#) ([caerIMU9Event](#) event)
- static void [caerIMU9EventSetCompX](#) ([caerIMU9Event](#) event, float compX)
- static float [caerIMU9EventGetCompY](#) ([caerIMU9Event](#) event)
- static void [caerIMU9EventSetCompY](#) ([caerIMU9Event](#) event, float compY)
- static float [caerIMU9EventGetCompZ](#) ([caerIMU9Event](#) event)
- static void [caerIMU9EventSetCompZ](#) ([caerIMU9Event](#) event, float compZ)

### 4.10.1 Detailed Description

IMU9 (9 axes) Events format definition and handling functions. This contains data coming from the Inertial Measurement Unit chip, with the 3-axes accelerometer and 3-axes gyroscope. Temperature is also included. Further, 3-axes from the magnetometer are included, which can be used to get a compass-like heading.

### 4.10.2 Macro Definition Documentation

#### 4.10.2.1 `#define CAER_IMU9_ITERATOR_ALL_END`

Iterator close statement.

#### 4.10.2.2 `#define CAER_IMU9_ITERATOR_ALL_START( IMU9_PACKET )`

**Value:**

```
for (int32_t caerIMU9IteratorCounter = 0; \
    caerIMU9IteratorCounter < caerEventPacketHeaderGetEventNumber(&(
IMU9_PACKET)->packetHeader); \
    caerIMU9IteratorCounter++) { \
    caerIMU9Event caerIMU9IteratorElement =
    caerIMU9EventPacketGetEvent(IMU9_PACKET, caerIMU9IteratorCounter);
```

Iterator over all IMU9 events in a packet. Returns the current index in the 'caerIMU9IteratorCounter' variable of type 'int32\_t' and the current event in the 'caerIMU9IteratorElement' variable of type caerIMU9Event.

IMU9\_PACKET: a valid IMU9EventPacket pointer. Cannot be NULL.

#### 4.10.2.3 `#define CAER_IMU9_ITERATOR_VALID_END}`

Iterator close statement.

#### 4.10.2.4 `#define CAER_IMU9_ITERATOR_VALID_START( IMU9_PACKET )`

**Value:**

```
for (int32_t caerIMU9IteratorCounter = 0; \
    caerIMU9IteratorCounter < caerEventPacketHeaderGetEventNumber(&(
IMU9_PACKET)->packetHeader); \
    caerIMU9IteratorCounter++) { \
    caerIMU9Event caerIMU9IteratorElement =
    caerIMU9EventPacketGetEvent(IMU9_PACKET, caerIMU9IteratorCounter); \
    if (!caerIMU9EventIsValid(caerIMU9IteratorElement)) { continue; }
```

Iterator over only the valid IMU9 events in a packet. Returns the current index in the 'caerIMU9IteratorCounter' variable of type 'int32\_t' and the current event in the 'caerIMU9IteratorElement' variable of type caerIMU9Event.

IMU9\_PACKET: a valid IMU9EventPacket pointer. Cannot be NULL.

### 4.10.3 Typedef Documentation

#### 4.10.3.1 `typedef struct caer_imu9_event* caerIMU9Event`

Type for pointer to IMU 9-axes event data structure.

#### 4.10.3.2 `typedef struct caer_imu9_event_packet* caerIMU9EventPacket`

Type for pointer to IMU 9-axes event packet data structure.

### 4.10.4 Function Documentation

#### 4.10.4.1 `static float caerIMU9EventGetAccelX ( caerIMU9Event event ) [inline],[static]`

Get the X axis acceleration reading (from accelerometer). This is in g (1 g = 9.81 m/s<sup>2</sup>).



## Parameters

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
--------------	--

## Returns

acceleration on the X axis.

**4.10.4.2 static float caerIMU9EventGetAccelY ( caerIMU9Event *event* )** [inline],[static]

Get the Y axis acceleration reading (from accelerometer). This is in g (1 g = 9.81 m/s<sup>2</sup>).

## Parameters

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
--------------	--

## Returns

acceleration on the Y axis.

**4.10.4.3 static float caerIMU9EventGetAccelZ ( caerIMU9Event *event* )** [inline],[static]

Get the Z axis acceleration reading (from accelerometer). This is in g (1 g = 9.81 m/s<sup>2</sup>).

## Parameters

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
--------------	--

## Returns

acceleration on the Z axis.

**4.10.4.4 static float caerIMU9EventGetCompX ( caerIMU9Event *event* )** [inline],[static]

Get the X axis compass heading (from magnetometer). This is in  $\mu$ T.

## Parameters

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
--------------	--

## Returns

X axis compass heading.

#### 4.10.4.5 `static float caerIMU9EventGetCompY ( caerIMU9Event event ) [inline],[static]`

Get the Y axis compass heading (from magnetometer). This is in  $\mu\text{T}$ .

##### Parameters

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
--------------	--

##### Returns

Y axis compass heading.

#### 4.10.4.6 `static float caerIMU9EventGetCompZ ( caerIMU9Event event ) [inline],[static]`

Get the Z axis compass heading (from magnetometer). This is in  $\mu\text{T}$ .

##### Parameters

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
--------------	--

##### Returns

Z axis compass heading.

#### 4.10.4.7 `static float caerIMU9EventGetGyroX ( caerIMU9Event event ) [inline],[static]`

Get the X axis (roll) angular velocity reading (from gyroscope). This is in  $^{\circ}/\text{s}$  (deg/sec).

##### Parameters

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
--------------	--

##### Returns

angular velocity on the X axis (roll).

#### 4.10.4.8 `static float caerIMU9EventGetGyroY ( caerIMU9Event event ) [inline],[static]`

Get the Y axis (pitch) angular velocity reading (from gyroscope). This is in  $^{\circ}/\text{s}$  (deg/sec).

##### Parameters

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
--------------	--

**Returns**

angular velocity on the Y axis (pitch).

**4.10.4.9** `static float caerIMU9EventGetGyroZ ( caerIMU9Event event )` `[inline],[static]`

Get the Z axis (yaw) angular velocity reading (from gyroscope). This is in %s (deg/sec).

**Parameters**

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
--------------	--

**Returns**

angular velocity on the Z axis (yaw).

**4.10.4.10** `static float caerIMU9EventGetTemp ( caerIMU9Event event )` `[inline],[static]`

Get the temperature reading. This is in °C.

**Parameters**

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
--------------	--

**Returns**

temperature in °C.

**4.10.4.11** `static int32_t caerIMU9EventGetTimestamp ( caerIMU9Event event )` `[inline],[static]`

Get the 32bit event timestamp, in microseconds. Be aware that this wraps around! You can either ignore this fact, or handle the special 'TIMESTAMP\_WRAP' event that is generated when this happens, or use the 64bit timestamp which never wraps around. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

**Parameters**

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
--------------	--

**Returns**

this event's 32bit microsecond timestamp.

**4.10.4.12** `static int64_t caerIMU9EventGetTimestamp64 ( caerIMU9Event event, caerIMU9EventPacket packet )`  
`[inline],[static]`

Get the 64bit event timestamp, in microseconds. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

#### Parameters

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
<i>packet</i>	the IMU9EventPacket pointer for the packet containing this event. Cannot be NULL.

#### Returns

this event's 64bit microsecond timestamp.

**4.10.4.13** `static void caerIMU9EventInvalidate ( caerIMU9Event event, caerIMU9EventPacket packet )` `[inline],[static]`

Invalidate the current event by setting its valid bit to false and decreasing the number of valid events held in the packet. Only works with events that are already valid!

#### Parameters

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
<i>packet</i>	the IMU9EventPacket pointer for the packet containing this event. Cannot be NULL.

**4.10.4.14** `static bool caerIMU9EventIsValid ( caerIMU9Event event )` `[inline],[static]`

Check if this IMU 9-axes event is valid.

#### Parameters

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
--------------	--

#### Returns

true if valid, false if not.

**4.10.4.15** `caerIMU9EventPacket caerIMU9EventPacketAllocate ( int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow )`

Allocate a new IMU 9-axes events packet. Use free() to reclaim this memory.

#### Parameters

<i>eventCapacity</i>	the maximum number of events this packet will hold.
<i>eventSource</i>	the unique ID representing the source/generator of this packet.
<i>tsOverflow</i>	the current timestamp overflow counter value for this packet.

**Returns**

a valid IMU9EventPacket handle or NULL on error.

**4.10.4.16** `static caerIMU9Event caerIMU9EventPacketGetEvent ( caerIMU9EventPacket packet, int32_t n )`  
[inline],[static]

Get the IMU 9-axes event at the given index from the event packet.

**Parameters**

<i>packet</i>	a valid IMU9EventPacket pointer. Cannot be NULL.
<i>n</i>	the index of the returned event. Must be within [0,eventCapacity[ bounds.

**Returns**

the requested IMU 9-axes event. NULL on error.

**4.10.4.17** `static void caerIMU9EventSetAccelX ( caerIMU9Event event, float accelX )` [inline],[static]

Set the X axis acceleration reading (from accelerometer). This is in g (1 g = 9.81 m/s<sup>2</sup>).

**Parameters**

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
<i>accelX</i>	acceleration on the X axis.

**4.10.4.18** `static void caerIMU9EventSetAccelY ( caerIMU9Event event, float accelY )` [inline],[static]

Set the Y axis acceleration reading (from accelerometer). This is in g (1 g = 9.81 m/s<sup>2</sup>).

**Parameters**

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
<i>accelY</i>	acceleration on the Y axis.

**4.10.4.19** `static void caerIMU9EventSetAccelZ ( caerIMU9Event event, float accelZ )` [inline],[static]

Set the Z axis acceleration reading (from accelerometer). This is in g (1 g = 9.81 m/s<sup>2</sup>).

**Parameters**

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
<i>accelZ</i>	acceleration on the Z axis.

4.10.4.20 `static void caerIMU9EventSetCompX ( caerIMU9Event event, float compX )` `[inline],[static]`

Set the X axis compass heading (from magnetometer). This is in  $\mu\text{T}$ .

#### Parameters

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
<i>compX</i>	X axis compass heading.

4.10.4.21 `static void caerIMU9EventSetCompY ( caerIMU9Event event, float compY )` `[inline],[static]`

Set the Y axis compass heading (from magnetometer). This is in  $\mu\text{T}$ .

#### Parameters

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
<i>compY</i>	Y axis compass heading.

4.10.4.22 `static void caerIMU9EventSetCompZ ( caerIMU9Event event, float compZ )` `[inline],[static]`

Set the Z axis compass heading (from magnetometer). This is in  $\mu\text{T}$ .

#### Parameters

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
<i>compZ</i>	Z axis compass heading.

4.10.4.23 `static void caerIMU9EventSetGyroX ( caerIMU9Event event, float gyroX )` `[inline],[static]`

Set the X axis (roll) angular velocity reading (from gyroscope). This is in  $\text{°/s}$  (deg/sec).

#### Parameters

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
<i>gyroX</i>	angular velocity on the X axis (roll).

4.10.4.24 `static void caerIMU9EventSetGyroY ( caerIMU9Event event, float gyroY )` `[inline],[static]`

Set the Y axis (pitch) angular velocity reading (from gyroscope). This is in  $\text{°/s}$  (deg/sec).

#### Parameters

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
<i>gyroY</i>	angular velocity on the Y axis (pitch).

4.10.4.25 `static void caerIMU9EventSetGyroZ ( caerIMU9Event event, float gyroZ )` `[inline]`, `[static]`

Set the Z axis (yaw) angular velocity reading (from gyroscope). This is in °/s (deg/sec).

#### Parameters

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
<i>gyroZ</i>	angular velocity on the Z axis (yaw).

4.10.4.26 `static void caerIMU9EventSetTemp ( caerIMU9Event event, float temp )` `[inline]`, `[static]`

Set the temperature reading. This is in °C.

#### Parameters

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
<i>temp</i>	temperature in °C.

4.10.4.27 `static void caerIMU9EventSetTimestamp ( caerIMU9Event event, int32_t timestamp )` `[inline]`, `[static]`

Set the 32bit event timestamp, the value has to be in microseconds.

#### Parameters

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
<i>timestamp</i>	a positive 32bit microsecond timestamp.

4.10.4.28 `static void caerIMU9EventValidate ( caerIMU9Event event, caerIMU9EventPacket packet )` `[inline]`, `[static]`

Validate the current event by setting its valid bit to true and increasing the event packet's event count and valid event count. Only works on events that are invalid. DO NOT CALL THIS AFTER HAVING PREVIOUSLY ALREADY INVALIDATED THIS EVENT, the total count will be incorrect.

#### Parameters

<i>event</i>	a valid IMU9Event pointer. Cannot be NULL.
<i>packet</i>	the IMU9EventPacket pointer for the packet containing this event. Cannot be NULL.

4.10.4.29 `PACKED_STRUCT ( struct caer_imu9_event{uint32_t info;int32_t timestamp;float accel_x;float accel_y;float accel_z;float gyro_x;float gyro_y;float gyro_z;float temp;float comp_x;float comp_y;float comp_z;} )`

IMU 9-axes event data structure definition. This contains accelerometer and gyroscope headings, plus temperature, and magnetometer readings. The X, Y and Z axes are referred to the camera plane. X increases to the right, Y going

up and Z towards where the lens is pointing. Rotation for the gyroscope is counter-clockwise along the increasing axis, for all three axes. Floats are in IEEE 754-2008 binary32 format. Signed integers are used for fields that are to be interpreted directly, for compatibility with languages that do not have unsigned integer types, such as Java.

4.10.4.30 **PACKED\_STRUCT** ( struct caer\_imu9\_event\_packet{struct caer\_event\_packet\_header packetHeader;struct caer\_imu9\_event events[ ];} )

IMU 9-axes event packet data structure definition. EventPackets are always made up of the common packet header, followed by 'eventCapacity' events. Everything has to be in one contiguous memory block.

## 4.11 events/packetContainer.h File Reference

```
#include "common.h"
```

### Macros

- #define [CAER\\_EVENT\\_PACKET\\_CONTAINER\\_ITERATOR\\_START](#)(PACKET\_CONTAINER)
- #define [CAER\\_EVENT\\_PACKET\\_CONTAINER\\_ITERATOR\\_END](#) }

### Typedefs

- typedef struct caer\_event\_packet\_container \* [caerEventPacketContainer](#)

### Functions

- [PACKED\\_STRUCT](#) (struct caer\_event\_packet\_container{int64\_t lowestEventTimestamp;int64\_t highestEventTimestamp;int32\_t eventsNumber;int32\_t eventsValidNumber;int32\_t eventPacketsNumber;caerEventPacketHeader eventPackets[ ];})
- [caerEventPacketContainer](#) caerEventPacketContainerAllocate (int32\_t eventPacketsNumber)
- void caerEventPacketContainerFree (caerEventPacketContainer container)
- static int32\_t caerEventPacketContainerGetEventPacketsNumber (caerEventPacketContainer container)
- static void caerEventPacketContainerSetEventPacketsNumber (caerEventPacketContainer container, int32\_t eventPacketsNumber)
- static caerEventPacketHeader caerEventPacketContainerGetEventPacket (caerEventPacketContainer container, int32\_t n)
- static void caerEventPacketContainerSetEventPacket (caerEventPacketContainer container, int32\_t n, caerEventPacketHeader packetHeader)
- static int64\_t caerEventPacketContainerGetLowestEventTimestamp (caerEventPacketContainer container)
- static int64\_t caerEventPacketContainerGetHighestEventTimestamp (caerEventPacketContainer container)
- static int32\_t caerEventPacketContainerGetEventsNumber (caerEventPacketContainer container)
- static int32\_t caerEventPacketContainerGetEventsValidNumber (caerEventPacketContainer container)
- static caerEventPacketHeader caerEventPacketContainerFindEventPacketByType (caerEventPacketContainer container, int16\_t typeId)
- static caerEventPacketContainer caerEventPacketContainerCopyAllEvents (caerEventPacketContainer container)
- static caerEventPacketContainer caerEventPacketContainerCopyValidEvents (caerEventPacketContainer container)



### 4.11.1 Detailed Description

EventPacketContainer format definition and handling functions. An EventPacketContainer is a logical construct that contains packets of events (EventPackets) of different event types, with the aim of keeping related events of differing types, such as DVS and IMU data, together. Such a relation is usually based on time intervals, trying to keep groups of event happening in a certain time-slice together. This time-order is based on the *main* time-stamp of an event, the one whose offset is referenced in the event packet header and that is used by the caerGenericEvent\*() functions. It's guaranteed that all conforming input modules keep to this rule, generating containers that include all events from all types within the given time-slice. The smallest and largest timestamps are tracked at the packet container level as a convenience, to avoid having to examine all packets for this often useful piece of information. All integers are in their native host format, as this is a purely internal, in-memory data structure, never meant for exchange between different systems (and different endianness).

== Packet Containers and Input Modules == The "packeting system" works in this way: events are accumulated by type in a packet, and that packet is part of a packet container, by an input module. The packet container is then sent out for processing when either the configured time limit or the size limit are hit. The time limit is always active, in microseconds, and basically tells you the time-span an event packet covers. This enables regular, constant delivery of packets, that cover a period of time. The size limit is an add-on to prevent packets to grow to immense sizes (like if the time limit is high and there is lots of activity). As soon as a packet hits the number of events in the size limit, it is sent out. The regular time limit is not reset in this case. This size limit can be disabled by setting it to 0. The cAER DVS128/DAVIS/File/Network input modules call these two configuration variables "PacketContainerInterval" and "PacketContainerMaxPacketSize". Too small packet sizes or intervals simply mean more packets, which may negatively affect performance. It's usually a good idea to set the size to something around 4-8K, and the time to a good value based on the application you're building, so if you need ms-reaction-time, you probably want to set it to 1000µs, so that you do get new data every ms. If on the other hand you're looking at a static scene and just want to detect that something is passing by once every while, a higher number like 100ms might also be perfectly appropriate.

### 4.11.2 Macro Definition Documentation

#### 4.11.2.1 #define CAER\_EVENT\_PACKET\_CONTAINER\_ITERATOR\_END {}

Iterator close statement.

#### 4.11.2.2 #define CAER\_EVENT\_PACKET\_CONTAINER\_ITERATOR\_START( PACKET\_CONTAINER )

**Value:**

```
if ((PACKET_CONTAINER) != NULL) { \
    for (int32_t caerEventPacketContainerIteratorCounter = 0; \
        caerEventPacketContainerIteratorCounter < \
        caerEventPacketContainerGetEventPacketsNumber(PACKET_CONTAINER); \
        caerEventPacketContainerIteratorCounter++) { \
        caerEventPacketHeader caerEventPacketContainerIteratorElement = \
        caerEventPacketContainerGetEventPacket(PACKET_CONTAINER, \
        caerEventPacketContainerIteratorCounter); \
        if (caerEventPacketContainerIteratorElement == NULL) { continue; }
```

Iterator over all event packets in an event packet container. Returns the current index in the 'caerEventPacketContainerIteratorCounter' variable of type 'int32\_t' and the current event packet in the 'caerEventPacketContainerIteratorElement' variable of type caerEventPacketHeader. The current packet may be NULL, in which case it is skipped during iteration.

PACKET\_CONTAINER: a valid EventPacketContainer handle. If NULL, no iteration is performed.

### 4.11.3 Typedef Documentation

#### 4.11.3.1 typedef struct caer\_event\_packet\_container\* caerEventPacketContainer

Type for pointer to EventPacketContainer data structure.

### 4.11.4 Function Documentation

#### 4.11.4.1 caerEventPacketContainer caerEventPacketContainerAllocate ( int32\_t eventPacketsNumber )

Allocate a new EventPacketContainer with enough space to store up to the given number of EventPacket references. All packet references will be NULL initially.

##### Parameters

<i>eventPacketsNumber</i>	the maximum number of EventPacket references that can be stored in this container.
---------------------------	--

##### Returns

a valid EventPacketContainer handle or NULL on error.

#### 4.11.4.2 static caerEventPacketContainer caerEventPacketContainerCopyAllEvents ( caerEventPacketContainer container ) [inline],[static]

Make a deep copy of an event packet container and all of its event packets and their current events.

##### Parameters

<i>container</i>	an event packet container to copy.
------------------	------------------------------------

##### Returns

a deep copy of an event packet container, containing all events.

#### 4.11.4.3 static caerEventPacketContainer caerEventPacketContainerCopyValidEvents ( caerEventPacketContainer container ) [inline],[static]

Make a deep copy of an event packet container, with its event packets sized down to only include the currently valid events (eventValid), and discarding everything else.

##### Parameters

<i>container</i>	an event packet container to copy.
------------------	------------------------------------

**Returns**

a deep copy of an event packet container, containing only valid events.

**4.11.4.4** `static caerEventPacketHeader caerEventPacketContainerFindEventPacketByType ( caerEventPacketContainer container, int16_t typeId ) [inline],[static]`

Get the reference for an EventPacket stored in this container with the given event type. This returns the first found event packet with that type ID, or NULL if we get to the end without finding any such event packet.

**Parameters**

<i>container</i>	a valid EventPacketContainer handle. If NULL, returns NULL too.
<i>typeID</i>	the event type to search for.

**Returns**

a reference to an EventPacket with a certain type or NULL if none found.

**4.11.4.5** `void caerEventPacketContainerFree ( caerEventPacketContainer container )`

Free the memory occupied by an EventPacketContainer, as well as freeing all of its contained EventPackets and their memory. If you don't want the contained EventPackets to be freed, make sure that you set their reference to NULL before calling this.

**Parameters**

<i>container</i>	the container to be freed.
------------------	----------------------------

**4.11.4.6** `static caerEventPacketHeader caerEventPacketContainerGetEventPacket ( caerEventPacketContainer container, int32_t n ) [inline],[static]`

Get the reference for the EventPacket stored in this container at the given index.

**Parameters**

<i>container</i>	a valid EventPacketContainer handle. If NULL, returns NULL too.
<i>n</i>	the index of the EventPacket to get.

**Returns**

a reference to an EventPacket or NULL on error.

**4.11.4.7** `static int32_t caerEventPacketContainerGetEventPacketsNumber ( caerEventPacketContainer container ) [inline],[static]`

Get the maximum number of EventPacket references that can be stored in this particular EventPacketContainer.

**Parameters**

<i>container</i>	a valid EventPacketContainer handle. If NULL, zero is returned.
------------------	---

**Returns**

the number of EventPacket references that can be contained.

**4.11.4.8** `static int32_t caerEventPacketContainerGetEventsNumber ( caerEventPacketContainer container )`  
[inline],[static]

Get the number of events contained in this event packet container.

**Parameters**

<i>container</i>	a valid EventPacketContainer handle. If NULL, 0 is returned.
------------------	--

**Returns**

the number of events in this container.

**4.11.4.9** `static int32_t caerEventPacketContainerGetEventsValidNumber ( caerEventPacketContainer container )`  
[inline],[static]

Get the number of valid events contained in this event packet container.

**Parameters**

<i>container</i>	a valid EventPacketContainer handle. If NULL, 0 is returned.
------------------	--

**Returns**

the number of valid events in this container.

**4.11.4.10** `static int64_t caerEventPacketContainerGetHighestEventTimestamp ( caerEventPacketContainer container )`  
[inline],[static]

Get the highest timestamp contained in this event packet container.

**Parameters**

<i>container</i>	a valid EventPacketContainer handle. If NULL, -1 is returned.
------------------	---

## Returns

the highest timestamp (in  $\mu$ s) or -1 if not initialized.

4.11.4.11 `static int64_t caerEventPacketContainerGetLowestEventTimestamp ( caerEventPacketContainer container )`  
`[inline],[static]`

Get the lowest timestamp contained in this event packet container.

## Parameters

<i>container</i>	a valid EventPacketContainer handle. If NULL, -1 is returned.
------------------	---

## Returns

the lowest timestamp (in  $\mu$ s) or -1 if not initialized.

4.11.4.12 `static void caerEventPacketContainerSetEventPacket ( caerEventPacketContainer container, int32_t n, caerEventPacketHeader packetHeader )`  
`[inline],[static]`

Set the reference for the EventPacket stored in this container at the given index.

## Parameters

<i>container</i>	a valid EventPacketContainer handle. If NULL, nothing happens.
<i>n</i>	the index of the EventPacket to set.
<i>packetHeader</i>	a reference to an EventPacket's header. Can be NULL.

4.11.4.13 `static void caerEventPacketContainerSetEventPacketsNumber ( caerEventPacketContainer container, int32_t eventPacketsNumber )`  
`[inline],[static]`

Set the maximum number of EventPacket references that can be stored in this particular EventPacketContainer. This should never be used directly, [caerEventPacketContainerAllocate\(\)](#) sets this for you.

## Parameters

<i>container</i>	a valid EventPacketContainer handle. If NULL, nothing happens.
<i>eventPacketsNumber</i>	the number of EventPacket references that can be contained.

4.11.4.14 `PACKED_STRUCT ( struct caer_event_packet_container{int64_t lowestEventTimestamp;int64_t highestEventTimestamp;int32_t eventsNumber;int32_t eventsValidNumber;int32_t eventPacketsNumber;caerEventPacketHeader eventPackets[ ];}`  
`)`

EventPacketContainer data structure definition. Signed integers are used for compatibility with languages that do not have unsigned ones, such as Java.

## 4.12 events/point1d.h File Reference

```
#include "common.h"
```

### Macros

- `#define CAER_POINT1D_ITERATOR_ALL_START`(POINT1D\_PACKET)
- `#define CAER_POINT1D_ITERATOR_ALL_END` }
- `#define CAER_POINT1D_ITERATOR_VALID_START`(POINT1D\_PACKET)
- `#define CAER_POINT1D_ITERATOR_VALID_END` }
- `#define POINT1D_TYPE_SHIFT` 1
- `#define POINT1D_TYPE_MASK` 0x0000007F
- `#define POINT1D_SCALE_SHIFT` 8
- `#define POINT1D_SCALE_MASK` 0x000000FF

### Typedefs

- `typedef struct caer_point1d_event * caerPoint1DEvent`
- `typedef struct caer_point1d_event_packet * caerPoint1DEventPacket`

### Functions

- `PACKED_STRUCT` (struct caer\_point1d\_event{uint32\_t info;float x;int32\_t timestamp;})
- `PACKED_STRUCT` (struct caer\_point1d\_event\_packet{struct caer\_event\_packet\_header packet←Header;struct caer\_point1d\_event events[;];})
- `caerPoint1DEventPacket caerPoint1DEventPacketAllocate` (int32\_t eventCapacity, int16\_t eventSource, int32\_t tsOverflow)
- `static caerPoint1DEvent caerPoint1DEventPacketGetEvent` (caerPoint1DEventPacket packet, int32\_t n)
- `static int32_t caerPoint1DEventGetTimestamp` (caerPoint1DEvent event)
- `static int64_t caerPoint1DEventGetTimestamp64` (caerPoint1DEvent event, caerPoint1DEventPacket packet)
- `static void caerPoint1DEventSetTimestamp` (caerPoint1DEvent event, int32\_t timestamp)
- `static bool caerPoint1DEventIsValid` (caerPoint1DEvent event)
- `static void caerPoint1DEventValidate` (caerPoint1DEvent event, caerPoint1DEventPacket packet)
- `static void caerPoint1DEventInvalidate` (caerPoint1DEvent event, caerPoint1DEventPacket packet)
- `static uint8_t caerPoint1DEventGetType` (caerPoint1DEvent event)
- `static void caerPoint1DEventSetType` (caerPoint1DEvent event, uint8\_t type)
- `static int8_t caerPoint1DEventGetScale` (caerPoint1DEvent event)
- `static void caerPoint1DEventSetScale` (caerPoint1DEvent event, int8\_t scale)
- `static float caerPoint1DEventGetX` (caerPoint1DEvent event)
- `static void caerPoint1DEventSetX` (caerPoint1DEvent event, float x)

#### 4.12.1 Detailed Description

THIS EVENT DEFINITIONS IS STILL TO BE CONSIDERED EXPERIMENTAL AND IS SUBJECT TO FUTURE CHANGES AND REVISIONS!

Point1D Events format definition and handling functions. This contains one dimensional data points as floats, together with support for distinguishing type and scale.

## 4.12.2 Macro Definition Documentation

### 4.12.2.1 `#define CAER_POINT1D_ITERATOR_ALL_END` }

Iterator close statement.

### 4.12.2.2 `#define CAER_POINT1D_ITERATOR_ALL_START( POINT1D_PACKET )`

**Value:**

```
for (int32_t caerPoint1DIteratorCounter = 0; \
    caerPoint1DIteratorCounter < caerEventPacketHeaderGetEventNumber
    (&(POINT1D_PACKET)->packetHeader); \
    caerPoint1DIteratorCounter++) { \
    caerPoint1DEvent caerPoint1DIteratorElement =
    caerPoint1DEventPacketGetEvent(POINT1D_PACKET, caerPoint1DIteratorCounter);
```

Iterator over all Point1D events in a packet. Returns the current index in the 'caerPoint1DIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerPoint1DIteratorElement' variable of type caerPoint1DEvent.

POINT1D\_PACKET: a valid Point1DEventPacket pointer. Cannot be NULL.

### 4.12.2.3 `#define CAER_POINT1D_ITERATOR_VALID_END` }

Iterator close statement.

### 4.12.2.4 `#define CAER_POINT1D_ITERATOR_VALID_START( POINT1D_PACKET )`

**Value:**

```
for (int32_t caerPoint1DIteratorCounter = 0; \
    caerPoint1DIteratorCounter < caerEventPacketHeaderGetEventNumber
    (&(POINT1D_PACKET)->packetHeader); \
    caerPoint1DIteratorCounter++) { \
    caerPoint1DEvent caerPoint1DIteratorElement =
    caerPoint1DEventPacketGetEvent(POINT1D_PACKET, caerPoint1DIteratorCounter); \
    if (!caerPoint1DEventIsValid(caerPoint1DIteratorElement)) { continue; }
```

Iterator over only the valid Point1D events in a packet. Returns the current index in the 'caerPoint1DIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerPoint1DIteratorElement' variable of type caerPoint1DEvent.

POINT1D\_PACKET: a valid Point1DEventPacket pointer. Cannot be NULL.

### 4.12.2.5 `#define POINT1D_SCALE_MASK 0x000000FF`

Shift and mask values for type and scale information associated with a Point1D event. Up to 128 types are supported. The scale is given as orders of magnitude, from  $10^{-128}$  to  $10^{127}$ . Bit 0 is the valid mark, see 'common.h' for more details.

#### 4.12.2.6 `#define POINT1D_SCALE_SHIFT 8`

Shift and mask values for type and scale information associated with a Point1D event. Up to 128 types are supported. The scale is given as orders of magnitude, from  $10^{-128}$  to  $10^{127}$ . Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.12.2.7 `#define POINT1D_TYPE_MASK 0x0000007F`

Shift and mask values for type and scale information associated with a Point1D event. Up to 128 types are supported. The scale is given as orders of magnitude, from  $10^{-128}$  to  $10^{127}$ . Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.12.2.8 `#define POINT1D_TYPE_SHIFT 1`

Shift and mask values for type and scale information associated with a Point1D event. Up to 128 types are supported. The scale is given as orders of magnitude, from  $10^{-128}$  to  $10^{127}$ . Bit 0 is the valid mark, see '[common.h](#)' for more details.

### 4.12.3 Typedef Documentation

#### 4.12.3.1 `typedef struct caer_point1d_event* caerPoint1DEvent`

Type for pointer to Point1D event data structure.

#### 4.12.3.2 `typedef struct caer_point1d_event_packet* caerPoint1DEventPacket`

Type for pointer to Point1D event packet data structure.

### 4.12.4 Function Documentation

#### 4.12.4.1 `static int8_t caerPoint1DEventGetScale ( caerPoint1DEvent event ) [inline],[static]`

Get the measurement scale. This allows order of magnitude shifts on the measured value to be applied automatically, such as having measurements of type Distance (meters) and storing the values as centimeters ( $10^{-2}$ ) for higher precision, but keeping that information around to allow easy changes of unit.

##### Parameters

<i>event</i>	a valid Point1DEvent pointer. Cannot be NULL.
--------------	---

##### Returns

the Point1D measurement scale.



#### 4.12.4.2 `static int32_t caerPoint1DEventGetTimestamp ( caerPoint1DEvent event ) [inline],[static]`

Get the 32bit event timestamp, in microseconds. Be aware that this wraps around! You can either ignore this fact, or handle the special 'TIMESTAMP\_WRAP' event that is generated when this happens, or use the 64bit timestamp which never wraps around. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

##### Parameters

<i>event</i>	a valid Point1DEvent pointer. Cannot be NULL.
--------------	---

##### Returns

this event's 32bit microsecond timestamp.

#### 4.12.4.3 `static int64_t caerPoint1DEventGetTimestamp64 ( caerPoint1DEvent event, caerPoint1DEventPacket packet ) [inline],[static]`

Get the 64bit event timestamp, in microseconds. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

##### Parameters

<i>event</i>	a valid Point1DEvent pointer. Cannot be NULL.
<i>packet</i>	the Point1DEventPacket pointer for the packet containing this event. Cannot be NULL.

##### Returns

this event's 64bit microsecond timestamp.

#### 4.12.4.4 `static uint8_t caerPoint1DEventGetType ( caerPoint1DEvent event ) [inline],[static]`

Get the measurement event type. This is useful to distinguish between different measurements, for example distance or weight.

##### Parameters

<i>event</i>	a valid Point1DEvent pointer. Cannot be NULL.
--------------	---

##### Returns

the Point1D measurement type.

#### 4.12.4.5 `static float caerPoint1DEventGetX ( caerPoint1DEvent event ) [inline],[static]`

Get the X axis measurement.

## Parameters

<i>event</i>	a valid Point1DEvent pointer. Cannot be NULL.
--------------	---

## Returns

X axis measurement.

**4.12.4.6** `static void caerPoint1DEventInvalidate ( caerPoint1DEvent event, caerPoint1DEventPacket packet )`  
`[inline], [static]`

Invalidate the current event by setting its valid bit to false and decreasing the number of valid events held in the packet. Only works with events that are already valid!

## Parameters

<i>event</i>	a valid Point1DEvent pointer. Cannot be NULL.
<i>packet</i>	the Point1DEventPacket pointer for the packet containing this event. Cannot be NULL.

**4.12.4.7** `static bool caerPoint1DEventIsValid ( caerPoint1DEvent event )` `[inline], [static]`

Check if this Point1D event is valid.

## Parameters

<i>event</i>	a valid Point1DEvent pointer. Cannot be NULL.
--------------	---

## Returns

true if valid, false if not.

**4.12.4.8** `caerPoint1DEventPacket caerPoint1DEventPacketAllocate ( int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow )`

Allocate a new Point1D events packet. Use free() to reclaim this memory.

## Parameters

<i>eventCapacity</i>	the maximum number of events this packet will hold.
<i>eventSource</i>	the unique ID representing the source/generator of this packet.
<i>tsOverflow</i>	the current timestamp overflow counter value for this packet.

## Returns

a valid Point1DEventPacket handle or NULL on error.

4.12.4.9 **static caerPoint1DEvent caerPoint1DEventPacketGetEvent ( caerPoint1DEventPacket *packet*, int32\_t *n* )**  
 [inline],[static]

Get the Point1D event at the given index from the event packet.

#### Parameters

<i>packet</i>	a valid Point1DEventPacket pointer. Cannot be NULL.
<i>n</i>	the index of the returned event. Must be within [0,eventCapacity[ bounds.

#### Returns

the requested Point1D event. NULL on error.

4.12.4.10 **static void caerPoint1DEventSetScale ( caerPoint1DEvent *event*, int8\_t *scale* )** [inline],[static]

Set the measurement scale. This allows order of magnitude shifts on the measured value to be applied automatically, such as having measurements of type Distance (meters) and storing the values as centimeters ( $10^{-2}$ ) for higher precision, but keeping that information around to allow easy changes of unit.

#### Parameters

<i>event</i>	a valid Point1DEvent pointer. Cannot be NULL.
<i>scale</i>	the Point1D measurement scale.

4.12.4.11 **static void caerPoint1DEventSetTimestamp ( caerPoint1DEvent *event*, int32\_t *timestamp* )** [inline],[static]

Set the 32bit event timestamp, the value has to be in microseconds.

#### Parameters

<i>event</i>	a valid Point1DEvent pointer. Cannot be NULL.
<i>timestamp</i>	a positive 32bit microsecond timestamp.

4.12.4.12 **static void caerPoint1DEventSetType ( caerPoint1DEvent *event*, uint8\_t *type* )** [inline],[static]

Set the measurement event type. This is useful to distinguish between different measurements, for example distance or weight.

#### Parameters

<i>event</i>	a valid Point1DEvent pointer. Cannot be NULL.
<i>type</i>	the Point1D measurement type.

**4.12.4.13** `static void caerPoint1DEventSetX ( caerPoint1DEvent event, float x )` `[inline],[static]`

Set the X axis measurement.

#### Parameters

<i>event</i>	a valid Point1DEvent pointer. Cannot be NULL.
<i>x</i>	X axis measurement.

**4.12.4.14** `static void caerPoint1DEventValidate ( caerPoint1DEvent event, caerPoint1DEventPacket packet )`  
`[inline],[static]`

Validate the current event by setting its valid bit to true and increasing the event packet's event count and valid event count. Only works on events that are invalid. DO NOT CALL THIS AFTER HAVING PREVIOUSLY ALREADY INVALIDATED THIS EVENT, the total count will be incorrect.

#### Parameters

<i>event</i>	a valid Point1DEvent pointer. Cannot be NULL.
<i>packet</i>	the Point1DEventPacket pointer for the packet containing this event. Cannot be NULL.

**4.12.4.15** `PACKED_STRUCT ( struct caer_point1d_event{uint32_t info;float x;int32_t timestamp;} )`

Point1D event data structure definition. This contains information about the measurement, such as a type and a scale field, together with the usual validity mark. The one measurement (x) is stored as a float. Floats are in IEEE 754-2008 binary32 format. Signed integers are used for fields that are to be interpreted directly, for compatibility with languages that do not have unsigned integer types, such as Java.

**4.12.4.16** `PACKED_STRUCT ( struct caer_point1d_event_packet{struct caer_event_packet_header packetHeader;struct caer_point1d_event events[ ];} )`

Point1D event packet data structure definition. EventPackets are always made up of the common packet header, followed by 'eventCapacity' events. Everything has to be in one contiguous memory block.

## 4.13 events/point2d.h File Reference

```
#include "common.h"
```

### Macros

- `#define CAER_POINT2D_ITERATOR_ALL_START(POINT2D_PACKET)`
- `#define CAER_POINT2D_ITERATOR_ALL_END }`
- `#define CAER_POINT2D_ITERATOR_VALID_START(POINT2D_PACKET)`
- `#define CAER_POINT2D_ITERATOR_VALID_END }`
  
- `#define POINT2D_TYPE_SHIFT 1`
- `#define POINT2D_TYPE_MASK 0x0000007F`
- `#define POINT2D_SCALE_SHIFT 8`
- `#define POINT2D_SCALE_MASK 0x000000FF`

## Typedefs

- typedef struct caer\_point2d\_event \* caerPoint2DEvent
- typedef struct caer\_point2d\_event\_packet \* caerPoint2DEventPacket

## Functions

- **PACKED\_STRUCT** (struct caer\_point2d\_event{uint32\_t info;float x;float y;int32\_t timestamp;})
- **PACKED\_STRUCT** (struct caer\_point2d\_event\_packet{struct caer\_event\_packet\_header packet←Header;struct caer\_point2d\_event events[];})
- caerPoint2DEventPacket caerPoint2DEventPacketAllocate (int32\_t eventCapacity, int16\_t eventSource, int32\_t tsOverflow)
- static caerPoint2DEvent caerPoint2DEventPacketGetEvent (caerPoint2DEventPacket packet, int32\_t n)
- static int32\_t caerPoint2DEventGetTimestamp (caerPoint2DEvent event)
- static int64\_t caerPoint2DEventGetTimestamp64 (caerPoint2DEvent event, caerPoint2DEventPacket packet)
- static void caerPoint2DEventSetTimestamp (caerPoint2DEvent event, int32\_t timestamp)
- static bool caerPoint2DEventIsValid (caerPoint2DEvent event)
- static void caerPoint2DEventValidate (caerPoint2DEvent event, caerPoint2DEventPacket packet)
- static void caerPoint2DEventInvalidate (caerPoint2DEvent event, caerPoint2DEventPacket packet)
- static uint8\_t caerPoint2DEventGetType (caerPoint2DEvent event)
- static void caerPoint2DEventSetType (caerPoint2DEvent event, uint8\_t type)
- static int8\_t caerPoint2DEventGetScale (caerPoint2DEvent event)
- static void caerPoint2DEventSetScale (caerPoint2DEvent event, int8\_t scale)
- static float caerPoint2DEventGetX (caerPoint2DEvent event)
- static void caerPoint2DEventSetX (caerPoint2DEvent event, float x)
- static float caerPoint2DEventGetY (caerPoint2DEvent event)
- static void caerPoint2DEventSetY (caerPoint2DEvent event, float y)

### 4.13.1 Detailed Description

THIS EVENT DEFINITIONS IS STILL TO BE CONSIDERED EXPERIMENTAL AND IS SUBJECT TO FUTURE CHANGES AND REVISIONS!

Point2D Events format definition and handling functions. This contains two dimensional data points as floats, together with support for distinguishing type and scale.

### 4.13.2 Macro Definition Documentation

#### 4.13.2.1 #define CAER\_POINT2D\_ITERATOR\_ALL\_END }

Iterator close statement.

#### 4.13.2.2 #define CAER\_POINT2D\_ITERATOR\_ALL\_START( POINT2D\_PACKET )

**Value:**

```
for (int32_t caerPoint2DIteratorCounter = 0; \
    caerPoint2DIteratorCounter < caerEventPacketHeaderGetEventNumber
    (&(POINT2D_PACKET)->packetHeader); \
    caerPoint2DIteratorCounter++) { \
    caerPoint2DEvent caerPoint2DIteratorElement =
    caerPoint2DEventPacketGetEvent (POINT2D_PACKET, caerPoint2DIteratorCounter);
```

Iterator over all Point2D events in a packet. Returns the current index in the 'caerPoint2DIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerPoint2DIteratorElement' variable of type caerPoint2DEvent.

POINT2D\_PACKET: a valid Point2DEventPacket pointer. Cannot be NULL.

#### 4.13.2.3 `#define CAER_POINT2D_ITERATOR_VALID_END }`

Iterator close statement.

#### 4.13.2.4 `#define CAER_POINT2D_ITERATOR_VALID_START( POINT2D_PACKET )`

**Value:**

```
for (int32_t caerPoint2DIteratorCounter = 0; \
     caerPoint2DIteratorCounter < caerEventPacketHeaderGetEventNumber
     (&(POINT2D_PACKET)->packetHeader); \
     caerPoint2DIteratorCounter++) { \
    caerPoint2DEvent caerPoint2DIteratorElement =
    caerPoint2DEventPacketGetEvent(POINT2D_PACKET, caerPoint2DIteratorCounter); \
    if (!caerPoint2DEventIsValid(caerPoint2DIteratorElement)) { continue; }
```

Iterator over only the valid Point2D events in a packet. Returns the current index in the 'caerPoint2DIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerPoint2DIteratorElement' variable of type caerPoint2DEvent.

POINT2D\_PACKET: a valid Point2DEventPacket pointer. Cannot be NULL.

#### 4.13.2.5 `#define POINT2D_SCALE_MASK 0x000000FF`

Shift and mask values for type and scale information associated with a Point2D event. Up to 128 types are supported. The scale is given as orders of magnitude, from  $10^{-128}$  to  $10^{127}$ . Bit 0 is the valid mark, see 'common.h' for more details.

#### 4.13.2.6 `#define POINT2D_SCALE_SHIFT 8`

Shift and mask values for type and scale information associated with a Point2D event. Up to 128 types are supported. The scale is given as orders of magnitude, from  $10^{-128}$  to  $10^{127}$ . Bit 0 is the valid mark, see 'common.h' for more details.

#### 4.13.2.7 `#define POINT2D_TYPE_MASK 0x0000007F`

Shift and mask values for type and scale information associated with a Point2D event. Up to 128 types are supported. The scale is given as orders of magnitude, from  $10^{-128}$  to  $10^{127}$ . Bit 0 is the valid mark, see 'common.h' for more details.

#### 4.13.2.8 `#define POINT2D_TYPE_SHIFT 1`

Shift and mask values for type and scale information associated with a Point2D event. Up to 128 types are supported. The scale is given as orders of magnitude, from  $10^{-128}$  to  $10^{127}$ . Bit 0 is the valid mark, see 'common.h' for more details.

### 4.13.3 Typedef Documentation

#### 4.13.3.1 `typedef struct caer_point2d_event* caerPoint2DEvent`

Type for pointer to Point2D event data structure.

#### 4.13.3.2 typedef struct caer\_point2d\_event\_packet\* caerPoint2DEventPacket

Type for pointer to Point2D event packet data structure.

### 4.13.4 Function Documentation

#### 4.13.4.1 static int8\_t caerPoint2DEventGetScale ( caerPoint2DEvent *event* ) [inline],[static]

Get the measurement scale. This allows order of magnitude shifts on the measured value to be applied automatically, such as having measurements of type Distance (meters) and storing the values as centimeters ( $10^{-2}$ ) for higher precision, but keeping that information around to allow easy changes of unit.

##### Parameters

<i>event</i>	a valid Point2DEvent pointer. Cannot be NULL.
--------------	---

##### Returns

the Point2D measurement scale.

#### 4.13.4.2 static int32\_t caerPoint2DEventGetTimestamp ( caerPoint2DEvent *event* ) [inline],[static]

Get the 32bit event timestamp, in microseconds. Be aware that this wraps around! You can either ignore this fact, or handle the special 'TIMESTAMP\_WRAP' event that is generated when this happens, or use the 64bit timestamp which never wraps around. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

##### Parameters

<i>event</i>	a valid Point2DEvent pointer. Cannot be NULL.
--------------	---

##### Returns

this event's 32bit microsecond timestamp.

#### 4.13.4.3 static int64\_t caerPoint2DEventGetTimestamp64 ( caerPoint2DEvent *event*, caerPoint2DEventPacket *packet* ) [inline],[static]

Get the 64bit event timestamp, in microseconds. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

##### Parameters

<i>event</i>	a valid Point2DEvent pointer. Cannot be NULL.
<i>packet</i>	the Point2DEventPacket pointer for the packet containing this event. Cannot be NULL.

**Returns**

this event's 64bit microsecond timestamp.

#### 4.13.4.4 `static uint8_t caerPoint2DEventGetType ( caerPoint2DEvent event )` `[inline],[static]`

Get the measurement event type. This is useful to distinguish between different measurements, for example distance or weight.

**Parameters**

<i>event</i>	a valid Point2DEvent pointer. Cannot be NULL.
--------------	---

**Returns**

the Point2D measurement type.

#### 4.13.4.5 `static float caerPoint2DEventGetX ( caerPoint2DEvent event )` `[inline],[static]`

Get the X axis measurement.

**Parameters**

<i>event</i>	a valid Point2DEvent pointer. Cannot be NULL.
--------------	---

**Returns**

X axis measurement.

#### 4.13.4.6 `static float caerPoint2DEventGetY ( caerPoint2DEvent event )` `[inline],[static]`

Get the Y axis measurement.

**Parameters**

<i>event</i>	a valid Point2DEvent pointer. Cannot be NULL.
--------------	---

**Returns**

Y axis measurement.

#### 4.13.4.7 `static void caerPoint2DEventInvalidate ( caerPoint2DEvent event, caerPoint2DEventPacket packet )` `[inline],[static]`

Invalidate the current event by setting its valid bit to false and decreasing the number of valid events held in the packet. Only works with events that are already valid!



## Parameters

<i>event</i>	a valid Point2DEvent pointer. Cannot be NULL.
<i>packet</i>	the Point2DEventPacket pointer for the packet containing this event. Cannot be NULL.

4.13.4.8 `static bool caerPoint2DEventsValid ( caerPoint2DEvent event )` `[inline],[static]`

Check if this Point2D event is valid.

## Parameters

<i>event</i>	a valid Point2DEvent pointer. Cannot be NULL.
--------------	---

## Returns

true if valid, false if not.

4.13.4.9 `caerPoint2DEventPacket caerPoint2DEventPacketAllocate ( int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow )`

Allocate a new Point2D events packet. Use free() to reclaim this memory.

## Parameters

<i>eventCapacity</i>	the maximum number of events this packet will hold.
<i>eventSource</i>	the unique ID representing the source/generator of this packet.
<i>tsOverflow</i>	the current timestamp overflow counter value for this packet.

## Returns

a valid Point2DEventPacket handle or NULL on error.

4.13.4.10 `static caerPoint2DEvent caerPoint2DEventPacketGetEvent ( caerPoint2DEventPacket packet, int32_t n )` `[inline],[static]`

Get the Point2D event at the given index from the event packet.

## Parameters

<i>packet</i>	a valid Point2DEventPacket pointer. Cannot be NULL.
<i>n</i>	the index of the returned event. Must be within [0,eventCapacity[ bounds.

## Returns

the requested Point2D event. NULL on error.

**4.13.4.11** `static void caerPoint2DEventSetScale ( caerPoint2DEvent event, int8_t scale )` `[inline],[static]`

Set the measurement scale. This allows order of magnitude shifts on the measured value to be applied automatically, such as having measurements of type Distance (meters) and storing the values as centimeters ( $10^{-2}$ ) for higher precision, but keeping that information around to allow easy changes of unit.

#### Parameters

<i>event</i>	a valid Point2DEvent pointer. Cannot be NULL.
<i>scale</i>	the Point2D measurement scale.

**4.13.4.12** `static void caerPoint2DEventSetTimestamp ( caerPoint2DEvent event, int32_t timestamp )` `[inline],[static]`

Set the 32bit event timestamp, the value has to be in microseconds.

#### Parameters

<i>event</i>	a valid Point2DEvent pointer. Cannot be NULL.
<i>timestamp</i>	a positive 32bit microsecond timestamp.

**4.13.4.13** `static void caerPoint2DEventSetType ( caerPoint2DEvent event, uint8_t type )` `[inline],[static]`

Set the measurement event type. This is useful to distinguish between different measurements, for example distance or weight.

#### Parameters

<i>event</i>	a valid Point2DEvent pointer. Cannot be NULL.
<i>type</i>	the Point2D measurement type.

**4.13.4.14** `static void caerPoint2DEventSetX ( caerPoint2DEvent event, float x )` `[inline],[static]`

Set the X axis measurement.

#### Parameters

<i>event</i>	a valid Point2DEvent pointer. Cannot be NULL.
<i>x</i>	X axis measurement.

**4.13.4.15** `static void caerPoint2DEventSetY ( caerPoint2DEvent event, float y )` `[inline],[static]`

Set the Y axis measurement.

## Parameters

<i>event</i>	a valid Point2DEvent pointer. Cannot be NULL.
<i>y</i>	Y axis measurement.

4.13.4.16 `static void caerPoint2DEventValidate ( caerPoint2DEvent event, caerPoint2DEventPacket packet )`  
`[inline],[static]`

Validate the current event by setting its valid bit to true and increasing the event packet's event count and valid event count. Only works on events that are invalid. DO NOT CALL THIS AFTER HAVING PREVIOUSLY ALREADY INVALIDATED THIS EVENT, the total count will be incorrect.

## Parameters

<i>event</i>	a valid Point2DEvent pointer. Cannot be NULL.
<i>packet</i>	the Point2DEventPacket pointer for the packet containing this event. Cannot be NULL.

4.13.4.17 `PACKED_STRUCT ( struct caer_point2d_event{uint32_t info;float x;float y;int32_t timestamp;} )`

Point2D event data structure definition. This contains information about the measurement, such as a type and a scale field, together with the usual validity mark. The two measurements (x, y) are stored as floats. Floats are in IEEE 754-2008 binary32 format. Signed integers are used for fields that are to be interpreted directly, for compatibility with languages that do not have unsigned integer types, such as Java.

4.13.4.18 `PACKED_STRUCT ( struct caer_point2d_event_packet{struct caer_event_packet_header packetHeader;struct caer_point2d_event events[ ];} )`

Point2D event packet data structure definition. EventPackets are always made up of the common packet header, followed by 'eventCapacity' events. Everything has to be in one contiguous memory block.

## 4.14 events/point3d.h File Reference

```
#include "common.h"
```

### Macros

- `#define CAER_POINT3D_ITERATOR_ALL_START(PPOINT3D_PACKET)`
- `#define CAER_POINT3D_ITERATOR_ALL_END }`
- `#define CAER_POINT3D_ITERATOR_VALID_START(PPOINT3D_PACKET)`
- `#define CAER_POINT3D_ITERATOR_VALID_END }`
  
- `#define POINT3D_TYPE_SHIFT 1`
- `#define POINT3D_TYPE_MASK 0x0000007F`
- `#define POINT3D_SCALE_SHIFT 8`
- `#define POINT3D_SCALE_MASK 0x000000FF`

## Typedefs

- typedef struct caer\_point3d\_event \* caerPoint3DEvent
- typedef struct caer\_point3d\_event\_packet \* caerPoint3DEventPacket

## Functions

- **PACKED\_STRUCT** (struct caer\_point3d\_event{uint32\_t info;float x;float y;float z;int32\_t timestamp;})
- **PACKED\_STRUCT** (struct caer\_point3d\_event\_packet{struct caer\_event\_packet\_header packet↵ Header;struct caer\_point3d\_event events[.];})
- caerPoint3DEventPacket caerPoint3DEventPacketAllocate (int32\_t eventCapacity, int16\_t eventSource, int32\_t tsOverflow)
- static caerPoint3DEvent caerPoint3DEventPacketGetEvent (caerPoint3DEventPacket packet, int32\_t n)
- static int32\_t caerPoint3DEventGetTimestamp (caerPoint3DEvent event)
- static int64\_t caerPoint3DEventGetTimestamp64 (caerPoint3DEvent event, caerPoint3DEventPacket packet)
- static void caerPoint3DEventSetTimestamp (caerPoint3DEvent event, int32\_t timestamp)
- static bool caerPoint3DEventIsValid (caerPoint3DEvent event)
- static void caerPoint3DEventValidate (caerPoint3DEvent event, caerPoint3DEventPacket packet)
- static void caerPoint3DEventInvalidate (caerPoint3DEvent event, caerPoint3DEventPacket packet)
- static uint8\_t caerPoint3DEventGetType (caerPoint3DEvent event)
- static void caerPoint3DEventSetType (caerPoint3DEvent event, uint8\_t type)
- static int8\_t caerPoint3DEventGetScale (caerPoint3DEvent event)
- static void caerPoint3DEventSetScale (caerPoint3DEvent event, int8\_t scale)
- static float caerPoint3DEventGetX (caerPoint3DEvent event)
- static void caerPoint3DEventSetX (caerPoint3DEvent event, float x)
- static float caerPoint3DEventGetY (caerPoint3DEvent event)
- static void caerPoint3DEventSetY (caerPoint3DEvent event, float y)
- static float caerPoint3DEventGetZ (caerPoint3DEvent event)
- static void caerPoint3DEventSetZ (caerPoint3DEvent event, float z)

### 4.14.1 Detailed Description

THIS EVENT DEFINITIONS IS STILL TO BE CONSIDERED EXPERIMENTAL AND IS SUBJECT TO FUTURE CHANGES AND REVISIONS!

Point3D Events format definition and handling functions. This contains three dimensional data points as floats, together with support for distinguishing type and scale.

### 4.14.2 Macro Definition Documentation

#### 4.14.2.1 #define CAER\_POINT3D\_ITERATOR\_ALL\_END }

Iterator close statement.

4.14.2.2 `#define CAER_POINT3D_ITERATOR_ALL_START( POINT3D_PACKET )`**Value:**

```
for (int32_t caerPoint3DIteratorCounter = 0; \
     caerPoint3DIteratorCounter < caerEventPacketHeaderGetEventNumber
    (&(POINT3D_PACKET)->packetHeader); \
     caerPoint3DIteratorCounter++) { \
    caerPoint3DEvent caerPoint3DIteratorElement =
    caerPoint3DEventPacketGetEvent(POINT3D_PACKET, caerPoint3DIteratorCounter);
```

Iterator over all Point3D events in a packet. Returns the current index in the 'caerPoint3DIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerPoint3DIteratorElement' variable of type caerPoint3DEvent.

POINT3D\_PACKET: a valid Point3DEventPacket pointer. Cannot be NULL.

4.14.2.3 `#define CAER_POINT3D_ITERATOR_VALID_END }`

Iterator close statement.

4.14.2.4 `#define CAER_POINT3D_ITERATOR_VALID_START( POINT3D_PACKET )`**Value:**

```
for (int32_t caerPoint3DIteratorCounter = 0; \
     caerPoint3DIteratorCounter < caerEventPacketHeaderGetEventNumber
    (&(POINT3D_PACKET)->packetHeader); \
     caerPoint3DIteratorCounter++) { \
    caerPoint3DEvent caerPoint3DIteratorElement =
    caerPoint3DEventPacketGetEvent(POINT3D_PACKET, caerPoint3DIteratorCounter); \
    if (!caerPoint3DEventIsValid(caerPoint3DIteratorElement)) { continue; }
```

Iterator over only the valid Point3D events in a packet. Returns the current index in the 'caerPoint3DIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerPoint3DIteratorElement' variable of type caerPoint3DEvent.

POINT3D\_PACKET: a valid Point3DEventPacket pointer. Cannot be NULL.

4.14.2.5 `#define POINT3D_SCALE_MASK 0x000000FF`

Shift and mask values for type and scale information associated with a Point3D event. Up to 128 types are supported. The scale is given as orders of magnitude, from  $10^{-128}$  to  $10^{127}$ . Bit 0 is the valid mark, see 'common.h' for more details.

4.14.2.6 `#define POINT3D_SCALE_SHIFT 8`

Shift and mask values for type and scale information associated with a Point3D event. Up to 128 types are supported. The scale is given as orders of magnitude, from  $10^{-128}$  to  $10^{127}$ . Bit 0 is the valid mark, see 'common.h' for more details.

#### 4.14.2.7 `#define POINT3D_TYPE_MASK 0x0000007F`

Shift and mask values for type and scale information associated with a Point3D event. Up to 128 types are supported. The scale is given as orders of magnitude, from  $10^{-128}$  to  $10^{127}$ . Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.14.2.8 `#define POINT3D_TYPE_SHIFT 1`

Shift and mask values for type and scale information associated with a Point3D event. Up to 128 types are supported. The scale is given as orders of magnitude, from  $10^{-128}$  to  $10^{127}$ . Bit 0 is the valid mark, see '[common.h](#)' for more details.

### 4.14.3 Typedef Documentation

#### 4.14.3.1 `typedef struct caer_point3d_event* caerPoint3DEvent`

Type for pointer to Point3D event data structure.

#### 4.14.3.2 `typedef struct caer_point3d_event_packet* caerPoint3DEventPacket`

Type for pointer to Point3D event packet data structure.

### 4.14.4 Function Documentation

#### 4.14.4.1 `static int8_t caerPoint3DEventGetScale ( caerPoint3DEvent event ) [inline],[static]`

Get the measurement scale. This allows order of magnitude shifts on the measured value to be applied automatically, such as having measurements of type Distance (meters) and storing the values as centimeters ( $10^{-2}$ ) for higher precision, but keeping that information around to allow easy changes of unit.

##### Parameters

<i>event</i>	a valid Point3DEvent pointer. Cannot be NULL.
--------------	---

##### Returns

the Point3D measurement scale.

#### 4.14.4.2 `static int32_t caerPoint3DEventGetTimestamp ( caerPoint3DEvent event ) [inline],[static]`

Get the 32bit event timestamp, in microseconds. Be aware that this wraps around! You can either ignore this fact, or handle the special 'TIMESTAMP\_WRAP' event that is generated when this happens, or use the 64bit timestamp which never wraps around. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

## Parameters

<i>event</i>	a valid Point3DEvent pointer. Cannot be NULL.
--------------	---

## Returns

this event's 32bit microsecond timestamp.

**4.14.4.3** `static int64_t caerPoint3DEventGetTimestamp64 ( caerPoint3DEvent event, caerPoint3DEventPacket packet )`  
[inline],[static]

Get the 64bit event timestamp, in microseconds. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

## Parameters

<i>event</i>	a valid Point3DEvent pointer. Cannot be NULL.
<i>packet</i>	the Point3DEventPacket pointer for the packet containing this event. Cannot be NULL.

## Returns

this event's 64bit microsecond timestamp.

**4.14.4.4** `static uint8_t caerPoint3DEventGetType ( caerPoint3DEvent event )` [inline],[static]

Get the measurement event type. This is useful to distinguish between different measurements, for example distance or weight.

## Parameters

<i>event</i>	a valid Point3DEvent pointer. Cannot be NULL.
--------------	---

## Returns

the Point3D measurement type.

**4.14.4.5** `static float caerPoint3DEventGetX ( caerPoint3DEvent event )` [inline],[static]

Get the X axis measurement.

## Parameters

<i>event</i>	a valid Point3DEvent pointer. Cannot be NULL.
--------------	---

**Returns**

X axis measurement.

**4.14.4.6** `static float caerPoint3DEventGetY ( caerPoint3DEvent event )` `[inline],[static]`

Get the Y axis measurement.

**Parameters**

<i>event</i>	a valid Point3DEvent pointer. Cannot be NULL.
--------------	---

**Returns**

Y axis measurement.

**4.14.4.7** `static float caerPoint3DEventGetZ ( caerPoint3DEvent event )` `[inline],[static]`

Get the Z axis measurement.

**Parameters**

<i>event</i>	a valid Point3DEvent pointer. Cannot be NULL.
--------------	---

**Returns**

Z axis measurement.

**4.14.4.8** `static void caerPoint3DEventInvalidate ( caerPoint3DEvent event, caerPoint3DEventPacket packet )`  
`[inline],[static]`

Invalidate the current event by setting its valid bit to false and decreasing the number of valid events held in the packet. Only works with events that are already valid!

**Parameters**

<i>event</i>	a valid Point3DEvent pointer. Cannot be NULL.
<i>packet</i>	the Point3DEventPacket pointer for the packet containing this event. Cannot be NULL.

**4.14.4.9** `static bool caerPoint3DEventIsValid ( caerPoint3DEvent event )` `[inline],[static]`

Check if this Point3D event is valid.



## Parameters

<i>event</i>	a valid Point3DEvent pointer. Cannot be NULL.
--------------	---

## Returns

true if valid, false if not.

#### 4.14.4.10 **caerPoint3DEventPacket** caerPoint3DEventPacketAllocate ( *int32\_t eventCapacity*, *int16\_t eventSource*, *int32\_t tsOverflow* )

Allocate a new Point3D events packet. Use free() to reclaim this memory.

## Parameters

<i>eventCapacity</i>	the maximum number of events this packet will hold.
<i>eventSource</i>	the unique ID representing the source/generator of this packet.
<i>tsOverflow</i>	the current timestamp overflow counter value for this packet.

## Returns

a valid Point3DEventPacket handle or NULL on error.

#### 4.14.4.11 **static caerPoint3DEvent** caerPoint3DEventPacketGetEvent ( *caerPoint3DEventPacket packet*, *int32\_t n* ) [inline], [static]

Get the Point3D event at the given index from the event packet.

## Parameters

<i>packet</i>	a valid Point3DEventPacket pointer. Cannot be NULL.
<i>n</i>	the index of the returned event. Must be within [0,eventCapacity[ bounds.

## Returns

the requested Point3D event. NULL on error.

#### 4.14.4.12 **static void** caerPoint3DEventSetScale ( *caerPoint3DEvent event*, *int8\_t scale* ) [inline], [static]

Set the measurement scale. This allows order of magnitude shifts on the measured value to be applied automatically, such as having measurements of type Distance (meters) and storing the values as centimeters ( $10^{-2}$ ) for higher precision, but keeping that information around to allow easy changes of unit.

## Parameters

<i>event</i>	a valid Point3DEvent pointer. Cannot be NULL.
<i>scale</i>	the Point3D measurement scale.

4.14.4.13 `static void caerPoint3DEventSetTimestamp ( caerPoint3DEvent event, int32_t timestamp ) [inline], [static]`

Set the 32bit event timestamp, the value has to be in microseconds.

#### Parameters

<i>event</i>	a valid Point3DEvent pointer. Cannot be NULL.
<i>timestamp</i>	a positive 32bit microsecond timestamp.

4.14.4.14 `static void caerPoint3DEventSetType ( caerPoint3DEvent event, uint8_t type ) [inline], [static]`

Set the measurement event type. This is useful to distinguish between different measurements, for example distance or weight.

#### Parameters

<i>event</i>	a valid Point3DEvent pointer. Cannot be NULL.
<i>type</i>	the Point3D measurement type.

4.14.4.15 `static void caerPoint3DEventSetX ( caerPoint3DEvent event, float x ) [inline], [static]`

Set the X axis measurement.

#### Parameters

<i>event</i>	a valid Point3DEvent pointer. Cannot be NULL.
<i>x</i>	X axis measurement.

4.14.4.16 `static void caerPoint3DEventSetY ( caerPoint3DEvent event, float y ) [inline], [static]`

Set the Y axis measurement.

#### Parameters

<i>event</i>	a valid Point3DEvent pointer. Cannot be NULL.
<i>y</i>	Y axis measurement.

4.14.4.17 `static void caerPoint3DEventSetZ ( caerPoint3DEvent event, float z ) [inline], [static]`

Set the Z axis measurement.

#### Parameters

<i>event</i>	a valid Point3DEvent pointer. Cannot be NULL.
<i>z</i>	Z axis measurement.

4.14.4.18 `static void caerPoint3DEventValidate ( caerPoint3DEvent event, caerPoint3DEventPacket packet )`  
`[inline], [static]`

Validate the current event by setting its valid bit to true and increasing the event packet's event count and valid event count. Only works on events that are invalid. DO NOT CALL THIS AFTER HAVING PREVIOUSLY ALREADY INVALIDATED THIS EVENT, the total count will be incorrect.

#### Parameters

<i>event</i>	a valid Point3DEvent pointer. Cannot be NULL.
<i>packet</i>	the Point3DEventPacket pointer for the packet containing this event. Cannot be NULL.

4.14.4.19 `PACKED_STRUCT ( struct caer_point3d_event{uint32_t info;float x;float y;float z;int32_t timestamp;} )`

Point3D event data structure definition. This contains information about the measurement, such as a type and a scale field, together with the usual validity mark. The three measurements (x, y, z) are stored as floats. Floats are in IEEE 754-2008 binary32 format. Signed integers are used for fields that are to be interpreted directly, for compatibility with languages that do not have unsigned integer types, such as Java.

4.14.4.20 `PACKED_STRUCT ( struct caer_point3d_event_packet{struct caer_event_packet_header packetHeader;struct caer_point3d_event events[ ];} )`

Point3D event packet data structure definition. EventPackets are always made up of the common packet header, followed by 'eventCapacity' events. Everything has to be in one contiguous memory block.

## 4.15 events/point4d.h File Reference

```
#include "common.h"
```

### Macros

- `#define CAER_POINT4D_ITERATOR_ALL_START(PPOINT4D_PACKET)`
- `#define CAER_POINT4D_ITERATOR_ALL_END }`
- `#define CAER_POINT4D_ITERATOR_VALID_START(PPOINT4D_PACKET)`
- `#define CAER_POINT4D_ITERATOR_VALID_END }`
- `#define POINT4D_TYPE_SHIFT 1`
- `#define POINT4D_TYPE_MASK 0x0000007F`
- `#define POINT4D_SCALE_SHIFT 8`
- `#define POINT4D_SCALE_MASK 0x000000FF`

### Typedefs

- `typedef struct caer_point4d_event * caerPoint4DEvent`
- `typedef struct caer_point4d_event_packet * caerPoint4DEventPacket`

## Functions

- [PACKED\\_STRUCT](#) (struct caer\_point4d\_event{uint32\_t info;float x;float y;float z;float w;int32\_t timestamp;})
- [PACKED\\_STRUCT](#) (struct caer\_point4d\_event\_packet{struct caer\_event\_packet\_header packet←Header;struct caer\_point4d\_event events[ ];})
- [caerPoint4DEventPacket](#) [caerPoint4DEventPacketAllocate](#) (int32\_t eventCapacity, int16\_t eventSource, int32\_t tsOverflow)
- static [caerPoint4DEvent](#) [caerPoint4DEventPacketGetEvent](#) ([caerPoint4DEventPacket](#) packet, int32\_t n)
- static int32\_t [caerPoint4DEventGetTimestamp](#) ([caerPoint4DEvent](#) event)
- static int64\_t [caerPoint4DEventGetTimestamp64](#) ([caerPoint4DEvent](#) event, [caerPoint4DEventPacket](#) packet)
- static void [caerPoint4DEventSetTimestamp](#) ([caerPoint4DEvent](#) event, int32\_t timestamp)
- static bool [caerPoint4DEventIsValid](#) ([caerPoint4DEvent](#) event)
- static void [caerPoint4DEventValidate](#) ([caerPoint4DEvent](#) event, [caerPoint4DEventPacket](#) packet)
- static void [caerPoint4DEventInvalidate](#) ([caerPoint4DEvent](#) event, [caerPoint4DEventPacket](#) packet)
- static uint8\_t [caerPoint4DEventGetType](#) ([caerPoint4DEvent](#) event)
- static void [caerPoint4DEventSetType](#) ([caerPoint4DEvent](#) event, uint8\_t type)
- static int8\_t [caerPoint4DEventGetScale](#) ([caerPoint4DEvent](#) event)
- static void [caerPoint4DEventSetScale](#) ([caerPoint4DEvent](#) event, int8\_t scale)
- static float [caerPoint4DEventGetX](#) ([caerPoint4DEvent](#) event)
- static void [caerPoint4DEventSetX](#) ([caerPoint4DEvent](#) event, float x)
- static float [caerPoint4DEventGetY](#) ([caerPoint4DEvent](#) event)
- static void [caerPoint4DEventSetY](#) ([caerPoint4DEvent](#) event, float y)
- static float [caerPoint4DEventGetZ](#) ([caerPoint4DEvent](#) event)
- static void [caerPoint4DEventSetZ](#) ([caerPoint4DEvent](#) event, float z)
- static float [caerPoint4DEventGetW](#) ([caerPoint4DEvent](#) event)
- static void [caerPoint4DEventSetW](#) ([caerPoint4DEvent](#) event, float w)

### 4.15.1 Detailed Description

THIS EVENT DEFINITION IS STILL TO BE CONSIDERED EXPERIMENTAL AND IS SUBJECT TO FUTURE CHANGES AND REVISIONS!

Point4D Events format definition and handling functions. This contains four dimensional data points as floats, together with support for distinguishing type and scale. Useful for homogeneous coordinates for example.

### 4.15.2 Macro Definition Documentation

#### 4.15.2.1 `#define CAER_POINT4D_ITERATOR_ALL_END`

Iterator close statement.

#### 4.15.2.2 `#define CAER_POINT4D_ITERATOR_ALL_START( POINT4D_PACKET )`

**Value:**

```
for (int32_t caerPoint4DIteratorCounter = 0; \
    caerPoint4DIteratorCounter < caerEventPacketHeaderGetEventNumber
    (&(POINT4D_PACKET)->packetHeader); \
    caerPoint4DIteratorCounter++) { \
    caerPoint4DEvent caerPoint4DIteratorElement =
    caerPoint4DEventPacketGetEvent (POINT4D_PACKET, caerPoint4DIteratorCounter);
```

Iterator over all Point4D events in a packet. Returns the current index in the 'caerPoint4DIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerPoint4DIteratorElement' variable of type caerPoint4DEvent.

POINT4D\_PACKET: a valid Point4DEventPacket pointer. Cannot be NULL.

4.15.2.3 `#define CAER_POINT4D_ITERATOR_VALID_END }`

Iterator close statement.

4.15.2.4 `#define CAER_POINT4D_ITERATOR_VALID_START( POINT4D_PACKET )`

**Value:**

```
for (int32_t caerPoint4DIteratorCounter = 0; \
     caerPoint4DIteratorCounter < caerEventPacketHeaderGetEventNumber
     (&(POINT4D_PACKET)->packetHeader); \
     caerPoint4DIteratorCounter++) { \
    caerPoint4DEvent caerPoint4DIteratorElement =
    caerPoint4DEventPacketGetEvent(POINT4D_PACKET, caerPoint4DIteratorCounter); \
    if (!caerPoint4DEventIsValid(caerPoint4DIteratorElement)) { continue; }
```

Iterator over only the valid Point4D events in a packet. Returns the current index in the 'caerPoint4DIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerPoint4DIteratorElement' variable of type caerPoint4DEvent.

POINT4D\_PACKET: a valid Point4DEventPacket pointer. Cannot be NULL.

4.15.2.5 `#define POINT4D_SCALE_MASK 0x000000FF`

Shift and mask values for type and scale information associated with a Point4D event. Up to 128 types are supported. The scale is given as orders of magnitude, from  $10^{-128}$  to  $10^{127}$ . Bit 0 is the valid mark, see 'common.h' for more details.

4.15.2.6 `#define POINT4D_SCALE_SHIFT 8`

Shift and mask values for type and scale information associated with a Point4D event. Up to 128 types are supported. The scale is given as orders of magnitude, from  $10^{-128}$  to  $10^{127}$ . Bit 0 is the valid mark, see 'common.h' for more details.

4.15.2.7 `#define POINT4D_TYPE_MASK 0x0000007F`

Shift and mask values for type and scale information associated with a Point4D event. Up to 128 types are supported. The scale is given as orders of magnitude, from  $10^{-128}$  to  $10^{127}$ . Bit 0 is the valid mark, see 'common.h' for more details.

4.15.2.8 `#define POINT4D_TYPE_SHIFT 1`

Shift and mask values for type and scale information associated with a Point4D event. Up to 128 types are supported. The scale is given as orders of magnitude, from  $10^{-128}$  to  $10^{127}$ . Bit 0 is the valid mark, see 'common.h' for more details.

## 4.15.3 Typedef Documentation

4.15.3.1 `typedef struct caer_point4d_event* caerPoint4DEvent`

Type for pointer to Point4D event data structure.

#### 4.15.3.2 `typedef struct caer_point4d_event_packet* caerPoint4DEventPacket`

Type for pointer to Point4D event packet data structure.

### 4.15.4 Function Documentation

#### 4.15.4.1 `static int8_t caerPoint4DEventGetScale ( caerPoint4DEvent event ) [inline],[static]`

Get the measurement scale. This allows order of magnitude shifts on the measured value to be applied automatically, such as having measurements of type Distance (meters) and storing the values as centimeters ( $10^{-2}$ ) for higher precision, but keeping that information around to allow easy changes of unit.

##### Parameters

<i>event</i>	a valid Point4DEvent pointer. Cannot be NULL.
--------------	---

##### Returns

the Point4D measurement scale.

#### 4.15.4.2 `static int32_t caerPoint4DEventGetTimestamp ( caerPoint4DEvent event ) [inline],[static]`

Get the 32bit event timestamp, in microseconds. Be aware that this wraps around! You can either ignore this fact, or handle the special 'TIMESTAMP\_WRAP' event that is generated when this happens, or use the 64bit timestamp which never wraps around. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

##### Parameters

<i>event</i>	a valid Point4DEvent pointer. Cannot be NULL.
--------------	---

##### Returns

this event's 32bit microsecond timestamp.

#### 4.15.4.3 `static int64_t caerPoint4DEventGetTimestamp64 ( caerPoint4DEvent event, caerPoint4DEventPacket packet ) [inline],[static]`

Get the 64bit event timestamp, in microseconds. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

##### Parameters

<i>event</i>	a valid Point4DEvent pointer. Cannot be NULL.
<i>packet</i>	the Point4DEventPacket pointer for the packet containing this event. Cannot be NULL.

**Returns**

this event's 64bit microsecond timestamp.

**4.15.4.4** `static uint8_t caerPoint4DEventGetType ( caerPoint4DEvent event )` `[inline],[static]`

Get the measurement event type. This is useful to distinguish between different measurements, for example distance or weight.

**Parameters**

<i>event</i>	a valid Point4DEvent pointer. Cannot be NULL.
--------------	---

**Returns**

the Point4D measurement type.

**4.15.4.5** `static float caerPoint4DEventGetW ( caerPoint4DEvent event )` `[inline],[static]`

Get the W axis measurement.

**Parameters**

<i>event</i>	a valid Point4DEvent pointer. Cannot be NULL.
--------------	---

**Returns**

W axis measurement.

**4.15.4.6** `static float caerPoint4DEventGetX ( caerPoint4DEvent event )` `[inline],[static]`

Get the X axis measurement.

**Parameters**

<i>event</i>	a valid Point4DEvent pointer. Cannot be NULL.
--------------	---

**Returns**

X axis measurement.

**4.15.4.7** `static float caerPoint4DEventGetY ( caerPoint4DEvent event )` `[inline],[static]`

Get the Y axis measurement.

**Parameters**

<i>event</i>	a valid Point4DEvent pointer. Cannot be NULL.
--------------	---

**Returns**

Y axis measurement.

**4.15.4.8** `static float caerPoint4DEventGetZ ( caerPoint4DEvent event )` `[inline],[static]`

Get the Z axis measurement.

**Parameters**

<i>event</i>	a valid Point4DEvent pointer. Cannot be NULL.
--------------	---

**Returns**

Z axis measurement.

**4.15.4.9** `static void caerPoint4DEventInvalidate ( caerPoint4DEvent event, caerPoint4DEventPacket packet )`  
`[inline],[static]`

Invalidate the current event by setting its valid bit to false and decreasing the number of valid events held in the packet. Only works with events that are already valid!

**Parameters**

<i>event</i>	a valid Point4DEvent pointer. Cannot be NULL.
<i>packet</i>	the Point4DEventPacket pointer for the packet containing this event. Cannot be NULL.

**4.15.4.10** `static bool caerPoint4DEventsValid ( caerPoint4DEvent event )` `[inline],[static]`

Check if this Point4D event is valid.

**Parameters**

<i>event</i>	a valid Point4DEvent pointer. Cannot be NULL.
--------------	---

**Returns**

true if valid, false if not.



**4.15.4.11** `caerPoint4DEventPacket caerPoint4DEventPacketAllocate ( int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow )`

Allocate a new Point4D events packet. Use free() to reclaim this memory.

#### Parameters

<i>eventCapacity</i>	the maximum number of events this packet will hold.
<i>eventSource</i>	the unique ID representing the source/generator of this packet.
<i>tsOverflow</i>	the current timestamp overflow counter value for this packet.

#### Returns

a valid Point4DEventPacket handle or NULL on error.

**4.15.4.12** `static caerPoint4DEvent caerPoint4DEventPacketGetEvent ( caerPoint4DEventPacket packet, int32_t n )`  
[inline],[static]

Get the Point4D event at the given index from the event packet.

#### Parameters

<i>packet</i>	a valid Point4DEventPacket pointer. Cannot be NULL.
<i>n</i>	the index of the returned event. Must be within [0,eventCapacity[ bounds.

#### Returns

the requested Point4D event. NULL on error.

**4.15.4.13** `static void caerPoint4DEventSetScale ( caerPoint4DEvent event, int8_t scale )` [inline],[static]

Set the measurement scale. This allows order of magnitude shifts on the measured value to be applied automatically, such as having measurements of type Distance (meters) and storing the values as centimeters ( $10^{-2}$ ) for higher precision, but keeping that information around to allow easy changes of unit.

#### Parameters

<i>event</i>	a valid Point4DEvent pointer. Cannot be NULL.
<i>scale</i>	the Point4D measurement scale.

**4.15.4.14** `static void caerPoint4DEventSetTimestamp ( caerPoint4DEvent event, int32_t timestamp )` [inline],[static]

Set the 32bit event timestamp, the value has to be in microseconds.

## Parameters

<i>event</i>	a valid Point4DEvent pointer. Cannot be NULL.
<i>timestamp</i>	a positive 32bit microsecond timestamp.

4.15.4.15 `static void caerPoint4DEventSetType ( caerPoint4DEvent event, uint8_t type )` `[inline],[static]`

Set the measurement event type. This is useful to distinguish between different measurements, for example distance or weight.

## Parameters

<i>event</i>	a valid Point4DEvent pointer. Cannot be NULL.
<i>type</i>	the Point4D measurement type.

4.15.4.16 `static void caerPoint4DEventSetW ( caerPoint4DEvent event, float w )` `[inline],[static]`

Set the W axis measurement.

## Parameters

<i>event</i>	a valid Point4DEvent pointer. Cannot be NULL.
<i>w</i>	W axis measurement.

4.15.4.17 `static void caerPoint4DEventSetX ( caerPoint4DEvent event, float x )` `[inline],[static]`

Set the X axis measurement.

## Parameters

<i>event</i>	a valid Point4DEvent pointer. Cannot be NULL.
<i>x</i>	X axis measurement.

4.15.4.18 `static void caerPoint4DEventSetY ( caerPoint4DEvent event, float y )` `[inline],[static]`

Set the Y axis measurement.

## Parameters

<i>event</i>	a valid Point4DEvent pointer. Cannot be NULL.
<i>y</i>	Y axis measurement.

4.15.4.19 `static void caerPoint4DEventSetZ ( caerPoint4DEvent event, float z ) [inline], [static]`

Set the Z axis measurement.

#### Parameters

<i>event</i>	a valid Point4DEvent pointer. Cannot be NULL.
<i>z</i>	Z axis measurement.

4.15.4.20 `static void caerPoint4DEventValidate ( caerPoint4DEvent event, caerPoint4DEventPacket packet ) [inline], [static]`

Validate the current event by setting its valid bit to true and increasing the event packet's event count and valid event count. Only works on events that are invalid. DO NOT CALL THIS AFTER HAVING PREVIOUSLY ALREADY INVALIDATED THIS EVENT, the total count will be incorrect.

#### Parameters

<i>event</i>	a valid Point4DEvent pointer. Cannot be NULL.
<i>packet</i>	the Point4DEventPacket pointer for the packet containing this event. Cannot be NULL.

4.15.4.21 `PACKED_STRUCT ( struct caer_point4d_event{uint32_t info;float x;float y;float z;float w;int32_t timestamp;} )`

Point4D event data structure definition. This contains information about the measurement, such as a type and a scale field, together with the usual validity mark. The four measurements (x, y, z, w) are stored as floats. Floats are in IEEE 754-2008 binary32 format. Signed integers are used for fields that are to be interpreted directly, for compatibility with languages that do not have unsigned integer types, such as Java.

4.15.4.22 `PACKED_STRUCT ( struct caer_point4d_event_packet{struct caer_event_packet_header packetHeader;struct caer_point4d_event events[];} )`

Point4D event packet data structure definition. EventPackets are always made up of the common packet header, followed by 'eventCapacity' events. Everything has to be in one contiguous memory block.

## 4.16 events/polarity.h File Reference

```
#include "common.h"
```

### Macros

- `#define CAER_POLARITY_ITERATOR_ALL_START(POLARITY_PACKET)`
- `#define CAER_POLARITY_ITERATOR_ALL_END }`
- `#define CAER_POLARITY_ITERATOR_VALID_START(POLARITY_PACKET)`
- `#define CAER_POLARITY_ITERATOR_VALID_END }`

- `#define CAER_POLARITY_REVERSE_ITERATOR_ALL_START(POLARITY_PACKET)`
- `#define CAER_POLARITY_REVERSE_ITERATOR_ALL_END }`
- `#define CAER_POLARITY_REVERSE_ITERATOR_VALID_START(POLARITY_PACKET)`
- `#define CAER_POLARITY_REVERSE_ITERATOR_VALID_END }`
- `#define POLARITY_SHIFT 1`
- `#define POLARITY_MASK 0x00000001`
- `#define Y_ADDR_SHIFT 2`
- `#define Y_ADDR_MASK 0x00007FFF`
- `#define X_ADDR_SHIFT 17`
- `#define X_ADDR_MASK 0x00007FFF`

## Typedefs

- `typedef struct caer_polarity_event * caerPolarityEvent`
- `typedef struct caer_polarity_event_packet * caerPolarityEventPacket`

## Functions

- `PACKED_STRUCT` (struct caer\_polarity\_event{uint32\_t data;int32\_t timestamp;})
- `PACKED_STRUCT` (struct caer\_polarity\_event\_packet{struct caer\_event\_packet\_header packet←Header;struct caer\_polarity\_event events[ ];})
- `caerPolarityEventPacket caerPolarityEventPacketAllocate` (int32\_t eventCapacity, int16\_t eventSource, int32\_t tsOverflow)
- static `caerPolarityEvent caerPolarityEventPacketGetEvent` (caerPolarityEventPacket packet, int32\_t n)
- static int32\_t `caerPolarityEventGetTimestamp` (caerPolarityEvent event)
- static int64\_t `caerPolarityEventGetTimestamp64` (caerPolarityEvent event, caerPolarityEventPacket packet)
- static void `caerPolarityEventSetTimestamp` (caerPolarityEvent event, int32\_t timestamp)
- static bool `caerPolarityEventIsValid` (caerPolarityEvent event)
- static void `caerPolarityEventValidate` (caerPolarityEvent event, caerPolarityEventPacket packet)
- static void `caerPolarityEventInvalidate` (caerPolarityEvent event, caerPolarityEventPacket packet)
- static bool `caerPolarityEventGetPolarity` (caerPolarityEvent event)
- static void `caerPolarityEventSetPolarity` (caerPolarityEvent event, bool polarity)
- static uint16\_t `caerPolarityEventGetY` (caerPolarityEvent event)
- static void `caerPolarityEventSetY` (caerPolarityEvent event, uint16\_t yAddress)
- static uint16\_t `caerPolarityEventGetX` (caerPolarityEvent event)
- static void `caerPolarityEventSetX` (caerPolarityEvent event, uint16\_t xAddress)

### 4.16.1 Detailed Description

Polarity Events format definition and handling functions. This event contains change information, with an X/Y address and an ON/OFF polarity. The (0, 0) address is in the upper left corner of the screen, like in OpenCV/computer graphics.

### 4.16.2 Macro Definition Documentation

#### 4.16.2.1 `#define CAER_POLARITY_ITERATOR_ALL_END }`

Iterator close statement.

4.16.2.2 `#define CAER_POLARITY_ITERATOR_ALL_START( POLARITY_PACKET )`**Value:**

```
for (int32_t caerPolarityIteratorCounter = 0; \
     caerPolarityIteratorCounter < caerEventPacketHeaderGetEventNumber
(&(POLARITY_PACKET)->packetHeader); \
     caerPolarityIteratorCounter++) { \
    caerPolarityEvent caerPolarityIteratorElement =
caerPolarityEventPacketGetEvent(POLARITY_PACKET, caerPolarityIteratorCounter
);
```

Iterator over all polarity events in a packet. Returns the current index in the 'caerPolarityIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerPolarityIteratorElement' variable of type caerPolarityEvent.

POLARITY\_PACKET: a valid PolarityEventPacket pointer. Cannot be NULL.

4.16.2.3 `#define CAER_POLARITY_ITERATOR_VALID_END }`

Iterator close statement.

4.16.2.4 `#define CAER_POLARITY_ITERATOR_VALID_START( POLARITY_PACKET )`**Value:**

```
for (int32_t caerPolarityIteratorCounter = 0; \
     caerPolarityIteratorCounter < caerEventPacketHeaderGetEventNumber
(&(POLARITY_PACKET)->packetHeader); \
     caerPolarityIteratorCounter++) { \
    caerPolarityEvent caerPolarityIteratorElement =
caerPolarityEventPacketGetEvent(POLARITY_PACKET, caerPolarityIteratorCounter
); \
    if (!caerPolarityEventIsValid(caerPolarityIteratorElement)) { continue; }
```

Iterator over only the valid polarity events in a packet. Returns the current index in the 'caerPolarityIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerPolarityIteratorElement' variable of type caerPolarityEvent.

POLARITY\_PACKET: a valid PolarityEventPacket pointer. Cannot be NULL.

4.16.2.5 `#define CAER_POLARITY_REVERSE_ITERATOR_ALL_END }`

Reverse iterator close statement.

4.16.2.6 `#define CAER_POLARITY_REVERSE_ITERATOR_ALL_START( POLARITY_PACKET )`**Value:**

```
for (int32_t caerPolarityIteratorCounter = caerEventPacketHeaderGetEventNumber
(&(POLARITY_PACKET)->packetHeader) - 1; \
     caerPolarityIteratorCounter >= 0; \
     caerPolarityIteratorCounter--) { \
    caerPolarityEvent caerPolarityIteratorElement =
caerPolarityEventPacketGetEvent(POLARITY_PACKET, caerPolarityIteratorCounter
);
```

Reverse iterator over all polarity events in a packet. Returns the current index in the 'caerPolarityIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerPolarityIteratorElement' variable of type caerPolarityEvent.

POLARITY\_PACKET: a valid PolarityEventPacket pointer. Cannot be NULL.

#### 4.16.2.7 `#define CAER_POLARITY_REVERSE_ITERATOR_VALID_END` }

Reverse iterator close statement.

#### 4.16.2.8 `#define CAER_POLARITY_REVERSE_ITERATOR_VALID_START( POLARITY_PACKET )`

**Value:**

```
for (int32_t caerPolarityIteratorCounter = caerEventPacketHeaderGetEventNumber
    (&(POLARITY_PACKET)->packetHeader) - 1; \
    caerPolarityIteratorCounter >= 0; \
    caerPolarityIteratorCounter--) { \
    caerPolarityEvent caerPolarityIteratorElement = \
    caerPolarityEventPacketGetEvent(POLARITY_PACKET, caerPolarityIteratorCounter
    ); \
    if (!caerPolarityEventIsValid(caerPolarityIteratorElement)) { continue; }
```

Reverse iterator over only the valid polarity events in a packet. Returns the current index in the 'caerPolarityIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerPolarityIteratorElement' variable of type caerPolarityEvent.

POLARITY\_PACKET: a valid PolarityEventPacket pointer. Cannot be NULL.

#### 4.16.2.9 `#define POLARITY_MASK 0x00000001`

Shift and mask values for the polarity, X and Y addresses of a polarity event. Addresses up to 14 bit are supported. Polarity is ON(=1) or OFF(=0). Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.16.2.10 `#define POLARITY_SHIFT 1`

Shift and mask values for the polarity, X and Y addresses of a polarity event. Addresses up to 14 bit are supported. Polarity is ON(=1) or OFF(=0). Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.16.2.11 `#define X_ADDR_MASK 0x00007FFF`

Shift and mask values for the polarity, X and Y addresses of a polarity event. Addresses up to 14 bit are supported. Polarity is ON(=1) or OFF(=0). Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.16.2.12 `#define X_ADDR_SHIFT 17`

Shift and mask values for the polarity, X and Y addresses of a polarity event. Addresses up to 14 bit are supported. Polarity is ON(=1) or OFF(=0). Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.16.2.13 `#define Y_ADDR_MASK 0x00007FFF`

Shift and mask values for the polarity, X and Y addresses of a polarity event. Addresses up to 14 bit are supported. Polarity is ON(=1) or OFF(=0). Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.16.2.14 `#define Y_ADDR_SHIFT 2`

Shift and mask values for the polarity, X and Y addresses of a polarity event. Addresses up to 14 bit are supported. Polarity is ON(=1) or OFF(=0). Bit 0 is the valid mark, see '[common.h](#)' for more details.

### 4.16.3 Typedef Documentation

#### 4.16.3.1 `typedef struct caer_polarity_event* caerPolarityEvent`

Type for pointer to polarity event data structure.

#### 4.16.3.2 `typedef struct caer_polarity_event_packet* caerPolarityEventPacket`

Type for pointer to polarity event packet data structure.

### 4.16.4 Function Documentation

#### 4.16.4.1 `static bool caerPolarityEventGetPolarity ( caerPolarityEvent event ) [inline], [static]`

Get the change event polarity. 1 is ON, 0 is OFF.

##### Parameters

<i>event</i>	a valid PolarityEvent pointer. Cannot be NULL.
--------------	--

##### Returns

event polarity value.

#### 4.16.4.2 `static int32_t caerPolarityEventGetTimestamp ( caerPolarityEvent event ) [inline], [static]`

Get the 32bit event timestamp, in microseconds. Be aware that this wraps around! You can either ignore this fact, or handle the special 'TIMESTAMP\_WRAP' event that is generated when this happens, or use the 64bit timestamp which never wraps around. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

##### Parameters

<i>event</i>	a valid PolarityEvent pointer. Cannot be NULL.
--------------	--

##### Returns

this event's 32bit microsecond timestamp.

**4.16.4.3** `static int64_t caerPolarityEventGetTimestamp64 ( caerPolarityEvent event, caerPolarityEventPacket packet )`  
`[inline],[static]`

Get the 64bit event timestamp, in microseconds. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

#### Parameters

<i>event</i>	a valid PolarityEvent pointer. Cannot be NULL.
<i>packet</i>	the PolarityEventPacket pointer for the packet containing this event. Cannot be NULL.

#### Returns

this event's 64bit microsecond timestamp.

**4.16.4.4** `static uint16_t caerPolarityEventGetX ( caerPolarityEvent event )` `[inline],[static]`

Get the X (column) address for a change event, in pixels. The (0, 0) address is in the upper left corner, like in OpenCV/computer graphics.

#### Parameters

<i>event</i>	a valid PolarityEvent pointer. Cannot be NULL.
--------------	--

#### Returns

the event X address.

**4.16.4.5** `static uint16_t caerPolarityEventGetY ( caerPolarityEvent event )` `[inline],[static]`

Get the Y (row) address for a change event, in pixels. The (0, 0) address is in the upper left corner, like in OpenC↔V/computer graphics.

#### Parameters

<i>event</i>	a valid PolarityEvent pointer. Cannot be NULL.
--------------	--

#### Returns

the event Y address.

**4.16.4.6** `static void caerPolarityEventInvalidate ( caerPolarityEvent event, caerPolarityEventPacket packet )`  
`[inline],[static]`

Invalidate the current event by setting its valid bit to false and decreasing the number of valid events held in the packet. Only works with events that are already valid!



## Parameters

<i>event</i>	a valid PolarityEvent pointer. Cannot be NULL.
<i>packet</i>	the PolarityEventPacket pointer for the packet containing this event. Cannot be NULL.

4.16.4.7 `static bool caerPolarityEventsValid ( caerPolarityEvent event )` `[inline], [static]`

Check if this polarity event is valid.

## Parameters

<i>event</i>	a valid PolarityEvent pointer. Cannot be NULL.
--------------	--

## Returns

true if valid, false if not.

4.16.4.8 `caerPolarityEventPacket caerPolarityEventPacketAllocate ( int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow )`

Allocate a new polarity events packet. Use free() to reclaim this memory.

## Parameters

<i>eventCapacity</i>	the maximum number of events this packet will hold.
<i>eventSource</i>	the unique ID representing the source/generator of this packet.
<i>tsOverflow</i>	the current timestamp overflow counter value for this packet.

## Returns

a valid PolarityEventPacket handle or NULL on error.

4.16.4.9 `static caerPolarityEvent caerPolarityEventPacketGetEvent ( caerPolarityEventPacket packet, int32_t n )` `[inline], [static]`

Get the polarity event at the given index from the event packet.

## Parameters

<i>packet</i>	a valid PolarityEventPacket pointer. Cannot be NULL.
<i>n</i>	the index of the returned event. Must be within [0,eventCapacity[ bounds.

**Returns**

the requested polarity event. NULL on error.

**4.16.4.10** `static void caerPolarityEventSetPolarity ( caerPolarityEvent event, bool polarity )` `[inline],[static]`

Set the change event polarity. 1 is ON, 0 is OFF.

**Parameters**

<i>event</i>	a valid PolarityEvent pointer. Cannot be NULL.
<i>polarity</i>	event polarity value.

**4.16.4.11** `static void caerPolarityEventSetTimestamp ( caerPolarityEvent event, int32_t timestamp )` `[inline],[static]`

Set the 32bit event timestamp, the value has to be in microseconds.

**Parameters**

<i>event</i>	a valid PolarityEvent pointer. Cannot be NULL.
<i>timestamp</i>	a positive 32bit microsecond timestamp.

**4.16.4.12** `static void caerPolarityEventSetX ( caerPolarityEvent event, uint16_t xAddress )` `[inline],[static]`

Set the X (column) address for a change event, in pixels. The (0, 0) address is in the upper left corner, like in OpenCV/computer graphics.

**Parameters**

<i>event</i>	a valid PolarityEvent pointer. Cannot be NULL.
<i>xAddress</i>	the event X address.

**4.16.4.13** `static void caerPolarityEventSetY ( caerPolarityEvent event, uint16_t yAddress )` `[inline],[static]`

Set the Y (row) address for a change event, in pixels. The (0, 0) address is in the upper left corner, like in OpenCV/computer graphics.

**Parameters**

<i>event</i>	a valid PolarityEvent pointer. Cannot be NULL.
<i>yAddress</i>	the event Y address.

4.16.4.14 `static void caerPolarityEventValidate ( caerPolarityEvent event, caerPolarityEventPacket packet )`  
`[inline], [static]`

Validate the current event by setting its valid bit to true and increasing the event packet's event count and valid event count. Only works on events that are invalid. DO NOT CALL THIS AFTER HAVING PREVIOUSLY ALREADY INVALIDATED THIS EVENT, the total count will be incorrect.

#### Parameters

<i>event</i>	a valid PolarityEvent pointer. Cannot be NULL.
<i>packet</i>	the PolarityEventPacket pointer for the packet containing this event. Cannot be NULL.

4.16.4.15 `PACKED_STRUCT ( struct caer_polarity_event{uint32_t data;int32_t timestamp;} )`

Polarity event data structure definition. This contains the actual X/Y addresses, the polarity, as well as the 32 bit event timestamp. The (0, 0) address is in the upper left corner of the screen, like in OpenCV/computer graphics. Signed integers are used for fields that are to be interpreted directly, for compatibility with languages that do not have unsigned integer types, such as Java.

4.16.4.16 `PACKED_STRUCT ( struct caer_polarity_event_packet{struct caer_event_packet_header packetHeader;struct caer_polarity_event events[ ];} )`

Polarity event packet data structure definition. EventPackets are always made up of the common packet header, followed by 'eventCapacity' events. Everything has to be in one contiguous memory block.

## 4.17 events/sample.h File Reference

```
#include "common.h"
```

### Macros

- `#define CAER_SAMPLE_ITERATOR_ALL_START(SAMPLE_PACKET)`
- `#define CAER_SAMPLE_ITERATOR_ALL_END }`
- `#define CAER_SAMPLE_ITERATOR_VALID_START(SAMPLE_PACKET)`
- `#define CAER_SAMPLE_ITERATOR_VALID_END }`
- `#define SAMPLE_TYPE_SHIFT 1`
- `#define SAMPLE_TYPE_MASK 0x0000007F`
- `#define SAMPLE_SHIFT 8`
- `#define SAMPLE_MASK 0x00FFFFFF`

### Typedefs

- `typedef struct caer_sample_event * caerSampleEvent`
- `typedef struct caer_sample_event_packet * caerSampleEventPacket`

## Functions

- **PACKED\_STRUCT** (struct caer\_sample\_event{uint32\_t data;int32\_t timestamp;})
- **PACKED\_STRUCT** (struct caer\_sample\_event\_packet{struct caer\_event\_packet\_header packet\_header;struct caer\_sample\_event events[ ];})
- caerSampleEventPacket caerSampleEventPacketAllocate (int32\_t eventCapacity, int16\_t eventSource, int32\_t tsOverflow)
- static caerSampleEvent caerSampleEventPacketGetEvent (caerSampleEventPacket packet, int32\_t n)
- static int32\_t caerSampleEventGetTimestamp (caerSampleEvent event)
- static int64\_t caerSampleEventGetTimestamp64 (caerSampleEvent event, caerSampleEventPacket packet)
- static void caerSampleEventSetTimestamp (caerSampleEvent event, int32\_t timestamp)
- static bool caerSampleEventIsValid (caerSampleEvent event)
- static void caerSampleEventValidate (caerSampleEvent event, caerSampleEventPacket packet)
- static void caerSampleEventInvalidate (caerSampleEvent event, caerSampleEventPacket packet)
- static uint8\_t caerSampleEventGetType (caerSampleEvent event)
- static void caerSampleEventSetType (caerSampleEvent event, uint8\_t type)
- static uint32\_t caerSampleEventGetSample (caerSampleEvent event)
- static void caerSampleEventSetSample (caerSampleEvent event, uint32\_t sample)

### 4.17.1 Detailed Description

Sample (ADC) Events format definition and handling functions. Represents different types of ADC readings, up to 24 bits of resolution.

### 4.17.2 Macro Definition Documentation

#### 4.17.2.1 #define CAER\_SAMPLE\_ITERATOR\_ALL\_END }

Iterator close statement.

#### 4.17.2.2 #define CAER\_SAMPLE\_ITERATOR\_ALL\_START( SAMPLE\_PACKET )

**Value:**

```
for (int32_t caerSampleIteratorCounter = 0; \
    caerSampleIteratorCounter < caerEventPacketHeaderGetEventNumber(
    &(SAMPLE_PACKET)->packetHeader); \
    caerSampleIteratorCounter++) { \
    caerSampleEvent caerSampleIteratorElement =
    caerSampleEventPacketGetEvent(SAMPLE_PACKET, caerSampleIteratorCounter);
```

Iterator over all sample events in a packet. Returns the current index in the 'caerSampleIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerSampleIteratorElement' variable of type caerSampleEvent.

SAMPLE\_PACKET: a valid SampleEventPacket pointer. Cannot be NULL.

#### 4.17.2.3 #define CAER\_SAMPLE\_ITERATOR\_VALID\_END }

Iterator close statement.

4.17.2.4 `#define CAER_SAMPLE_ITERATOR_VALID_START( SAMPLE_PACKET )`**Value:**

```
for (int32_t caerSampleIteratorCounter = 0; \
    caerSampleIteratorCounter < caerEventPacketHeaderGetEventNumber(
    &(SAMPLE_PACKET->packetHeader); \
    caerSampleIteratorCounter++) { \
    caerSampleEvent caerSampleIteratorElement =
    caerSampleEventPacketGetEvent(SAMPLE_PACKET, caerSampleIteratorCounter); \
    if (!caerSampleEventIsValid(caerSampleIteratorElement)) { continue; }
```

Iterator over only the valid sample events in a packet. Returns the current index in the 'caerSampleIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerSampleIteratorElement' variable of type caerSampleEvent.

SAMPLE\_PACKET: a valid SampleEventPacket pointer. Cannot be NULL.

4.17.2.5 `#define SAMPLE_MASK 0x00FFFFFF`

Shift and mask values for the sample type and the actual sample value of an ADC sample. Up to 128 sample types are supported, with 24 bits of data per sample. Higher values mean a higher voltage, 0 is ground. Bit 0 is the valid mark, see '[common.h](#)' for more details.

4.17.2.6 `#define SAMPLE_SHIFT 8`

Shift and mask values for the sample type and the actual sample value of an ADC sample. Up to 128 sample types are supported, with 24 bits of data per sample. Higher values mean a higher voltage, 0 is ground. Bit 0 is the valid mark, see '[common.h](#)' for more details.

4.17.2.7 `#define SAMPLE_TYPE_MASK 0x0000007F`

Shift and mask values for the sample type and the actual sample value of an ADC sample. Up to 128 sample types are supported, with 24 bits of data per sample. Higher values mean a higher voltage, 0 is ground. Bit 0 is the valid mark, see '[common.h](#)' for more details.

4.17.2.8 `#define SAMPLE_TYPE_SHIFT 1`

Shift and mask values for the sample type and the actual sample value of an ADC sample. Up to 128 sample types are supported, with 24 bits of data per sample. Higher values mean a higher voltage, 0 is ground. Bit 0 is the valid mark, see '[common.h](#)' for more details.

## 4.17.3 Typedef Documentation

4.17.3.1 `typedef struct caer_sample_event* caerSampleEvent`

Type for pointer to ADC sample event data structure.

4.17.3.2 `typedef struct caer_sample_event_packet* caerSampleEventPacket`

Type for pointer to ADC sample event packet data structure.

## 4.17.4 Function Documentation

4.17.4.1 `static uint32_t caerSampleEventGetSample( caerSampleEvent event ) [inline], [static]`

Get the ADC sample value. Up to 24 bits of resolution are possible. Higher values mean a higher voltage, 0 is ground.

## Parameters

<i>event</i>	a valid SampleEvent pointer. Cannot be NULL.
--------------	--

## Returns

the ADC sample value.

#### 4.17.4.2 static int32\_t caerSampleEventGetTimestamp ( caerSampleEvent event ) [inline],[static]

Get the 32bit event timestamp, in microseconds. Be aware that this wraps around! You can either ignore this fact, or handle the special 'TIMESTAMP\_WRAP' event that is generated when this happens, or use the 64bit timestamp which never wraps around. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

## Parameters

<i>event</i>	a valid SampleEvent pointer. Cannot be NULL.
--------------	--

## Returns

this event's 32bit microsecond timestamp.

#### 4.17.4.3 static int64\_t caerSampleEventGetTimestamp64 ( caerSampleEvent event, caerSampleEventPacket packet ) [inline],[static]

Get the 64bit event timestamp, in microseconds. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

## Parameters

<i>event</i>	a valid SampleEvent pointer. Cannot be NULL.
<i>packet</i>	the SampleEventPacket pointer for the packet containing this event. Cannot be NULL.

## Returns

this event's 64bit microsecond timestamp.

#### 4.17.4.4 static uint8\_t caerSampleEventGetType ( caerSampleEvent event ) [inline],[static]

Get the ADC sample event type. This is useful to distinguish between different measurements, for example from two separate microphones on a device.

## Parameters

<i>event</i>	a valid SampleEvent pointer. Cannot be NULL.
--------------	--

## Returns

the ADC sample type.

**4.17.4.5** `static void caerSampleEventInvalidate ( caerSampleEvent event, caerSampleEventPacket packet )`  
`[inline], [static]`

Invalidate the current event by setting its valid bit to false and decreasing the number of valid events held in the packet. Only works with events that are already valid!

## Parameters

<i>event</i>	a valid SampleEvent pointer. Cannot be NULL.
<i>packet</i>	the SampleEventPacket pointer for the packet containing this event. Cannot be NULL.

**4.17.4.6** `static bool caerSampleEventsValid ( caerSampleEvent event )` `[inline], [static]`

Check if this ADC sample event is valid.

## Parameters

<i>event</i>	a valid SampleEvent pointer. Cannot be NULL.
--------------	--

## Returns

true if valid, false if not.

**4.17.4.7** `caerSampleEventPacket caerSampleEventPacketAllocate ( int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow )`

Allocate a new ADC sample events packet. Use free() to reclaim this memory.

## Parameters

<i>eventCapacity</i>	the maximum number of events this packet will hold.
<i>eventSource</i>	the unique ID representing the source/generator of this packet.
<i>tsOverflow</i>	the current timestamp overflow counter value for this packet.

## Returns

a valid SampleEventPacket handle or NULL on error.

**4.17.4.8** `static caerSampleEvent caerSampleEventPacketGetEvent ( caerSampleEventPacket packet, int32_t n )`  
`[inline], [static]`

Get the ADC sample event at the given index from the event packet.

## Parameters

<i>packet</i>	a valid SampleEventPacket pointer. Cannot be NULL.
<i>n</i>	the index of the returned event. Must be within [0,eventCapacity[ bounds.

## Returns

the requested ADC sample event. NULL on error.

4.17.4.9 `static void caerSampleEventSetSample ( caerSampleEvent event, uint32_t sample ) [inline],[static]`

Set the ADC sample value. Up to 24 bits of resolution are possible. Higher values mean a higher voltage, 0 is ground.

## Parameters

<i>event</i>	a valid SampleEvent pointer. Cannot be NULL.
<i>sample</i>	the ADC sample value.

4.17.4.10 `static void caerSampleEventSetTimestamp ( caerSampleEvent event, int32_t timestamp ) [inline],[static]`

Set the 32bit event timestamp, the value has to be in microseconds.

## Parameters

<i>event</i>	a valid SampleEvent pointer. Cannot be NULL.
<i>timestamp</i>	a positive 32bit microsecond timestamp.

4.17.4.11 `static void caerSampleEventSetType ( caerSampleEvent event, uint8_t type ) [inline],[static]`

Set the ADC sample event type. This is useful to distinguish between different measurements, for example from two separate microphones on a device.

## Parameters

<i>event</i>	a valid SampleEvent pointer. Cannot be NULL.
<i>type</i>	the ADC sample type.

4.17.4.12 `static void caerSampleEventValidate ( caerSampleEvent event, caerSampleEventPacket packet ) [inline],[static]`

Validate the current event by setting its valid bit to true and increasing the event packet's event count and valid event count. Only works on events that are invalid. DO NOT CALL THIS AFTER HAVING PREVIOUSLY ALREADY INVALIDATED THIS EVENT, the total count will be incorrect.



## Parameters

<i>event</i>	a valid SampleEvent pointer. Cannot be NULL.
<i>packet</i>	the SampleEventPacket pointer for the packet containing this event. Cannot be NULL.

## 4.17.4.13 PACKED\_STRUCT ( struct caer\_sample\_event{uint32\_t data;int32\_t timestamp;} )

ADC sample event data structure definition. Contains a type indication to separate different ADC readouts, as well as a value for that readout, up to 24 bits resolution. Signed integers are used for fields that are to be interpreted directly, for compatibility with languages that do not have unsigned integer types, such as Java.

## 4.17.4.14 PACKED\_STRUCT ( struct caer\_sample\_event\_packet{struct caer\_event\_packet\_header packetHeader;struct caer\_sample\_event events[ ];} )

ADC sample event packet data structure definition. EventPackets are always made up of the common packet header, followed by 'eventCapacity' events. Everything has to be in one contiguous memory block.

## 4.18 events/special.h File Reference

```
#include "common.h"
```

## Macros

- `#define CAER_SPECIAL_ITERATOR_ALL_START(SPECIAL_PACKET)`
- `#define CAER_SPECIAL_ITERATOR_ALL_END }`
- `#define CAER_SPECIAL_ITERATOR_VALID_START(SPECIAL_PACKET)`
- `#define CAER_SPECIAL_ITERATOR_VALID_END }`
- `#define TYPE_SHIFT 1`
- `#define TYPE_MASK 0x0000007F`
- `#define DATA_SHIFT 8`
- `#define DATA_MASK 0x00FFFFFF`

## Typedefs

- `typedef struct caer_special_event * caerSpecialEvent`
- `typedef struct caer_special_event_packet * caerSpecialEventPacket`

## Enumerations

- enum `caer_special_event_types` {  
`TIMESTAMP_WRAP` = 0, `TIMESTAMP_RESET` = 1, `EXTERNAL_INPUT_RISING_EDGE` = 2, `EXTERNAL_INPUT_FALLING_EDGE` = 3,  
`EXTERNAL_INPUT_PULSE` = 4, `DVS_ROW_ONLY` = 5, `EXTERNAL_INPUT1_RISING_EDGE` = 6, `EXTERNAL_INPUT1_FALLING_EDGE` = 7,  
`EXTERNAL_INPUT1_PULSE` = 8, `EXTERNAL_INPUT2_RISING_EDGE` = 9, `EXTERNAL_INPUT2_FALLING_EDGE` = 10, `EXTERNAL_INPUT2_PULSE` = 11,  
`EXTERNAL_GENERATOR_RISING_EDGE` = 12, `EXTERNAL_GENERATOR_FALLING_EDGE` = 13, `APS_FRAME_START` = 14, `APS_FRAME_END` = 15,  
`APS_EXPOSURE_START` = 16, `APS_EXPOSURE_END` = 17 }

## Functions

- `PACKED_STRUCT` (struct `caer_special_event`{uint32\_t data;int32\_t timestamp;})
- `PACKED_STRUCT` (struct `caer_special_event_packet`{struct `caer_event_packet_header` packet\_header;struct `caer_special_event` events[ ];})
- `caerSpecialEventPacket caerSpecialEventPacketAllocate` (int32\_t eventCapacity, int16\_t eventSource, int32\_t tsOverflow)
- static `caerSpecialEvent caerSpecialEventPacketGetEvent` (`caerSpecialEventPacket` packet, int32\_t n)
- static int32\_t `caerSpecialEventGetTimestamp` (`caerSpecialEvent` event)
- static int64\_t `caerSpecialEventGetTimestamp64` (`caerSpecialEvent` event, `caerSpecialEventPacket` packet)
- static void `caerSpecialEventSetTimestamp` (`caerSpecialEvent` event, int32\_t timestamp)
- static bool `caerSpecialEventIsValid` (`caerSpecialEvent` event)
- static void `caerSpecialEventValidate` (`caerSpecialEvent` event, `caerSpecialEventPacket` packet)
- static void `caerSpecialEventInvalidate` (`caerSpecialEvent` event, `caerSpecialEventPacket` packet)
- static uint8\_t `caerSpecialEventGetType` (`caerSpecialEvent` event)
- static void `caerSpecialEventSetType` (`caerSpecialEvent` event, uint8\_t type)
- static uint32\_t `caerSpecialEventGetData` (`caerSpecialEvent` event)
- static void `caerSpecialEventSetData` (`caerSpecialEvent` event, uint32\_t data)
- static `caerSpecialEvent caerSpecialEventPacketFindEventByType` (`caerSpecialEventPacket` packet, uint8\_t type)
- static `caerSpecialEvent caerSpecialEventPacketFindValidEventByType` (`caerSpecialEventPacket` packet, uint8\_t type)

### 4.18.1 Detailed Description

Special Events format definition and handling functions. This event type encodes special occurrences, such as timestamp related notifications or external input events.

### 4.18.2 Macro Definition Documentation

#### 4.18.2.1 #define CAER\_SPECIAL\_ITERATOR\_ALL\_END }

Iterator close statement.

4.18.2.2 `#define CAER_SPECIAL_ITERATOR_ALL_START( SPECIAL_PACKET )`**Value:**

```
for (int32_t caerSpecialIteratorCounter = 0; \
    caerSpecialIteratorCounter < caerEventPacketHeaderGetEventNumber
    (&(SPECIAL_PACKET)->packetHeader); \
    caerSpecialIteratorCounter++) { \
    caerSpecialEvent caerSpecialIteratorElement =
    caerSpecialEventPacketGetEvent(SPECIAL_PACKET, caerSpecialIteratorCounter);
```

Iterator over all special events in a packet. Returns the current index in the 'caerSpecialIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerSpecialIteratorElement' variable of type caerSpecialEvent.

SPECIAL\_PACKET: a valid SpecialEventPacket pointer. Cannot be NULL.

4.18.2.3 `#define CAER_SPECIAL_ITERATOR_VALID_END }`

Iterator close statement.

4.18.2.4 `#define CAER_SPECIAL_ITERATOR_VALID_START( SPECIAL_PACKET )`**Value:**

```
for (int32_t caerSpecialIteratorCounter = 0; \
    caerSpecialIteratorCounter < caerEventPacketHeaderGetEventNumber
    (&(SPECIAL_PACKET)->packetHeader); \
    caerSpecialIteratorCounter++) { \
    caerSpecialEvent caerSpecialIteratorElement =
    caerSpecialEventPacketGetEvent(SPECIAL_PACKET, caerSpecialIteratorCounter); \
    if (!caerSpecialEventIsValid(caerSpecialIteratorElement)) { continue; }
```

Iterator over only the valid special events in a packet. Returns the current index in the 'caerSpecialIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerSpecialIteratorElement' variable of type caerSpecialEvent.

SPECIAL\_PACKET: a valid SpecialEventPacket pointer. Cannot be NULL.

4.18.2.5 `#define DATA_MASK 0x00FFFFFF`

Shift and mask values for the type and data portions of a special event. Up to 128 types, with 24 bits of data each, are possible. Bit 0 is the valid mark, see [common.h](#) for more details.

4.18.2.6 `#define DATA_SHIFT 8`

Shift and mask values for the type and data portions of a special event. Up to 128 types, with 24 bits of data each, are possible. Bit 0 is the valid mark, see [common.h](#) for more details.

4.18.2.7 `#define TYPE_MASK 0x0000007F`

Shift and mask values for the type and data portions of a special event. Up to 128 types, with 24 bits of data each, are possible. Bit 0 is the valid mark, see [common.h](#) for more details.

#### 4.18.2.8 `#define TYPE_SHIFT 1`

Shift and mask values for the type and data portions of a special event. Up to 128 types, with 24 bits of data each, are possible. Bit 0 is the valid mark, see '[common.h](#)' for more details.

### 4.18.3 Typedef Documentation

#### 4.18.3.1 `typedef struct caer_special_event* caerSpecialEvent`

Type for pointer to special event data structure.

#### 4.18.3.2 `typedef struct caer_special_event_packet* caerSpecialEventPacket`

Type for pointer to special event packet data structure.

### 4.18.4 Enumeration Type Documentation

#### 4.18.4.1 `enum caer_special_event_types`

List of all special event type identifiers. Used to interpret the special event type field.

Enumerator

***TIMESTAMP\_WRAP*** A 32 bit timestamp wrap occurred.

***TIMESTAMP\_RESET*** A timestamp reset occurred.

***EXTERNAL\_INPUT\_RISING\_EDGE*** A rising edge was detected (External Input module on device).

***EXTERNAL\_INPUT\_FALLING\_EDGE*** A falling edge was detected (External Input module on device).

***EXTERNAL\_INPUT\_PULSE*** A pulse was detected (External Input module on device).

***DVS\_ROW\_ONLY*** A DVS row-only event was detected (a row address without any following column addresses).

***EXTERNAL\_INPUT1\_RISING\_EDGE*** A rising edge was detected (External Input 1 module on device).

***EXTERNAL\_INPUT1\_FALLING\_EDGE*** A falling edge was detected (External Input 1 module on device).

***EXTERNAL\_INPUT1\_PULSE*** A pulse was detected (External Input 1 module on device).

***EXTERNAL\_INPUT2\_RISING\_EDGE*** A rising edge was detected (External Input 2 module on device).

***EXTERNAL\_INPUT2\_FALLING\_EDGE*** A falling edge was detected (External Input 2 module on device).

***EXTERNAL\_INPUT2\_PULSE*** A pulse was detected (External Input 2 module on device).

***EXTERNAL\_GENERATOR\_RISING\_EDGE*** A rising edge was generated (External Input Generator module on device).

***EXTERNAL\_GENERATOR\_FALLING\_EDGE*** A falling edge was generated (External Input Generator module on device).

***APS\_FRAME\_START*** An APS frame capture has started (Frame Event will follow).

***APS\_FRAME\_END*** An APS frame capture has completed (Frame Event is alongside).

***APS\_EXPOSURE\_START*** An APS frame exposure has started (Frame Event will follow).

***APS\_EXPOSURE\_END*** An APS frame exposure has completed (Frame Event will follow).

### 4.18.5 Function Documentation

#### 4.18.5.1 `static uint32_t caerSpecialEventGetData ( caerSpecialEvent event ) [inline], [static]`

Get the special event data. Its meaning depends on the type. Current types that make use of it are (see '`enum caer_special_event_types`')

- `DVS_ROW_ONLY`: encodes the address of the row from the row-only event.

## Parameters

<i>event</i>	a valid SpecialEvent pointer. Cannot be NULL.
--------------	---

## Returns

the special event data.

#### 4.18.5.2 static int32\_t caerSpecialEventGetTimestamp ( caerSpecialEvent *event* ) [inline],[static]

Get the 32bit event timestamp, in microseconds. Be aware that this wraps around! You can either ignore this fact, or handle the special 'TIMESTAMP\_WRAP' event that is generated when this happens, or use the 64bit timestamp which never wraps around. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

## Parameters

<i>event</i>	a valid SpecialEvent pointer. Cannot be NULL.
--------------	---

## Returns

this event's 32bit microsecond timestamp.

#### 4.18.5.3 static int64\_t caerSpecialEventGetTimestamp64 ( caerSpecialEvent *event*, caerSpecialEventPacket *packet* ) [inline],[static]

Get the 64bit event timestamp, in microseconds. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

## Parameters

<i>event</i>	a valid SpecialEvent pointer. Cannot be NULL.
<i>packet</i>	the SpecialEventPacket pointer for the packet containing this event. Cannot be NULL.

## Returns

this event's 64bit microsecond timestamp.

#### 4.18.5.4 static uint8\_t caerSpecialEventGetType ( caerSpecialEvent *event* ) [inline],[static]

Get the numerical special event type.

## Parameters

<i>event</i>	a valid SpecialEvent pointer. Cannot be NULL.
--------------	---

**Returns**

the special event type (see 'enum caer\_special\_event\_types').

**4.18.5.5** `static void caerSpecialEventInvalidate ( caerSpecialEvent event, caerSpecialEventPacket packet )`  
`[inline], [static]`

Invalidate the current event by setting its valid bit to false and decreasing the number of valid events held in the packet. Only works with events that are already valid!

**Parameters**

<i>event</i>	a valid SpecialEvent pointer. Cannot be NULL.
<i>packet</i>	the SpecialEventPacket pointer for the packet containing this event. Cannot be NULL.

**4.18.5.6** `static bool caerSpecialEventsValid ( caerSpecialEvent event )` `[inline], [static]`

Check if this special event is valid.

**Parameters**

<i>event</i>	a valid SpecialEvent pointer. Cannot be NULL.
--------------	---

**Returns**

true if valid, false if not.

**4.18.5.7** `caerSpecialEventPacket caerSpecialEventPacketAllocate ( int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow )`

Allocate a new special events packet. Use free() to reclaim this memory.

**Parameters**

<i>eventCapacity</i>	the maximum number of events this packet will hold.
<i>eventSource</i>	the unique ID representing the source/generator of this packet.
<i>tsOverflow</i>	the current timestamp overflow counter value for this packet.

**Returns**

a valid SpecialEventPacket handle or NULL on error.

**4.18.5.8** `static caerSpecialEvent caerSpecialEventPacketFindEventByType ( caerSpecialEventPacket packet, uint8_t type )` `[inline], [static]`

Get the first special event with the given event type in this event packet. This returns the first found event with that type ID, or NULL if we get to the end without finding any such event.

## Parameters

<i>packet</i>	a valid SpecialEventPacket pointer. Cannot be NULL.
<i>type</i>	the special event type to search for.

## Returns

the requested special event or NULL on error/not found.

**4.18.5.9** `static caerSpecialEvent caerSpecialEventPacketFindValidEventByType ( caerSpecialEventPacket packet, uint8_t type ) [inline],[static]`

Get the first valid special event with the given event type in this event packet. This returns the first found valid event with that type ID, or NULL if we get to the end without finding any such event.

## Parameters

<i>packet</i>	a valid SpecialEventPacket pointer. Cannot be NULL.
<i>type</i>	the special event type to search for.

## Returns

the requested valid special event or NULL on error/not found.

**4.18.5.10** `static caerSpecialEvent caerSpecialEventPacketGetEvent ( caerSpecialEventPacket packet, int32_t n ) [inline],[static]`

Get the special event at the given index from the event packet.

## Parameters

<i>packet</i>	a valid SpecialEventPacket pointer. Cannot be NULL.
<i>n</i>	the index of the returned event. Must be within [0,eventCapacity[ bounds.

## Returns

the requested special event. NULL on error.

**4.18.5.11** `static void caerSpecialEventSetData ( caerSpecialEvent event, uint32_t data ) [inline],[static]`

Set the special event data. Its meaning depends on the type. Current types that make use of it are (see 'enum caer\_special\_event\_types'):

- DVS\_ROW\_ONLY: encodes the address of the row from the row-only event.

## Parameters

<i>event</i>	a valid SpecialEvent pointer. Cannot be NULL.
<i>data</i>	the special event data.

**4.18.5.12** `static void caerSpecialEventSetTimestamp ( caerSpecialEvent event, int32_t timestamp )` `[inline],`  
`[static]`

Set the 32bit event timestamp, the value has to be in microseconds.

## Parameters

<i>event</i>	a valid SpecialEvent pointer. Cannot be NULL.
<i>timestamp</i>	a positive 32bit microsecond timestamp.

**4.18.5.13** `static void caerSpecialEventSetType ( caerSpecialEvent event, uint8_t type )` `[inline],``[static]`

Set the numerical special event type.

## Parameters

<i>event</i>	a valid SpecialEvent pointer. Cannot be NULL.
<i>type</i>	the special event type (see 'enum caer_special_event_types').

**4.18.5.14** `static void caerSpecialEventValidate ( caerSpecialEvent event, caerSpecialEventPacket packet )`  
`[inline],``[static]`

Validate the current event by setting its valid bit to true and increasing the event packet's event count and valid event count. Only works on events that are invalid. DO NOT CALL THIS AFTER HAVING PREVIOUSLY ALREADY INVALIDATED THIS EVENT, the total count will be incorrect.

## Parameters

<i>event</i>	a valid SpecialEvent pointer. Cannot be NULL.
<i>packet</i>	the SpecialEventPacket pointer for the packet containing this event. Cannot be NULL.

**4.18.5.15** `PACKED_STRUCT ( struct caer_special_event{uint32_t data;int32_t timestamp;} )`

Special event data structure definition. This contains the actual data, as well as the 32 bit event timestamp. Signed integers are used for fields that are to be interpreted directly, for compatibility with languages that do not have unsigned integer types, such as Java.



4.18.5.16 **PACKED\_STRUCT** ( struct caer\_special\_event\_packet{struct caer\_event\_packet\_header packetHeader;struct caer\_special\_event events[ ];} )

Special event packet data structure definition. EventPackets are always made up of the common packet header, followed by 'eventCapacity' events. Everything has to be in one contiguous memory block.

## 4.19 events/spike.h File Reference

```
#include "common.h"
```

### Macros

- `#define CAER_SPIKE_ITERATOR_ALL_START(SPIKE_PACKET)`
- `#define CAER_SPIKE_ITERATOR_ALL_END }`
- `#define CAER_SPIKE_ITERATOR_VALID_START(SPIKE_PACKET)`
- `#define CAER_SPIKE_ITERATOR_VALID_END }`
- `#define SPIKE_SOURCE_CORE_ID_SHIFT 1`
- `#define SPIKE_SOURCE_CORE_ID_MASK 0x0000001F`
- `#define SPIKE_CHIP_ID_SHIFT 6`
- `#define SPIKE_CHIP_ID_MASK 0x0000003F`
- `#define SPIKE_NEURON_ID_SHIFT 12`
- `#define SPIKE_NEURON_ID_MASK 0x000FFFFF`

### Typedefs

- `typedef struct caer_spike_event * caerSpikeEvent`
- `typedef struct caer_spike_event_packet * caerSpikeEventPacket`

### Functions

- `PACKED_STRUCT` (struct caer\_spike\_event{uint32\_t data;int32\_t timestamp;})
- `PACKED_STRUCT` (struct caer\_spike\_event\_packet{struct caer\_event\_packet\_header packetHeader;struct caer\_spike\_event events[ ];})
- `caerSpikeEventPacket caerSpikeEventPacketAllocate` (int32\_t eventCapacity, int16\_t eventSource, int32\_t tsOverflow)
- `static caerSpikeEvent caerSpikeEventPacketGetEvent` (caerSpikeEventPacket packet, int32\_t n)
- `static int32_t caerSpikeEventGetTimestamp` (caerSpikeEvent event)
- `static int64_t caerSpikeEventGetTimestamp64` (caerSpikeEvent event, caerSpikeEventPacket packet)
- `static void caerSpikeEventSetTimestamp` (caerSpikeEvent event, int32\_t timestamp)
- `static bool caerSpikeEventsValid` (caerSpikeEvent event)
- `static void caerSpikeEventValidate` (caerSpikeEvent event, caerSpikeEventPacket packet)
- `static void caerSpikeEventInvalidate` (caerSpikeEvent event, caerSpikeEventPacket packet)
- `static uint8_t caerSpikeEventGetSourceCoreID` (caerSpikeEvent event)
- `static void caerSpikeEventSetSourceCoreID` (caerSpikeEvent event, uint8\_t sourceCoreID)
- `static uint8_t caerSpikeEventGetChipID` (caerSpikeEvent event)
- `static void caerSpikeEventSetChipID` (caerSpikeEvent event, uint8\_t chipID)
- `static uint8_t caerSpikeEventGetNeuronID` (caerSpikeEvent event)
- `static void caerSpikeEventSetNeuronID` (caerSpikeEvent event, uint8\_t neuronID)

### 4.19.1 Detailed Description

THIS EVENT DEFINITIONS IS STILL TO BE CONSIDERED EXPERIMENTAL AND IS SUBJECT TO FUTURE CHANGES AND REVISIONS!

Spike Events format definition and handling functions. This contains spikes generated by a neuron-array chip.

### 4.19.2 Macro Definition Documentation

#### 4.19.2.1 `#define CAER_SPIKE_ITERATOR_ALL_END`

Iterator close statement.

#### 4.19.2.2 `#define CAER_SPIKE_ITERATOR_ALL_START( SPIKE_PACKET )`

**Value:**

```
for (int32_t caerSpikeIteratorCounter = 0; \
    caerSpikeIteratorCounter < caerEventPacketHeaderGetEventNumber(&
(SPIKE_PACKET)->packetHeader); \
    caerSpikeIteratorCounter++) { \
    caerSpikeEvent caerSpikeIteratorElement =
caerSpikeEventPacketGetEvent(SPIKE_PACKET, caerSpikeIteratorCounter);
```

Iterator over all Spike events in a packet. Returns the current index in the 'caerSpikeIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerSpikeIteratorElement' variable of type caerSpikeEvent.

SPIKE\_PACKET: a valid SpikeEventPacket pointer. Cannot be NULL.

#### 4.19.2.3 `#define CAER_SPIKE_ITERATOR_VALID_END`

Iterator close statement.

#### 4.19.2.4 `#define CAER_SPIKE_ITERATOR_VALID_START( SPIKE_PACKET )`

**Value:**

```
for (int32_t caerSpikeIteratorCounter = 0; \
    caerSpikeIteratorCounter < caerEventPacketHeaderGetEventNumber(&
(SPIKE_PACKET)->packetHeader); \
    caerSpikeIteratorCounter++) { \
    caerSpikeEvent caerSpikeIteratorElement =
caerSpikeEventPacketGetEvent(SPIKE_PACKET, caerSpikeIteratorCounter); \
    if (!caerSpikeEventIsValid(caerSpikeIteratorElement)) { continue; }
```

Iterator over only the valid Spike events in a packet. Returns the current index in the 'caerSpikeIteratorCounter' variable of type 'int32\_t' and the current event in the 'caerSpikeIteratorElement' variable of type caerSpikeEvent.

SPIKE\_PACKET: a valid SpikeEventPacket pointer. Cannot be NULL.

#### 4.19.2.5 `#define SPIKE_CHIP_ID_MASK 0x0000003F`

Shift and mask values for spike information associated with a Spike event. 32 core IDs, 64 chip IDs and up to a million neuron IDs are supported. Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.19.2.6 `#define SPIKE_CHIP_ID_SHIFT 6`

Shift and mask values for spike information associated with a Spike event. 32 core IDs, 64 chip IDs and up to a million neuron IDs are supported. Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.19.2.7 `#define SPIKE_NEURON_ID_MASK 0x000FFFFF`

Shift and mask values for spike information associated with a Spike event. 32 core IDs, 64 chip IDs and up to a million neuron IDs are supported. Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.19.2.8 `#define SPIKE_NEURON_ID_SHIFT 12`

Shift and mask values for spike information associated with a Spike event. 32 core IDs, 64 chip IDs and up to a million neuron IDs are supported. Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.19.2.9 `#define SPIKE_SOURCE_CORE_ID_MASK 0x0000001F`

Shift and mask values for spike information associated with a Spike event. 32 core IDs, 64 chip IDs and up to a million neuron IDs are supported. Bit 0 is the valid mark, see '[common.h](#)' for more details.

#### 4.19.2.10 `#define SPIKE_SOURCE_CORE_ID_SHIFT 1`

Shift and mask values for spike information associated with a Spike event. 32 core IDs, 64 chip IDs and up to a million neuron IDs are supported. Bit 0 is the valid mark, see '[common.h](#)' for more details.

### 4.19.3 Typedef Documentation

#### 4.19.3.1 `typedef struct caer_spike_event* caerSpikeEvent`

Type for pointer to Spike event data structure.

#### 4.19.3.2 `typedef struct caer_spike_event_packet* caerSpikeEventPacket`

Type for pointer to Spike event packet data structure.

### 4.19.4 Function Documentation

#### 4.19.4.1 `static uint8_t caerSpikeEventGetChipID ( caerSpikeEvent event ) [inline],[static]`

Get the chip ID.

**Parameters**

<i>event</i>	a valid SpikeEvent pointer. Cannot be NULL.
--------------	---

**Returns**

the Spike's chip ID.

4.19.4.2 `static uint8_t caerSpikeEventGetNeuronID ( caerSpikeEvent event )` `[inline],[static]`

Get the neuron ID.

**Parameters**

<i>event</i>	a valid SpikeEvent pointer. Cannot be NULL.
--------------	---

**Returns**

the Spike's neuron ID.

4.19.4.3 `static uint8_t caerSpikeEventGetSourceCoreID ( caerSpikeEvent event )` `[inline],[static]`

Get the source core ID.

**Parameters**

<i>event</i>	a valid SpikeEvent pointer. Cannot be NULL.
--------------	---

**Returns**

the Spike's source core ID.

4.19.4.4 `static int32_t caerSpikeEventGetTimestamp ( caerSpikeEvent event )` `[inline],[static]`

Get the 32bit event timestamp, in microseconds. Be aware that this wraps around! You can either ignore this fact, or handle the special 'TIMESTAMP\_WRAP' event that is generated when this happens, or use the 64bit timestamp which never wraps around. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

**Parameters**

<i>event</i>	a valid SpikeEvent pointer. Cannot be NULL.
--------------	---

**Returns**

this event's 32bit microsecond timestamp.

**4.19.4.5** `static int64_t caerSpikeEventGetTimestamp64 ( caerSpikeEvent event, caerSpikeEventPacket packet )`  
`[inline], [static]`

Get the 64bit event timestamp, in microseconds. See '[caerEventPacketHeaderGetEventTSOverflow\(\)](#)' documentation for more details on the 64bit timestamp.

**Parameters**

<i>event</i>	a valid SpikeEvent pointer. Cannot be NULL.
<i>packet</i>	the SpikeEventPacket pointer for the packet containing this event. Cannot be NULL.

**Returns**

this event's 64bit microsecond timestamp.

**4.19.4.6** `static void caerSpikeEventInvalidate ( caerSpikeEvent event, caerSpikeEventPacket packet )` `[inline],`  
`[static]`

Invalidate the current event by setting its valid bit to false and decreasing the number of valid events held in the packet. Only works with events that are already valid!

**Parameters**

<i>event</i>	a valid SpikeEvent pointer. Cannot be NULL.
<i>packet</i>	the SpikeEventPacket pointer for the packet containing this event. Cannot be NULL.

**4.19.4.7** `static bool caerSpikeEventsValid ( caerSpikeEvent event )` `[inline], [static]`

Check if this Spike event is valid.

**Parameters**

<i>event</i>	a valid SpikeEvent pointer. Cannot be NULL.
--------------	---

**Returns**

true if valid, false if not.

**4.19.4.8** `caerSpikeEventPacket caerSpikeEventPacketAllocate ( int32_t eventCapacity, int16_t eventSource, int32_t tsOverflow )`

Allocate a new Spike events packet. Use free() to reclaim this memory.

## Parameters

<i>eventCapacity</i>	the maximum number of events this packet will hold.
<i>eventSource</i>	the unique ID representing the source/generator of this packet.
<i>tsOverflow</i>	the current timestamp overflow counter value for this packet.

## Returns

a valid SpikeEventPacket handle or NULL on error.

**4.19.4.9** `static caerSpikeEvent caerSpikeEventPacketGetEvent ( caerSpikeEventPacket packet, int32_t n )`  
`[inline],[static]`

Get the Spike event at the given index from the event packet.

## Parameters

<i>packet</i>	a valid SpikeEventPacket pointer. Cannot be NULL.
<i>n</i>	the index of the returned event. Must be within [0,eventCapacity[ bounds.

## Returns

the requested Spike event. NULL on error.

**4.19.4.10** `static void caerSpikeEventSetChipID ( caerSpikeEvent event, uint8_t chipID )` `[inline],[static]`

Set the chip ID.

## Parameters

<i>event</i>	a valid SpikeEvent pointer. Cannot be NULL.
<i>chipID</i>	the Spike's chip ID.

**4.19.4.11** `static void caerSpikeEventSetNeuronID ( caerSpikeEvent event, uint8_t neuronID )` `[inline],[static]`

Set the neuron ID.

## Parameters

<i>event</i>	a valid SpikeEvent pointer. Cannot be NULL.
<i>neuronID</i>	the Spike's neuron ID.

4.19.4.12 `static void caerSpikeEventSetSourceCoreID ( caerSpikeEvent event, uint8_t sourceCoreID ) [inline], [static]`

Set the source core ID.

#### Parameters

<i>event</i>	a valid SpikeEvent pointer. Cannot be NULL.
<i>sourceCoreID</i>	the Spike's source core ID.

4.19.4.13 `static void caerSpikeEventSetTimestamp ( caerSpikeEvent event, int32_t timestamp ) [inline], [static]`

Set the 32bit event timestamp, the value has to be in microseconds.

#### Parameters

<i>event</i>	a valid SpikeEvent pointer. Cannot be NULL.
<i>timestamp</i>	a positive 32bit microsecond timestamp.

4.19.4.14 `static void caerSpikeEventValidate ( caerSpikeEvent event, caerSpikeEventPacket packet ) [inline], [static]`

Validate the current event by setting its valid bit to true and increasing the event packet's event count and valid event count. Only works on events that are invalid. DO NOT CALL THIS AFTER HAVING PREVIOUSLY ALREADY INVALIDATED THIS EVENT, the total count will be incorrect.

#### Parameters

<i>event</i>	a valid SpikeEvent pointer. Cannot be NULL.
<i>packet</i>	the SpikeEventPacket pointer for the packet containing this event. Cannot be NULL.

4.19.4.15 `PACKED_STRUCT ( struct caer_spike_event{uint32_t data;int32_t timestamp;} )`

Spike event data structure definition. This contains the core ID, the neuron ID and the timestamp of the received spike, together with the usual validity mark. Signed integers are used for fields that are to be interpreted directly, for compatibility with languages that do not have unsigned integer types, such as Java.

4.19.4.16 `PACKED_STRUCT ( struct caer_spike_event_packet{struct caer_event_packet_header packetHeader;struct caer_spike_event events[];} )`

Spike event packet data structure definition. EventPackets are always made up of the common packet header, followed by 'eventCapacity' events. Everything has to be in one contiguous memory block.

## 4.20 frame\_utils.h File Reference

```
#include "events/frame.h"
```

### Functions

- [caerFrameEventPacket](#) **caerFrameUtilsDemosaic** ([caerFrameEventPacket](#) framePacket)
- void **caerFrameUtilsContrast** ([caerFrameEventPacket](#) framePacket)
- void **caerFrameUtilsWhiteBalance** ([caerFrameEventPacket](#) framePacket)

### 4.20.1 Detailed Description

Basic functions for frame enhancement and demosaicing, that don't require any external dependencies, such as OpenCV. Use of the OpenCV variants is recommended for quality and performance.

## 4.21 frame\_utils\_opencv.h File Reference

```
#include "events/frame.h"
```

### Enumerations

- enum **caer\_frame\_utils\_opencv\_demosaic** { DEMOSAIC\_NORMAL, DEMOSAIC\_EDGE\_AWARE }
- enum **caer\_frame\_utils\_opencv\_contrast** { CONTRAST\_NORMALIZATION, CONTRAST\_HISTOGRAM\_EQUALIZATION, CONTRAST\_CLAHE }
- enum **caer\_frame\_utils\_opencv\_white\_balance** { WHITEBALANCE\_SIMPLE, WHITEBALANCE\_GRAYWORLD }

### Functions

- [caerFrameEventPacket](#) **caerFrameUtilsOpenCVDemosaic** ([caerFrameEventPacket](#) framePacket, enum caer\_frame\_utils\_opencv\_demosaic demoType)
- void **caerFrameUtilsOpenCVContrast** ([caerFrameEventPacket](#) framePacket, enum caer\_frame\_utils\_opencv\_contrast contrastType)
- void **caerFrameUtilsOpenCVWhiteBalance** ([caerFrameEventPacket](#) framePacket, enum caer\_frame\_utils\_opencv\_white\_balance whiteBalanceType)

### 4.21.1 Detailed Description

Functions for frame enhancement and demosaicing, using the popular OpenCV image processing library.



## 4.22 libcaer.h File Reference

```
#include <stddef.h>
#include <stdlib.h>
#include <stdbool.h>
#include <stdio.h>
#include <stdint.h>
#include <inttypes.h>
#include <string.h>
#include <errno.h>
#include "portable_endian.h"
#include "log.h"
```

### Macros

- #define [LIBCAER\\_VERSION](#) ((2 \* 10000) + (0 \* 100) + 0)
- #define [LIBCAER\\_NAME\\_STRING](#) "libcaer"
- #define [LIBCAER\\_VERSION\\_STRING](#) "2.0.0"
- #define [U8T](#)(X) ((uint8\_t) (X))
- #define [U16T](#)(X) ((uint16\_t) (X))
- #define [U32T](#)(X) ((uint32\_t) (X))
- #define [U64T](#)(X) ((uint64\_t) (X))
- #define [I8T](#)(X) ((int8\_t) (X))
- #define [I16T](#)(X) ((int16\_t) (X))
- #define [I32T](#)(X) ((int32\_t) (X))
- #define [I64T](#)(X) ((int64\_t) (X))
- #define [MASK\\_NUMBITS32](#)(X) [U32T](#)([U32T](#)([U32T](#)(1) << X) - 1)
- #define [MASK\\_NUMBITS64](#)(X) [U64T](#)([U64T](#)([U64T](#)(1) << X) - 1)
- #define [SWAP\\_VAR](#)(type, x, y) { type tmpv; tmpv = (x); (x) = (y); (y) = tmpv; }
  
- #define [CLEAR\\_NUMBITS32](#)(VAR, SHIFT, MASK) (VAR) &= htobe32(~([U32T](#)([U32T](#)(MASK) << (SHIFT))))
- #define [CLEAR\\_NUMBITS16](#)(VAR, SHIFT, MASK) (VAR) &= htobe16(~([U16T](#)([U16T](#)(MASK) << (SHIFT))))
- #define [CLEAR\\_NUMBITS8](#)(VAR, SHIFT, MASK) (VAR) &= [U8T](#)(~([U8T](#)([U8T](#)(MASK) << (SHIFT))))
  
- #define [SET\\_NUMBITS32](#)(VAR, SHIFT, MASK, VALUE) (VAR) |= htobe32([U32T](#)(([U32T](#)(VALUE) & (MASK)) << (SHIFT)))
- #define [SET\\_NUMBITS16](#)(VAR, SHIFT, MASK, VALUE) (VAR) |= htobe16([U16T](#)(([U16T](#)(VALUE) & (MASK)) << (SHIFT)))
- #define [SET\\_NUMBITS8](#)(VAR, SHIFT, MASK, VALUE) (VAR) |= [U8T](#)(([U8T](#)(VALUE) & (MASK)) << (SHIFT))
  
- #define [GET\\_NUMBITS32](#)(VAR, SHIFT, MASK) ((le32toh(VAR) >> (SHIFT)) & (MASK))
- #define [GET\\_NUMBITS16](#)(VAR, SHIFT, MASK) ((le16toh(VAR) >> (SHIFT)) & (MASK))
- #define [GET\\_NUMBITS8](#)(VAR, SHIFT, MASK) (([U8T](#)(VAR) >> (SHIFT)) & (MASK))

### Functions

- static bool [caerStrEquals](#) (const char \*s1, const char \*s2)
- static bool [caerStrEqualsUpTo](#) (const char \*s1, const char \*s2, size\_t len)
- static void [caerIntegerToByteArray](#) (uint32\_t integer, uint8\_t \*byteArray, uint8\_t byteArrayLength)
- static uint32\_t [caerByteArrayToInteger](#) (uint8\_t \*byteArray, uint8\_t byteArrayLength)

### 4.22.1 Detailed Description

Main libcaer header; provides inclusions for common system functions and definitions for useful macros used often in the code. Also includes the logging functions and definitions and several useful static inline functions for string comparison and byte array manipulation. When including libcaer, please make sure to always use the full path, ie. `#include <libcaer/libcaer.h>` and not just `#include <libcaer.h>`.

### 4.22.2 Macro Definition Documentation

**4.22.2.1** `#define CLEAR_NUMBITS16( VAR, SHIFT, MASK ) (VAR) &= htogle16(~(U16T(U16T(MASK) << (SHIFT))))`

Clear bits given by mask (amount) and shift (position).

**4.22.2.2** `#define CLEAR_NUMBITS32( VAR, SHIFT, MASK ) (VAR) &= htogle32(~(U32T(U32T(MASK) << (SHIFT))))`

Clear bits given by mask (amount) and shift (position).

**4.22.2.3** `#define CLEAR_NUMBITS8( VAR, SHIFT, MASK ) (VAR) &= U8T(~(U8T(U8T(MASK) << (SHIFT))))`

Clear bits given by mask (amount) and shift (position).

**4.22.2.4** `#define GET_NUMBITS16( VAR, SHIFT, MASK ) ((le16toh(VAR) >> (SHIFT)) & (MASK))`

Get value of bits given by mask (amount) and shift (position).

**4.22.2.5** `#define GET_NUMBITS32( VAR, SHIFT, MASK ) ((le32toh(VAR) >> (SHIFT)) & (MASK))`

Get value of bits given by mask (amount) and shift (position).

**4.22.2.6** `#define GET_NUMBITS8( VAR, SHIFT, MASK ) ((U8T(VAR) >> (SHIFT)) & (MASK))`

Get value of bits given by mask (amount) and shift (position).

**4.22.2.7** `#define I16T( X ) ((int16_t) (X))`

Cast argument to int16\_t (16bit signed integer).

**4.22.2.8** `#define I32T( X ) ((int32_t) (X))`

Cast argument to int32\_t (32bit signed integer).

4.22.2.9 `#define I64T( X ) ((int64_t) (X))`

Cast argument to int64\_t (64bit signed integer).

4.22.2.10 `#define I8T( X ) ((int8_t) (X))`

Cast argument to int8\_t (8bit signed integer).

4.22.2.11 `#define LIBCAER_NAME_STRING "libcaer"`

libcaer name string.

4.22.2.12 `#define LIBCAER_VERSION ((2 * 10000) + (0 * 100) + 0)`

libcaer version (MAJOR \* 10000 + MINOR \* 100 + PATCH).

4.22.2.13 `#define LIBCAER_VERSION_STRING "2.0.0"`

libcaer version string.

4.22.2.14 `#define MASK_NUMBITS32( X ) U32T(U32T(U32T(1) << X) - 1)`

Mask and keep only the lower X bits of a 32bit (unsigned) integer.

4.22.2.15 `#define MASK_NUMBITS64( X ) U64T(U64T(U64T(1) << X) - 1)`

Mask and keep only the lower X bits of a 64bit (unsigned) integer.

4.22.2.16 `#define SET_NUMBITS16( VAR, SHIFT, MASK, VALUE ) (VAR) |= htobe16(U16T((U16T(VALUE) & (MASK)) << (SHIFT)))`

Set bits given by mask (amount) and shift (position) to a value.

4.22.2.17 `#define SET_NUMBITS32( VAR, SHIFT, MASK, VALUE ) (VAR) |= htobe32(U32T((U32T(VALUE) & (MASK)) << (SHIFT)))`

Set bits given by mask (amount) and shift (position) to a value.

4.22.2.18 `#define SET_NUMBITS8( VAR, SHIFT, MASK, VALUE ) (VAR) |= U8T((U8T(VALUE) & (MASK)) << (SHIFT))`

Set bits given by mask (amount) and shift (position) to a value.

4.22.2.19 `#define SWAP_VAR( type, x, y ) { type tmpv; tmpv = (x); (x) = (y); (y) = tmpv; }`

Swap the two values of the two variables X and Y, of a common type TYPE.

4.22.2.20 `#define U16T( X ) ((uint16_t) (X))`

Cast argument to `uint16_t` (16bit unsigned integer).

4.22.2.21 `#define U32T( X ) ((uint32_t) (X))`

Cast argument to `uint32_t` (32bit unsigned integer).

4.22.2.22 `#define U64T( X ) ((uint64_t) (X))`

Cast argument to `uint64_t` (64bit unsigned integer).

4.22.2.23 `#define U8T( X ) ((uint8_t) (X))`

Cast argument to `uint8_t` (8bit unsigned integer).

### 4.22.3 Function Documentation

4.22.3.1 `static uint32_t caerByteArrayToInteger ( uint8_t * byteArray, uint8_t byteArrayLength ) [inline], [static]`

Convert an unsigned byte array of up to four bytes into a 32bit unsigned integer. The byte array length decides how many resulting bits in the integer are set, and the single bytes are placed in the integer following big-endian ordering.

#### Parameters

<i>byteArray</i>	pointer to the byte array with parts of the value stored.
<i>byteArrayLength</i>	length of the array from which to convert.

#### Returns

integer representing the value stored in the byte array.

4.22.3.2 `static void caerIntegerToByteArray ( uint32_t integer, uint8_t * byteArray, uint8_t byteArrayLength ) [inline], [static]`

Convert a 32bit unsigned integer into an unsigned byte array of up to four bytes. The integer will be stored in big-endian order, and the length will specify how many bits to convert, starting from the lowest bit.

## Parameters

<i>integer</i>	the integer to convert.
<i>byteArray</i>	pointer to the byte array in which to store the converted values.
<i>byteArrayLength</i>	length of the byte array to convert to.

4.22.3.3 `static bool caerStrEquals ( const char * s1, const char * s2 )` `[inline],[static]`

Compare two strings for equality.

## Parameters

<i>s1</i>	the first string, cannot be NULL.
<i>s2</i>	the second string, cannot be NULL.

## Returns

true if equal, false otherwise.

4.22.3.4 `static bool caerStrEqualsUpTo ( const char * s1, const char * s2, size_t len )` `[inline],[static]`

Compare two strings for equality, up to a specified maximum length.

## Parameters

<i>s1</i>	the first string, cannot be NULL.
<i>s2</i>	the second string, cannot be NULL.
<i>len</i>	maximum comparison length, cannot be zero.

## Returns

true if equal, false otherwise.

## 4.23 log.h File Reference

```
#include <stdint.h>
```

### Macros

- `#define ATTRIBUTE_FORMAT`
- `#define CAER_LOG_EMERGENCY (0)`
- `#define CAER_LOG_ALERT (1)`

- `#define CAER_LOG_CRITICAL` (2)
- `#define CAER_LOG_ERROR` (3)
- `#define CAER_LOG_WARNING` (4)
- `#define CAER_LOG_NOTICE` (5)
- `#define CAER_LOG_INFO` (6)
- `#define CAER_LOG_DEBUG` (7)

## Functions

- void `caerLogLevelSet` (uint8\_t logLevel)
- uint8\_t `caerLogLevelGet` (void)
- void `caerLogFileDescriptorsSet` (int fd1, int fd2)
- void `caerLog` (uint8\_t logLevel, const char \*subSystem, const char \*format,...) `ATTRIBUTE_FORMAT`

### 4.23.1 Detailed Description

Logging functions to print useful messages for the user.

### 4.23.2 Macro Definition Documentation

#### 4.23.2.1 `#define CAER_LOG_ALERT` (1)

Log levels for `caerLog()` logging function. Log messages only get printed if their log level is equal or above the global system log level, which can be set with `caerLogLevelSet()`. The default log level is `CAER_LOG_ERROR`. `CAER_LOG_EMERGENCY` is the most urgent log level and will always be printed, while `CAER_LOG_DEBUG` is the least urgent log level and will only be delivered if configured by the user.

#### 4.23.2.2 `#define CAER_LOG_CRITICAL` (2)

Log levels for `caerLog()` logging function. Log messages only get printed if their log level is equal or above the global system log level, which can be set with `caerLogLevelSet()`. The default log level is `CAER_LOG_ERROR`. `CAER_LOG_EMERGENCY` is the most urgent log level and will always be printed, while `CAER_LOG_DEBUG` is the least urgent log level and will only be delivered if configured by the user.

#### 4.23.2.3 `#define CAER_LOG_DEBUG` (7)

Log levels for `caerLog()` logging function. Log messages only get printed if their log level is equal or above the global system log level, which can be set with `caerLogLevelSet()`. The default log level is `CAER_LOG_ERROR`. `CAER_LOG_EMERGENCY` is the most urgent log level and will always be printed, while `CAER_LOG_DEBUG` is the least urgent log level and will only be delivered if configured by the user.

#### 4.23.2.4 `#define CAER_LOG_EMERGENCY` (0)

Log levels for `caerLog()` logging function. Log messages only get printed if their log level is equal or above the global system log level, which can be set with `caerLogLevelSet()`. The default log level is `CAER_LOG_ERROR`. `CAER_LOG_EMERGENCY` is the most urgent log level and will always be printed, while `CAER_LOG_DEBUG` is the least urgent log level and will only be delivered if configured by the user.

#### 4.23.2.5 #define CAER\_LOG\_ERROR (3)

Log levels for [caerLog\(\)](#) logging function. Log messages only get printed if their log level is equal or above the global system log level, which can be set with [caerLogLevelSet\(\)](#). The default log level is CAER\_LOG\_ERROR. CAER\_LOG\_EMERGENCY is the most urgent log level and will always be printed, while CAER\_LOG\_DEBUG is the least urgent log level and will only be delivered if configured by the user.

#### 4.23.2.6 #define CAER\_LOG\_INFO (6)

Log levels for [caerLog\(\)](#) logging function. Log messages only get printed if their log level is equal or above the global system log level, which can be set with [caerLogLevelSet\(\)](#). The default log level is CAER\_LOG\_ERROR. CAER\_LOG\_EMERGENCY is the most urgent log level and will always be printed, while CAER\_LOG\_DEBUG is the least urgent log level and will only be delivered if configured by the user.

#### 4.23.2.7 #define CAER\_LOG\_NOTICE (5)

Log levels for [caerLog\(\)](#) logging function. Log messages only get printed if their log level is equal or above the global system log level, which can be set with [caerLogLevelSet\(\)](#). The default log level is CAER\_LOG\_ERROR. CAER\_LOG\_EMERGENCY is the most urgent log level and will always be printed, while CAER\_LOG\_DEBUG is the least urgent log level and will only be delivered if configured by the user.

#### 4.23.2.8 #define CAER\_LOG\_WARNING (4)

Log levels for [caerLog\(\)](#) logging function. Log messages only get printed if their log level is equal or above the global system log level, which can be set with [caerLogLevelSet\(\)](#). The default log level is CAER\_LOG\_ERROR. CAER\_LOG\_EMERGENCY is the most urgent log level and will always be printed, while CAER\_LOG\_DEBUG is the least urgent log level and will only be delivered if configured by the user.

### 4.23.3 Function Documentation

#### 4.23.3.1 void caerLog ( uint8\_t *logLevel*, const char \* *subSystem*, const char \* *format*, ... )

Main logging function. This function takes messages, formats them and sends them out to a file descriptor, respecting the system-wide log level setting and prepending the current time, the log level and a user-specified common string to the actual formatted output. The format is specified exactly as with the printf() family of functions. Please see their manual-page for more information.

##### Parameters

<i>logLevel</i>	the message-specific log level.
<i>subSystem</i>	a common, user-specified string to prepend before the message.
<i>format</i>	the message format string (see printf()).
...	the parameters to be formatted according to the format string (see printf()).

#### 4.23.3.2 void caerLogFileDescriptorsSet ( int *fd1*, int *fd2* )

Set to which file descriptors log messages are sent. Up to two different file descriptors can be configured here. By default logging to STDERR only is enabled. If both file descriptors are identical, logging to it will only happen once, as if the second one was disabled.

##### Parameters

<i>fd1</i>	first file descriptor to log to. A negative value will disable it.
<i>fd2</i>	second file descriptor to log to. A negative value will disable it.

#### 4.23.3.3 uint8\_t caerLogLevelGet ( void )

Get the current system-wide log level. Log messages are only printed if their level is equal or above this level.

##### Returns

the current system-wide log level.

#### 4.23.3.4 void caerLogLevelSet ( uint8\_t *logLevel* )

Set the system-wide log level. Log messages will only be printed if their level is equal or above this level.

##### Parameters

<i>logLevel</i>	the system-wide log level.
-----------------	----------------------------

## 4.24 portable\_endian.h File Reference

### 4.24.1 Detailed Description

Endianness conversion functions for a wide variety of systems, including Linux, FreeBSD, MacOS X and Windows.



# Index

APS\_EXPOSURE\_END  
    special.h, [224](#)

APS\_EXPOSURE\_START  
    special.h, [224](#)

APS\_FRAME\_END  
    special.h, [224](#)

APS\_FRAME\_START  
    special.h, [224](#)

BGRG  
    frame.h, [140](#)

BWRG  
    frame.h, [140](#)

CAER\_CONFIGURATION\_ITERATOR\_ALL\_END  
    config.h, [123](#)

CAER\_CONFIGURATION\_ITERATOR\_ALL\_START  
    config.h, [123](#)

CAER\_CONFIGURATION\_ITERATOR\_VALID\_END  
    config.h, [123](#)

CAER\_CONFIGURATION\_ITERATOR\_VALID\_START  
    config.h, [124](#)

CAER\_DEVICE\_DAVIS\_FX2  
    davis.h, [20](#)

CAER\_DEVICE\_DAVIS\_FX3  
    davis.h, [20](#)

CAER\_DEVICE\_DVS128  
    dvs128.h, [98](#)

CAER\_DEVICE\_DYNAPSE  
    dynapse.h, [102](#)

CAER\_EAR\_ITERATOR\_ALL\_END  
    ear.h, [130](#)

CAER\_EAR\_ITERATOR\_ALL\_START  
    ear.h, [130](#)

CAER\_EAR\_ITERATOR\_VALID\_END  
    ear.h, [130](#)

CAER\_EAR\_ITERATOR\_VALID\_START  
    ear.h, [130](#)

CAER\_EVENT\_PACKET\_CONTAINER\_ITERATOR↔  
    \_END  
    packetContainer.h, [173](#)

CAER\_EVENT\_PACKET\_CONTAINER\_ITERATOR↔  
    \_START  
    packetContainer.h, [173](#)

CAER\_EVENT\_PACKET\_HEADER\_SIZE  
    common.h, [113](#)

CAER\_FRAME\_ITERATOR\_ALL\_END  
    frame.h, [137](#)

CAER\_FRAME\_ITERATOR\_ALL\_START  
    frame.h, [137](#)

CAER\_FRAME\_ITERATOR\_VALID\_END  
    frame.h, [138](#)

CAER\_FRAME\_ITERATOR\_VALID\_START  
    frame.h, [138](#)

CAER\_FRAME\_REVERSE\_ITERATOR\_ALL\_END  
    frame.h, [138](#)

CAER\_FRAME\_REVERSE\_ITERATOR\_ALL\_START  
    frame.h, [138](#)

CAER\_FRAME\_REVERSE\_ITERATOR\_VALID\_END  
    frame.h, [138](#)

CAER\_FRAME\_REVERSE\_ITERATOR\_VALID\_STA↔  
    RT  
    frame.h, [139](#)

CAER\_HOST\_CONFIG\_DATAEXCHANGE\_BLOCKI↔  
    NG  
    usb.h, [106](#)

CAER\_HOST\_CONFIG\_DATAEXCHANGE\_BUF↔  
    R\_SIZE  
    usb.h, [107](#)

CAER\_HOST\_CONFIG\_DATAEXCHANGE\_START↔  
    \_PRODUCERS  
    usb.h, [107](#)

CAER\_HOST\_CONFIG\_DATAEXCHANGE\_STOP↔  
    PRODUCERS  
    usb.h, [107](#)

CAER\_HOST\_CONFIG\_DATAEXCHANGE  
    usb.h, [106](#)

CAER\_HOST\_CONFIG\_PACKETS\_MAX\_CONTAIN↔  
    ER\_INTERVAL  
    usb.h, [107](#)

CAER\_HOST\_CONFIG\_PACKETS\_MAX\_CONTAIN↔  
    ER\_PACKET\_SIZE  
    usb.h, [107](#)

CAER\_HOST\_CONFIG\_PACKETS  
    usb.h, [107](#)

CAER\_HOST\_CONFIG\_USB\_BUFFER\_NUMBER  
    usb.h, [108](#)

CAER\_HOST\_CONFIG\_USB\_BUFFER\_SIZE  
    usb.h, [108](#)

CAER\_HOST\_CONFIG\_USB  
    usb.h, [107](#)

CAER\_IMU6\_ITERATOR\_ALL\_END  
    imu6.h, [155](#)

CAER\_IMU6\_ITERATOR\_ALL\_START  
    imu6.h, [155](#)

CAER\_IMU6\_ITERATOR\_VALID\_END  
    imu6.h, [156](#)

CAER\_IMU6\_ITERATOR\_VALID\_START  
    imu6.h, [156](#)

CAER\_IMU9\_ITERATOR\_ALL\_END  
     imu9.h, [163](#)  
 CAER\_IMU9\_ITERATOR\_ALL\_START  
     imu9.h, [163](#)  
 CAER\_IMU9\_ITERATOR\_VALID\_END  
     imu9.h, [164](#)  
 CAER\_IMU9\_ITERATOR\_VALID\_START  
     imu9.h, [164](#)  
 CAER\_ITERATOR\_ALL\_END  
     common.h, [113](#)  
 CAER\_ITERATOR\_ALL\_START  
     common.h, [113](#)  
 CAER\_ITERATOR\_VALID\_END  
     common.h, [113](#)  
 CAER\_ITERATOR\_VALID\_START  
     common.h, [113](#)  
 CAER\_LOG\_ALERT  
     log.h, [242](#)  
 CAER\_LOG\_CRITICAL  
     log.h, [242](#)  
 CAER\_LOG\_DEBUG  
     log.h, [242](#)  
 CAER\_LOG\_EMERGENCY  
     log.h, [242](#)  
 CAER\_LOG\_ERROR  
     log.h, [242](#)  
 CAER\_LOG\_INFO  
     log.h, [243](#)  
 CAER\_LOG\_NOTICE  
     log.h, [243](#)  
 CAER\_LOG\_WARNING  
     log.h, [243](#)  
 CAER\_POINT1D\_ITERATOR\_ALL\_END  
     point1d.h, [179](#)  
 CAER\_POINT1D\_ITERATOR\_ALL\_START  
     point1d.h, [179](#)  
 CAER\_POINT1D\_ITERATOR\_VALID\_END  
     point1d.h, [179](#)  
 CAER\_POINT1D\_ITERATOR\_VALID\_START  
     point1d.h, [179](#)  
 CAER\_POINT2D\_ITERATOR\_ALL\_END  
     point2d.h, [185](#)  
 CAER\_POINT2D\_ITERATOR\_ALL\_START  
     point2d.h, [185](#)  
 CAER\_POINT2D\_ITERATOR\_VALID\_END  
     point2d.h, [185](#)  
 CAER\_POINT2D\_ITERATOR\_VALID\_START  
     point2d.h, [186](#)  
 CAER\_POINT3D\_ITERATOR\_ALL\_END  
     point3d.h, [192](#)  
 CAER\_POINT3D\_ITERATOR\_ALL\_START  
     point3d.h, [192](#)  
 CAER\_POINT3D\_ITERATOR\_VALID\_END  
     point3d.h, [193](#)  
 CAER\_POINT3D\_ITERATOR\_VALID\_START  
     point3d.h, [193](#)  
 CAER\_POINT4D\_ITERATOR\_ALL\_END  
     point4d.h, [200](#)  
 CAER\_POINT4D\_ITERATOR\_ALL\_START  
     point4d.h, [200](#)  
 CAER\_POINT4D\_ITERATOR\_VALID\_END  
     point4d.h, [200](#)  
 CAER\_POINT4D\_ITERATOR\_VALID\_START  
     point4d.h, [201](#)  
 CAER\_POLARITY\_ITERATOR\_ALL\_END  
     polarity.h, [208](#)  
 CAER\_POLARITY\_ITERATOR\_ALL\_START  
     polarity.h, [208](#)  
 CAER\_POLARITY\_ITERATOR\_VALID\_END  
     polarity.h, [209](#)  
 CAER\_POLARITY\_ITERATOR\_VALID\_START  
     polarity.h, [209](#)  
 CAER\_POLARITY\_REVERSE\_ITERATOR\_ALL\_END  
     polarity.h, [209](#)  
 CAER\_POLARITY\_REVERSE\_ITERATOR\_ALL\_START  
     polarity.h, [209](#)  
 CAER\_POLARITY\_REVERSE\_ITERATOR\_VALID\_END  
     polarity.h, [209](#)  
 CAER\_POLARITY\_REVERSE\_ITERATOR\_VALID\_START  
     polarity.h, [210](#)  
 CAER\_SAMPLE\_ITERATOR\_ALL\_END  
     sample.h, [216](#)  
 CAER\_SAMPLE\_ITERATOR\_ALL\_START  
     sample.h, [216](#)  
 CAER\_SAMPLE\_ITERATOR\_VALID\_END  
     sample.h, [216](#)  
 CAER\_SAMPLE\_ITERATOR\_VALID\_START  
     sample.h, [216](#)  
 CAER\_SPECIAL\_ITERATOR\_ALL\_END  
     special.h, [222](#)  
 CAER\_SPECIAL\_ITERATOR\_ALL\_START  
     special.h, [222](#)  
 CAER\_SPECIAL\_ITERATOR\_VALID\_END  
     special.h, [223](#)  
 CAER\_SPECIAL\_ITERATOR\_VALID\_START  
     special.h, [223](#)  
 CAER\_SPIKE\_ITERATOR\_ALL\_END  
     spike.h, [230](#)  
 CAER\_SPIKE\_ITERATOR\_ALL\_START  
     spike.h, [230](#)  
 CAER\_SPIKE\_ITERATOR\_VALID\_END  
     spike.h, [230](#)  
 CAER\_SPIKE\_ITERATOR\_VALID\_START  
     spike.h, [230](#)  
 CHANNEL\_MASK  
     ear.h, [130](#)  
 CHANNEL\_SHIFT  
     ear.h, [131](#)  
 CLEAR\_NUMBITS16  
     libcaer.h, [238](#)  
 CLEAR\_NUMBITS32  
     libcaer.h, [238](#)  
 CLEAR\_NUMBITS8

libcaer.h, 238  
COLOR\_CHANNELS\_MASK  
  frame.h, 139  
COLOR\_CHANNELS\_SHIFT  
  frame.h, 139  
COLOR\_FILTER\_MASK  
  frame.h, 139  
COLOR\_FILTER\_SHIFT  
  frame.h, 139  
CONFIG\_EVENT  
  common.h, 114  
caer\_bias\_coarsefine, 5  
caer\_bias\_shiftedsources, 5  
caer\_bias\_shiftedsources\_operating\_mode  
  davis.h, 95  
caer\_bias\_shiftedsources\_voltage\_level  
  davis.h, 95  
caer\_bias\_vdac, 6  
caer\_davis\_info, 6  
caer\_default\_event\_types  
  common.h, 114  
caer\_dvs128\_info, 8  
caer\_dynapse\_info, 8  
caer\_frame\_event\_color\_channels  
  frame.h, 140  
caer\_frame\_event\_color\_filter  
  frame.h, 140  
caer\_special\_event\_types  
  special.h, 224  
caerBiasCoarseFineGenerate  
  davis.h, 95  
caerBiasCoarseFineParse  
  davis.h, 95  
caerBiasShiftedSourceGenerate  
  davis.h, 96  
caerBiasShiftedSourceParse  
  davis.h, 96  
caerBiasVDACGenerate  
  davis.h, 96  
caerBiasVDACParse  
  davis.h, 97  
caerByteArrayToInteger  
  libcaer.h, 240  
caerCleanEventPacket  
  common.h, 115  
caerConfigurationEvent  
  config.h, 124  
caerConfigurationEventGetModuleAddress  
  config.h, 124  
caerConfigurationEventGetParameter  
  config.h, 125  
caerConfigurationEventGetParameterAddress  
  config.h, 125  
caerConfigurationEventGetTimestamp  
  config.h, 125  
caerConfigurationEventGetTimestamp64  
  config.h, 126  
caerConfigurationEventInvalidate  
  config.h, 126  
caerConfigurationEventIsValid  
  config.h, 126  
caerConfigurationEventPacket  
  config.h, 124  
caerConfigurationEventPacketAllocate  
  config.h, 126  
caerConfigurationEventPacketGetEvent  
  config.h, 127  
caerConfigurationEventSetModuleAddress  
  config.h, 127  
caerConfigurationEventSetParameter  
  config.h, 127  
caerConfigurationEventSetParameterAddress  
  config.h, 127  
caerConfigurationEventSetTimestamp  
  config.h, 128  
caerConfigurationEventValidate  
  config.h, 128  
caerCopyEventPacket  
  common.h, 115  
caerCopyEventPacketOnlyEvents  
  common.h, 115  
caerCopyEventPacketOnlyValidEvents  
  common.h, 115  
caerDVS128InfoGet  
  dvs128.h, 100  
caerDavisInfoGet  
  davis.h, 97  
caerDeviceClose  
  usb.h, 108  
caerDeviceConfigGet  
  usb.h, 108  
caerDeviceConfigSet  
  usb.h, 109  
caerDeviceDataGet  
  usb.h, 109  
caerDeviceDataStart  
  usb.h, 109  
caerDeviceDataStop  
  usb.h, 110  
caerDeviceHandle  
  usb.h, 108  
caerDeviceOpen  
  usb.h, 110  
caerDeviceSendDefaultConfig  
  usb.h, 111  
caerDynapseInfoGet  
  dynapse.h, 105  
caerEarEvent  
  ear.h, 132  
caerEarEventGetChannel  
  ear.h, 132  
caerEarEventGetEar  
  ear.h, 132  
caerEarEventGetTimestamp  
  ear.h, 132  
caerEarEventGetTimestamp64

- ear.h, 133
- caerEarEventInvalidate
  - ear.h, 133
- caerEarEventIsValid
  - ear.h, 133
- caerEarEventPacket
  - ear.h, 132
- caerEarEventPacketAllocate
  - ear.h, 133
- caerEarEventPacketGetEvent
  - ear.h, 134
- caerEarEventSetChannel
  - ear.h, 134
- caerEarEventSetEar
  - ear.h, 134
- caerEarEventSetTimestamp
  - ear.h, 135
- caerEarEventValidate
  - ear.h, 135
- caerEventPacketContainer
  - packetContainer.h, 174
- caerEventPacketContainerAllocate
  - packetContainer.h, 174
- caerEventPacketContainerCopyAllEvents
  - packetContainer.h, 174
- caerEventPacketContainerCopyValidEvents
  - packetContainer.h, 174
- caerEventPacketContainerFindEventPacketByType
  - packetContainer.h, 175
- caerEventPacketContainerFree
  - packetContainer.h, 175
- caerEventPacketContainerGetEventPacket
  - packetContainer.h, 175
- caerEventPacketContainerGetEventPacketsNumber
  - packetContainer.h, 175
- caerEventPacketContainerGetEventsNumber
  - packetContainer.h, 176
- caerEventPacketContainerGetEventsValidNumber
  - packetContainer.h, 176
- caerEventPacketContainerGetHighestEventTimestamp
  - packetContainer.h, 176
- caerEventPacketContainerGetLowestEventTimestamp
  - packetContainer.h, 177
- caerEventPacketContainerSetEventPacket
  - packetContainer.h, 177
- caerEventPacketContainerSetEventPacketsNumber
  - packetContainer.h, 177
- caerEventPacketHeader
  - common.h, 114
- caerEventPacketHeaderGetEventCapacity
  - common.h, 116
- caerEventPacketHeaderGetEventNumber
  - common.h, 116
- caerEventPacketHeaderGetEventSize
  - common.h, 116
- caerEventPacketHeaderGetEventSource
  - common.h, 116
- caerEventPacketHeaderGetEventTSOffset
  - common.h, 117
- caerEventPacketHeaderGetEventTSOverflow
  - common.h, 117
- caerEventPacketHeaderGetEventType
  - common.h, 117
- caerEventPacketHeaderGetEventValid
  - common.h, 118
- caerEventPacketHeaderSetEventCapacity
  - common.h, 118
- caerEventPacketHeaderSetEventNumber
  - common.h, 118
- caerEventPacketHeaderSetEventSize
  - common.h, 118
- caerEventPacketHeaderSetEventSource
  - common.h, 119
- caerEventPacketHeaderSetEventTSOffset
  - common.h, 119
- caerEventPacketHeaderSetEventTSOverflow
  - common.h, 119
- caerEventPacketHeaderSetEventType
  - common.h, 120
- caerEventPacketHeaderSetEventValid
  - common.h, 120
- caerFrameEvent
  - frame.h, 140
- caerFrameEventGetChannelNumber
  - frame.h, 141
- caerFrameEventGetColorFilter
  - frame.h, 141
- caerFrameEventGetExposureLength
  - frame.h, 141
- caerFrameEventGetLengthX
  - frame.h, 141
- caerFrameEventGetLengthY
  - frame.h, 142
- caerFrameEventGetPixel
  - frame.h, 142
- caerFrameEventGetPixelArrayUnsafe
  - frame.h, 142
- caerFrameEventGetPixelForChannel
  - frame.h, 143
- caerFrameEventGetPixelForChannelUnsafe
  - frame.h, 143
- caerFrameEventGetPixelUnsafe
  - frame.h, 144
- caerFrameEventGetPixelsMaxIndex
  - frame.h, 143
- caerFrameEventGetPixelsSize
  - frame.h, 144
- caerFrameEventGetPositionX
  - frame.h, 144
- caerFrameEventGetPositionY
  - frame.h, 145
- caerFrameEventGetROIIdentifier
  - frame.h, 145
- caerFrameEventGetTSEndOfExposure
  - frame.h, 146
- caerFrameEventGetTSEndOfExposure64

- frame.h, [146](#)
- caerFrameEventGetTSEndOfFrame
  - frame.h, [146](#)
- caerFrameEventGetTSEndOfFrame64
  - frame.h, [147](#)
- caerFrameEventGetTSSStartOfExposure
  - frame.h, [147](#)
- caerFrameEventGetTSSStartOfExposure64
  - frame.h, [147](#)
- caerFrameEventGetTSSStartOfFrame
  - frame.h, [148](#)
- caerFrameEventGetTSSStartOfFrame64
  - frame.h, [148](#)
- caerFrameEventGetTimestamp
  - frame.h, [145](#)
- caerFrameEventGetTimestamp64
  - frame.h, [145](#)
- caerFrameEventInvalidate
  - frame.h, [148](#)
- caerFrameEventsValid
  - frame.h, [149](#)
- caerFrameEventPacket
  - frame.h, [140](#)
- caerFrameEventPacketAllocate
  - frame.h, [149](#)
- caerFrameEventPacketGetEvent
  - frame.h, [149](#)
- caerFrameEventPacketGetPixelsMaxIndex
  - frame.h, [150](#)
- caerFrameEventPacketGetPixelsSize
  - frame.h, [150](#)
- caerFrameEventSetColorFilter
  - frame.h, [150](#)
- caerFrameEventSetLengthXLengthYChannelNumber
  - frame.h, [151](#)
- caerFrameEventSetPixel
  - frame.h, [151](#)
- caerFrameEventSetPixelForChannel
  - frame.h, [151](#)
- caerFrameEventSetPixelForChannelUnsafe
  - frame.h, [152](#)
- caerFrameEventSetPixelUnsafe
  - frame.h, [152](#)
- caerFrameEventSetPositionX
  - frame.h, [152](#)
- caerFrameEventSetPositionY
  - frame.h, [152](#)
- caerFrameEventSetROIIdentifier
  - frame.h, [153](#)
- caerFrameEventSetTSEndOfExposure
  - frame.h, [153](#)
- caerFrameEventSetTSEndOfFrame
  - frame.h, [153](#)
- caerFrameEventSetTSSStartOfExposure
  - frame.h, [153](#)
- caerFrameEventSetTSSStartOfFrame
  - frame.h, [153](#)
- caerFrameEventValidate
  - frame.h, [154](#)
- caerGenericEventGetEvent
  - common.h, [120](#)
- caerGenericEventGetTimestamp
  - common.h, [120](#)
- caerGenericEventGetTimestamp64
  - common.h, [121](#)
- caerGenericEventsValid
  - common.h, [121](#)
- caerGenericEventPacketAppend
  - common.h, [121](#)
- caerGenericEventPacketGrow
  - common.h, [122](#)
- caerIMU6Event
  - imu6.h, [156](#)
- caerIMU6EventGetAccelX
  - imu6.h, [156](#)
- caerIMU6EventGetAccelY
  - imu6.h, [157](#)
- caerIMU6EventGetAccelZ
  - imu6.h, [157](#)
- caerIMU6EventGetGyroX
  - imu6.h, [157](#)
- caerIMU6EventGetGyroY
  - imu6.h, [157](#)
- caerIMU6EventGetGyroZ
  - imu6.h, [158](#)
- caerIMU6EventGetTemp
  - imu6.h, [158](#)
- caerIMU6EventGetTimestamp
  - imu6.h, [158](#)
- caerIMU6EventGetTimestamp64
  - imu6.h, [159](#)
- caerIMU6EventInvalidate
  - imu6.h, [159](#)
- caerIMU6EventsValid
  - imu6.h, [159](#)
- caerIMU6EventPacket
  - imu6.h, [156](#)
- caerIMU6EventPacketAllocate
  - imu6.h, [159](#)
- caerIMU6EventPacketGetEvent
  - imu6.h, [160](#)
- caerIMU6EventSetAccelX
  - imu6.h, [160](#)
- caerIMU6EventSetAccelY
  - imu6.h, [160](#)
- caerIMU6EventSetAccelZ
  - imu6.h, [160](#)
- caerIMU6EventSetGyroX
  - imu6.h, [161](#)
- caerIMU6EventSetGyroY
  - imu6.h, [161](#)
- caerIMU6EventSetGyroZ
  - imu6.h, [161](#)
- caerIMU6EventSetTemp
  - imu6.h, [161](#)
- caerIMU6EventSetTimestamp
  - imu6.h, [161](#)

- imu6.h, [162](#)
- caerIMU6EventValidate
  - imu6.h, [162](#)
- caerIMU9Event
  - imu9.h, [164](#)
- caerIMU9EventGetAccelX
  - imu9.h, [164](#)
- caerIMU9EventGetAccelY
  - imu9.h, [165](#)
- caerIMU9EventGetAccelZ
  - imu9.h, [165](#)
- caerIMU9EventGetCompX
  - imu9.h, [165](#)
- caerIMU9EventGetCompY
  - imu9.h, [165](#)
- caerIMU9EventGetCompZ
  - imu9.h, [166](#)
- caerIMU9EventGetGyroX
  - imu9.h, [166](#)
- caerIMU9EventGetGyroY
  - imu9.h, [166](#)
- caerIMU9EventGetGyroZ
  - imu9.h, [167](#)
- caerIMU9EventGetTemp
  - imu9.h, [167](#)
- caerIMU9EventGetTimestamp
  - imu9.h, [167](#)
- caerIMU9EventGetTimestamp64
  - imu9.h, [167](#)
- caerIMU9EventInvalidate
  - imu9.h, [168](#)
- caerIMU9EventsIsValid
  - imu9.h, [168](#)
- caerIMU9EventPacket
  - imu9.h, [164](#)
- caerIMU9EventPacketAllocate
  - imu9.h, [168](#)
- caerIMU9EventPacketGetEvent
  - imu9.h, [169](#)
- caerIMU9EventSetAccelX
  - imu9.h, [169](#)
- caerIMU9EventSetAccelY
  - imu9.h, [169](#)
- caerIMU9EventSetAccelZ
  - imu9.h, [169](#)
- caerIMU9EventSetCompX
  - imu9.h, [169](#)
- caerIMU9EventSetCompY
  - imu9.h, [170](#)
- caerIMU9EventSetCompZ
  - imu9.h, [170](#)
- caerIMU9EventSetGyroX
  - imu9.h, [170](#)
- caerIMU9EventSetGyroY
  - imu9.h, [170](#)
- caerIMU9EventSetGyroZ
  - imu9.h, [171](#)
- caerIMU9EventSetTemp
  - imu9.h, [171](#)
- caerIMU9EventSetTimestamp
  - imu9.h, [171](#)
- caerIMU9EventValidate
  - imu9.h, [171](#)
- caerIntegerToByteArray
  - libcaer.h, [240](#)
- caerLog
  - log.h, [243](#)
- caerLogFileDescriptorsSet
  - log.h, [243](#)
- caerLogLevelGet
  - log.h, [244](#)
- caerLogLevelSet
  - log.h, [244](#)
- caerPoint1DEvent
  - point1d.h, [180](#)
- caerPoint1DEventGetScale
  - point1d.h, [180](#)
- caerPoint1DEventGetTimestamp
  - point1d.h, [180](#)
- caerPoint1DEventGetTimestamp64
  - point1d.h, [181](#)
- caerPoint1DEventGetType
  - point1d.h, [181](#)
- caerPoint1DEventGetX
  - point1d.h, [181](#)
- caerPoint1DEventInvalidate
  - point1d.h, [182](#)
- caerPoint1DEventsIsValid
  - point1d.h, [182](#)
- caerPoint1DEventPacket
  - point1d.h, [180](#)
- caerPoint1DEventPacketAllocate
  - point1d.h, [182](#)
- caerPoint1DEventPacketGetEvent
  - point1d.h, [182](#)
- caerPoint1DEventSetScale
  - point1d.h, [183](#)
- caerPoint1DEventSetTimestamp
  - point1d.h, [183](#)
- caerPoint1DEventSetType
  - point1d.h, [183](#)
- caerPoint1DEventSetX
  - point1d.h, [183](#)
- caerPoint1DEventValidate
  - point1d.h, [184](#)
- caerPoint2DEvent
  - point2d.h, [186](#)
- caerPoint2DEventGetScale
  - point2d.h, [187](#)
- caerPoint2DEventGetTimestamp
  - point2d.h, [187](#)
- caerPoint2DEventGetTimestamp64
  - point2d.h, [187](#)
- caerPoint2DEventGetType
  - point2d.h, [188](#)
- caerPoint2DEventGetX

point2d.h, [188](#)  
caerPoint2DEventGetY  
point2d.h, [188](#)  
caerPoint2DEventInvalidate  
point2d.h, [188](#)  
caerPoint2DEventIsValid  
point2d.h, [189](#)  
caerPoint2DEventPacket  
point2d.h, [186](#)  
caerPoint2DEventPacketAllocate  
point2d.h, [189](#)  
caerPoint2DEventPacketGetEvent  
point2d.h, [189](#)  
caerPoint2DEventSetScale  
point2d.h, [189](#)  
caerPoint2DEventSetTimestamp  
point2d.h, [190](#)  
caerPoint2DEventSetType  
point2d.h, [190](#)  
caerPoint2DEventSetX  
point2d.h, [190](#)  
caerPoint2DEventSetY  
point2d.h, [190](#)  
caerPoint2DEventValidate  
point2d.h, [191](#)  
caerPoint3DEvent  
point3d.h, [194](#)  
caerPoint3DEventGetScale  
point3d.h, [194](#)  
caerPoint3DEventGetTimestamp  
point3d.h, [194](#)  
caerPoint3DEventGetTimestamp64  
point3d.h, [195](#)  
caerPoint3DEventGetType  
point3d.h, [195](#)  
caerPoint3DEventGetX  
point3d.h, [195](#)  
caerPoint3DEventGetY  
point3d.h, [196](#)  
caerPoint3DEventGetZ  
point3d.h, [196](#)  
caerPoint3DEventInvalidate  
point3d.h, [196](#)  
caerPoint3DEventIsValid  
point3d.h, [196](#)  
caerPoint3DEventPacket  
point3d.h, [194](#)  
caerPoint3DEventPacketAllocate  
point3d.h, [197](#)  
caerPoint3DEventPacketGetEvent  
point3d.h, [197](#)  
caerPoint3DEventSetScale  
point3d.h, [197](#)  
caerPoint3DEventSetTimestamp  
point3d.h, [198](#)  
caerPoint3DEventSetType  
point3d.h, [198](#)  
caerPoint3DEventSetX  
point3d.h, [198](#)  
caerPoint3DEventSetY  
point3d.h, [198](#)  
caerPoint3DEventSetZ  
point3d.h, [198](#)  
caerPoint3DEventValidate  
point3d.h, [199](#)  
caerPoint4DEvent  
point4d.h, [201](#)  
caerPoint4DEventGetScale  
point4d.h, [202](#)  
caerPoint4DEventGetTimestamp  
point4d.h, [202](#)  
caerPoint4DEventGetTimestamp64  
point4d.h, [202](#)  
caerPoint4DEventGetType  
point4d.h, [203](#)  
caerPoint4DEventGetW  
point4d.h, [203](#)  
caerPoint4DEventGetX  
point4d.h, [203](#)  
caerPoint4DEventGetY  
point4d.h, [203](#)  
caerPoint4DEventGetZ  
point4d.h, [204](#)  
caerPoint4DEventInvalidate  
point4d.h, [204](#)  
caerPoint4DEventIsValid  
point4d.h, [204](#)  
caerPoint4DEventPacket  
point4d.h, [201](#)  
caerPoint4DEventPacketAllocate  
point4d.h, [204](#)  
caerPoint4DEventPacketGetEvent  
point4d.h, [205](#)  
caerPoint4DEventSetScale  
point4d.h, [205](#)  
caerPoint4DEventSetTimestamp  
point4d.h, [205](#)  
caerPoint4DEventSetType  
point4d.h, [206](#)  
caerPoint4DEventSetW  
point4d.h, [206](#)  
caerPoint4DEventSetX  
point4d.h, [206](#)  
caerPoint4DEventSetY  
point4d.h, [206](#)  
caerPoint4DEventSetZ  
point4d.h, [206](#)  
caerPoint4DEventValidate  
point4d.h, [207](#)  
caerPolarityEvent  
polarity.h, [211](#)  
caerPolarityEventGetPolarity  
polarity.h, [211](#)  
caerPolarityEventGetTimestamp  
polarity.h, [211](#)  
caerPolarityEventGetTimestamp64

- polarity.h, [211](#)
- caerPolarityEventGetX
  - polarity.h, [212](#)
- caerPolarityEventGetY
  - polarity.h, [212](#)
- caerPolarityEventInvalidate
  - polarity.h, [212](#)
- caerPolarityEventIsValid
  - polarity.h, [213](#)
- caerPolarityEventPacket
  - polarity.h, [211](#)
- caerPolarityEventPacketAllocate
  - polarity.h, [213](#)
- caerPolarityEventPacketGetEvent
  - polarity.h, [213](#)
- caerPolarityEventSetPolarity
  - polarity.h, [214](#)
- caerPolarityEventSetTimestamp
  - polarity.h, [214](#)
- caerPolarityEventSetX
  - polarity.h, [214](#)
- caerPolarityEventSetY
  - polarity.h, [214](#)
- caerPolarityEventValidate
  - polarity.h, [214](#)
- caerSampleEvent
  - sample.h, [217](#)
- caerSampleEventGetSample
  - sample.h, [217](#)
- caerSampleEventGetTimestamp
  - sample.h, [218](#)
- caerSampleEventGetTimestamp64
  - sample.h, [218](#)
- caerSampleEventGetType
  - sample.h, [218](#)
- caerSampleEventInvalidate
  - sample.h, [219](#)
- caerSampleEventIsValid
  - sample.h, [219](#)
- caerSampleEventPacket
  - sample.h, [217](#)
- caerSampleEventPacketAllocate
  - sample.h, [219](#)
- caerSampleEventPacketGetEvent
  - sample.h, [219](#)
- caerSampleEventSetSample
  - sample.h, [220](#)
- caerSampleEventSetTimestamp
  - sample.h, [220](#)
- caerSampleEventSetType
  - sample.h, [220](#)
- caerSampleEventValidate
  - sample.h, [220](#)
- caerSpecialEvent
  - special.h, [224](#)
- caerSpecialEventGetData
  - special.h, [224](#)
- caerSpecialEventGetTimestamp
  - special.h, [225](#)
- caerSpecialEventGetTimestamp64
  - special.h, [225](#)
- caerSpecialEventGetType
  - special.h, [225](#)
- caerSpecialEventInvalidate
  - special.h, [226](#)
- caerSpecialEventIsValid
  - special.h, [226](#)
- caerSpecialEventPacket
  - special.h, [224](#)
- caerSpecialEventPacketAllocate
  - special.h, [226](#)
- caerSpecialEventPacketFindEventByType
  - special.h, [226](#)
- caerSpecialEventPacketFindValidEventByType
  - special.h, [227](#)
- caerSpecialEventPacketGetEvent
  - special.h, [227](#)
- caerSpecialEventSetData
  - special.h, [227](#)
- caerSpecialEventSetTimestamp
  - special.h, [228](#)
- caerSpecialEventSetType
  - special.h, [228](#)
- caerSpecialEventValidate
  - special.h, [228](#)
- caerSpikeEvent
  - spike.h, [231](#)
- caerSpikeEventGetChipID
  - spike.h, [231](#)
- caerSpikeEventGetNeuronID
  - spike.h, [232](#)
- caerSpikeEventGetSourceCoreID
  - spike.h, [232](#)
- caerSpikeEventGetTimestamp
  - spike.h, [232](#)
- caerSpikeEventGetTimestamp64
  - spike.h, [233](#)
- caerSpikeEventInvalidate
  - spike.h, [233](#)
- caerSpikeEventIsValid
  - spike.h, [233](#)
- caerSpikeEventPacket
  - spike.h, [231](#)
- caerSpikeEventPacketAllocate
  - spike.h, [233](#)
- caerSpikeEventPacketGetEvent
  - spike.h, [234](#)
- caerSpikeEventSetChipID
  - spike.h, [234](#)
- caerSpikeEventSetNeuronID
  - spike.h, [234](#)
- caerSpikeEventSetSourceCoreID
  - spike.h, [234](#)
- caerSpikeEventSetTimestamp
  - spike.h, [235](#)
- caerSpikeEventValidate



- spike.h, 235
- caerStrEquals
  - libcaer.h, 241
- caerStrEqualsUpTo
  - libcaer.h, 241
- common.h
  - CAER\_EVENT\_PACKET\_HEADER\_SIZE, 113
  - CAER\_ITERATOR\_ALL\_END, 113
  - CAER\_ITERATOR\_ALL\_START, 113
  - CAER\_ITERATOR\_VALID\_END, 113
  - CAER\_ITERATOR\_VALID\_START, 113
  - CONFIG\_EVENT, 114
  - caer\_default\_event\_types, 114
  - caerCleanEventPacket, 115
  - caerCopyEventPacket, 115
  - caerCopyEventPacketOnlyEvents, 115
  - caerCopyEventPacketOnlyValidEvents, 115
  - caerEventPacketHeader, 114
  - caerEventPacketHeaderGetEventCapacity, 116
  - caerEventPacketHeaderGetEventNumber, 116
  - caerEventPacketHeaderGetEventSize, 116
  - caerEventPacketHeaderGetEventSource, 116
  - caerEventPacketHeaderGetEventTSOffset, 117
  - caerEventPacketHeaderGetEventTSOverflow, 117
  - caerEventPacketHeaderGetEventType, 117
  - caerEventPacketHeaderGetEventValid, 118
  - caerEventPacketHeaderSetEventCapacity, 118
  - caerEventPacketHeaderSetEventNumber, 118
  - caerEventPacketHeaderSetEventSize, 118
  - caerEventPacketHeaderSetEventSource, 119
  - caerEventPacketHeaderSetEventTSOffset, 119
  - caerEventPacketHeaderSetEventTSOverflow, 119
  - caerEventPacketHeaderSetEventType, 120
  - caerEventPacketHeaderSetEventValid, 120
  - caerGenericEventGetEvent, 120
  - caerGenericEventGetTimestamp, 120
  - caerGenericEventGetTimestamp64, 121
  - caerGenericEventIsValid, 121
  - caerGenericEventPacketAppend, 121
  - caerGenericEventPacketGrow, 122
  - EAR\_EVENT, 114
  - FRAME\_EVENT, 114
  - IMU6\_EVENT, 114
  - IMU9\_EVENT, 114
  - PACKED\_STRUCT, 122
  - POINT1D\_EVENT, 114
  - POINT2D\_EVENT, 114
  - POINT3D\_EVENT, 114
  - POINT4D\_EVENT, 114
  - POLARITY\_EVENT, 114
  - SAMPLE\_EVENT, 114
  - SPECIAL\_EVENT, 114
  - SPIKE\_EVENT, 114
  - TS\_OVERFLOW\_SHIFT, 113
  - VALID\_MARK\_MASK, 114
  - VALID\_MARK\_SHIFT, 114
- config.h
  - CAER\_CONFIGURATION\_ITERATOR\_ALL\_END, 123
  - CAER\_CONFIGURATION\_ITERATOR\_ALL\_START, 123
  - CAER\_CONFIGURATION\_ITERATOR\_VALID\_END, 123
  - CAER\_CONFIGURATION\_ITERATOR\_VALID\_START, 124
  - caerConfigurationEvent, 124
  - caerConfigurationEventGetModuleAddress, 124
  - caerConfigurationEventGetParameter, 125
  - caerConfigurationEventGetParameterAddress, 125
  - caerConfigurationEventGetTimestamp, 125
  - caerConfigurationEventGetTimestamp64, 126
  - caerConfigurationEventInvalidate, 126
  - caerConfigurationEventIsValid, 126
  - caerConfigurationEventPacket, 124
  - caerConfigurationEventPacketAllocate, 126
  - caerConfigurationEventPacketGetEvent, 127
  - caerConfigurationEventSetModuleAddress, 127
  - caerConfigurationEventSetParameter, 127
  - caerConfigurationEventSetParameterAddress, 127
  - caerConfigurationEventSetTimestamp, 128
  - caerConfigurationEventValidate, 128
  - MODULE\_ADDR\_MASK, 124
  - MODULE\_ADDR\_SHIFT, 124
  - PACKED\_STRUCT, 128
- DATA\_MASK
  - special.h, 223
- DATA\_SHIFT
  - special.h, 223
- DAVIS128\_CONFIG\_BIAS\_ADCCOMPBP
  - davis.h, 20
- DAVIS128\_CONFIG\_BIAS\_ADCREFHIGH
  - davis.h, 20
- DAVIS128\_CONFIG\_BIAS\_ADCREFLOW
  - davis.h, 21
- DAVIS128\_CONFIG\_BIAS\_AEPDBN
  - davis.h, 21
- DAVIS128\_CONFIG\_BIAS\_AEPUXBP
  - davis.h, 21
- DAVIS128\_CONFIG\_BIAS\_AEPUYBP
  - davis.h, 21
- DAVIS128\_CONFIG\_BIAS\_APSCAS
  - davis.h, 22
- DAVIS128\_CONFIG\_BIAS\_APSEVERFLOWLEVEL
  - davis.h, 22
- DAVIS128\_CONFIG\_BIAS\_APSROSFBN
  - davis.h, 22
- DAVIS128\_CONFIG\_BIAS\_BIASBUFFER
  - davis.h, 22
- DAVIS128\_CONFIG\_BIAS\_COLSELLOWBN
  - davis.h, 23
- DAVIS128\_CONFIG\_BIAS\_DACBUFBP
  - davis.h, 23
- DAVIS128\_CONFIG\_BIAS\_DIFFBN
  - davis.h, 23
- DAVIS128\_CONFIG\_BIAS\_IFREFRBN

- davis.h, [23](#)
- DAVIS128\_CONFIG\_BIAS\_IFTHRBN
  - davis.h, [24](#)
- DAVIS128\_CONFIG\_BIAS\_LCOLTIMEOUTBN
  - davis.h, [24](#)
- DAVIS128\_CONFIG\_BIAS\_LOCALBUFBN
  - davis.h, [24](#)
- DAVIS128\_CONFIG\_BIAS\_OFFBN
  - davis.h, [24](#)
- DAVIS128\_CONFIG\_BIAS\_ONBN
  - davis.h, [25](#)
- DAVIS128\_CONFIG\_BIAS\_PADFOLLBN
  - davis.h, [25](#)
- DAVIS128\_CONFIG\_BIAS\_PIXINBN
  - davis.h, [25](#)
- DAVIS128\_CONFIG\_BIAS\_PRBP
  - davis.h, [25](#)
- DAVIS128\_CONFIG\_BIAS\_PRSFBP
  - davis.h, [26](#)
- DAVIS128\_CONFIG\_BIAS\_READOUTBUFBP
  - davis.h, [26](#)
- DAVIS128\_CONFIG\_BIAS\_REFRBP
  - davis.h, [26](#)
- DAVIS128\_CONFIG\_BIAS\_SSN
  - davis.h, [26](#)
- DAVIS128\_CONFIG\_BIAS\_SSP
  - davis.h, [27](#)
- DAVIS128\_CONFIG\_CHIP\_AERNAROW
  - davis.h, [27](#)
- DAVIS128\_CONFIG\_CHIP\_ANALOGMUX0
  - davis.h, [27](#)
- DAVIS128\_CONFIG\_CHIP\_ANALOGMUX1
  - davis.h, [27](#)
- DAVIS128\_CONFIG\_CHIP\_ANALOGMUX2
  - davis.h, [27](#)
- DAVIS128\_CONFIG\_CHIP\_BIASMUX0
  - davis.h, [27](#)
- DAVIS128\_CONFIG\_CHIP\_DIGITALMUX0
  - davis.h, [28](#)
- DAVIS128\_CONFIG\_CHIP\_DIGITALMUX1
  - davis.h, [28](#)
- DAVIS128\_CONFIG\_CHIP\_DIGITALMUX2
  - davis.h, [28](#)
- DAVIS128\_CONFIG\_CHIP\_DIGITALMUX3
  - davis.h, [28](#)
- DAVIS128\_CONFIG\_CHIP\_GLOBAL\_SHUTTER
  - davis.h, [28](#)
- DAVIS128\_CONFIG\_CHIP\_RESETCALIBNEURON
  - davis.h, [28](#)
- DAVIS128\_CONFIG\_CHIP\_RESETTESTPIXEL
  - davis.h, [28](#)
- DAVIS128\_CONFIG\_CHIP\_SELECTGRAYCOUNTER
  - davis.h, [28](#)
- DAVIS128\_CONFIG\_CHIP\_TYPENCALIBNEURON
  - davis.h, [29](#)
- DAVIS128\_CONFIG\_CHIP\_USEAOUT
  - davis.h, [29](#)
- DAVIS208\_CONFIG\_BIAS\_ADCCOMPBP
  - davis.h, [29](#)
- DAVIS208\_CONFIG\_BIAS\_ADCCREFHIGH
  - davis.h, [29](#)
- DAVIS208\_CONFIG\_BIAS\_ADCCREFLOW
  - davis.h, [29](#)
- DAVIS208\_CONFIG\_BIAS\_AEPDBN
  - davis.h, [29](#)
- DAVIS208\_CONFIG\_BIAS\_AEPUXBP
  - davis.h, [30](#)
- DAVIS208\_CONFIG\_BIAS\_AEPUYBP
  - davis.h, [30](#)
- DAVIS208\_CONFIG\_BIAS\_APSCAS
  - davis.h, [30](#)
- DAVIS208\_CONFIG\_BIAS\_APSEVERFLOWLEVEL
  - davis.h, [30](#)
- DAVIS208\_CONFIG\_BIAS\_APSROSFBN
  - davis.h, [31](#)
- DAVIS208\_CONFIG\_BIAS\_BIASBUFFER
  - davis.h, [31](#)
- DAVIS208\_CONFIG\_BIAS\_COLSEOLLOWBN
  - davis.h, [31](#)
- DAVIS208\_CONFIG\_BIAS\_DACBUFBP
  - davis.h, [31](#)
- DAVIS208\_CONFIG\_BIAS\_DIFFBN
  - davis.h, [32](#)
- DAVIS208\_CONFIG\_BIAS\_IFREFRBN
  - davis.h, [32](#)
- DAVIS208\_CONFIG\_BIAS\_IFTHRBN
  - davis.h, [32](#)
- DAVIS208\_CONFIG\_BIAS\_LCOLTIMEOUTBN
  - davis.h, [32](#)
- DAVIS208\_CONFIG\_BIAS\_LOCALBUFBN
  - davis.h, [33](#)
- DAVIS208\_CONFIG\_BIAS\_OFFBN
  - davis.h, [33](#)
- DAVIS208\_CONFIG\_BIAS\_ONBN
  - davis.h, [33](#)
- DAVIS208\_CONFIG\_BIAS\_PADFOLLBN
  - davis.h, [33](#)
- DAVIS208\_CONFIG\_BIAS\_PIXINBN
  - davis.h, [34](#)
- DAVIS208\_CONFIG\_BIAS\_PRBP
  - davis.h, [34](#)
- DAVIS208\_CONFIG\_BIAS\_PRSFBP
  - davis.h, [34](#)
- DAVIS208\_CONFIG\_BIAS\_READOUTBUFBP
  - davis.h, [34](#)
- DAVIS208\_CONFIG\_BIAS\_REFRBP
  - davis.h, [35](#)
- DAVIS208\_CONFIG\_BIAS\_REFSSBN
  - davis.h, [35](#)
- DAVIS208\_CONFIG\_BIAS\_REFSS
  - davis.h, [35](#)
- DAVIS208\_CONFIG\_BIAS\_REGBIASBP
  - davis.h, [35](#)
- DAVIS208\_CONFIG\_BIAS\_RESETHIGHPASS
  - davis.h, [36](#)
- DAVIS208\_CONFIG\_BIAS\_SSN
  - davis.h, [36](#)

- davis.h, [36](#)  
DAVIS208\_CONFIG\_BIAS\_SSP  
davis.h, [36](#)  
DAVIS208\_CONFIG\_CHIP\_AERNAROW  
davis.h, [36](#)  
DAVIS208\_CONFIG\_CHIP\_ANALOGMUX0  
davis.h, [37](#)  
DAVIS208\_CONFIG\_CHIP\_ANALOGMUX1  
davis.h, [37](#)  
DAVIS208\_CONFIG\_CHIP\_ANALOGMUX2  
davis.h, [37](#)  
DAVIS208\_CONFIG\_CHIP\_BIASMUX0  
davis.h, [37](#)  
DAVIS208\_CONFIG\_CHIP\_DIGITALMUX0  
davis.h, [37](#)  
DAVIS208\_CONFIG\_CHIP\_DIGITALMUX1  
davis.h, [37](#)  
DAVIS208\_CONFIG\_CHIP\_DIGITALMUX2  
davis.h, [37](#)  
DAVIS208\_CONFIG\_CHIP\_DIGITALMUX3  
davis.h, [37](#)  
DAVIS208\_CONFIG\_CHIP\_GLOBAL\_SHUTTER  
davis.h, [38](#)  
DAVIS208\_CONFIG\_CHIP\_RESETCALIBNEURON  
davis.h, [38](#)  
DAVIS208\_CONFIG\_CHIP\_RESETTESTPIXEL  
davis.h, [38](#)  
DAVIS208\_CONFIG\_CHIP\_SELECTBIASREFSS  
davis.h, [38](#)  
DAVIS208\_CONFIG\_CHIP\_SELECTGRAYCOUNTER  
davis.h, [38](#)  
DAVIS208\_CONFIG\_CHIP\_SELECTHIGHPASS  
davis.h, [38](#)  
DAVIS208\_CONFIG\_CHIP\_SELECTPOSF  
davis.h, [38](#)  
DAVIS208\_CONFIG\_CHIP\_SELECTPREMPAVG  
davis.h, [38](#)  
DAVIS208\_CONFIG\_CHIP\_SELECTSENSE  
davis.h, [39](#)  
DAVIS208\_CONFIG\_CHIP\_TYPCALIBNEURON  
davis.h, [39](#)  
DAVIS208\_CONFIG\_CHIP\_USEAOUT  
davis.h, [39](#)  
DAVIS240\_CONFIG\_BIAS\_AEPDBN  
davis.h, [39](#)  
DAVIS240\_CONFIG\_BIAS\_AEPUXBP  
davis.h, [39](#)  
DAVIS240\_CONFIG\_BIAS\_AEPUYBP  
davis.h, [39](#)  
DAVIS240\_CONFIG\_BIAS\_APSCASEPC  
davis.h, [40](#)  
DAVIS240\_CONFIG\_BIAS\_APSOEVERFLOWLEVELBN  
davis.h, [40](#)  
DAVIS240\_CONFIG\_BIAS\_APSROSFBN  
davis.h, [40](#)  
DAVIS240\_CONFIG\_BIAS\_BIASBUFFER  
davis.h, [40](#)  
DAVIS240\_CONFIG\_BIAS\_DIFFBN  
davis.h, [40](#)  
DAVIS240\_CONFIG\_BIAS\_DIFFCASBNC  
davis.h, [41](#)  
DAVIS240\_CONFIG\_BIAS\_IFREFRBN  
davis.h, [41](#)  
DAVIS240\_CONFIG\_BIAS\_IFTHRBN  
davis.h, [41](#)  
DAVIS240\_CONFIG\_BIAS\_LCOLTIMEOUTBN  
davis.h, [41](#)  
DAVIS240\_CONFIG\_BIAS\_LOCALBUFBN  
davis.h, [41](#)  
DAVIS240\_CONFIG\_BIAS\_OFFBN  
davis.h, [42](#)  
DAVIS240\_CONFIG\_BIAS\_ONBN  
davis.h, [42](#)  
DAVIS240\_CONFIG\_BIAS\_PADFOLLBN  
davis.h, [42](#)  
DAVIS240\_CONFIG\_BIAS\_PIXINVBN  
davis.h, [42](#)  
DAVIS240\_CONFIG\_BIAS\_PRBP  
davis.h, [42](#)  
DAVIS240\_CONFIG\_BIAS\_PRSFBP  
davis.h, [43](#)  
DAVIS240\_CONFIG\_BIAS\_REFRBP  
davis.h, [43](#)  
DAVIS240\_CONFIG\_BIAS\_SSN  
davis.h, [43](#)  
DAVIS240\_CONFIG\_BIAS\_SSP  
davis.h, [43](#)  
DAVIS240\_CONFIG\_CHIP\_AERNAROW  
davis.h, [43](#)  
DAVIS240\_CONFIG\_CHIP\_ANALOGMUX0  
davis.h, [44](#)  
DAVIS240\_CONFIG\_CHIP\_ANALOGMUX1  
davis.h, [44](#)  
DAVIS240\_CONFIG\_CHIP\_ANALOGMUX2  
davis.h, [44](#)  
DAVIS240\_CONFIG\_CHIP\_BIASMUX0  
davis.h, [44](#)  
DAVIS240\_CONFIG\_CHIP\_DIGITALMUX0  
davis.h, [44](#)  
DAVIS240\_CONFIG\_CHIP\_DIGITALMUX1  
davis.h, [44](#)  
DAVIS240\_CONFIG\_CHIP\_DIGITALMUX2  
davis.h, [44](#)  
DAVIS240\_CONFIG\_CHIP\_DIGITALMUX3  
davis.h, [45](#)  
DAVIS240\_CONFIG\_CHIP\_GLOBAL\_SHUTTER  
davis.h, [45](#)  
DAVIS240\_CONFIG\_CHIP\_RESETCALIBNEURON  
davis.h, [45](#)  
DAVIS240\_CONFIG\_CHIP\_RESETTESTPIXEL  
davis.h, [45](#)  
DAVIS240\_CONFIG\_CHIP\_SPECIALPIXELCONTROL  
davis.h, [45](#)  
DAVIS240\_CONFIG\_CHIP\_TYPCALIBNEURON  
davis.h, [45](#)  
DAVIS240\_CONFIG\_CHIP\_USEAOUT

- davis.h, [45](#)
- DAVIS346\_CONFIG\_BIAS\_ADCCOMPBP
  - davis.h, [46](#)
- DAVIS346\_CONFIG\_BIAS\_ADCREFHIGH
  - davis.h, [46](#)
- DAVIS346\_CONFIG\_BIAS\_ADCREFLOW
  - davis.h, [46](#)
- DAVIS346\_CONFIG\_BIAS\_ADCTESTVOLTAGE
  - davis.h, [46](#)
- DAVIS346\_CONFIG\_BIAS\_AEPDBN
  - davis.h, [46](#)
- DAVIS346\_CONFIG\_BIAS\_AEPUXBP
  - davis.h, [47](#)
- DAVIS346\_CONFIG\_BIAS\_AEPUYBP
  - davis.h, [47](#)
- DAVIS346\_CONFIG\_BIAS\_APSCAS
  - davis.h, [47](#)
- DAVIS346\_CONFIG\_BIAS\_APSOEVERFLOWLEVEL
  - davis.h, [47](#)
- DAVIS346\_CONFIG\_BIAS\_APSROSFBN
  - davis.h, [48](#)
- DAVIS346\_CONFIG\_BIAS\_BIASBUFFER
  - davis.h, [48](#)
- DAVIS346\_CONFIG\_BIAS\_COLSELLOWBN
  - davis.h, [48](#)
- DAVIS346\_CONFIG\_BIAS\_DACBUFBP
  - davis.h, [48](#)
- DAVIS346\_CONFIG\_BIAS\_DIFFBN
  - davis.h, [49](#)
- DAVIS346\_CONFIG\_BIAS\_IFREFRBN
  - davis.h, [49](#)
- DAVIS346\_CONFIG\_BIAS\_IFTHRBN
  - davis.h, [49](#)
- DAVIS346\_CONFIG\_BIAS\_LCOLTIMEOUTBN
  - davis.h, [49](#)
- DAVIS346\_CONFIG\_BIAS\_LOCALBUFBN
  - davis.h, [50](#)
- DAVIS346\_CONFIG\_BIAS\_OFFBN
  - davis.h, [50](#)
- DAVIS346\_CONFIG\_BIAS\_ONBN
  - davis.h, [50](#)
- DAVIS346\_CONFIG\_BIAS\_PADFOLLBN
  - davis.h, [50](#)
- DAVIS346\_CONFIG\_BIAS\_PIXINBN
  - davis.h, [51](#)
- DAVIS346\_CONFIG\_BIAS\_PRBP
  - davis.h, [51](#)
- DAVIS346\_CONFIG\_BIAS\_PRSFBP
  - davis.h, [51](#)
- DAVIS346\_CONFIG\_BIAS\_READOUTBUFBP
  - davis.h, [51](#)
- DAVIS346\_CONFIG\_BIAS\_REFRBP
  - davis.h, [52](#)
- DAVIS346\_CONFIG\_BIAS\_SSN
  - davis.h, [52](#)
- DAVIS346\_CONFIG\_BIAS\_SSP
  - davis.h, [52](#)
- DAVIS346\_CONFIG\_CHIP\_AERNAROW
  - davis.h, [52](#)
- DAVIS346\_CONFIG\_CHIP\_ANALOGMUX0
  - davis.h, [53](#)
- DAVIS346\_CONFIG\_CHIP\_ANALOGMUX1
  - davis.h, [53](#)
- DAVIS346\_CONFIG\_CHIP\_ANALOGMUX2
  - davis.h, [53](#)
- DAVIS346\_CONFIG\_CHIP\_BIASMUX0
  - davis.h, [53](#)
- DAVIS346\_CONFIG\_CHIP\_DIGITALMUX0
  - davis.h, [53](#)
- DAVIS346\_CONFIG\_CHIP\_DIGITALMUX1
  - davis.h, [53](#)
- DAVIS346\_CONFIG\_CHIP\_DIGITALMUX2
  - davis.h, [53](#)
- DAVIS346\_CONFIG\_CHIP\_DIGITALMUX3
  - davis.h, [53](#)
- DAVIS346\_CONFIG\_CHIP\_GLOBAL\_SHUTTER
  - davis.h, [54](#)
- DAVIS346\_CONFIG\_CHIP\_RESETCALIBNEURON
  - davis.h, [54](#)
- DAVIS346\_CONFIG\_CHIP\_RESETTESTPIXEL
  - davis.h, [54](#)
- DAVIS346\_CONFIG\_CHIP\_SELECTGRAYCOUNTER
  - davis.h, [54](#)
- DAVIS346\_CONFIG\_CHIP\_TESTADC
  - davis.h, [54](#)
- DAVIS346\_CONFIG\_CHIP\_TYPENCALIBNEURON
  - davis.h, [54](#)
- DAVIS346\_CONFIG\_CHIP\_USEAOUT
  - davis.h, [54](#)
- DAVIS640\_CONFIG\_BIAS\_ADCCOMPBP
  - davis.h, [54](#)
- DAVIS640\_CONFIG\_BIAS\_ADCREFHIGH
  - davis.h, [55](#)
- DAVIS640\_CONFIG\_BIAS\_ADCREFLOW
  - davis.h, [55](#)
- DAVIS640\_CONFIG\_BIAS\_ADCTESTVOLTAGE
  - davis.h, [55](#)
- DAVIS640\_CONFIG\_BIAS\_AEPDBN
  - davis.h, [55](#)
- DAVIS640\_CONFIG\_BIAS\_AEPUXBP
  - davis.h, [56](#)
- DAVIS640\_CONFIG\_BIAS\_AEPUYBP
  - davis.h, [56](#)
- DAVIS640\_CONFIG\_BIAS\_APSCAS
  - davis.h, [56](#)
- DAVIS640\_CONFIG\_BIAS\_APSOEVERFLOWLEVEL
  - davis.h, [56](#)
- DAVIS640\_CONFIG\_BIAS\_APSROSFBN
  - davis.h, [57](#)
- DAVIS640\_CONFIG\_BIAS\_BIASBUFFER
  - davis.h, [57](#)
- DAVIS640\_CONFIG\_BIAS\_COLSELLOWBN
  - davis.h, [57](#)
- DAVIS640\_CONFIG\_BIAS\_DACBUFBP
  - davis.h, [57](#)
- DAVIS640\_CONFIG\_BIAS\_DIFFBN
  - davis.h, [57](#)

- davis.h, [58](#)
- DAVIS640\_CONFIG\_BIAS\_IFREFRBN
  - davis.h, [58](#)
- DAVIS640\_CONFIG\_BIAS\_IFTHRBN
  - davis.h, [58](#)
- DAVIS640\_CONFIG\_BIAS\_LCOLTIMEOUTBN
  - davis.h, [58](#)
- DAVIS640\_CONFIG\_BIAS\_LOCALBUFBN
  - davis.h, [59](#)
- DAVIS640\_CONFIG\_BIAS\_OFFBN
  - davis.h, [59](#)
- DAVIS640\_CONFIG\_BIAS\_ONBN
  - davis.h, [59](#)
- DAVIS640\_CONFIG\_BIAS\_PADFOLLBN
  - davis.h, [59](#)
- DAVIS640\_CONFIG\_BIAS\_PIXINVBN
  - davis.h, [60](#)
- DAVIS640\_CONFIG\_BIAS\_PRBP
  - davis.h, [60](#)
- DAVIS640\_CONFIG\_BIAS\_PRSFBP
  - davis.h, [60](#)
- DAVIS640\_CONFIG\_BIAS\_READOUTBUFBP
  - davis.h, [60](#)
- DAVIS640\_CONFIG\_BIAS\_REFRBP
  - davis.h, [61](#)
- DAVIS640\_CONFIG\_BIAS\_SSN
  - davis.h, [61](#)
- DAVIS640\_CONFIG\_BIAS\_SSP
  - davis.h, [61](#)
- DAVIS640\_CONFIG\_CHIP\_AERNAROW
  - davis.h, [61](#)
- DAVIS640\_CONFIG\_CHIP\_ANALOGMUX0
  - davis.h, [62](#)
- DAVIS640\_CONFIG\_CHIP\_ANALOGMUX1
  - davis.h, [62](#)
- DAVIS640\_CONFIG\_CHIP\_ANALOGMUX2
  - davis.h, [62](#)
- DAVIS640\_CONFIG\_CHIP\_BIASMUX0
  - davis.h, [62](#)
- DAVIS640\_CONFIG\_CHIP\_DIGITALMUX0
  - davis.h, [62](#)
- DAVIS640\_CONFIG\_CHIP\_DIGITALMUX1
  - davis.h, [62](#)
- DAVIS640\_CONFIG\_CHIP\_DIGITALMUX2
  - davis.h, [62](#)
- DAVIS640\_CONFIG\_CHIP\_DIGITALMUX3
  - davis.h, [62](#)
- DAVIS640\_CONFIG\_CHIP\_GLOBAL\_SHUTTER
  - davis.h, [63](#)
- DAVIS640\_CONFIG\_CHIP\_RESETCALIBNEURON
  - davis.h, [63](#)
- DAVIS640\_CONFIG\_CHIP\_RESETTESTPIXEL
  - davis.h, [63](#)
- DAVIS640\_CONFIG\_CHIP\_SELECTGRAYCOUNTER
  - davis.h, [63](#)
- DAVIS640\_CONFIG\_CHIP\_TESTADC
  - davis.h, [63](#)
- DAVIS640\_CONFIG\_CHIP\_TYPENCALIBNEURON
  - davis.h, [63](#)
- DAVIS640\_CONFIG\_CHIP\_USEAOUT
  - davis.h, [63](#)
- DAVIS\_CHIP\_DAVIS128
  - davis.h, [63](#)
- DAVIS\_CHIP\_DAVIS208
  - davis.h, [64](#)
- DAVIS\_CHIP\_DAVIS240A
  - davis.h, [64](#)
- DAVIS\_CHIP\_DAVIS240B
  - davis.h, [64](#)
- DAVIS\_CHIP\_DAVIS240C
  - davis.h, [64](#)
- DAVIS\_CHIP\_DAVIS346A
  - davis.h, [64](#)
- DAVIS\_CHIP\_DAVIS346B
  - davis.h, [64](#)
- DAVIS\_CHIP\_DAVIS346C
  - davis.h, [64](#)
- DAVIS\_CHIP\_DAVIS640
  - davis.h, [64](#)
- DAVIS\_CHIP\_DAVISRGB
  - davis.h, [64](#)
- DAVIS\_CONFIG\_APS\_ADC\_TEST\_MODE
  - davis.h, [65](#)
- DAVIS\_CONFIG\_APS\_COLOR\_FILTER
  - davis.h, [65](#)
- DAVIS\_CONFIG\_APS\_COLUMN\_SETTLE
  - davis.h, [65](#)
- DAVIS\_CONFIG\_APS\_END\_COLUMN\_0
  - davis.h, [65](#)
- DAVIS\_CONFIG\_APS\_END\_COLUMN\_1
  - davis.h, [65](#)
- DAVIS\_CONFIG\_APS\_END\_COLUMN\_2
  - davis.h, [65](#)
- DAVIS\_CONFIG\_APS\_END\_COLUMN\_3
  - davis.h, [65](#)
- DAVIS\_CONFIG\_APS\_END\_ROW\_0
  - davis.h, [65](#)
- DAVIS\_CONFIG\_APS\_END\_ROW\_1
  - davis.h, [65](#)
- DAVIS\_CONFIG\_APS\_END\_ROW\_2
  - davis.h, [66](#)
- DAVIS\_CONFIG\_APS\_END\_ROW\_3
  - davis.h, [66](#)
- DAVIS\_CONFIG\_APS\_EXPOSURE
  - davis.h, [66](#)
- DAVIS\_CONFIG\_APS\_FRAME\_DELAY
  - davis.h, [66](#)
- DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER
  - davis.h, [66](#)
- DAVIS\_CONFIG\_APS\_HAS\_EXTERNAL\_ADC
  - davis.h, [66](#)
- DAVIS\_CONFIG\_APS\_HAS\_GLOBAL\_SHUTTER
  - davis.h, [66](#)
- DAVIS\_CONFIG\_APS\_HAS\_INTERNAL\_ADC
  - davis.h, [66](#)
- DAVIS\_CONFIG\_APS\_HAS\_QUAD\_ROI

- davis.h, [67](#)
- DAVIS\_CONFIG\_APS\_NULL\_SETTLE
  - davis.h, [67](#)
- DAVIS\_CONFIG\_APS\_ORIENTATION\_INFO
  - davis.h, [67](#)
- DAVIS\_CONFIG\_APS\_RAMP\_RESET
  - davis.h, [67](#)
- DAVIS\_CONFIG\_APS\_RAMP\_SHORT\_RESET
  - davis.h, [67](#)
- DAVIS\_CONFIG\_APS\_RESET\_READ
  - davis.h, [67](#)
- DAVIS\_CONFIG\_APS\_RESET\_SETTLE
  - davis.h, [67](#)
- DAVIS\_CONFIG\_APS\_ROW\_SETTLE
  - davis.h, [67](#)
- DAVIS\_CONFIG\_APS\_RUN
  - davis.h, [68](#)
- DAVIS\_CONFIG\_APS\_SAMPLE\_ENABLE
  - davis.h, [68](#)
- DAVIS\_CONFIG\_APS\_SAMPLE\_SETTLE
  - davis.h, [68](#)
- DAVIS\_CONFIG\_APS\_SIZE\_COLUMNS
  - davis.h, [68](#)
- DAVIS\_CONFIG\_APS\_SIZE\_ROWS
  - davis.h, [68](#)
- DAVIS\_CONFIG\_APS\_SNAPSHOT
  - davis.h, [68](#)
- DAVIS\_CONFIG\_APS\_START\_COLUMN\_0
  - davis.h, [68](#)
- DAVIS\_CONFIG\_APS\_START\_COLUMN\_1
  - davis.h, [68](#)
- DAVIS\_CONFIG\_APS\_START\_COLUMN\_2
  - davis.h, [69](#)
- DAVIS\_CONFIG\_APS\_START\_COLUMN\_3
  - davis.h, [69](#)
- DAVIS\_CONFIG\_APS\_START\_ROW\_0
  - davis.h, [69](#)
- DAVIS\_CONFIG\_APS\_START\_ROW\_1
  - davis.h, [69](#)
- DAVIS\_CONFIG\_APS\_START\_ROW\_2
  - davis.h, [69](#)
- DAVIS\_CONFIG\_APS\_START\_ROW\_3
  - davis.h, [69](#)
- DAVIS\_CONFIG\_APS\_USE\_INTERNAL\_ADC
  - davis.h, [69](#)
- DAVIS\_CONFIG\_APS\_WAIT\_ON\_TRANSFER\_STALL
  - davis.h, [69](#)
- DAVIS\_CONFIG\_APS
  - davis.h, [64](#)
- DAVIS\_CONFIG\_BIAS
  - davis.h, [70](#)
- DAVIS\_CONFIG\_CHIP
  - davis.h, [70](#)
- DAVIS\_CONFIG\_DVS\_ACK\_DELAY\_COLUMN
  - davis.h, [70](#)
- DAVIS\_CONFIG\_DVS\_ACK\_DELAY\_ROW
  - davis.h, [70](#)
- DAVIS\_CONFIG\_DVS\_ACK\_EXTENSION\_COLUMN
  - davis.h, [70](#)
- DAVIS\_CONFIG\_DVS\_ACK\_EXTENSION\_ROW
  - davis.h, [70](#)
- DAVIS\_CONFIG\_DVS\_EXTERNAL\_AER\_CONTROL
  - davis.h, [70](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_BACKGROUND\_ACTIVITY\_DELTAT
  - davis.h, [71](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_BACKGROUND\_ACTIVITY
  - davis.h, [71](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_0\_COLUMN
  - davis.h, [71](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_0\_ROW
  - davis.h, [71](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_1\_COLUMN
  - davis.h, [71](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_1\_ROW
  - davis.h, [71](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_2\_COLUMN
  - davis.h, [71](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_2\_ROW
  - davis.h, [71](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_3\_COLUMN
  - davis.h, [72](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_3\_ROW
  - davis.h, [72](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_4\_COLUMN
  - davis.h, [72](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_4\_ROW
  - davis.h, [72](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_5\_COLUMN
  - davis.h, [72](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_5\_ROW
  - davis.h, [72](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_6\_COLUMN
  - davis.h, [72](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_6\_ROW
  - davis.h, [72](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_7\_COLUMN
  - davis.h, [72](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_7\_ROW
  - davis.h, [73](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_ROW\_ONLY\_EVENT\_GENERATOR
  - davis.h, [73](#)
- DAVIS\_CONFIG\_DVS\_ORIENTATION\_INFO
  - davis.h, [73](#)
- DAVIS\_CONFIG\_DVS\_RUN
  - davis.h, [73](#)



- DAVIS\_CONFIG\_DVS\_SIZE\_COLUMNS
  - davis.h, [73](#)
- DAVIS\_CONFIG\_DVS\_SIZE\_ROWS
  - davis.h, [74](#)
- DAVIS\_CONFIG\_DVS\_TEST\_EVENT\_GENERATOR\_ENABLE
  - davis.h, [74](#)
- DAVIS\_CONFIG\_DVS\_WAIT\_ON\_TRANSFER\_STALL
  - davis.h, [74](#)
- DAVIS\_CONFIG\_DVS
  - davis.h, [70](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_FALLING\_EDGE1
  - davis.h, [74](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_FALLING\_EDGE2
  - davis.h, [74](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_FALLING\_EDGE3
  - davis.h, [74](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_LENGTH1
  - davis.h, [75](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_LENGTH2
  - davis.h, [75](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_LENGTH
  - davis.h, [75](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_POLARITY1
  - davis.h, [75](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_POLARITY2
  - davis.h, [75](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_POLARITY
  - davis.h, [75](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSES1
  - davis.h, [76](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSES2
  - davis.h, [76](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSES
  - davis.h, [75](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_RISING\_EDGE1
  - davis.h, [76](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_RISING\_EDGE2
  - davis.h, [76](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_RISING\_EDGE3
  - davis.h, [76](#)
- DAVIS\_CONFIG\_EXTINPUT\_GENERATE\_INJECT\_ON\_FALLING\_EDGE
  - davis.h, [76](#)
- DAVIS\_CONFIG\_EXTINPUT\_GENERATE\_INJECT\_ON\_RISING\_EDGE
  - davis.h, [76](#)
- DAVIS\_CONFIG\_EXTINPUT\_GENERATE\_PULSE\_INTERVAL
  - davis.h, [76](#)
- DAVIS\_CONFIG\_EXTINPUT\_GENERATE\_PULSE\_LENGTH
  - davis.h, [77](#)
- DAVIS\_CONFIG\_EXTINPUT\_GENERATE\_PULSE\_POLARITY
  - davis.h, [77](#)
- DAVIS\_CONFIG\_EXTINPUT\_GENERATE\_USE\_CUSTOM\_SIGNAL
  - davis.h, [77](#)
- DAVIS\_CONFIG\_EXTINPUT\_HAS\_EXTRA\_DETECTORS
  - davis.h, [77](#)
- DAVIS\_CONFIG\_EXTINPUT\_HAS\_GENERATOR
  - davis.h, [77](#)
- DAVIS\_CONFIG\_EXTINPUT\_RUN\_DETECTOR1
  - davis.h, [77](#)
- DAVIS\_CONFIG\_EXTINPUT\_RUN\_DETECTOR2
  - davis.h, [78](#)
- DAVIS\_CONFIG\_EXTINPUT\_RUN\_DETECTOR
  - davis.h, [77](#)
- DAVIS\_CONFIG\_EXTINPUT\_RUN\_GENERATOR
  - davis.h, [78](#)
- DAVIS\_CONFIG\_EXTINPUT
  - davis.h, [74](#)
- DAVIS\_CONFIG\_IMU\_ACCEL\_FULL\_SCALE
  - davis.h, [78](#)
- DAVIS\_CONFIG\_IMU\_ACCEL\_STANDBY
  - davis.h, [78](#)
- DAVIS\_CONFIG\_IMU\_DIGITAL\_LOW\_PASS\_FILTER
  - davis.h, [78](#)
- DAVIS\_CONFIG\_IMU\_GYRO\_FULL\_SCALE
  - davis.h, [78](#)
- DAVIS\_CONFIG\_IMU\_GYRO\_STANDBY
  - davis.h, [79](#)
- DAVIS\_CONFIG\_IMU\_LP\_CYCLE
  - davis.h, [79](#)
- DAVIS\_CONFIG\_IMU\_LP\_WAKEUP
  - davis.h, [79](#)
- DAVIS\_CONFIG\_IMU\_RUN
  - davis.h, [79](#)
- DAVIS\_CONFIG\_IMU\_SAMPLE\_RATE\_DIVIDER
  - davis.h, [79](#)
- DAVIS\_CONFIG\_IMU\_TEMP\_STANDBY
  - davis.h, [79](#)
- DAVIS\_CONFIG\_IMU
  - davis.h, [78](#)
- DAVIS\_CONFIG\_MUX\_DROP\_APS\_ON\_TRANSFER\_STALL
  - davis.h, [79](#)
- DAVIS\_CONFIG\_MUX\_DROP\_DVS\_ON\_TRANSFER\_STALL
  - davis.h, [80](#)
- DAVIS\_CONFIG\_MUX\_DROP\_EXTINPUT\_ON\_TRANSFER\_STALL

- davis.h, [80](#)
- DAVIS\_CONFIG\_MUX\_DROP\_IMU\_ON\_TRANSFE←  
R\_STALL
- davis.h, [80](#)
- DAVIS\_CONFIG\_MUX\_FORCE\_CHIP\_BIAS\_ENABLE
- davis.h, [80](#)
- DAVIS\_CONFIG\_MUX\_RUN
- davis.h, [80](#)
- DAVIS\_CONFIG\_MUX\_TIMESTAMP\_RESET
- davis.h, [80](#)
- DAVIS\_CONFIG\_MUX\_TIMESTAMP\_RUN
- davis.h, [80](#)
- DAVIS\_CONFIG\_MUX
- davis.h, [79](#)
- DAVIS\_CONFIG\_SYSINFO\_ADC\_CLOCK
- davis.h, [81](#)
- DAVIS\_CONFIG\_SYSINFO\_CHIP\_IDENTIFIER
- davis.h, [81](#)
- DAVIS\_CONFIG\_SYSINFO\_DEVICE\_IS\_MASTER
- davis.h, [81](#)
- DAVIS\_CONFIG\_SYSINFO\_LOGIC\_CLOCK
- davis.h, [81](#)
- DAVIS\_CONFIG\_SYSINFO\_LOGIC\_VERSION
- davis.h, [81](#)
- DAVIS\_CONFIG\_SYSINFO
- davis.h, [80](#)
- DAVIS\_CONFIG\_USB\_EARLY\_PACKET\_DELAY
- davis.h, [81](#)
- DAVIS\_CONFIG\_USB\_RUN
- davis.h, [82](#)
- DAVIS\_CONFIG\_USB
- davis.h, [81](#)
- DAVISRGB\_CONFIG\_APS\_GSFDRESET
- davis.h, [82](#)
- DAVISRGB\_CONFIG\_APS\_GSPDRESET
- davis.h, [82](#)
- DAVISRGB\_CONFIG\_APS\_GSRESETFALL
- davis.h, [82](#)
- DAVISRGB\_CONFIG\_APS\_GSTXFALL
- davis.h, [82](#)
- DAVISRGB\_CONFIG\_APS\_RSFDSETTLE
- davis.h, [82](#)
- DAVISRGB\_CONFIG\_APS\_TRANSFER
- davis.h, [82](#)
- DAVISRGB\_CONFIG\_BIAS\_ADCCOMPBP
- davis.h, [82](#)
- DAVISRGB\_CONFIG\_BIAS\_ADCREFHIGH
- davis.h, [83](#)
- DAVISRGB\_CONFIG\_BIAS\_ADCREFLOW
- davis.h, [83](#)
- DAVISRGB\_CONFIG\_BIAS\_ADCTESTVOLTAGE
- davis.h, [83](#)
- DAVISRGB\_CONFIG\_BIAS\_AEPDBN
- davis.h, [83](#)
- DAVISRGB\_CONFIG\_BIAS\_AEPUXBP
- davis.h, [84](#)
- DAVISRGB\_CONFIG\_BIAS\_AEPUYBP
- davis.h, [84](#)
- DAVISRGB\_CONFIG\_BIAS\_APSCAS
- davis.h, [84](#)
- DAVISRGB\_CONFIG\_BIAS\_APSROSFBN
- davis.h, [84](#)
- DAVISRGB\_CONFIG\_BIAS\_ARRAYBIASBUFFERBN
- davis.h, [85](#)
- DAVISRGB\_CONFIG\_BIAS\_ARRAYLOGICBUFFER←  
BN
- davis.h, [85](#)
- DAVISRGB\_CONFIG\_BIAS\_BIASBUFFER
- davis.h, [85](#)
- DAVISRGB\_CONFIG\_BIAS\_DACBUFBP
- davis.h, [85](#)
- DAVISRGB\_CONFIG\_BIAS\_DIFFBN
- davis.h, [86](#)
- DAVISRGB\_CONFIG\_BIAS\_FALLTIMEBN
- davis.h, [86](#)
- DAVISRGB\_CONFIG\_BIAS\_GND07
- davis.h, [86](#)
- DAVISRGB\_CONFIG\_BIAS\_IFREFRBN
- davis.h, [86](#)
- DAVISRGB\_CONFIG\_BIAS\_IFTHRBN
- davis.h, [87](#)
- DAVISRGB\_CONFIG\_BIAS\_LCOLTIMEOUTBN
- davis.h, [87](#)
- DAVISRGB\_CONFIG\_BIAS\_LOCALBUFBN
- davis.h, [87](#)
- DAVISRGB\_CONFIG\_BIAS\_OFFBN
- davis.h, [87](#)
- DAVISRGB\_CONFIG\_BIAS\_ONBN
- davis.h, [88](#)
- DAVISRGB\_CONFIG\_BIAS\_OVG1LO
- davis.h, [88](#)
- DAVISRGB\_CONFIG\_BIAS\_OVG2LO
- davis.h, [88](#)
- DAVISRGB\_CONFIG\_BIAS\_PADFOLLBN
- davis.h, [88](#)
- DAVISRGB\_CONFIG\_BIAS\_PIXINVBN
- davis.h, [89](#)
- DAVISRGB\_CONFIG\_BIAS\_PRBP
- davis.h, [89](#)
- DAVISRGB\_CONFIG\_BIAS\_PRSFBP
- davis.h, [89](#)
- DAVISRGB\_CONFIG\_BIAS\_READOUTBUFBP
- davis.h, [89](#)
- DAVISRGB\_CONFIG\_BIAS\_REFRBP
- davis.h, [90](#)
- DAVISRGB\_CONFIG\_BIAS\_RISETIMEBP
- davis.h, [90](#)
- DAVISRGB\_CONFIG\_BIAS\_SSN
- davis.h, [90](#)
- DAVISRGB\_CONFIG\_BIAS\_SSP
- davis.h, [90](#)
- DAVISRGB\_CONFIG\_BIAS\_TX2OVG2HI
- davis.h, [91](#)
- DAVISRGB\_CONFIG\_CHIP\_ADJUSTOVG1LO
- davis.h, [91](#)
- DAVISRGB\_CONFIG\_CHIP\_ADJUSTOVG2LO



- davis.h, [91](#)
- DAVISRGB\_CONFIG\_CHIP\_ADJUSTTX2OVG2HI
  - davis.h, [91](#)
- DAVISRGB\_CONFIG\_CHIP\_AERNAROW
  - davis.h, [91](#)
- DAVISRGB\_CONFIG\_CHIP\_ANALOGMUX0
  - davis.h, [91](#)
- DAVISRGB\_CONFIG\_CHIP\_ANALOGMUX1
  - davis.h, [92](#)
- DAVISRGB\_CONFIG\_CHIP\_ANALOGMUX2
  - davis.h, [92](#)
- DAVISRGB\_CONFIG\_CHIP\_BIASMUX0
  - davis.h, [92](#)
- DAVISRGB\_CONFIG\_CHIP\_DIGITALMUX0
  - davis.h, [92](#)
- DAVISRGB\_CONFIG\_CHIP\_DIGITALMUX1
  - davis.h, [92](#)
- DAVISRGB\_CONFIG\_CHIP\_DIGITALMUX2
  - davis.h, [92](#)
- DAVISRGB\_CONFIG\_CHIP\_DIGITALMUX3
  - davis.h, [92](#)
- DAVISRGB\_CONFIG\_CHIP\_RESETCALIBNEURON
  - davis.h, [92](#)
- DAVISRGB\_CONFIG\_CHIP\_RESETTESTPIXEL
  - davis.h, [93](#)
- DAVISRGB\_CONFIG\_CHIP\_SELECTGRAYCOUNT↔
  - ER
  - davis.h, [93](#)
- DAVISRGB\_CONFIG\_CHIP\_TESTADC
  - davis.h, [93](#)
- DAVISRGB\_CONFIG\_CHIP\_TYPCENCALIBNEURON
  - davis.h, [93](#)
- DAVISRGB\_CONFIG\_CHIP\_USEAOUT
  - davis.h, [93](#)
- DOUBLE\_DIODE
  - davis.h, [95](#)
- DVS128\_CONFIG\_BIAS\_CAS
  - dvs128.h, [98](#)
- DVS128\_CONFIG\_BIAS\_DIFFOFF
  - dvs128.h, [99](#)
- DVS128\_CONFIG\_BIAS\_DIFFON
  - dvs128.h, [99](#)
- DVS128\_CONFIG\_BIAS\_DIFF
  - dvs128.h, [98](#)
- DVS128\_CONFIG\_BIAS\_FOLL
  - dvs128.h, [99](#)
- DVS128\_CONFIG\_BIAS\_INJGND
  - dvs128.h, [99](#)
- DVS128\_CONFIG\_BIAS\_PUX
  - dvs128.h, [99](#)
- DVS128\_CONFIG\_BIAS\_PUY
  - dvs128.h, [99](#)
- DVS128\_CONFIG\_BIAS\_PR
  - dvs128.h, [99](#)
- DVS128\_CONFIG\_BIAS\_REFR
  - dvs128.h, [99](#)
- DVS128\_CONFIG\_BIAS\_REQPD
  - dvs128.h, [100](#)
- DVS128\_CONFIG\_BIAS\_REQ
  - dvs128.h, [99](#)
- DVS128\_CONFIG\_BIAS
  - dvs128.h, [98](#)
- DVS128\_CONFIG\_DVS\_ARRAY\_RESET
  - dvs128.h, [100](#)
- DVS128\_CONFIG\_DVS\_RUN
  - dvs128.h, [100](#)
- DVS128\_CONFIG\_DVS\_TIMESTAMP\_RESET
  - dvs128.h, [100](#)
- DVS128\_CONFIG\_DVS\_TS\_MASTER
  - dvs128.h, [100](#)
- DVS128\_CONFIG\_DVS
  - dvs128.h, [100](#)
- DVS\_ROW\_ONLY
  - special.h, [224](#)
- DYNAPSE\_CHIP\_DYNAPSE
  - dynapse.h, [102](#)
- DYNAPSE\_CONFIG\_AER\_ACK\_DELAY
  - dynapse.h, [102](#)
- DYNAPSE\_CONFIG\_AER\_ACK\_EXTENSION
  - dynapse.h, [102](#)
- DYNAPSE\_CONFIG\_AER\_EXTERNAL\_AER\_CONT↔
  - ROL
  - dynapse.h, [102](#)
- DYNAPSE\_CONFIG\_AER\_RUN
  - dynapse.h, [102](#)
- DYNAPSE\_CONFIG\_AER\_WAIT\_ON\_TRANSFER↔
  - STALL
  - dynapse.h, [102](#)
- DYNAPSE\_CONFIG\_AER
  - dynapse.h, [102](#)
- DYNAPSE\_CONFIG\_CHIP\_CONTENT
  - dynapse.h, [103](#)
- DYNAPSE\_CONFIG\_CHIP\_ID
  - dynapse.h, [103](#)
- DYNAPSE\_CONFIG\_CHIP\_REQ\_DELAY
  - dynapse.h, [103](#)
- DYNAPSE\_CONFIG\_CHIP\_REQ\_EXTENSION
  - dynapse.h, [103](#)
- DYNAPSE\_CONFIG\_CHIP\_RUN
  - dynapse.h, [103](#)
- DYNAPSE\_CONFIG\_CHIP
  - dynapse.h, [103](#)
- DYNAPSE\_CONFIG\_MUX\_DROP\_AER\_ON\_TRAN↔
  - SFER\_STALL
  - dynapse.h, [103](#)
- DYNAPSE\_CONFIG\_MUX\_FORCE\_CHIP\_BIAS\_E↔
  - NABLE
  - dynapse.h, [104](#)
- DYNAPSE\_CONFIG\_MUX\_RUN
  - dynapse.h, [104](#)
- DYNAPSE\_CONFIG\_MUX\_TIMESTAMP\_RESET
  - dynapse.h, [104](#)
- DYNAPSE\_CONFIG\_MUX\_TIMESTAMP\_RUN
  - dynapse.h, [104](#)
- DYNAPSE\_CONFIG\_MUX
  - dynapse.h, [103](#)

- DYNAPSE\_CONFIG\_SYSINFO\_CHIP\_IDENTIFIER  
dynapse.h, [104](#)
- DYNAPSE\_CONFIG\_SYSINFO\_DEVICE\_IS\_MASTER  
dynapse.h, [104](#)
- DYNAPSE\_CONFIG\_SYSINFO\_LOGIC\_CLOCK  
dynapse.h, [104](#)
- DYNAPSE\_CONFIG\_SYSINFO\_LOGIC\_VERSION  
dynapse.h, [105](#)
- DYNAPSE\_CONFIG\_SYSINFO  
dynapse.h, [104](#)
- DYNAPSE\_CONFIG\_USB\_EARLY\_PACKET\_DELAY  
dynapse.h, [105](#)
- DYNAPSE\_CONFIG\_USB\_RUN  
dynapse.h, [105](#)
- DYNAPSE\_CONFIG\_USB  
dynapse.h, [105](#)
- davis.h
  - CAER\_DEVICE\_DAVIS\_FX2, [20](#)
  - CAER\_DEVICE\_DAVIS\_FX3, [20](#)
  - caer\_bias\_shiftedsourcesource\_operating\_mode, [95](#)
  - caer\_bias\_shiftedsourcesource\_voltage\_level, [95](#)
  - caerBiasCoarseFineGenerate, [95](#)
  - caerBiasCoarseFineParse, [95](#)
  - caerBiasShiftedSourceGenerate, [96](#)
  - caerBiasShiftedSourceParse, [96](#)
  - caerBiasVDACGenerate, [96](#)
  - caerBiasVDACParse, [97](#)
  - caerDavisInfoGet, [97](#)
  - DAVIS128\_CONFIG\_BIAS\_ADCCOMPBP, [20](#)
  - DAVIS128\_CONFIG\_BIAS\_ADCREFHIGH, [20](#)
  - DAVIS128\_CONFIG\_BIAS\_ADCREFLOW, [21](#)
  - DAVIS128\_CONFIG\_BIAS\_AEPDBN, [21](#)
  - DAVIS128\_CONFIG\_BIAS\_AEPUXBP, [21](#)
  - DAVIS128\_CONFIG\_BIAS\_AEPUYBP, [21](#)
  - DAVIS128\_CONFIG\_BIAS\_APSCAS, [22](#)
  - DAVIS128\_CONFIG\_BIAS\_APSOEVERFLOWLEVEL, [22](#)
  - DAVIS128\_CONFIG\_BIAS\_APSROSFBN, [22](#)
  - DAVIS128\_CONFIG\_BIAS\_BIASBUFFER, [22](#)
  - DAVIS128\_CONFIG\_BIAS\_COLSELLOWBN, [23](#)
  - DAVIS128\_CONFIG\_BIAS\_DACBUFBP, [23](#)
  - DAVIS128\_CONFIG\_BIAS\_DIFFBN, [23](#)
  - DAVIS128\_CONFIG\_BIAS\_IFREFRBN, [23](#)
  - DAVIS128\_CONFIG\_BIAS\_IFTHRBN, [24](#)
  - DAVIS128\_CONFIG\_BIAS\_LCOLTIMEOUTBN, [24](#)
  - DAVIS128\_CONFIG\_BIAS\_LOCALBUFBN, [24](#)
  - DAVIS128\_CONFIG\_BIAS\_OFFBN, [24](#)
  - DAVIS128\_CONFIG\_BIAS\_ONBN, [25](#)
  - DAVIS128\_CONFIG\_BIAS\_PADFOLLBN, [25](#)
  - DAVIS128\_CONFIG\_BIAS\_PIXINBN, [25](#)
  - DAVIS128\_CONFIG\_BIAS\_PRBP, [25](#)
  - DAVIS128\_CONFIG\_BIAS\_PRSFBN, [26](#)
  - DAVIS128\_CONFIG\_BIAS\_READOUTBUFBP, [26](#)
  - DAVIS128\_CONFIG\_BIAS\_REFRBN, [26](#)
  - DAVIS128\_CONFIG\_BIAS\_SSN, [26](#)
  - DAVIS128\_CONFIG\_BIAS\_SSP, [27](#)
  - DAVIS128\_CONFIG\_CHIP\_AERNAROW, [27](#)
  - DAVIS128\_CONFIG\_CHIP\_ANALOGMUX0, [27](#)
  - DAVIS128\_CONFIG\_CHIP\_ANALOGMUX1, [27](#)
  - DAVIS128\_CONFIG\_CHIP\_ANALOGMUX2, [27](#)
  - DAVIS128\_CONFIG\_CHIP\_BIASMUX0, [27](#)
  - DAVIS128\_CONFIG\_CHIP\_DIGITALMUX0, [28](#)
  - DAVIS128\_CONFIG\_CHIP\_DIGITALMUX1, [28](#)
  - DAVIS128\_CONFIG\_CHIP\_DIGITALMUX2, [28](#)
  - DAVIS128\_CONFIG\_CHIP\_DIGITALMUX3, [28](#)
  - DAVIS128\_CONFIG\_CHIP\_GLOBAL\_SHUTTER, [28](#)
  - DAVIS128\_CONFIG\_CHIP\_RESETCALIBNEURON, [28](#)
  - DAVIS128\_CONFIG\_CHIP\_RESETTESTPIXEL, [28](#)
  - DAVIS128\_CONFIG\_CHIP\_SELECTGRAYCONTROLLER, [28](#)
  - DAVIS128\_CONFIG\_CHIP\_TYPENCALIBNEURON, [29](#)
  - DAVIS128\_CONFIG\_CHIP\_USEAOUT, [29](#)
  - DAVIS208\_CONFIG\_BIAS\_ADCCOMPBP, [29](#)
  - DAVIS208\_CONFIG\_BIAS\_ADCREFHIGH, [29](#)
  - DAVIS208\_CONFIG\_BIAS\_ADCREFLOW, [29](#)
  - DAVIS208\_CONFIG\_BIAS\_AEPDBN, [29](#)
  - DAVIS208\_CONFIG\_BIAS\_AEPUXBP, [30](#)
  - DAVIS208\_CONFIG\_BIAS\_AEPUYBP, [30](#)
  - DAVIS208\_CONFIG\_BIAS\_APSCAS, [30](#)
  - DAVIS208\_CONFIG\_BIAS\_APSOEVERFLOWLEVEL, [30](#)
  - DAVIS208\_CONFIG\_BIAS\_APSROSFBN, [31](#)
  - DAVIS208\_CONFIG\_BIAS\_BIASBUFFER, [31](#)
  - DAVIS208\_CONFIG\_BIAS\_COLSELLOWBN, [31](#)
  - DAVIS208\_CONFIG\_BIAS\_DACBUFBP, [31](#)
  - DAVIS208\_CONFIG\_BIAS\_DIFFBN, [32](#)
  - DAVIS208\_CONFIG\_BIAS\_IFREFRBN, [32](#)
  - DAVIS208\_CONFIG\_BIAS\_IFTHRBN, [32](#)
  - DAVIS208\_CONFIG\_BIAS\_LCOLTIMEOUTBN, [32](#)
  - DAVIS208\_CONFIG\_BIAS\_LOCALBUFBN, [33](#)
  - DAVIS208\_CONFIG\_BIAS\_OFFBN, [33](#)
  - DAVIS208\_CONFIG\_BIAS\_ONBN, [33](#)
  - DAVIS208\_CONFIG\_BIAS\_PADFOLLBN, [33](#)
  - DAVIS208\_CONFIG\_BIAS\_PIXINBN, [34](#)
  - DAVIS208\_CONFIG\_BIAS\_PRBP, [34](#)
  - DAVIS208\_CONFIG\_BIAS\_PRSFBN, [34](#)
  - DAVIS208\_CONFIG\_BIAS\_READOUTBUFBP, [34](#)
  - DAVIS208\_CONFIG\_BIAS\_REFRBN, [35](#)
  - DAVIS208\_CONFIG\_BIAS\_REFSSBN, [35](#)
  - DAVIS208\_CONFIG\_BIAS\_REFSS, [35](#)
  - DAVIS208\_CONFIG\_BIAS\_REGBIASBP, [35](#)
  - DAVIS208\_CONFIG\_BIAS\_RESETHIGHPASS, [36](#)
  - DAVIS208\_CONFIG\_BIAS\_SSN, [36](#)
  - DAVIS208\_CONFIG\_BIAS\_SSP, [36](#)
  - DAVIS208\_CONFIG\_CHIP\_AERNAROW, [36](#)
  - DAVIS208\_CONFIG\_CHIP\_ANALOGMUX0, [37](#)
  - DAVIS208\_CONFIG\_CHIP\_ANALOGMUX1, [37](#)
  - DAVIS208\_CONFIG\_CHIP\_ANALOGMUX2, [37](#)
  - DAVIS208\_CONFIG\_CHIP\_BIASMUX0, [37](#)

- DAVIS208\_CONFIG\_CHIP\_DIGITALMUX0, [37](#)
- DAVIS208\_CONFIG\_CHIP\_DIGITALMUX1, [37](#)
- DAVIS208\_CONFIG\_CHIP\_DIGITALMUX2, [37](#)
- DAVIS208\_CONFIG\_CHIP\_DIGITALMUX3, [37](#)
- DAVIS208\_CONFIG\_CHIP\_GLOBAL\_SHUTTER, [38](#)
- DAVIS208\_CONFIG\_CHIP\_RESETCALIBNEU↔RON, [38](#)
- DAVIS208\_CONFIG\_CHIP\_RESETTESTPIXEL, [38](#)
- DAVIS208\_CONFIG\_CHIP\_SELECTBIASREFSS, [38](#)
- DAVIS208\_CONFIG\_CHIP\_SELECTGRAYCO↔UNTER, [38](#)
- DAVIS208\_CONFIG\_CHIP\_SELECTHIGHPASS, [38](#)
- DAVIS208\_CONFIG\_CHIP\_SELECTPOSF, [38](#)
- DAVIS208\_CONFIG\_CHIP\_SELECTPREAMPA↔VG, [38](#)
- DAVIS208\_CONFIG\_CHIP\_SELECTSENSE, [39](#)
- DAVIS208\_CONFIG\_CHIP\_TYPENCALIBNEU↔RON, [39](#)
- DAVIS208\_CONFIG\_CHIP\_USEAOUT, [39](#)
- DAVIS240\_CONFIG\_BIAS\_AEPDBN, [39](#)
- DAVIS240\_CONFIG\_BIAS\_AEPUXBP, [39](#)
- DAVIS240\_CONFIG\_BIAS\_AEPUYBP, [39](#)
- DAVIS240\_CONFIG\_BIAS\_APSCASEPC, [40](#)
- DAVIS240\_CONFIG\_BIAS\_APSEVERFLOWLE↔VELBN, [40](#)
- DAVIS240\_CONFIG\_BIAS\_APSROSFBN, [40](#)
- DAVIS240\_CONFIG\_BIAS\_BIASBUFFER, [40](#)
- DAVIS240\_CONFIG\_BIAS\_DIFFBN, [40](#)
- DAVIS240\_CONFIG\_BIAS\_DIFFCASBNC, [41](#)
- DAVIS240\_CONFIG\_BIAS\_IFREFRBN, [41](#)
- DAVIS240\_CONFIG\_BIAS\_IFTHRBN, [41](#)
- DAVIS240\_CONFIG\_BIAS\_LCOLTIMEOUTBN, [41](#)
- DAVIS240\_CONFIG\_BIAS\_LOCALBUFBN, [41](#)
- DAVIS240\_CONFIG\_BIAS\_OFFBN, [42](#)
- DAVIS240\_CONFIG\_BIAS\_ONBN, [42](#)
- DAVIS240\_CONFIG\_BIAS\_PADFOLLBN, [42](#)
- DAVIS240\_CONFIG\_BIAS\_PIXINBN, [42](#)
- DAVIS240\_CONFIG\_BIAS\_PRBP, [42](#)
- DAVIS240\_CONFIG\_BIAS\_PRSEFBP, [43](#)
- DAVIS240\_CONFIG\_BIAS\_REFRBP, [43](#)
- DAVIS240\_CONFIG\_BIAS\_SSN, [43](#)
- DAVIS240\_CONFIG\_BIAS\_SSP, [43](#)
- DAVIS240\_CONFIG\_CHIP\_AERNAROW, [43](#)
- DAVIS240\_CONFIG\_CHIP\_ANALOGMUX0, [44](#)
- DAVIS240\_CONFIG\_CHIP\_ANALOGMUX1, [44](#)
- DAVIS240\_CONFIG\_CHIP\_ANALOGMUX2, [44](#)
- DAVIS240\_CONFIG\_CHIP\_BIASMUX0, [44](#)
- DAVIS240\_CONFIG\_CHIP\_DIGITALMUX0, [44](#)
- DAVIS240\_CONFIG\_CHIP\_DIGITALMUX1, [44](#)
- DAVIS240\_CONFIG\_CHIP\_DIGITALMUX2, [44](#)
- DAVIS240\_CONFIG\_CHIP\_DIGITALMUX3, [45](#)
- DAVIS240\_CONFIG\_CHIP\_GLOBAL\_SHUTTER, [45](#)
- DAVIS240\_CONFIG\_CHIP\_RESETCALIBNEU↔RON, [45](#)
- DAVIS240\_CONFIG\_CHIP\_RESETTESTPIXEL, [45](#)
- DAVIS240\_CONFIG\_CHIP\_SPECIALPIXELCO↔NTROL, [45](#)
- DAVIS240\_CONFIG\_CHIP\_TYPENCALIBNEU↔RON, [45](#)
- DAVIS240\_CONFIG\_CHIP\_USEAOUT, [45](#)
- DAVIS346\_CONFIG\_BIAS\_ADCCOMPBP, [46](#)
- DAVIS346\_CONFIG\_BIAS\_ADCREFHIGH, [46](#)
- DAVIS346\_CONFIG\_BIAS\_ADCREFLOW, [46](#)
- DAVIS346\_CONFIG\_BIAS\_ADCTESTVOLTAGE, [46](#)
- DAVIS346\_CONFIG\_BIAS\_AEPDBN, [46](#)
- DAVIS346\_CONFIG\_BIAS\_AEPUXBP, [47](#)
- DAVIS346\_CONFIG\_BIAS\_AEPUYBP, [47](#)
- DAVIS346\_CONFIG\_BIAS\_APSCAS, [47](#)
- DAVIS346\_CONFIG\_BIAS\_APSEVERFLOWLE↔VEL, [47](#)
- DAVIS346\_CONFIG\_BIAS\_APSROSFBN, [48](#)
- DAVIS346\_CONFIG\_BIAS\_BIASBUFFER, [48](#)
- DAVIS346\_CONFIG\_BIAS\_COLSELOWBN, [48](#)
- DAVIS346\_CONFIG\_BIAS\_DACBUFBP, [48](#)
- DAVIS346\_CONFIG\_BIAS\_DIFFBN, [49](#)
- DAVIS346\_CONFIG\_BIAS\_IFREFRBN, [49](#)
- DAVIS346\_CONFIG\_BIAS\_IFTHRBN, [49](#)
- DAVIS346\_CONFIG\_BIAS\_LCOLTIMEOUTBN, [49](#)
- DAVIS346\_CONFIG\_BIAS\_LOCALBUFBN, [50](#)
- DAVIS346\_CONFIG\_BIAS\_OFFBN, [50](#)
- DAVIS346\_CONFIG\_BIAS\_ONBN, [50](#)
- DAVIS346\_CONFIG\_BIAS\_PADFOLLBN, [50](#)
- DAVIS346\_CONFIG\_BIAS\_PIXINBN, [51](#)
- DAVIS346\_CONFIG\_BIAS\_PRBP, [51](#)
- DAVIS346\_CONFIG\_BIAS\_PRSEFBP, [51](#)
- DAVIS346\_CONFIG\_BIAS\_READOUTBUFBP, [51](#)
- DAVIS346\_CONFIG\_BIAS\_REFRBP, [52](#)
- DAVIS346\_CONFIG\_BIAS\_SSN, [52](#)
- DAVIS346\_CONFIG\_BIAS\_SSP, [52](#)
- DAVIS346\_CONFIG\_CHIP\_AERNAROW, [52](#)
- DAVIS346\_CONFIG\_CHIP\_ANALOGMUX0, [53](#)
- DAVIS346\_CONFIG\_CHIP\_ANALOGMUX1, [53](#)
- DAVIS346\_CONFIG\_CHIP\_ANALOGMUX2, [53](#)
- DAVIS346\_CONFIG\_CHIP\_BIASMUX0, [53](#)
- DAVIS346\_CONFIG\_CHIP\_DIGITALMUX0, [53](#)
- DAVIS346\_CONFIG\_CHIP\_DIGITALMUX1, [53](#)
- DAVIS346\_CONFIG\_CHIP\_DIGITALMUX2, [53](#)
- DAVIS346\_CONFIG\_CHIP\_DIGITALMUX3, [53](#)
- DAVIS346\_CONFIG\_CHIP\_GLOBAL\_SHUTTER, [54](#)
- DAVIS346\_CONFIG\_CHIP\_RESETCALIBNEU↔RON, [54](#)
- DAVIS346\_CONFIG\_CHIP\_RESETTESTPIXEL, [54](#)
- DAVIS346\_CONFIG\_CHIP\_SELECTGRAYCO↔UNTER, [54](#)
- DAVIS346\_CONFIG\_CHIP\_TESTADC, [54](#)

- DAVIS346\_CONFIG\_CHIP\_TYPENCALIBNEU↔  
RON, [54](#)
- DAVIS346\_CONFIG\_CHIP\_USEAOUT, [54](#)
- DAVIS640\_CONFIG\_BIAS\_ADCCOMPBP, [54](#)
- DAVIS640\_CONFIG\_BIAS\_ADCREFHIGH, [55](#)
- DAVIS640\_CONFIG\_BIAS\_ADCREFLOW, [55](#)
- DAVIS640\_CONFIG\_BIAS\_ADCTESTVOLTAGE,  
[55](#)
- DAVIS640\_CONFIG\_BIAS\_AEPDBN, [55](#)
- DAVIS640\_CONFIG\_BIAS\_AEPUXBP, [56](#)
- DAVIS640\_CONFIG\_BIAS\_AEPUYBP, [56](#)
- DAVIS640\_CONFIG\_BIAS\_APSCAS, [56](#)
- DAVIS640\_CONFIG\_BIAS\_APSOEVERFLOWLE↔  
VEL, [56](#)
- DAVIS640\_CONFIG\_BIAS\_APSROSFBN, [57](#)
- DAVIS640\_CONFIG\_BIAS\_BIASBUFFER, [57](#)
- DAVIS640\_CONFIG\_BIAS\_COLSEOLLOWBN, [57](#)
- DAVIS640\_CONFIG\_BIAS\_DACBUFBP, [57](#)
- DAVIS640\_CONFIG\_BIAS\_DIFFBN, [58](#)
- DAVIS640\_CONFIG\_BIAS\_IFREFRBN, [58](#)
- DAVIS640\_CONFIG\_BIAS\_IFTHRBN, [58](#)
- DAVIS640\_CONFIG\_BIAS\_LCOLTIMEOUTBN,  
[58](#)
- DAVIS640\_CONFIG\_BIAS\_LOCALBUFBN, [59](#)
- DAVIS640\_CONFIG\_BIAS\_OFFBN, [59](#)
- DAVIS640\_CONFIG\_BIAS\_ONBN, [59](#)
- DAVIS640\_CONFIG\_BIAS\_PADFOLLBN, [59](#)
- DAVIS640\_CONFIG\_BIAS\_PIXINVBN, [60](#)
- DAVIS640\_CONFIG\_BIAS\_PRBP, [60](#)
- DAVIS640\_CONFIG\_BIAS\_PRSFBP, [60](#)
- DAVIS640\_CONFIG\_BIAS\_READOUTBUFBP, [60](#)
- DAVIS640\_CONFIG\_BIAS\_REFRBP, [61](#)
- DAVIS640\_CONFIG\_BIAS\_SSN, [61](#)
- DAVIS640\_CONFIG\_BIAS\_SSP, [61](#)
- DAVIS640\_CONFIG\_CHIP\_AERNAROW, [61](#)
- DAVIS640\_CONFIG\_CHIP\_ANALOGMUX0, [62](#)
- DAVIS640\_CONFIG\_CHIP\_ANALOGMUX1, [62](#)
- DAVIS640\_CONFIG\_CHIP\_ANALOGMUX2, [62](#)
- DAVIS640\_CONFIG\_CHIP\_BIASMUX0, [62](#)
- DAVIS640\_CONFIG\_CHIP\_DIGITALMUX0, [62](#)
- DAVIS640\_CONFIG\_CHIP\_DIGITALMUX1, [62](#)
- DAVIS640\_CONFIG\_CHIP\_DIGITALMUX2, [62](#)
- DAVIS640\_CONFIG\_CHIP\_DIGITALMUX3, [62](#)
- DAVIS640\_CONFIG\_CHIP\_GLOBAL\_SHUTTER,  
[63](#)
- DAVIS640\_CONFIG\_CHIP\_RESETCALIBNEU↔  
RON, [63](#)
- DAVIS640\_CONFIG\_CHIP\_RESETTESTPIXEL,  
[63](#)
- DAVIS640\_CONFIG\_CHIP\_SELECTGRAYCO↔  
UNTER, [63](#)
- DAVIS640\_CONFIG\_CHIP\_TESTADC, [63](#)
- DAVIS640\_CONFIG\_CHIP\_TYPENCALIBNEU↔  
RON, [63](#)
- DAVIS640\_CONFIG\_CHIP\_USEAOUT, [63](#)
- DAVIS\_CHIP\_DAVIS128, [63](#)
- DAVIS\_CHIP\_DAVIS208, [64](#)
- DAVIS\_CHIP\_DAVIS240A, [64](#)
- DAVIS\_CHIP\_DAVIS240B, [64](#)
- DAVIS\_CHIP\_DAVIS240C, [64](#)
- DAVIS\_CHIP\_DAVIS346A, [64](#)
- DAVIS\_CHIP\_DAVIS346B, [64](#)
- DAVIS\_CHIP\_DAVIS346C, [64](#)
- DAVIS\_CHIP\_DAVIS640, [64](#)
- DAVIS\_CHIP\_DAVISRGB, [64](#)
- DAVIS\_CONFIG\_APS\_ADC\_TEST\_MODE, [65](#)
- DAVIS\_CONFIG\_APS\_COLOR\_FILTER, [65](#)
- DAVIS\_CONFIG\_APS\_COLUMN\_SETTLE, [65](#)
- DAVIS\_CONFIG\_APS\_END\_COLUMN\_0, [65](#)
- DAVIS\_CONFIG\_APS\_END\_COLUMN\_1, [65](#)
- DAVIS\_CONFIG\_APS\_END\_COLUMN\_2, [65](#)
- DAVIS\_CONFIG\_APS\_END\_COLUMN\_3, [65](#)
- DAVIS\_CONFIG\_APS\_END\_ROW\_0, [65](#)
- DAVIS\_CONFIG\_APS\_END\_ROW\_1, [65](#)
- DAVIS\_CONFIG\_APS\_END\_ROW\_2, [66](#)
- DAVIS\_CONFIG\_APS\_END\_ROW\_3, [66](#)
- DAVIS\_CONFIG\_APS\_EXPOSURE, [66](#)
- DAVIS\_CONFIG\_APS\_FRAME\_DELAY, [66](#)
- DAVIS\_CONFIG\_APS\_GLOBAL\_SHUTTER, [66](#)
- DAVIS\_CONFIG\_APS\_HAS\_EXTERNAL\_ADC,  
[66](#)
- DAVIS\_CONFIG\_APS\_HAS\_GLOBAL\_SHUTT↔  
ER, [66](#)
- DAVIS\_CONFIG\_APS\_HAS\_INTERNAL\_ADC, [66](#)
- DAVIS\_CONFIG\_APS\_HAS\_QUAD\_ROI, [67](#)
- DAVIS\_CONFIG\_APS\_NULL\_SETTLE, [67](#)
- DAVIS\_CONFIG\_APS\_ORIENTATION\_INFO, [67](#)
- DAVIS\_CONFIG\_APS\_RAMP\_RESET, [67](#)
- DAVIS\_CONFIG\_APS\_RAMP\_SHORT\_RESET,  
[67](#)
- DAVIS\_CONFIG\_APS\_RESET\_READ, [67](#)
- DAVIS\_CONFIG\_APS\_RESET\_SETTLE, [67](#)
- DAVIS\_CONFIG\_APS\_ROW\_SETTLE, [67](#)
- DAVIS\_CONFIG\_APS\_RUN, [68](#)
- DAVIS\_CONFIG\_APS\_SAMPLE\_ENABLE, [68](#)
- DAVIS\_CONFIG\_APS\_SAMPLE\_SETTLE, [68](#)
- DAVIS\_CONFIG\_APS\_SIZE\_COLUMNS, [68](#)
- DAVIS\_CONFIG\_APS\_SIZE\_ROWS, [68](#)
- DAVIS\_CONFIG\_APS\_SNAPSHOT, [68](#)
- DAVIS\_CONFIG\_APS\_START\_COLUMN\_0, [68](#)
- DAVIS\_CONFIG\_APS\_START\_COLUMN\_1, [68](#)
- DAVIS\_CONFIG\_APS\_START\_COLUMN\_2, [69](#)
- DAVIS\_CONFIG\_APS\_START\_COLUMN\_3, [69](#)
- DAVIS\_CONFIG\_APS\_START\_ROW\_0, [69](#)
- DAVIS\_CONFIG\_APS\_START\_ROW\_1, [69](#)
- DAVIS\_CONFIG\_APS\_START\_ROW\_2, [69](#)
- DAVIS\_CONFIG\_APS\_START\_ROW\_3, [69](#)
- DAVIS\_CONFIG\_APS\_USE\_INTERNAL\_ADC, [69](#)
- DAVIS\_CONFIG\_APS\_WAIT\_ON\_TRANSFER↔  
\_STALL, [69](#)
- DAVIS\_CONFIG\_APS, [64](#)
- DAVIS\_CONFIG\_BIAS, [70](#)
- DAVIS\_CONFIG\_CHIP, [70](#)
- DAVIS\_CONFIG\_DVS\_ACK\_DELAY\_COLUMN,  
[70](#)
- DAVIS\_CONFIG\_DVS\_ACK\_DELAY\_ROW, [70](#)

- DAVIS\_CONFIG\_DVS\_ACK\_EXTENSION\_COLUMN, [70](#)
- DAVIS\_CONFIG\_DVS\_ACK\_EXTENSION\_ROW, [70](#)
- DAVIS\_CONFIG\_DVS\_EXTERNAL\_AER\_CONTROL, [70](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_BACKGROUND\_ACTIVITY\_DELTA, [71](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_BACKGROUND\_ACTIVITY, [71](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_0\_COLUMN, [71](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_0\_ROW, [71](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_1\_COLUMN, [71](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_1\_ROW, [71](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_2\_COLUMN, [71](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_2\_ROW, [71](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_3\_COLUMN, [72](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_3\_ROW, [72](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_4\_COLUMN, [72](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_4\_ROW, [72](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_5\_COLUMN, [72](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_5\_ROW, [72](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_6\_COLUMN, [72](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_6\_ROW, [72](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_7\_COLUMN, [72](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_PIXEL\_7\_ROW, [73](#)
- DAVIS\_CONFIG\_DVS\_FILTER\_ROW\_ONLY\_EVENTS, [73](#)
- DAVIS\_CONFIG\_DVS\_HAS\_BACKGROUND\_ACTIVITY\_FILTER, [73](#)
- DAVIS\_CONFIG\_DVS\_HAS\_PIXEL\_FILTER, [73](#)
- DAVIS\_CONFIG\_DVS\_HAS\_TEST\_EVENT\_GENERATOR, [73](#)
- DAVIS\_CONFIG\_DVS\_ORIENTATION\_INFO, [73](#)
- DAVIS\_CONFIG\_DVS\_RUN, [73](#)
- DAVIS\_CONFIG\_DVS\_SIZE\_COLUMNS, [73](#)
- DAVIS\_CONFIG\_DVS\_SIZE\_ROWS, [74](#)
- DAVIS\_CONFIG\_DVS\_TEST\_EVENT\_GENERATOR\_ENABLE, [74](#)
- DAVIS\_CONFIG\_DVS\_WAIT\_ON\_TRANSFER\_STALL, [74](#)
- DAVIS\_CONFIG\_DVS, [70](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_FALLING\_EDGES1, [74](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_FALLING\_EDGES2, [74](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_FALLING\_EDGES, [74](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_LENGTH1, [75](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_LENGTH2, [75](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_LENGTH, [75](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_POLARITY1, [75](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_POLARITY2, [75](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_POLARITY, [75](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_S1, [76](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSE\_S2, [76](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_PULSES, [75](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_RISING\_EDGES1, [76](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_RISING\_EDGES2, [76](#)
- DAVIS\_CONFIG\_EXTINPUT\_DETECT\_RISING\_EDGES, [76](#)
- DAVIS\_CONFIG\_EXTINPUT\_GENERATE\_INTERRUPT\_ON\_FALLING\_EDGE, [76](#)
- DAVIS\_CONFIG\_EXTINPUT\_GENERATE\_INTERRUPT\_ON\_RISING\_EDGE, [76](#)
- DAVIS\_CONFIG\_EXTINPUT\_GENERATE\_PULSE\_INTERVAL, [76](#)
- DAVIS\_CONFIG\_EXTINPUT\_GENERATE\_PULSE\_LENGTH, [77](#)
- DAVIS\_CONFIG\_EXTINPUT\_GENERATE\_PULSE\_POLARITY, [77](#)
- DAVIS\_CONFIG\_EXTINPUT\_GENERATE\_USER\_CUSTOM\_SIGNAL, [77](#)
- DAVIS\_CONFIG\_EXTINPUT\_HAS\_EXTRA\_DETECTORS, [77](#)
- DAVIS\_CONFIG\_EXTINPUT\_HAS\_GENERATOR, [77](#)
- DAVIS\_CONFIG\_EXTINPUT\_RUN\_DETECTOR\_R1, [77](#)
- DAVIS\_CONFIG\_EXTINPUT\_RUN\_DETECTOR\_R2, [78](#)
- DAVIS\_CONFIG\_EXTINPUT\_RUN\_DETECTOR, [77](#)
- DAVIS\_CONFIG\_EXTINPUT\_RUN\_GENERATOR\_OR, [78](#)
- DAVIS\_CONFIG\_EXTINPUT, [74](#)
- DAVIS\_CONFIG\_IMU\_ACCEL\_FULL\_SCALE, [78](#)
- DAVIS\_CONFIG\_IMU\_ACCEL\_STANDBY, [78](#)
- DAVIS\_CONFIG\_IMU\_DIGITAL\_LOW\_PASS\_FILTER, [78](#)



- ILTER, [78](#)
- DAVIS\_CONFIG\_IMU\_GYRO\_FULL\_SCALE, [78](#)
- DAVIS\_CONFIG\_IMU\_GYRO\_STANDBY, [79](#)
- DAVIS\_CONFIG\_IMU\_LP\_CYCLE, [79](#)
- DAVIS\_CONFIG\_IMU\_LP\_WAKEUP, [79](#)
- DAVIS\_CONFIG\_IMU\_RUN, [79](#)
- DAVIS\_CONFIG\_IMU\_SAMPLE\_RATE\_DIVID←  
ER, [79](#)
- DAVIS\_CONFIG\_IMU\_TEMP\_STANDBY, [79](#)
- DAVIS\_CONFIG\_IMU, [78](#)
- DAVIS\_CONFIG\_MUX\_DROP\_APS\_ON\_TRA←  
NSFER\_STALL, [79](#)
- DAVIS\_CONFIG\_MUX\_DROP\_DVS\_ON\_TRA←  
NSFER\_STALL, [80](#)
- DAVIS\_CONFIG\_MUX\_DROP\_EXTINPUT\_ON←  
\_TRANSFER\_STALL, [80](#)
- DAVIS\_CONFIG\_MUX\_DROP\_IMU\_ON\_TRAN←  
SFER\_STALL, [80](#)
- DAVIS\_CONFIG\_MUX\_FORCE\_CHIP\_BIAS\_E←  
NABLE, [80](#)
- DAVIS\_CONFIG\_MUX\_RUN, [80](#)
- DAVIS\_CONFIG\_MUX\_TIMESTAMP\_RESET, [80](#)
- DAVIS\_CONFIG\_MUX\_TIMESTAMP\_RUN, [80](#)
- DAVIS\_CONFIG\_MUX, [79](#)
- DAVIS\_CONFIG\_SYSINFO\_ADC\_CLOCK, [81](#)
- DAVIS\_CONFIG\_SYSINFO\_CHIP\_IDENTIFIER,  
[81](#)
- DAVIS\_CONFIG\_SYSINFO\_DEVICE\_IS\_MAST←  
ER, [81](#)
- DAVIS\_CONFIG\_SYSINFO\_LOGIC\_CLOCK, [81](#)
- DAVIS\_CONFIG\_SYSINFO\_LOGIC\_VERSION,  
[81](#)
- DAVIS\_CONFIG\_SYSINFO, [80](#)
- DAVIS\_CONFIG\_USB\_EARLY\_PACKET\_DELAY,  
[81](#)
- DAVIS\_CONFIG\_USB\_RUN, [82](#)
- DAVIS\_CONFIG\_USB, [81](#)
- DAVISRGB\_CONFIG\_APS\_GSFDRESET, [82](#)
- DAVISRGB\_CONFIG\_APS\_GSPDRESET, [82](#)
- DAVISRGB\_CONFIG\_APS\_GSRESETFALL, [82](#)
- DAVISRGB\_CONFIG\_APS\_GSTXFALL, [82](#)
- DAVISRGB\_CONFIG\_APS\_RSFDSETTLE, [82](#)
- DAVISRGB\_CONFIG\_APS\_TRANSFER, [82](#)
- DAVISRGB\_CONFIG\_BIAS\_ADCCOMPBP, [82](#)
- DAVISRGB\_CONFIG\_BIAS\_ADCREFHIGH, [83](#)
- DAVISRGB\_CONFIG\_BIAS\_ADCREFLOW, [83](#)
- DAVISRGB\_CONFIG\_BIAS\_ADCTESTVOLTA←  
GE, [83](#)
- DAVISRGB\_CONFIG\_BIAS\_AEPDBN, [83](#)
- DAVISRGB\_CONFIG\_BIAS\_AEPUXBP, [84](#)
- DAVISRGB\_CONFIG\_BIAS\_AEPUYBP, [84](#)
- DAVISRGB\_CONFIG\_BIAS\_APSCAS, [84](#)
- DAVISRGB\_CONFIG\_BIAS\_APSROSFBN, [84](#)
- DAVISRGB\_CONFIG\_BIAS\_ARRAYBIASBUFF←  
ERBN, [85](#)
- DAVISRGB\_CONFIG\_BIAS\_ARRAYLOGICBU←  
FFERBN, [85](#)
- DAVISRGB\_CONFIG\_BIAS\_BIASBUFFER, [85](#)
- DAVISRGB\_CONFIG\_BIAS\_DACBUFBP, [85](#)
- DAVISRGB\_CONFIG\_BIAS\_DIFFBN, [86](#)
- DAVISRGB\_CONFIG\_BIAS\_FALLTIMEBN, [86](#)
- DAVISRGB\_CONFIG\_BIAS\_GND07, [86](#)
- DAVISRGB\_CONFIG\_BIAS\_IFREFRBN, [86](#)
- DAVISRGB\_CONFIG\_BIAS\_IFTHRBN, [87](#)
- DAVISRGB\_CONFIG\_BIAS\_LCOLTIMEOUTBN,  
[87](#)
- DAVISRGB\_CONFIG\_BIAS\_LOCALBUFBN, [87](#)
- DAVISRGB\_CONFIG\_BIAS\_OFFBN, [87](#)
- DAVISRGB\_CONFIG\_BIAS\_ONBN, [88](#)
- DAVISRGB\_CONFIG\_BIAS\_OVG1LO, [88](#)
- DAVISRGB\_CONFIG\_BIAS\_OVG2LO, [88](#)
- DAVISRGB\_CONFIG\_BIAS\_PADFOLLBN, [88](#)
- DAVISRGB\_CONFIG\_BIAS\_PIXINBN, [89](#)
- DAVISRGB\_CONFIG\_BIAS\_PRBP, [89](#)
- DAVISRGB\_CONFIG\_BIAS\_PRSFBP, [89](#)
- DAVISRGB\_CONFIG\_BIAS\_READOUTBUFBP,  
[89](#)
- DAVISRGB\_CONFIG\_BIAS\_REFRBP, [90](#)
- DAVISRGB\_CONFIG\_BIAS\_RISETIMEBP, [90](#)
- DAVISRGB\_CONFIG\_BIAS\_SSN, [90](#)
- DAVISRGB\_CONFIG\_BIAS\_SSP, [90](#)
- DAVISRGB\_CONFIG\_BIAS\_TX2OVG2HI, [91](#)
- DAVISRGB\_CONFIG\_CHIP\_ADJUSTOVG1LO,  
[91](#)
- DAVISRGB\_CONFIG\_CHIP\_ADJUSTOVG2LO,  
[91](#)
- DAVISRGB\_CONFIG\_CHIP\_ADJUSTTX2OV←  
G2HI, [91](#)
- DAVISRGB\_CONFIG\_CHIP\_AERNAROW, [91](#)
- DAVISRGB\_CONFIG\_CHIP\_ANALOGMUX0, [91](#)
- DAVISRGB\_CONFIG\_CHIP\_ANALOGMUX1, [92](#)
- DAVISRGB\_CONFIG\_CHIP\_ANALOGMUX2, [92](#)
- DAVISRGB\_CONFIG\_CHIP\_BIASMUX0, [92](#)
- DAVISRGB\_CONFIG\_CHIP\_DIGITALMUX0, [92](#)
- DAVISRGB\_CONFIG\_CHIP\_DIGITALMUX1, [92](#)
- DAVISRGB\_CONFIG\_CHIP\_DIGITALMUX2, [92](#)
- DAVISRGB\_CONFIG\_CHIP\_DIGITALMUX3, [92](#)
- DAVISRGB\_CONFIG\_CHIP\_RESETCALIBNEU←  
RON, [92](#)
- DAVISRGB\_CONFIG\_CHIP\_RESETTESTPIXEL,  
[93](#)
- DAVISRGB\_CONFIG\_CHIP\_SELECTGRAYCO←  
UNTER, [93](#)
- DAVISRGB\_CONFIG\_CHIP\_TESTADC, [93](#)
- DAVISRGB\_CONFIG\_CHIP\_TYPENCALIBNEU←  
RON, [93](#)
- DAVISRGB\_CONFIG\_CHIP\_USEAOUT, [93](#)
- DOUBLE\_DIODE, [95](#)
- HI\_Z, [95](#)
- IS\_DAVIS128, [93](#)
- IS\_DAVIS208, [93](#)
- IS\_DAVIS240, [93](#)
- IS\_DAVIS240A, [94](#)
- IS\_DAVIS240B, [94](#)
- IS\_DAVIS240C, [94](#)
- IS\_DAVIS346, [94](#)

- IS\_DAVIS346A, [94](#)
- IS\_DAVIS346B, [94](#)
- IS\_DAVIS346C, [94](#)
- IS\_DAVIS640, [94](#)
- IS\_DAVISRGB, [94](#)
- SHIFTED\_SOURCE, [95](#)
- SINGLE\_DIODE, [95](#)
- SPLIT\_GATE, [95](#)
- TIED\_TO\_RAIL, [95](#)
- devices/davis.h, [11](#)
- devices/dvs128.h, [97](#)
- devices/dynapse.h, [101](#)
- devices/usb.h, [106](#)
- dvs128.h
  - CAER\_DEVICE\_DVS128, [98](#)
  - caerDVS128InfoGet, [100](#)
  - DVS128\_CONFIG\_BIAS\_CAS, [98](#)
  - DVS128\_CONFIG\_BIAS\_DIFFOFF, [99](#)
  - DVS128\_CONFIG\_BIAS\_DIFFON, [99](#)
  - DVS128\_CONFIG\_BIAS\_DIFF, [98](#)
  - DVS128\_CONFIG\_BIAS\_FOLL, [99](#)
  - DVS128\_CONFIG\_BIAS\_INJGND, [99](#)
  - DVS128\_CONFIG\_BIAS\_PUX, [99](#)
  - DVS128\_CONFIG\_BIAS\_PUY, [99](#)
  - DVS128\_CONFIG\_BIAS\_PR, [99](#)
  - DVS128\_CONFIG\_BIAS\_REFR, [99](#)
  - DVS128\_CONFIG\_BIAS\_REQPD, [100](#)
  - DVS128\_CONFIG\_BIAS\_REQ, [99](#)
  - DVS128\_CONFIG\_BIAS, [98](#)
  - DVS128\_CONFIG\_DVS\_ARRAY\_RESET, [100](#)
  - DVS128\_CONFIG\_DVS\_RUN, [100](#)
  - DVS128\_CONFIG\_DVS\_TIMESTAMP\_RESET, [100](#)
  - DVS128\_CONFIG\_DVS\_TS\_MASTER, [100](#)
  - DVS128\_CONFIG\_DVS, [100](#)
- dynapse.h
  - CAER\_DEVICE\_DYNAPSE, [102](#)
  - caerDynapseInfoGet, [105](#)
  - DYNAPSE\_CHIP\_DYNAPSE, [102](#)
  - DYNAPSE\_CONFIG\_AER\_ACK\_DELAY, [102](#)
  - DYNAPSE\_CONFIG\_AER\_ACK\_EXTENSION, [102](#)
  - DYNAPSE\_CONFIG\_AER\_EXTERNAL\_AER\_↔CONTROL, [102](#)
  - DYNAPSE\_CONFIG\_AER\_RUN, [102](#)
  - DYNAPSE\_CONFIG\_AER\_WAIT\_ON\_TRANSFER\_STALL, [102](#)
  - DYNAPSE\_CONFIG\_AER, [102](#)
  - DYNAPSE\_CONFIG\_CHIP\_CONTENT, [103](#)
  - DYNAPSE\_CONFIG\_CHIP\_ID, [103](#)
  - DYNAPSE\_CONFIG\_CHIP\_REQ\_DELAY, [103](#)
  - DYNAPSE\_CONFIG\_CHIP\_REQ\_EXTENSION, [103](#)
  - DYNAPSE\_CONFIG\_CHIP\_RUN, [103](#)
  - DYNAPSE\_CONFIG\_CHIP, [103](#)
  - DYNAPSE\_CONFIG\_MUX\_DROP\_AER\_ON\_TRANSFER\_STALL, [103](#)
  - DYNAPSE\_CONFIG\_MUX\_FORCE\_CHIP\_BIAS\_ENABLE, [104](#)
  - DYNAPSE\_CONFIG\_MUX\_RUN, [104](#)
  - DYNAPSE\_CONFIG\_MUX\_TIMESTAMP\_RESET, [104](#)
  - DYNAPSE\_CONFIG\_MUX\_TIMESTAMP\_RUN, [104](#)
  - DYNAPSE\_CONFIG\_MUX, [103](#)
  - DYNAPSE\_CONFIG\_SYSINFO\_CHIP\_IDENTIFIER, [104](#)
  - DYNAPSE\_CONFIG\_SYSINFO\_DEVICE\_IS\_MASTER, [104](#)
  - DYNAPSE\_CONFIG\_SYSINFO\_LOGIC\_CLOCK, [104](#)
  - DYNAPSE\_CONFIG\_SYSINFO\_LOGIC\_VERSION, [105](#)
  - DYNAPSE\_CONFIG\_SYSINFO, [104](#)
  - DYNAPSE\_CONFIG\_USB\_EARLY\_PACKET\_DELAY, [105](#)
  - DYNAPSE\_CONFIG\_USB\_RUN, [105](#)
  - DYNAPSE\_CONFIG\_USB, [105](#)
- EAR\_EVENT
  - common.h, [114](#)
- EAR\_MASK
  - ear.h, [131](#)
- EAR\_SHIFT
  - ear.h, [131](#)
- EXTERNAL\_GENERATOR\_FALLING\_EDGE
  - special.h, [224](#)
- EXTERNAL\_GENERATOR\_RISING\_EDGE
  - special.h, [224](#)
- EXTERNAL\_INPUT1\_FALLING\_EDGE
  - special.h, [224](#)
- EXTERNAL\_INPUT1\_PULSE
  - special.h, [224](#)
- EXTERNAL\_INPUT1\_RISING\_EDGE
  - special.h, [224](#)
- EXTERNAL\_INPUT2\_FALLING\_EDGE
  - special.h, [224](#)
- EXTERNAL\_INPUT2\_PULSE
  - special.h, [224](#)
- EXTERNAL\_INPUT2\_RISING\_EDGE
  - special.h, [224](#)
- EXTERNAL\_INPUT\_FALLING\_EDGE
  - special.h, [224](#)
- EXTERNAL\_INPUT\_PULSE
  - special.h, [224](#)
- EXTERNAL\_INPUT\_RISING\_EDGE
  - special.h, [224](#)
- ear.h
  - CAER\_EAR\_ITERATOR\_ALL\_END, [130](#)
  - CAER\_EAR\_ITERATOR\_ALL\_START, [130](#)
  - CAER\_EAR\_ITERATOR\_VALID\_END, [130](#)
  - CAER\_EAR\_ITERATOR\_VALID\_START, [130](#)
  - CHANNEL\_MASK, [130](#)
  - CHANNEL\_SHIFT, [131](#)
  - caerEarEvent, [132](#)
  - caerEarEventGetChannel, [132](#)

- caerEarEventGetEar, 132
- caerEarEventGetTimestamp, 132
- caerEarEventGetTimestamp64, 133
- caerEarEventInvalidate, 133
- caerEarEventsValid, 133
- caerEarEventPacket, 132
- caerEarEventPacketAllocate, 133
- caerEarEventPacketGetEvent, 134
- caerEarEventSetChannel, 134
- caerEarEventSetEar, 134
- caerEarEventSetTimestamp, 135
- caerEarEventValidate, 135
- EAR\_MASK, 131
- EAR\_SHIFT, 131
- FILTER\_MASK, 131
- FILTER\_SHIFT, 131
- NEURON\_MASK, 131
- NEURON\_SHIFT, 131
- PACKED\_STRUCT, 135
- events/common.h, 111
- events/config.h, 122
- events/ear.h, 129
- events/frame.h, 135
- events/imu6.h, 154
- events/imu9.h, 162
- events/packetContainer.h, 172
- events/point1d.h, 178
- events/point2d.h, 184
- events/point3d.h, 191
- events/point4d.h, 199
- events/polarity.h, 207
- events/sample.h, 215
- events/special.h, 221
- events/spike.h, 229
- FILTER\_MASK
  - ear.h, 131
- FILTER\_SHIFT
  - ear.h, 131
- FRAME\_EVENT
  - common.h, 114
- frame.h
  - BGRG, 140
  - BWRG, 140
  - CAER\_FRAME\_ITERATOR\_ALL\_END, 137
  - CAER\_FRAME\_ITERATOR\_ALL\_START, 137
  - CAER\_FRAME\_ITERATOR\_VALID\_END, 138
  - CAER\_FRAME\_ITERATOR\_VALID\_START, 138
  - CAER\_FRAME\_REVERSE\_ITERATOR\_ALL\_END, 138
  - CAER\_FRAME\_REVERSE\_ITERATOR\_ALL\_START, 138
  - CAER\_FRAME\_REVERSE\_ITERATOR\_VALID\_END, 138
  - CAER\_FRAME\_REVERSE\_ITERATOR\_VALID\_START, 139
  - COLOR\_CHANNELS\_MASK, 139
  - COLOR\_CHANNELS\_SHIFT, 139
  - COLOR\_FILTER\_MASK, 139
  - COLOR\_FILTER\_SHIFT, 139
  - caer\_frame\_event\_color\_channels, 140
  - caer\_frame\_event\_color\_filter, 140
  - caerFrameEvent, 140
  - caerFrameEventGetChannelNumber, 141
  - caerFrameEventGetColorFilter, 141
  - caerFrameEventGetExposureLength, 141
  - caerFrameEventGetLengthX, 141
  - caerFrameEventGetLengthY, 142
  - caerFrameEventGetPixel, 142
  - caerFrameEventGetPixelArrayUnsafe, 142
  - caerFrameEventGetPixelForChannel, 143
  - caerFrameEventGetPixelForChannelUnsafe, 143
  - caerFrameEventGetPixelUnsafe, 144
  - caerFrameEventGetPixelsMaxIndex, 143
  - caerFrameEventGetPixelsSize, 144
  - caerFrameEventGetPositionX, 144
  - caerFrameEventGetPositionY, 145
  - caerFrameEventGetROIIdentifier, 145
  - caerFrameEventGetTSEndOfExposure, 146
  - caerFrameEventGetTSEndOfExposure64, 146
  - caerFrameEventGetTSEndOfFrame, 146
  - caerFrameEventGetTSEndOfFrame64, 147
  - caerFrameEventGetTSSStartOfExposure, 147
  - caerFrameEventGetTSSStartOfExposure64, 147
  - caerFrameEventGetTSSStartOfFrame, 148
  - caerFrameEventGetTSSStartOfFrame64, 148
  - caerFrameEventGetTimestamp, 145
  - caerFrameEventGetTimestamp64, 145
  - caerFrameEventInvalidate, 148
  - caerFrameEventsValid, 149
  - caerFrameEventPacket, 140
  - caerFrameEventPacketAllocate, 149
  - caerFrameEventPacketGetEvent, 149
  - caerFrameEventPacketGetPixelsMaxIndex, 150
  - caerFrameEventPacketGetPixelsSize, 150
  - caerFrameEventSetColorFilter, 150
  - caerFrameEventSetLengthXLengthYChannel↔
    - Number, 151
  - caerFrameEventSetPixel, 151
  - caerFrameEventSetPixelForChannel, 151
  - caerFrameEventSetPixelForChannelUnsafe, 152
  - caerFrameEventSetPixelUnsafe, 152
  - caerFrameEventSetPositionX, 152
  - caerFrameEventSetPositionY, 152
  - caerFrameEventSetROIIdentifier, 153
  - caerFrameEventSetTSEndOfExposure, 153
  - caerFrameEventSetTSEndOfFrame, 153
  - caerFrameEventSetTSSStartOfExposure, 153
  - caerFrameEventSetTSSStartOfFrame, 153
  - caerFrameEventValidate, 154
  - GBGR, 140
  - GRAYSCALE, 140
  - GRGB, 140
  - GRWB, 140
  - MONO, 140
  - PACKED\_STRUCT, 154
  - RGBA, 140



- RGBG, [140](#)
  - RGBW, [140](#)
  - RGB, [140](#)
  - ROI\_IDENTIFIER\_MASK, [139](#)
  - ROI\_IDENTIFIER\_SHIFT, [140](#)
  - WBGR, [140](#)
- frame\_utils.h, [236](#)
- frame\_utils\_opencv.h, [236](#)
- GBGR
  - frame.h, [140](#)
- GET\_NUMBITS16
  - libcaer.h, [238](#)
- GET\_NUMBITS32
  - libcaer.h, [238](#)
- GET\_NUMBITS8
  - libcaer.h, [238](#)
- GRAYSCALE
  - frame.h, [140](#)
- GRGB
  - frame.h, [140](#)
- GRWB
  - frame.h, [140](#)
- HI\_Z
  - davis.h, [95](#)
- I16T
  - libcaer.h, [238](#)
- I32T
  - libcaer.h, [238](#)
- I64T
  - libcaer.h, [238](#)
- I8T
  - libcaer.h, [239](#)
- IMU6\_EVENT
  - common.h, [114](#)
- IMU9\_EVENT
  - common.h, [114](#)
- IS\_DAVIS128
  - davis.h, [93](#)
- IS\_DAVIS208
  - davis.h, [93](#)
- IS\_DAVIS240
  - davis.h, [93](#)
- IS\_DAVIS240A
  - davis.h, [94](#)
- IS\_DAVIS240B
  - davis.h, [94](#)
- IS\_DAVIS240C
  - davis.h, [94](#)
- IS\_DAVIS346
  - davis.h, [94](#)
- IS\_DAVIS346A
  - davis.h, [94](#)
- IS\_DAVIS346B
  - davis.h, [94](#)
- IS\_DAVIS346C
  - davis.h, [94](#)
- IS\_DAVIS640
  - davis.h, [94](#)
- IS\_DAVISRGB
  - davis.h, [94](#)
- imu6.h
  - CAER\_IMU6\_ITERATOR\_ALL\_END, [155](#)
  - CAER\_IMU6\_ITERATOR\_ALL\_START, [155](#)
  - CAER\_IMU6\_ITERATOR\_VALID\_END, [156](#)
  - CAER\_IMU6\_ITERATOR\_VALID\_START, [156](#)
  - caerIMU6Event, [156](#)
  - caerIMU6EventGetAccelX, [156](#)
  - caerIMU6EventGetAccelY, [157](#)
  - caerIMU6EventGetAccelZ, [157](#)
  - caerIMU6EventGetGyroX, [157](#)
  - caerIMU6EventGetGyroY, [157](#)
  - caerIMU6EventGetGyroZ, [158](#)
  - caerIMU6EventGetTemp, [158](#)
  - caerIMU6EventGetTimestamp, [158](#)
  - caerIMU6EventGetTimestamp64, [159](#)
  - caerIMU6EventInvalidate, [159](#)
  - caerIMU6EventIsValid, [159](#)
  - caerIMU6EventPacket, [156](#)
  - caerIMU6EventPacketAllocate, [159](#)
  - caerIMU6EventPacketGetEvent, [160](#)
  - caerIMU6EventSetAccelX, [160](#)
  - caerIMU6EventSetAccelY, [160](#)
  - caerIMU6EventSetAccelZ, [160](#)
  - caerIMU6EventSetGyroX, [161](#)
  - caerIMU6EventSetGyroY, [161](#)
  - caerIMU6EventSetGyroZ, [161](#)
  - caerIMU6EventSetTemp, [161](#)
  - caerIMU6EventSetTimestamp, [162](#)
  - caerIMU6EventValidate, [162](#)
  - PACKED\_STRUCT, [162](#)
- imu9.h
  - CAER\_IMU9\_ITERATOR\_ALL\_END, [163](#)
  - CAER\_IMU9\_ITERATOR\_ALL\_START, [163](#)
  - CAER\_IMU9\_ITERATOR\_VALID\_END, [164](#)
  - CAER\_IMU9\_ITERATOR\_VALID\_START, [164](#)
  - caerIMU9Event, [164](#)
  - caerIMU9EventGetAccelX, [164](#)
  - caerIMU9EventGetAccelY, [165](#)
  - caerIMU9EventGetAccelZ, [165](#)
  - caerIMU9EventGetCompX, [165](#)
  - caerIMU9EventGetCompY, [165](#)
  - caerIMU9EventGetCompZ, [166](#)
  - caerIMU9EventGetGyroX, [166](#)
  - caerIMU9EventGetGyroY, [166](#)
  - caerIMU9EventGetGyroZ, [167](#)
  - caerIMU9EventGetTemp, [167](#)
  - caerIMU9EventGetTimestamp, [167](#)
  - caerIMU9EventGetTimestamp64, [167](#)
  - caerIMU9EventInvalidate, [168](#)
  - caerIMU9EventIsValid, [168](#)
  - caerIMU9EventPacket, [164](#)
  - caerIMU9EventPacketAllocate, [168](#)
  - caerIMU9EventPacketGetEvent, [169](#)
  - caerIMU9EventSetAccelX, [169](#)

- caerIMU9EventSetAccelY, 169
- caerIMU9EventSetAccelZ, 169
- caerIMU9EventSetCompX, 169
- caerIMU9EventSetCompY, 170
- caerIMU9EventSetCompZ, 170
- caerIMU9EventSetGyroX, 170
- caerIMU9EventSetGyroY, 170
- caerIMU9EventSetGyroZ, 171
- caerIMU9EventSetTemp, 171
- caerIMU9EventSetTimestamp, 171
- caerIMU9EventValidate, 171
- PACKED\_STRUCT, 171, 172
- LIBCAER\_NAME\_STRING
  - libcaer.h, 239
- LIBCAER\_VERSION\_STRING
  - libcaer.h, 239
- LIBCAER\_VERSION
  - libcaer.h, 239
- libcaer.h, 237
  - CLEAR\_NUMBITS16, 238
  - CLEAR\_NUMBITS32, 238
  - CLEAR\_NUMBITS8, 238
  - caerByteArrayToInteger, 240
  - caerIntegerToByteArray, 240
  - caerStrEquals, 241
  - caerStrEqualsUpTo, 241
  - GET\_NUMBITS16, 238
  - GET\_NUMBITS32, 238
  - GET\_NUMBITS8, 238
  - I16T, 238
  - I32T, 238
  - I64T, 238
  - I8T, 239
  - LIBCAER\_NAME\_STRING, 239
  - LIBCAER\_VERSION\_STRING, 239
  - LIBCAER\_VERSION, 239
  - MASK\_NUMBITS32, 239
  - MASK\_NUMBITS64, 239
  - SET\_NUMBITS16, 239
  - SET\_NUMBITS32, 239
  - SET\_NUMBITS8, 239
  - SWAP\_VAR, 239
  - U16T, 240
  - U32T, 240
  - U64T, 240
  - U8T, 240
- log.h, 241
  - CAER\_LOG\_ALERT, 242
  - CAER\_LOG\_CRITICAL, 242
  - CAER\_LOG\_DEBUG, 242
  - CAER\_LOG\_EMERGENCY, 242
  - CAER\_LOG\_ERROR, 242
  - CAER\_LOG\_INFO, 243
  - CAER\_LOG\_NOTICE, 243
  - CAER\_LOG\_WARNING, 243
  - caerLog, 243
  - caerLogFileDescriptorsSet, 243
  - caerLogLevelGet, 244
  - caerLogLevelSet, 244
- MASK\_NUMBITS32
  - libcaer.h, 239
- MASK\_NUMBITS64
  - libcaer.h, 239
- MODULE\_ADDR\_MASK
  - config.h, 124
- MODULE\_ADDR\_SHIFT
  - config.h, 124
- MONO
  - frame.h, 140
- NEURON\_MASK
  - ear.h, 131
- NEURON\_SHIFT
  - ear.h, 131
- PACKED\_STRUCT
  - common.h, 122
  - config.h, 128
  - ear.h, 135
  - frame.h, 154
  - imu6.h, 162
  - imu9.h, 171, 172
  - packetContainer.h, 177
  - point1d.h, 184
  - point2d.h, 191
  - point3d.h, 199
  - point4d.h, 207
  - polarity.h, 215
  - sample.h, 221
  - special.h, 228
  - spike.h, 235
- POINT1D\_EVENT
  - common.h, 114
- POINT1D\_SCALE\_MASK
  - point1d.h, 179
- POINT1D\_SCALE\_SHIFT
  - point1d.h, 179
- POINT1D\_TYPE\_MASK
  - point1d.h, 180
- POINT1D\_TYPE\_SHIFT
  - point1d.h, 180
- POINT2D\_EVENT
  - common.h, 114
- POINT2D\_SCALE\_MASK
  - point2d.h, 186
- POINT2D\_SCALE\_SHIFT
  - point2d.h, 186
- POINT2D\_TYPE\_MASK
  - point2d.h, 186
- POINT2D\_TYPE\_SHIFT
  - point2d.h, 186
- POINT3D\_EVENT
  - common.h, 114
- POINT3D\_SCALE\_MASK
  - point3d.h, 193
- POINT3D\_SCALE\_SHIFT

- point3d.h, 193
- POINT3D\_TYPE\_MASK
  - point3d.h, 193
- POINT3D\_TYPE\_SHIFT
  - point3d.h, 194
- POINT4D\_EVENT
  - common.h, 114
- POINT4D\_SCALE\_MASK
  - point4d.h, 201
- POINT4D\_SCALE\_SHIFT
  - point4d.h, 201
- POINT4D\_TYPE\_MASK
  - point4d.h, 201
- POINT4D\_TYPE\_SHIFT
  - point4d.h, 201
- POLARITY\_EVENT
  - common.h, 114
- POLARITY\_MASK
  - polarity.h, 210
- POLARITY\_SHIFT
  - polarity.h, 210
- packetContainer.h
  - CAER\_EVENT\_PACKET\_CONTAINER\_ITERA↔  
TOR\_END, 173
  - CAER\_EVENT\_PACKET\_CONTAINER\_ITERA↔  
TOR\_START, 173
  - caerEventPacketContainer, 174
  - caerEventPacketContainerAllocate, 174
  - caerEventPacketContainerCopyAllEvents, 174
  - caerEventPacketContainerCopyValidEvents, 174
  - caerEventPacketContainerFindEventPacketBy↔  
Type, 175
  - caerEventPacketContainerFree, 175
  - caerEventPacketContainerGetEventPacket, 175
  - caerEventPacketContainerGetEventPackets↔  
Number, 175
  - caerEventPacketContainerGetEventsNumber, 176
  - caerEventPacketContainerGetEventsValidNumber,  
176
  - caerEventPacketContainerGetHighestEvent↔  
Timestamp, 176
  - caerEventPacketContainerGetLowestEvent↔  
Timestamp, 177
  - caerEventPacketContainerSetEventPacket, 177
  - caerEventPacketContainerSetEventPackets↔  
Number, 177
  - PACKED\_STRUCT, 177
- point1d.h
  - CAER\_POINT1D\_ITERATOR\_ALL\_END, 179
  - CAER\_POINT1D\_ITERATOR\_ALL\_START, 179
  - CAER\_POINT1D\_ITERATOR\_VALID\_END, 179
  - CAER\_POINT1D\_ITERATOR\_VALID\_START,  
179
  - caerPoint1DEvent, 180
  - caerPoint1DEventGetScale, 180
  - caerPoint1DEventGetTimestamp, 180
  - caerPoint1DEventGetTimestamp64, 181
  - caerPoint1DEventGetType, 181
  - caerPoint1DEventGetX, 181
  - caerPoint1DEventInvalidate, 182
  - caerPoint1DEventIsValid, 182
  - caerPoint1DEventPacket, 180
  - caerPoint1DEventPacketAllocate, 182
  - caerPoint1DEventPacketGetEvent, 182
  - caerPoint1DEventSetScale, 183
  - caerPoint1DEventSetTimestamp, 183
  - caerPoint1DEventSetType, 183
  - caerPoint1DEventSetX, 183
  - caerPoint1DEventValidate, 184
  - PACKED\_STRUCT, 184
  - POINT1D\_SCALE\_MASK, 179
  - POINT1D\_SCALE\_SHIFT, 179
  - POINT1D\_TYPE\_MASK, 180
  - POINT1D\_TYPE\_SHIFT, 180
- point2d.h
  - CAER\_POINT2D\_ITERATOR\_ALL\_END, 185
  - CAER\_POINT2D\_ITERATOR\_ALL\_START, 185
  - CAER\_POINT2D\_ITERATOR\_VALID\_END, 185
  - CAER\_POINT2D\_ITERATOR\_VALID\_START,  
186
  - caerPoint2DEvent, 186
  - caerPoint2DEventGetScale, 187
  - caerPoint2DEventGetTimestamp, 187
  - caerPoint2DEventGetTimestamp64, 187
  - caerPoint2DEventGetType, 188
  - caerPoint2DEventGetX, 188
  - caerPoint2DEventGetY, 188
  - caerPoint2DEventInvalidate, 188
  - caerPoint2DEventIsValid, 189
  - caerPoint2DEventPacket, 186
  - caerPoint2DEventPacketAllocate, 189
  - caerPoint2DEventPacketGetEvent, 189
  - caerPoint2DEventSetScale, 189
  - caerPoint2DEventSetTimestamp, 190
  - caerPoint2DEventSetType, 190
  - caerPoint2DEventSetX, 190
  - caerPoint2DEventSetY, 190
  - caerPoint2DEventValidate, 191
  - PACKED\_STRUCT, 191
  - POINT2D\_SCALE\_MASK, 186
  - POINT2D\_SCALE\_SHIFT, 186
  - POINT2D\_TYPE\_MASK, 186
  - POINT2D\_TYPE\_SHIFT, 186
- point3d.h
  - CAER\_POINT3D\_ITERATOR\_ALL\_END, 192
  - CAER\_POINT3D\_ITERATOR\_ALL\_START, 192
  - CAER\_POINT3D\_ITERATOR\_VALID\_END, 193
  - CAER\_POINT3D\_ITERATOR\_VALID\_START,  
193
  - caerPoint3DEvent, 194
  - caerPoint3DEventGetScale, 194
  - caerPoint3DEventGetTimestamp, 194
  - caerPoint3DEventGetTimestamp64, 195
  - caerPoint3DEventGetType, 195
  - caerPoint3DEventGetX, 195
  - caerPoint3DEventGetY, 196

- caerPoint3DEventGetZ, 196
- caerPoint3DEventInvalidate, 196
- caerPoint3DEventIsValid, 196
- caerPoint3DEventPacket, 194
- caerPoint3DEventPacketAllocate, 197
- caerPoint3DEventPacketGetEvent, 197
- caerPoint3DEventSetScale, 197
- caerPoint3DEventSetTimestamp, 198
- caerPoint3DEventSetType, 198
- caerPoint3DEventSetX, 198
- caerPoint3DEventSetY, 198
- caerPoint3DEventSetZ, 198
- caerPoint3DEventValidate, 199
- PACKED\_STRUCT, 199
- POINT3D\_SCALE\_MASK, 193
- POINT3D\_SCALE\_SHIFT, 193
- POINT3D\_TYPE\_MASK, 193
- POINT3D\_TYPE\_SHIFT, 194
- point4d.h
  - CAER\_POINT4D\_ITERATOR\_ALL\_END, 200
  - CAER\_POINT4D\_ITERATOR\_ALL\_START, 200
  - CAER\_POINT4D\_ITERATOR\_VALID\_END, 200
  - CAER\_POINT4D\_ITERATOR\_VALID\_START, 201
  - caerPoint4DEvent, 201
  - caerPoint4DEventGetScale, 202
  - caerPoint4DEventGetTimestamp, 202
  - caerPoint4DEventGetTimestamp64, 202
  - caerPoint4DEventGetType, 203
  - caerPoint4DEventGetW, 203
  - caerPoint4DEventGetX, 203
  - caerPoint4DEventGetY, 203
  - caerPoint4DEventGetZ, 204
  - caerPoint4DEventInvalidate, 204
  - caerPoint4DEventIsValid, 204
  - caerPoint4DEventPacket, 201
  - caerPoint4DEventPacketAllocate, 204
  - caerPoint4DEventPacketGetEvent, 205
  - caerPoint4DEventSetScale, 205
  - caerPoint4DEventSetTimestamp, 205
  - caerPoint4DEventSetType, 206
  - caerPoint4DEventSetW, 206
  - caerPoint4DEventSetX, 206
  - caerPoint4DEventSetY, 206
  - caerPoint4DEventSetZ, 206
  - caerPoint4DEventValidate, 207
  - PACKED\_STRUCT, 207
  - POINT4D\_SCALE\_MASK, 201
  - POINT4D\_SCALE\_SHIFT, 201
  - POINT4D\_TYPE\_MASK, 201
  - POINT4D\_TYPE\_SHIFT, 201
- polarity.h
  - CAER\_POLARITY\_ITERATOR\_ALL\_END, 208
  - CAER\_POLARITY\_ITERATOR\_ALL\_START, 208
  - CAER\_POLARITY\_ITERATOR\_VALID\_END, 209
  - CAER\_POLARITY\_ITERATOR\_VALID\_START, 209
  - CAER\_POLARITY\_REVERSE\_ITERATOR\_ALL\_END, 209
  - CAER\_POLARITY\_REVERSE\_ITERATOR\_ALL\_START, 209
  - CAER\_POLARITY\_REVERSE\_ITERATOR\_VALID\_END, 209
  - CAER\_POLARITY\_REVERSE\_ITERATOR\_VALID\_START, 210
  - caerPolarityEvent, 211
  - caerPolarityEventGetPolarity, 211
  - caerPolarityEventGetTimestamp, 211
  - caerPolarityEventGetTimestamp64, 211
  - caerPolarityEventGetX, 212
  - caerPolarityEventGetY, 212
  - caerPolarityEventInvalidate, 212
  - caerPolarityEventIsValid, 213
  - caerPolarityEventPacket, 211
  - caerPolarityEventPacketAllocate, 213
  - caerPolarityEventPacketGetEvent, 213
  - caerPolarityEventSetPolarity, 214
  - caerPolarityEventSetTimestamp, 214
  - caerPolarityEventSetX, 214
  - caerPolarityEventSetY, 214
  - caerPolarityEventValidate, 214
  - PACKED\_STRUCT, 215
  - POLARITY\_MASK, 210
  - POLARITY\_SHIFT, 210
  - X\_ADDR\_MASK, 210
  - X\_ADDR\_SHIFT, 210
  - Y\_ADDR\_MASK, 210
  - Y\_ADDR\_SHIFT, 210
- portable\_endian.h, 244
- RGBA
  - frame.h, 140
- RGBG
  - frame.h, 140
- RGBW
  - frame.h, 140
- RGB
  - frame.h, 140
- ROI\_IDENTIFIER\_MASK
  - frame.h, 139
- ROI\_IDENTIFIER\_SHIFT
  - frame.h, 140
- SAMPLE\_EVENT
  - common.h, 114
- SAMPLE\_MASK
  - sample.h, 217
- SAMPLE\_SHIFT
  - sample.h, 217
- SAMPLE\_TYPE\_MASK
  - sample.h, 217
- SAMPLE\_TYPE\_SHIFT
  - sample.h, 217
- SET\_NUMBITS16
  - libcaer.h, 239
- SET\_NUMBITS32

- libcaer.h, [239](#)
- SET\_NUMBITS8
  - libcaer.h, [239](#)
- SHIFTED\_SOURCE
  - davis.h, [95](#)
- SINGLE\_DIODE
  - davis.h, [95](#)
- SPECIAL\_EVENT
  - common.h, [114](#)
- SPIKE\_CHIP\_ID\_MASK
  - spike.h, [230](#)
- SPIKE\_CHIP\_ID\_SHIFT
  - spike.h, [231](#)
- SPIKE\_EVENT
  - common.h, [114](#)
- SPIKE\_NEURON\_ID\_MASK
  - spike.h, [231](#)
- SPIKE\_NEURON\_ID\_SHIFT
  - spike.h, [231](#)
- SPIKE\_SOURCE\_CORE\_ID\_MASK
  - spike.h, [231](#)
- SPIKE\_SOURCE\_CORE\_ID\_SHIFT
  - spike.h, [231](#)
- SPLIT\_GATE
  - davis.h, [95](#)
- SWAP\_VAR
  - libcaer.h, [239](#)
- sample.h
  - CAER\_SAMPLE\_ITERATOR\_ALL\_END, [216](#)
  - CAER\_SAMPLE\_ITERATOR\_ALL\_START, [216](#)
  - CAER\_SAMPLE\_ITERATOR\_VALID\_END, [216](#)
  - CAER\_SAMPLE\_ITERATOR\_VALID\_START, [216](#)
  - caerSampleEvent, [217](#)
  - caerSampleEventGetSample, [217](#)
  - caerSampleEventGetTimestamp, [218](#)
  - caerSampleEventGetTimestamp64, [218](#)
  - caerSampleEventGetType, [218](#)
  - caerSampleEventInvalidate, [219](#)
  - caerSampleEventIsValid, [219](#)
  - caerSampleEventPacket, [217](#)
  - caerSampleEventPacketAllocate, [219](#)
  - caerSampleEventPacketGetEvent, [219](#)
  - caerSampleEventSetSample, [220](#)
  - caerSampleEventSetTimestamp, [220](#)
  - caerSampleEventSetType, [220](#)
  - caerSampleEventValidate, [220](#)
  - PACKED\_STRUCT, [221](#)
  - SAMPLE\_MASK, [217](#)
  - SAMPLE\_SHIFT, [217](#)
  - SAMPLE\_TYPE\_MASK, [217](#)
  - SAMPLE\_TYPE\_SHIFT, [217](#)
- special.h
  - APS\_EXPOSURE\_END, [224](#)
  - APS\_EXPOSURE\_START, [224](#)
  - APS\_FRAME\_END, [224](#)
  - APS\_FRAME\_START, [224](#)
  - CAER\_SPECIAL\_ITERATOR\_ALL\_END, [222](#)
  - CAER\_SPECIAL\_ITERATOR\_ALL\_START, [222](#)
  - CAER\_SPECIAL\_ITERATOR\_VALID\_END, [223](#)
  - CAER\_SPECIAL\_ITERATOR\_VALID\_START, [223](#)
  - caer\_special\_event\_types, [224](#)
  - caerSpecialEvent, [224](#)
  - caerSpecialEventGetData, [224](#)
  - caerSpecialEventGetTimestamp, [225](#)
  - caerSpecialEventGetTimestamp64, [225](#)
  - caerSpecialEventGetType, [225](#)
  - caerSpecialEventInvalidate, [226](#)
  - caerSpecialEventIsValid, [226](#)
  - caerSpecialEventPacket, [224](#)
  - caerSpecialEventPacketAllocate, [226](#)
  - caerSpecialEventPacketFindEventByType, [226](#)
  - caerSpecialEventPacketFindValidEventByType, [227](#)
  - caerSpecialEventPacketGetEvent, [227](#)
  - caerSpecialEventSetData, [227](#)
  - caerSpecialEventSetTimestamp, [228](#)
  - caerSpecialEventSetType, [228](#)
  - caerSpecialEventValidate, [228](#)
  - DATA\_MASK, [223](#)
  - DATA\_SHIFT, [223](#)
  - DVS\_ROW\_ONLY, [224](#)
  - EXTERNAL\_GENERATOR\_FALLING\_EDGE, [224](#)
  - EXTERNAL\_GENERATOR\_RISING\_EDGE, [224](#)
  - EXTERNAL\_INPUT1\_FALLING\_EDGE, [224](#)
  - EXTERNAL\_INPUT1\_PULSE, [224](#)
  - EXTERNAL\_INPUT1\_RISING\_EDGE, [224](#)
  - EXTERNAL\_INPUT2\_FALLING\_EDGE, [224](#)
  - EXTERNAL\_INPUT2\_PULSE, [224](#)
  - EXTERNAL\_INPUT2\_RISING\_EDGE, [224](#)
  - EXTERNAL\_INPUT\_FALLING\_EDGE, [224](#)
  - EXTERNAL\_INPUT\_PULSE, [224](#)
  - EXTERNAL\_INPUT\_RISING\_EDGE, [224](#)
  - PACKED\_STRUCT, [228](#)
  - TIMESTAMP\_RESET, [224](#)
  - TIMESTAMP\_WRAP, [224](#)
  - TYPE\_MASK, [223](#)
  - TYPE\_SHIFT, [223](#)
- spike.h
  - CAER\_SPIKE\_ITERATOR\_ALL\_END, [230](#)
  - CAER\_SPIKE\_ITERATOR\_ALL\_START, [230](#)
  - CAER\_SPIKE\_ITERATOR\_VALID\_END, [230](#)
  - CAER\_SPIKE\_ITERATOR\_VALID\_START, [230](#)
  - caerSpikeEvent, [231](#)
  - caerSpikeEventGetChipID, [231](#)
  - caerSpikeEventGetNeuronID, [232](#)
  - caerSpikeEventGetSourceCoreID, [232](#)
  - caerSpikeEventGetTimestamp, [232](#)
  - caerSpikeEventGetTimestamp64, [233](#)
  - caerSpikeEventInvalidate, [233](#)
  - caerSpikeEventIsValid, [233](#)
  - caerSpikeEventPacket, [231](#)
  - caerSpikeEventPacketAllocate, [233](#)
  - caerSpikeEventPacketGetEvent, [234](#)
  - caerSpikeEventSetChipID, [234](#)
  - caerSpikeEventSetNeuronID, [234](#)

- caerSpikeEventSetSourceCoreID, [234](#)
- caerSpikeEventSetTimestamp, [235](#)
- caerSpikeEventValidate, [235](#)
- PACKED\_STRUCT, [235](#)
- SPIKE\_CHIP\_ID\_MASK, [230](#)
- SPIKE\_CHIP\_ID\_SHIFT, [231](#)
- SPIKE\_NEURON\_ID\_MASK, [231](#)
- SPIKE\_NEURON\_ID\_SHIFT, [231](#)
- SPIKE\_SOURCE\_CORE\_ID\_MASK, [231](#)
- SPIKE\_SOURCE\_CORE\_ID\_SHIFT, [231](#)
- TIED\_TO\_RAIL
  - davis.h, [95](#)
- TIMESTAMP\_RESET
  - special.h, [224](#)
- TIMESTAMP\_WRAP
  - special.h, [224](#)
- TS\_OVERFLOW\_SHIFT
  - common.h, [113](#)
- TYPE\_MASK
  - special.h, [223](#)
- TYPE\_SHIFT
  - special.h, [223](#)
- U16T
  - libcaer.h, [240](#)
- U32T
  - libcaer.h, [240](#)
- U64T
  - libcaer.h, [240](#)
- U8T
  - libcaer.h, [240](#)
- usb.h
  - CAER\_HOST\_CONFIG\_DATAEXCHANGE\_BLOCKING, [106](#)
  - CAER\_HOST\_CONFIG\_DATAEXCHANGE\_BUFFER\_SIZE, [107](#)
  - CAER\_HOST\_CONFIG\_DATAEXCHANGE\_START\_PRODUCERS, [107](#)
  - CAER\_HOST\_CONFIG\_DATAEXCHANGE\_STOP\_PRODUCERS, [107](#)
  - CAER\_HOST\_CONFIG\_DATAEXCHANGE, [106](#)
  - CAER\_HOST\_CONFIG\_PACKETS\_MAX\_CONTAINER\_INTERVAL, [107](#)
  - CAER\_HOST\_CONFIG\_PACKETS\_MAX\_CONTAINER\_PACKET\_SIZE, [107](#)
  - CAER\_HOST\_CONFIG\_PACKETS, [107](#)
  - CAER\_HOST\_CONFIG\_USB\_BUFFER\_NUMBER, [108](#)
  - CAER\_HOST\_CONFIG\_USB\_BUFFER\_SIZE, [108](#)
  - CAER\_HOST\_CONFIG\_USB, [107](#)
  - caerDeviceClose, [108](#)
  - caerDeviceConfigGet, [108](#)
  - caerDeviceConfigSet, [109](#)
  - caerDeviceDataGet, [109](#)
  - caerDeviceDataStart, [109](#)
  - caerDeviceDataStop, [110](#)
  - caerDeviceHandle, [108](#)
  - caerDeviceOpen, [110](#)
  - caerDeviceSendDefaultConfig, [111](#)
  - VALID\_MARK\_MASK
    - common.h, [114](#)
  - VALID\_MARK\_SHIFT
    - common.h, [114](#)
  - WBGR
    - frame.h, [140](#)
  - X\_ADDR\_MASK
    - polarity.h, [210](#)
  - X\_ADDR\_SHIFT
    - polarity.h, [210](#)
  - Y\_ADDR\_MASK
    - polarity.h, [210](#)
  - Y\_ADDR\_SHIFT
    - polarity.h, [210](#)