REV	DESCRIPTION	DATE	BY
A3a	* PMIC reset threshold was increased from 0.84 volts to 3.07 volts by changing U13 from TPS3808G09DBVR to TPS3808G33DBVR. *SYSBOOT[15] was changed from "0" to "1", by changing R432 to DNI and installing a 10k resistor in R197. *eMMC signals connected to MMC2 were pulled high by installing 47k resistors in R250,R251,R252,R253,R254,R255,R256,R257,R258, and R259.	7/11/2016	TI

P01 TITLE PAGE
P02 12V INPUT, 3.3V AND 5V SWITCHERS
P03 TPS6590376 1 CONTROL
P04 TPS6590376 2 RESET, & RTC
P05 TPS6590376 3 MPU,DDR, & DSP SWITCHERS
P06 TPS6590376 4 CORE & 1.8V
P07 AM572X CORE POWER & EEPROM
P08 AM572X CLOCKS AND RESET
P09 AM572X IO POWER AND GROUND
P10 AM572X DDR3 POWER & JTAG
P11 AM572X ETHERNET 1
P12 AM572X ETHERNET 2
P13 AM572X GPMC & LCD
P14 AM572X USB INTERFACES
P15 AM572X I/O, LEDs, AND GPIO
P16 AM572X AUDIO & DEBUG SERIAL
P17 AM572X HDMI PORT
P18 AM572X eMMC, MMC3, & uSD
P19 AM572X DDR INTERFACE 1
P20 AM572X DDR INTERFACE 2
P21 DDR3 BANK 1
P22 DDR3 BANK 2
P23 DDR3 BANK 2 CAPS & TERM
P24 DDR3 BANK 1 CAPS & TERM
P25 USB & SATA CONNECTOR
P26 PCIe, SATA, & SERDES CLOCK
P27 EXPANSION HEADERS
P28 MECHANICAL

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TEXAS INSTRUMENTS

AM572x General Purpose EVM Processor Module

Title Page

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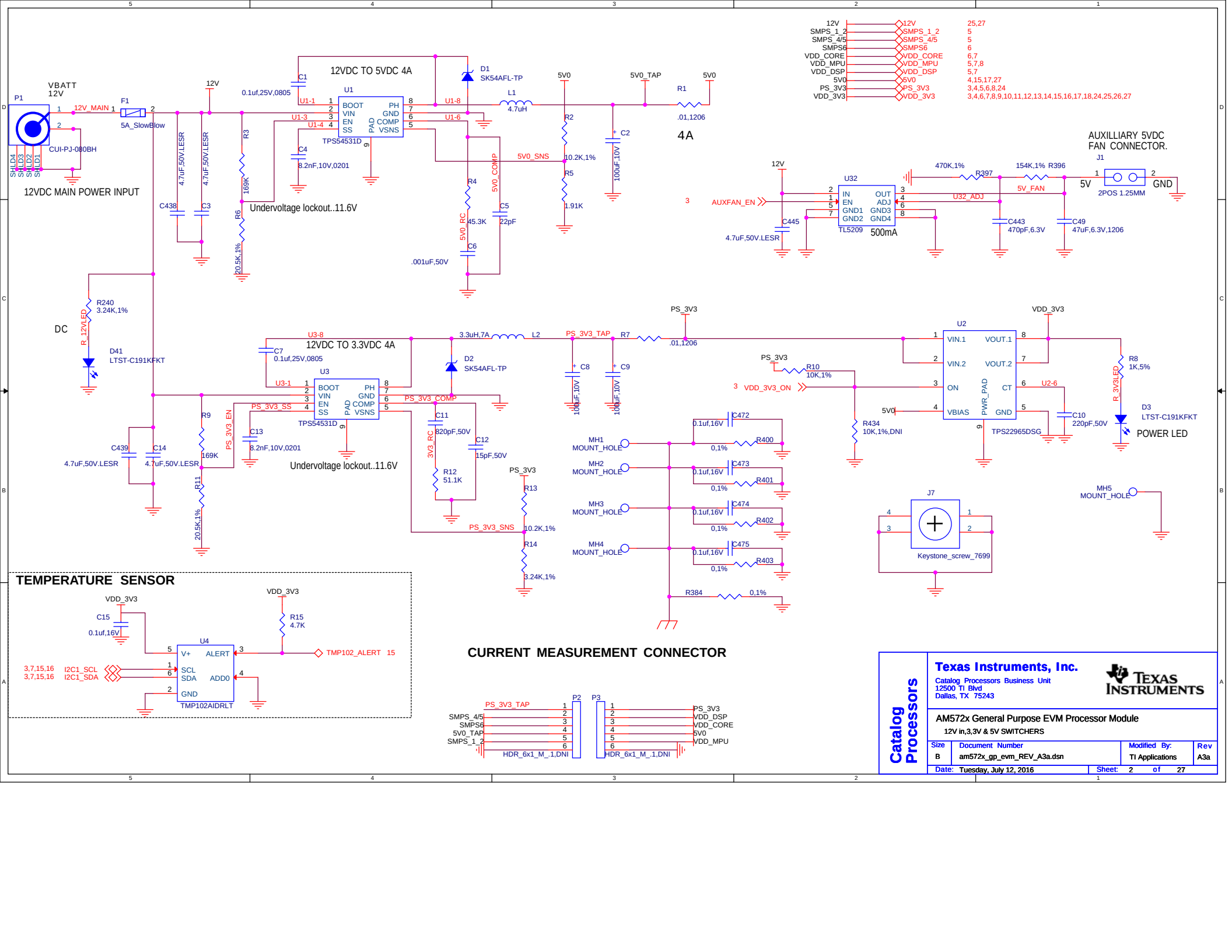
12V SMPS_1_2 SMPS_4/5 SMPS6 VDD_CORE VDD_MPU VDD_DSP 5V0 PS_3V3 VDD_3V3
12V SMPS_1_2 SMPS_4/5 SMPS6 VDD_CORE VDD_MPU VDD_DSP 5V0 PS_3V3 VDD_3V3
25,27 5 5 6 6,7 5,7,8 5,7 4,15,17,27 3,4,5,6,8,24 3,4,6,7,8,9,10,11,12,13,14,15,16,17,18,24,25,26,27
12VDC TO 5VDC 4A
VBATT 12V
P1
CUI-PJ-080BH
12V_MAIN
F1
5A_SlowBlow
12VDC MAIN POWER INPUT
C1 0.1uf,25V,0805
U1
BOOT PH VIN GND EN COMP SS VSNS PAD
TPS54531D
U1-1 U1-3 U1-4 U1-8 U1-6
C4 8.2nF,10V,0201
D1 SK54AFL-TP
L1 4.7uH
5V0 5V0_TAP
R1 .01,1206
4A
R2 10.2K,1%
R5 1.91K
C2 100uF,10V
5V0_SNS 5V0_COMP
R4 45.3K
C5 22pF
C6 .001uF,50V
R3 169K
R6 20.5K,1%
C438 C3 4.7uF,50V,LESR
Undervoltage lockout..11.6V
AUXILLIARY 5VDC FAN CONNECTOR.
J1 2POS 1.25MM
5V GND
U32 TL5209 500mA
IN OUT EN ADJ GND1 GND3 GND2 GND4
AUXFAN_EN
C445 4.7uF,50V.LESR
470K,1% R397
154K,1% R396
U32_ADJ
5V_FAN
C443 470pF,6.3V
C49 47uF,6.3V,1206
DC
R240 3.24K,1%
R_12VLED
D41 LTST-C191KFKT
12VDC TO 3.3VDC 4A
C7 0.1uf,25V,0805
U3
U3-8 U3-1
TPS54531D
3.3uH,7A L2
PS_3V3_TAP
D2 SK54AFL-TP
C8 C9 100uF,10V
R7 .01,1206
PS_3V3
PS_3V3_SS PS_3V3_EN PS_3V3_COMP
C13 8.2nF,10V,0201
C11 820pF,50V
C12 15pF,50V
3V3_RC
R12 51.1K
R13 10.2K,1%
R14 3.24K,1%
PS_3V3_SNS
C439 C14 4.7uF,50V.LESR
R9 169K
R11 20.5K,1%
Undervoltage lockout..11.6V
U2 TPS22965DSG
VIN.1 VIN.2 ON VBIAS VOUT.1 VOUT.2 CT GND PWR_PAD
VDD_3V3
R10 10K,1%
VDD_3V3_ON
R434 10K,1%,DNI
U2-6
C10 220pF,50V
R8 1K,5%
R_3V3LED
D3 LTST-C191KFKT
POWER LED
MH1 MH2 MH3 MH4 MOUNT_HOLE
C472 C473 C474 C475 0.1uf,16V
R400 R401 R402 R403 0,1%
R384 0,1%
J7 Keystone_screw_7699
MH5 MOUNT_HOLE
TEMPERATURE SENSOR
VDD_3V3
C15 0.1uf,16V
U4 TMP102AIDRLT
V+ ALERT SCL SDA ADD0 GND
R15 4.7K
TMP102_ALERT 15
3,7,15,16 I2C1_SCL
3,7,15,16 I2C1_SDA
CURRENT MEASUREMENT CONNECTOR
P2 P3
PS_3V3_TAP SMPS_4/5 SMPS6 5V0_TAP SMPS_1_2
HDR_6x1_M_.1,DNI
PS_3V3 VDD_DSP VDD_CORE 5V0 VDD_MPU
HDR_6x1_M_.1,DNI
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AM572x General Purpose EVM Processor Module
12V in,3V & 5V SWITCHERS
Size B
Document Number am572x_gp_evm_REV_A3a.dsn
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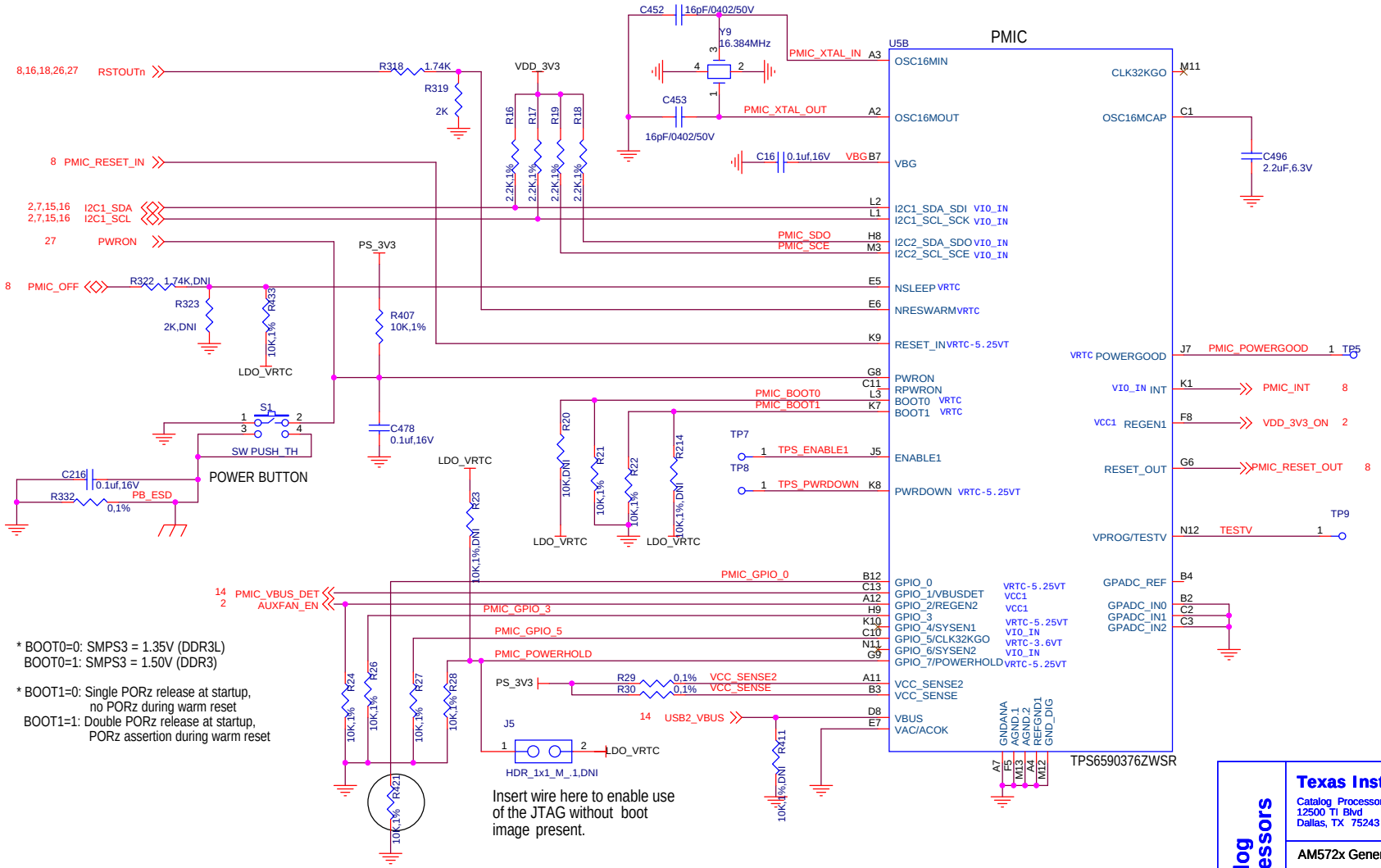
PMIC
U5B
TPS6590376ZWSR
8,16,18,26,27 RSTOUTn
R318 1.74K
R319 2K
VDD_3V3
C452 16pF/0402/50V
Y9 16.384MHz
C453 16pF/0402/50V
PMIC_XTAL_IN A3 OSC16MIN
PMIC_XTAL_OUT A2 OSC16MOUT
C16 0.1uf,16V VBG B7 VBG
CLK32KGO M11
OSC16MCAP C1
C496 2.2uF,6.3V
8 PMIC_RESET_IN
2,7,15,16 I2C1_SDA
2,7,15,16 I2C1_SCL
L2 I2C1_SDA_SDI VIO_IN
L1 I2C1_SCL_SCK VIO_IN
27 PWRON
PMIC_SDO H8
PMIC_SCE M3
I2C2_SDA_SDO VIO_IN
I2C2_SCL_SCE VIO_IN
PS_3V3
8 PMIC_OFF
R322 1.74K,DNI
R323 2K,DNI
R433 10K,1%
LDO_VRTC
R407 10K,1%
E5 NSLEEP VRTC
E6 NRESWARM VRTC
K9 RESET_IN VRTC-5.25VT
G8 PWRON
C11 RPWRON
L3 BOOT0 VRTC
K7 BOOT1 VRTC
PMIC_BOOT0
PMIC_BOOT1
S1
SW PUSH_TH
C478 0.1uf,16V
POWER BUTTON
C216 0.1uf,16V
R332 0,1%
PB_ESD
TP7
TP8
TPS_ENABLE1 J5 ENABLE1
TPS_PWRDOWN K8 PWRDOWN VRTC-5.25VT
R20 10K,DNI
R21 10K,1%
R22 10K,1%
R214 10K,1%,DNI
R23 10K,1%,DNI
VRTC POWERGOOD
J7 PMIC_POWERGOOD 1 TP5
VIO_IN INT
K1 PMIC_INT 8
VCC1 REGEN1
F8 VDD_3V3_ON 2
RESET_OUT
G6 PMIC_RESET_OUT 8
TP9
VPROG/TESTV
N12 TESTV 1
GPADC_REF B4
GPADC_IN0 B2
GPADC_IN1 C2
GPADC_IN2 C3
14 PMIC_VBUS_DET
2 AUXFAN_EN
PMIC_GPIO_0 B12 GPIO_0 VRTC-5.25VT
C13 GPIO_1/VBUSDET VCC1
PMIC_GPIO_3 A12 GPIO_2/REGEN2 VCC1
H9 GPIO_3
PMIC_GPIO_5 K10 GPIO_4/SYSEN1 VRTC-5.25VT
C10 GPIO_5/CLK32KGO VIO_IN
PMIC_POWERHOLD N11 GPIO_6/SYSEN2 VRTC-3.6VT
G9 GPIO_7/POWERHOLD VIO_IN VRTC-5.25VT
R24 10K,1%
R26 10K,1%
R27 10K,1%
R28 10K,1%
R421 10K,1%
R29 0,1% VCC_SENSE2
R30 0,1% VCC_SENSE
A11 VCC_SENSE2
B3 VCC_SENSE
14 USB2_VBUS
D8 VBUS
E7 VAC/ACOK
R411 10K,1%,DNI
GNDANA A7
AGND_1 F5
AGND_2 M13
REFGND1 A4
GND_DIG M12
J5
HDR_1x1_M_.1,DNI
LDO_VRTC
Insert wire here to enable use of the JTAG without boot image present.
* BOOT0=0: SMPS3 = 1.35V (DDR3L)
BOOT0=1: SMPS3 = 1.50V (DDR3)
* BOOT1=0: Single PORz release at startup, no PORz during warm reset
BOOT1=1: Double PORz release at startup, PORz assertion during warm reset
VDD_3V3 VDD_3V3 2,4,6,7,8,9,10,11,12,13,14,15,16,17,18,24,25,26,27
PS_3V3 PS_3V3 2,4,5,6,8,24
LDO_VRTC LDO_VRTC 4,8
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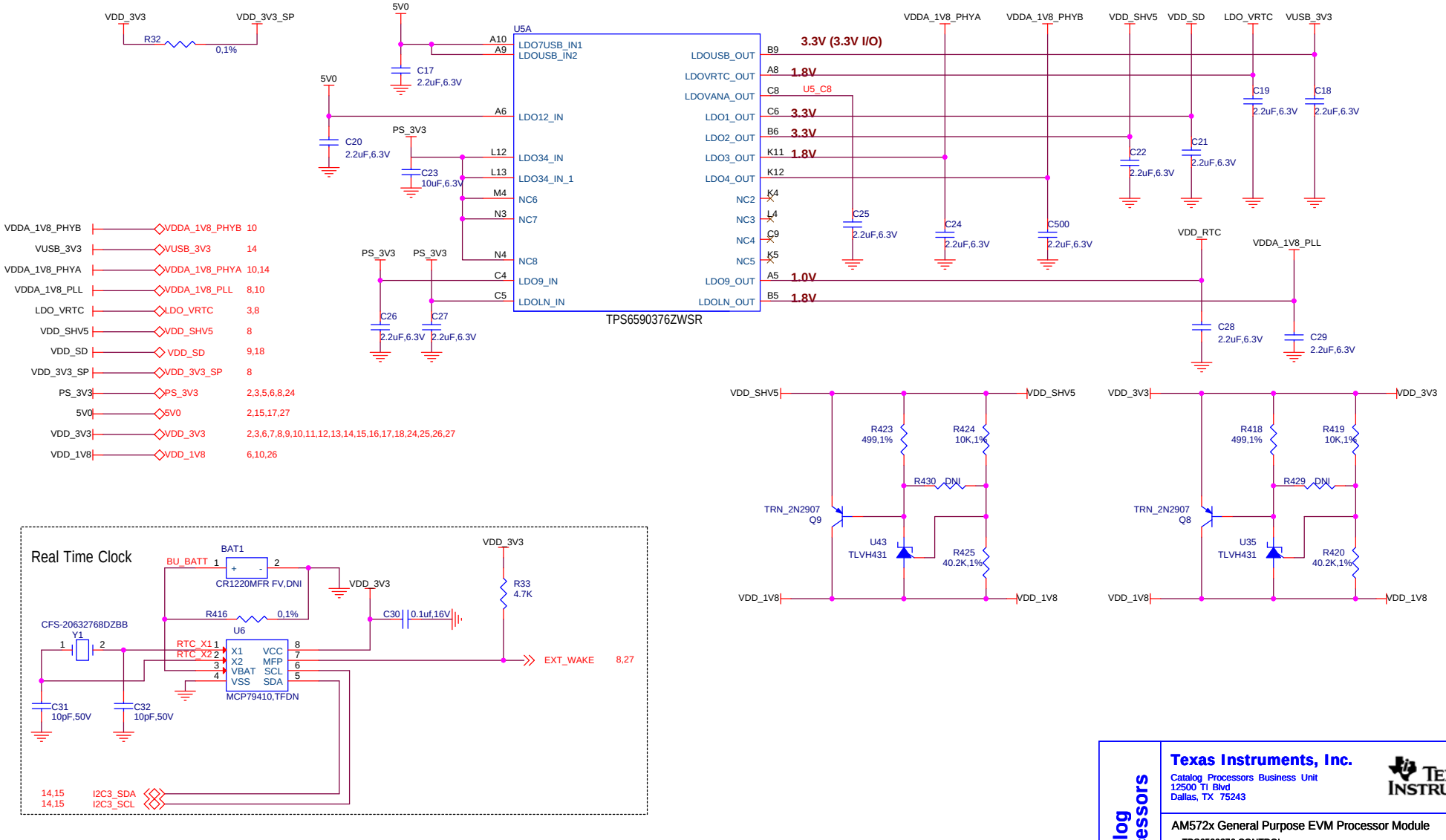

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AM572x General Purpose EVM Processor Module
TPS6590376 CONTROL

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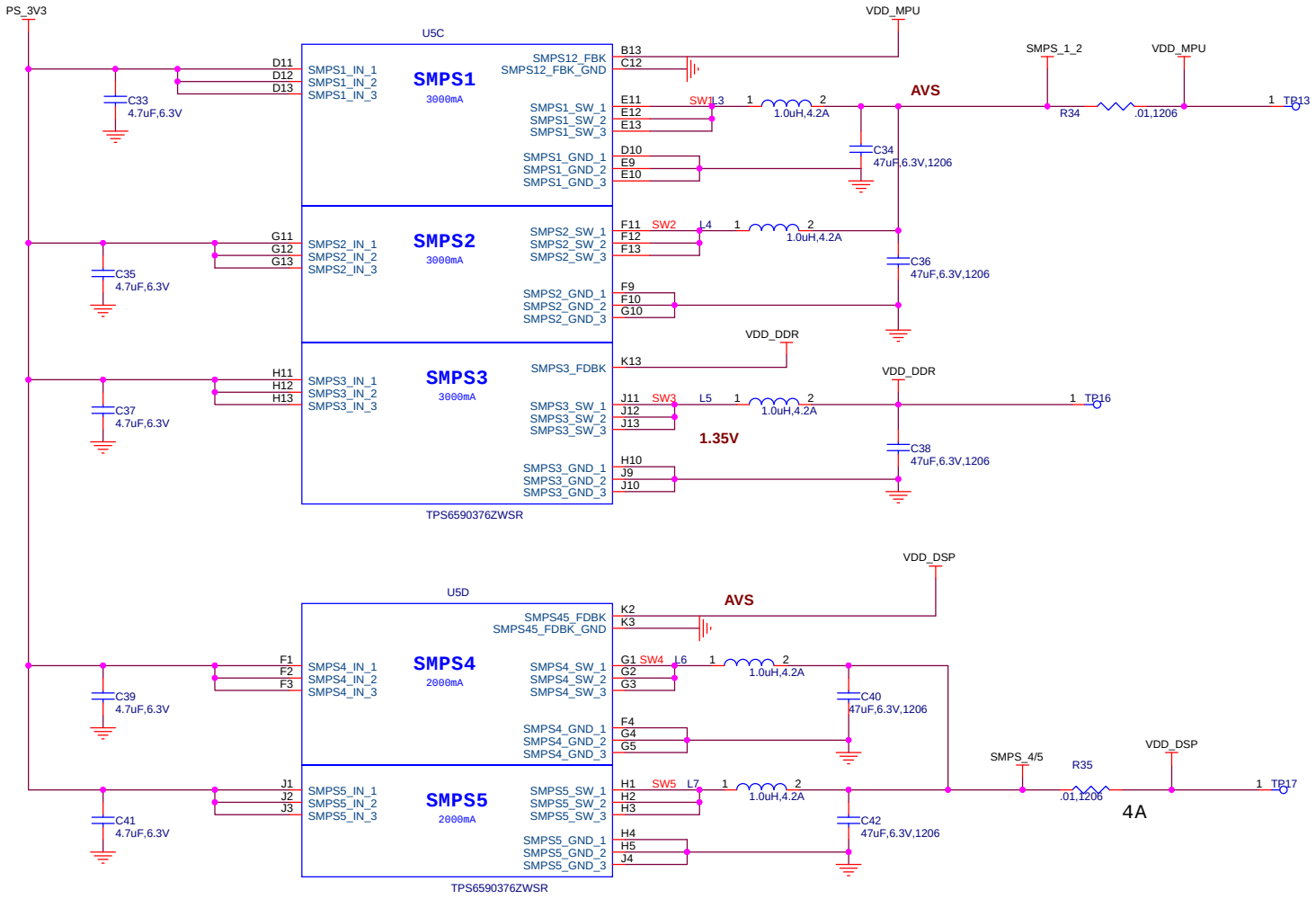

SMPS_1_2	SMPS_1_2	2
VDD_MPU	VDD_MPU	2,7,8
VDD_DDR	VDD_DDR	10,19,21,22,23,24
VDD_DSP	VDD_DSP	2,7
PS_3V3	PS_3V3	2,3,4,6,8,24
SMPS_4/5	SMPS_4/5	2

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AM572x General Purpose EVM Processor Module
TPS6590376 MPU, DDR, & DSP

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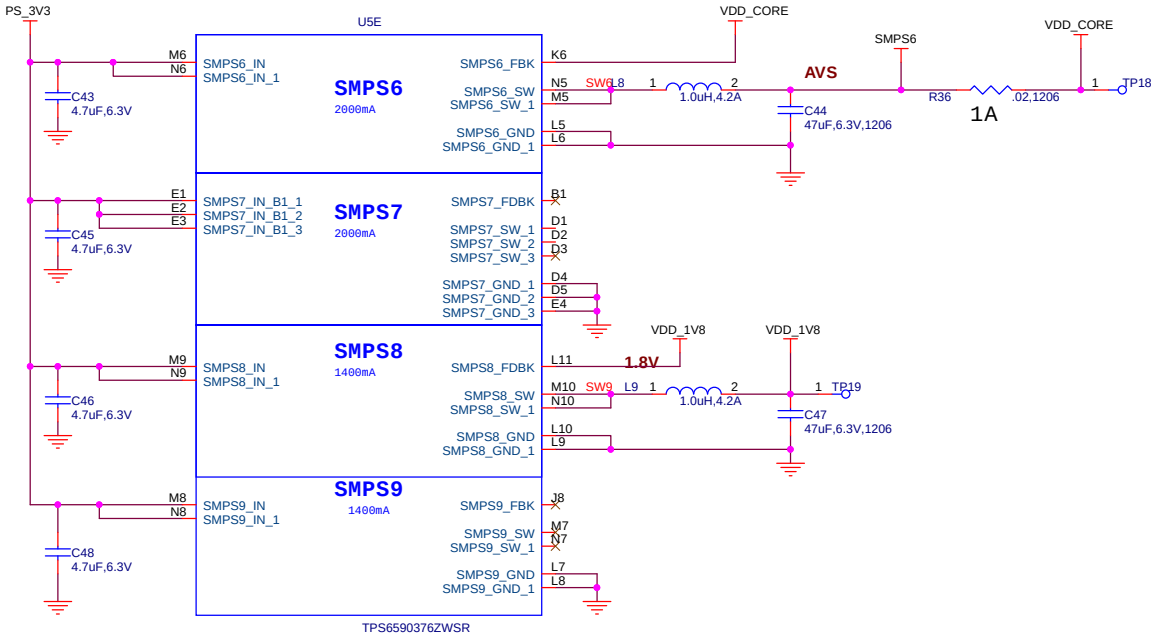

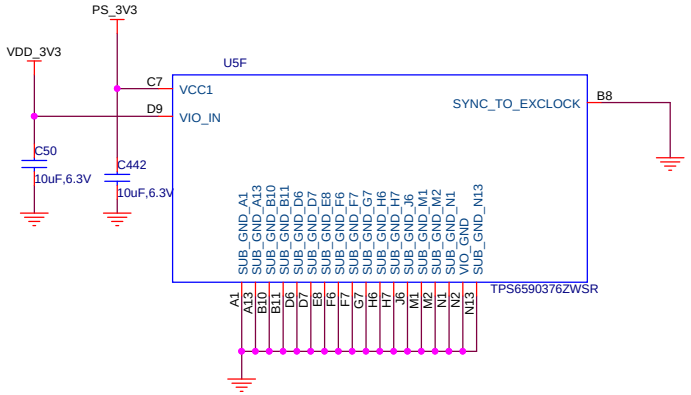

SMPS6	SMPS6	2
PS_3V3	PS_3V3	2,3,4,5,8,24
VDD_3V3	VDD_3V3	2,3,4,7,8,9,10,11,12,13,14,15,16,17,18,24,25,26,27
VDD_1V8	VDD_1V8	4,10,26
VDD_CORE	VDD_CORE	2,7

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AM572x General Purpose EVM Processor Module
TPS6590376 SWITCHERS

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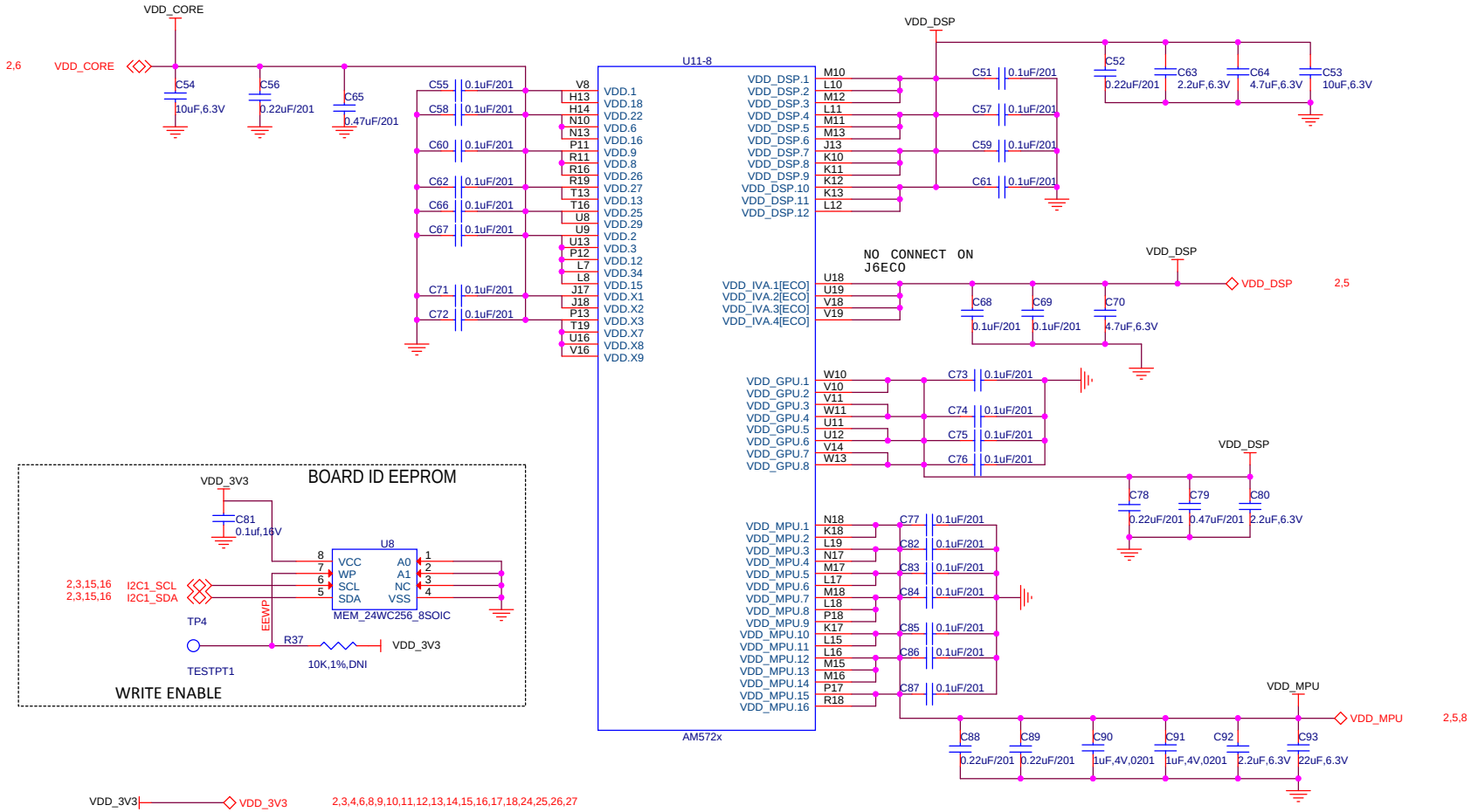

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AM572x General Purpose EVM Processor Module

AM5728 CORE POWER

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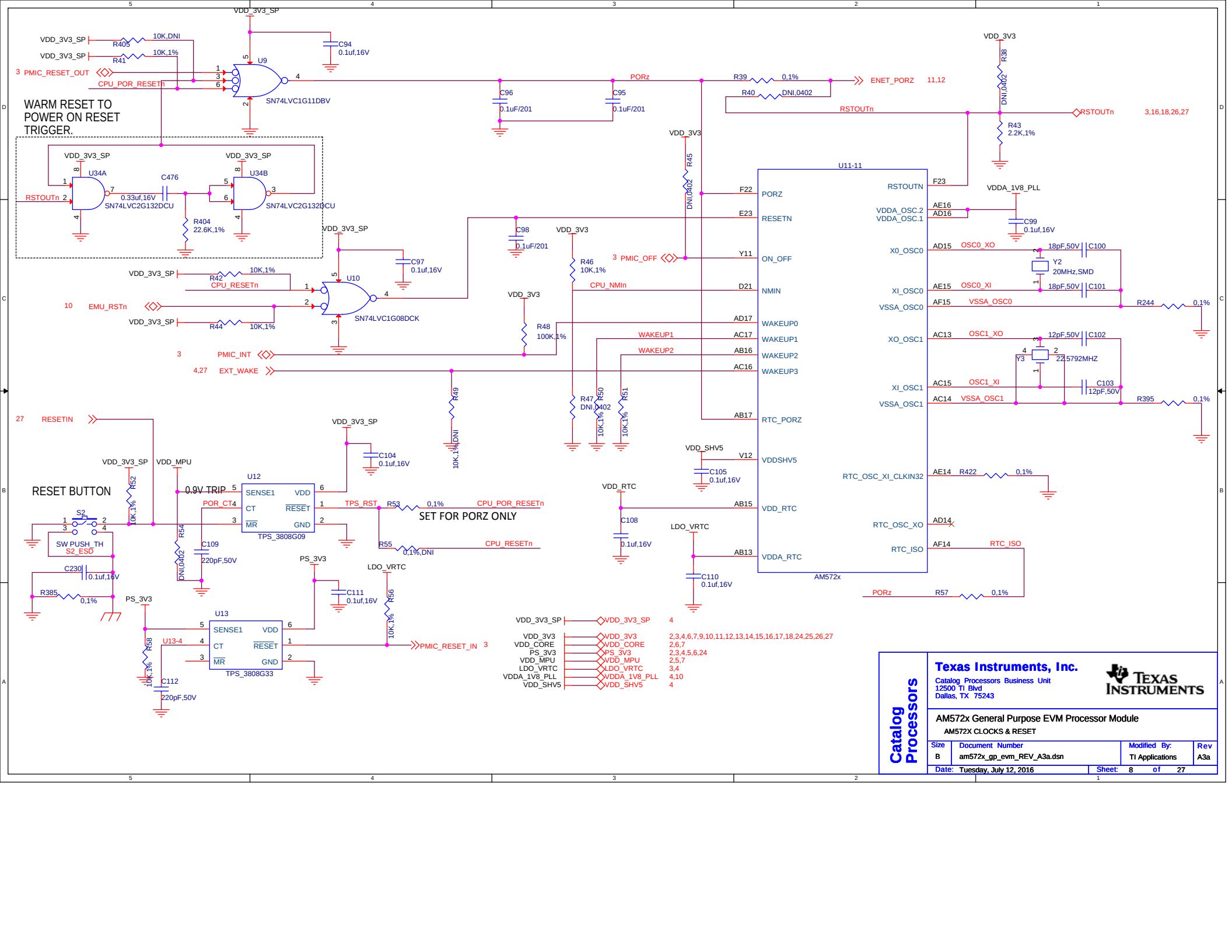
WARM RESET TO POWER ON RESET TRIGGER.
RESET BUTTON
SET FOR PORZ ONLY
U11-11
AM572x
SN74LVC1G11DBV
SN74LVC2G132DCU
SN74LVC1G08DCK
TPS_3808G09
TPS_3808G33
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AM572x General Purpose EVM Processor Module
AM572X CLOCKS & RESET
Size B
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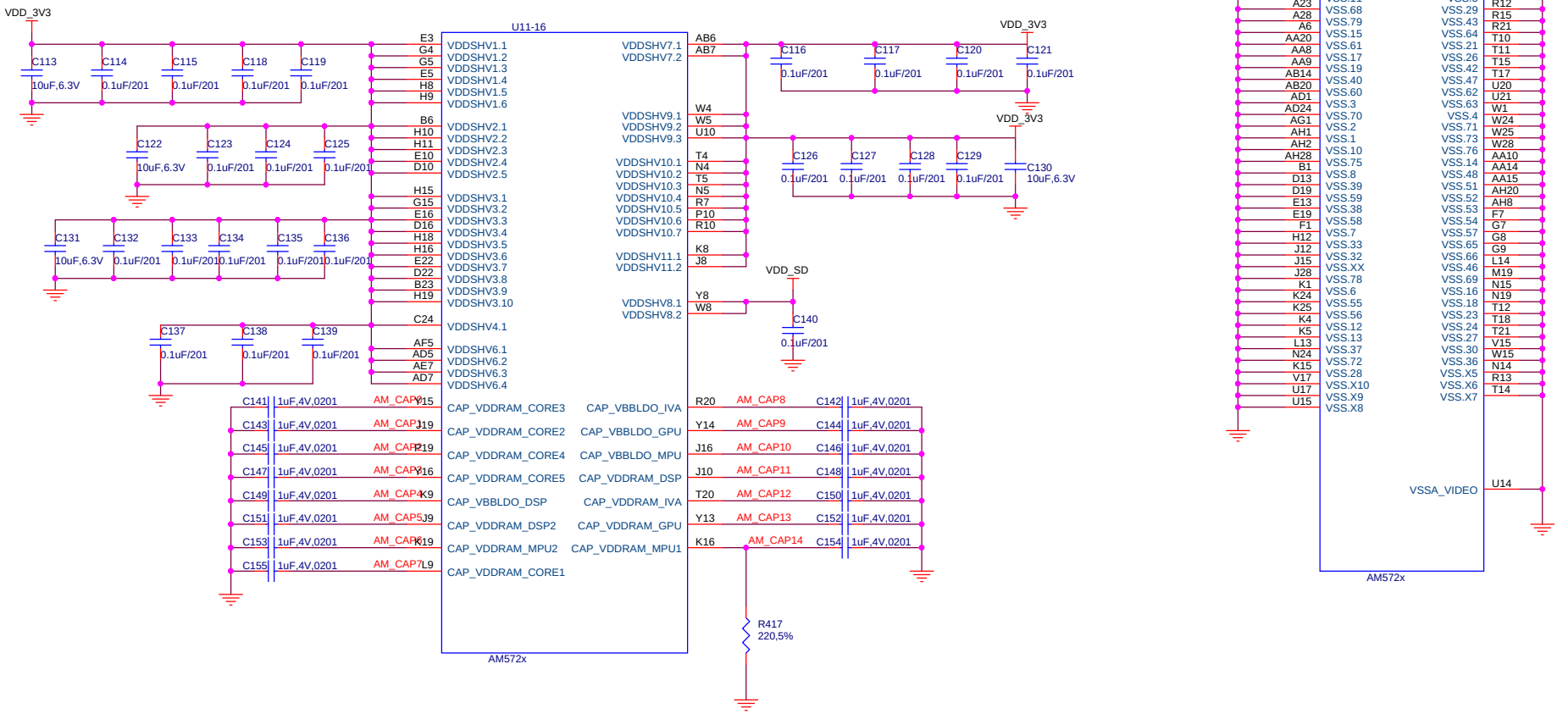

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AM572x General Purpose EVM Processor Module

AM572X I/O POWER & GROUND

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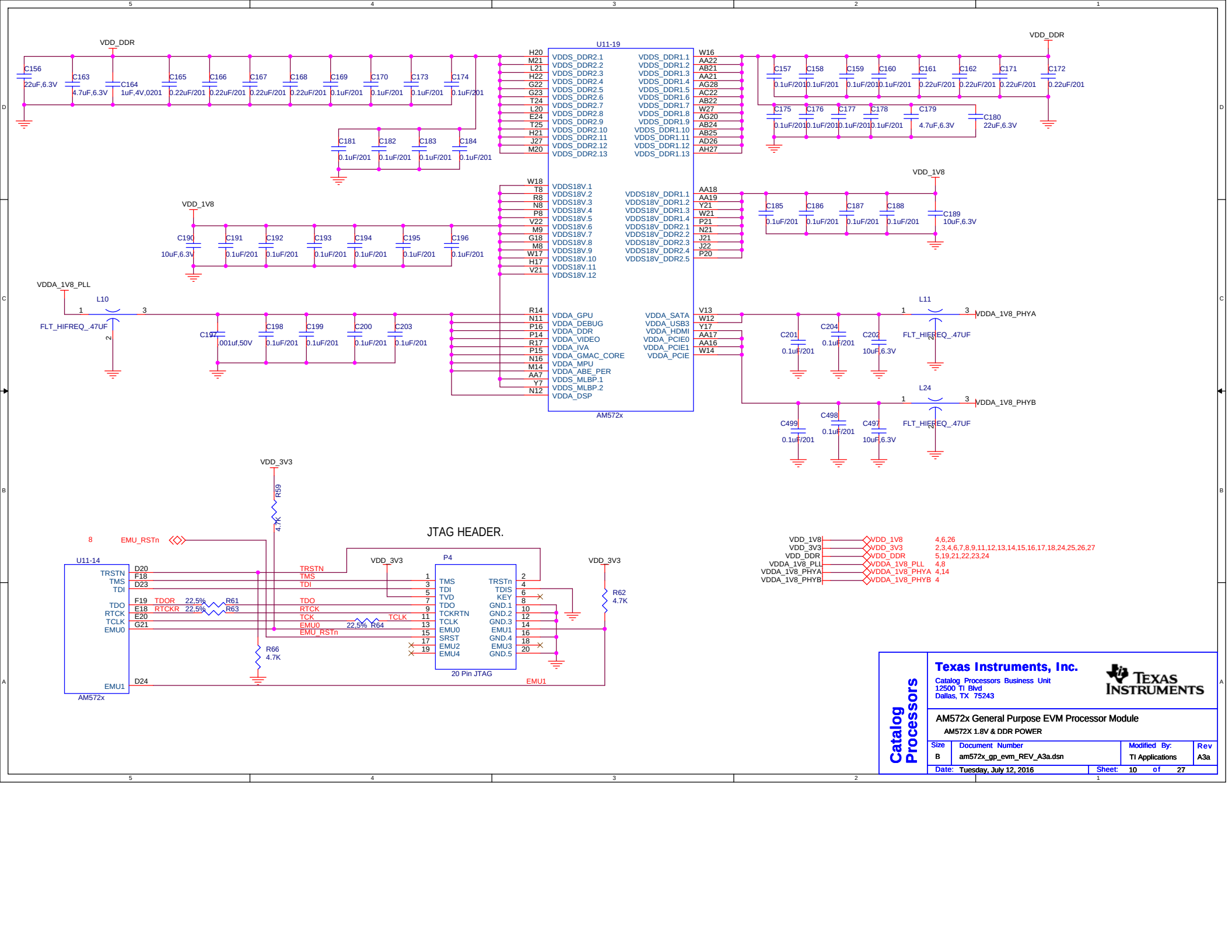
VDD_DDR
C156 22uF,6.3V
C163 4.7uF,6.3V
C164 1uF,4V,0201
C165 0.22uF/201
C166 0.22uF/201
C167 0.22uF/201
C168 0.22uF/201
C169 0.1uF/201
C170 0.1uF/201
C173 0.1uF/201
C174 0.1uF/201
C181 0.1uF/201
C182 0.1uF/201
C183 0.1uF/201
C184 0.1uF/201
U11-19
VDDS_DDR2.1
VDDS_DDR2.2
VDDS_DDR2.3
VDDS_DDR2.4
VDDS_DDR2.5
VDDS_DDR2.6
VDDS_DDR2.7
VDDS_DDR2.8
VDDS_DDR2.9
VDDS_DDR2.10
VDDS_DDR2.11
VDDS_DDR2.12
VDDS_DDR2.13
VDDS_DDR1.1
VDDS_DDR1.2
VDDS_DDR1.3
VDDS_DDR1.4
VDDS_DDR1.5
VDDS_DDR1.6
VDDS_DDR1.7
VDDS_DDR1.8
VDDS_DDR1.9
VDDS_DDR1.10
VDDS_DDR1.11
VDDS_DDR1.12
VDDS_DDR1.13
C157 0.1uF/201
C158 0.1uF/201
C159 0.1uF/201
C160 0.1uF/201
C161 0.22uF/201
C162 0.22uF/201
C171 0.22uF/201
C172 0.22uF/201
C175 0.1uF/201
C176 0.1uF/201
C177 0.1uF/201
C178 0.1uF/201
C179 4.7uF,6.3V
C180 22uF,6.3V
VDD_1V8
C190 10uF,6.3V
C191 0.1uF/201
C192 0.1uF/201
C193 0.1uF/201
C194 0.1uF/201
C195 0.1uF/201
C196 0.1uF/201
VDDS18V.1
VDDS18V.2
VDDS18V.3
VDDS18V.4
VDDS18V.5
VDDS18V.6
VDDS18V.7
VDDS18V.8
VDDS18V.9
VDDS18V.10
VDDS18V.11
VDDS18V.12
VDDS18V_DDR1.1
VDDS18V_DDR1.2
VDDS18V_DDR1.3
VDDS18V_DDR1.4
VDDS18V_DDR2.1
VDDS18V_DDR2.2
VDDS18V_DDR2.3
VDDS18V_DDR2.4
VDDS18V_DDR2.5
C185 0.1uF/201
C186 0.1uF/201
C187 0.1uF/201
C188 0.1uF/201
C189 10uF,6.3V
VDDA_1V8_PLL
L10
FLT_HIFREQ_.47UF
C197 .001uf,50V
C198 0.1uF/201
C199 0.1uF/201
C200 0.1uF/201
C203 0.1uF/201
VDDA_GPU
VDDA_DEBUG
VDDA_DDR
VDDA_VIDEO
VDDA_IVA
VDDA_GMAC_CORE
VDDA_MPU
VDDA_ABE_PER
VDDS_MLBP.1
VDDS_MLBP.2
VDDA_DSP
VDDA_SATA
VDDA_USB3
VDDA_HDMI
VDDA_PCIE0
VDDA_PCIE1
VDDA_PCIE
AM572x
L11
VDDA_1V8_PHYA
C201 0.1uF/201
C204 0.1uF/201
C202 10uF,6.3V
FLT_HIFREQ_.47UF
L24
VDDA_1V8_PHYB
C499 0.1uF/201
C498 0.1uF/201
C497 10uF,6.3V
FLT_HIFREQ_.47UF
VDD_3V3
R59 4.7K
JTAG HEADER.
8 EMU_RSTn
U11-14
TRSTN
TMS
TDI
TDO
RTCK
TCLK
EMU0
EMU1
D20
F18
D23
F19 TDOR 22,5% R61
E18 RTCKR 22,5% R63
E20
G21
D24
R66 4.7K
P4
20 Pin JTAG
R64 22,5%
R62 4.7K
VDD_1V8 4,6,26
VDD_3V3 2,3,4,6,7,8,9,11,12,13,14,15,16,17,18,24,25,26,27
VDD_DDR 5,19,21,22,23,24
VDDA_1V8_PLL 4,8
VDDA_1V8_PHYA 4,14
VDDA_1V8_PHYB 4
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AM572x General Purpose EVM Processor Module
AM572X 1.8V & DDR POWER
Size B
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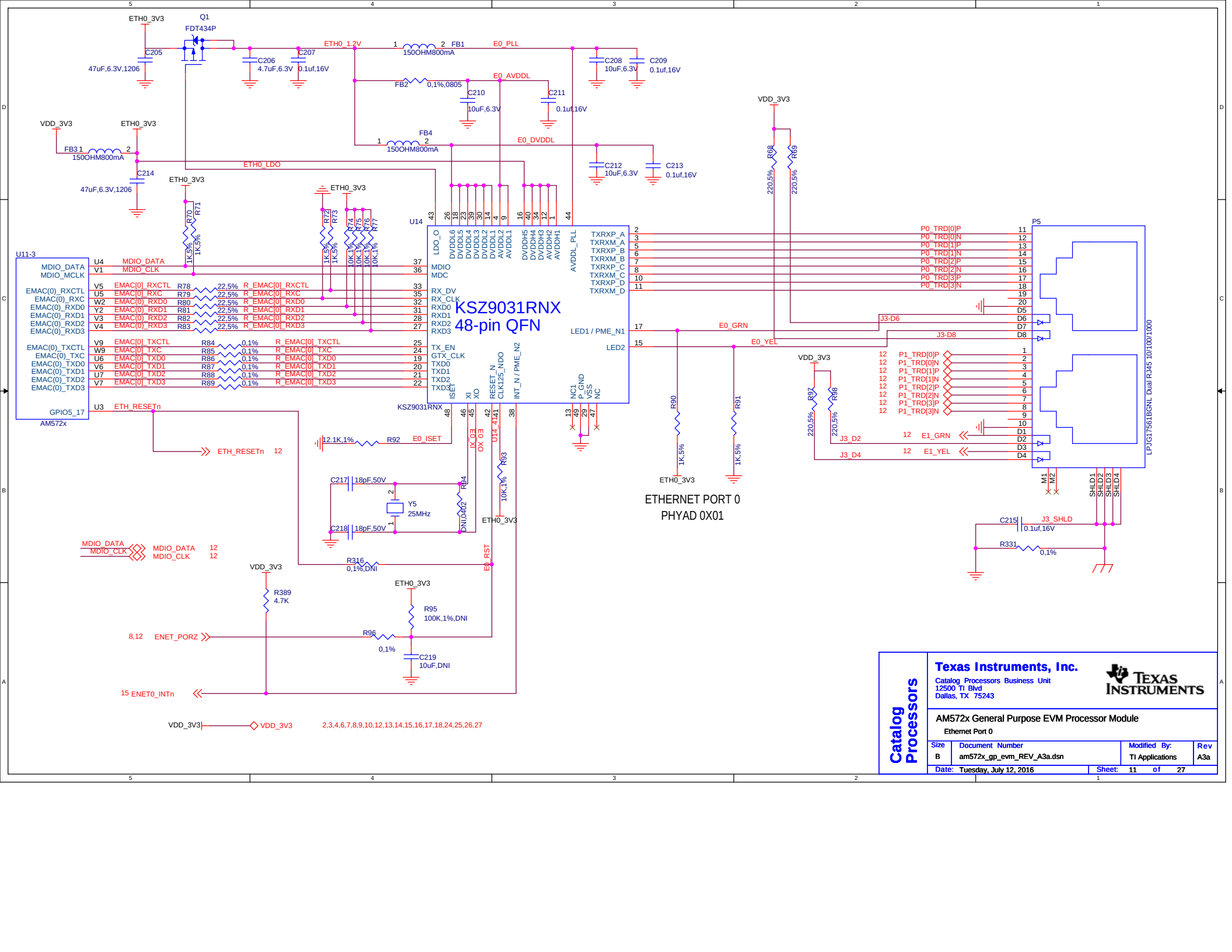
KSZ9031RNX
48-pin QFN
ETHERNET PORT 0
PHYAD 0X01
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Catalog Processors
AM572x General Purpose EVM Processor Module
Ethernet Port 0
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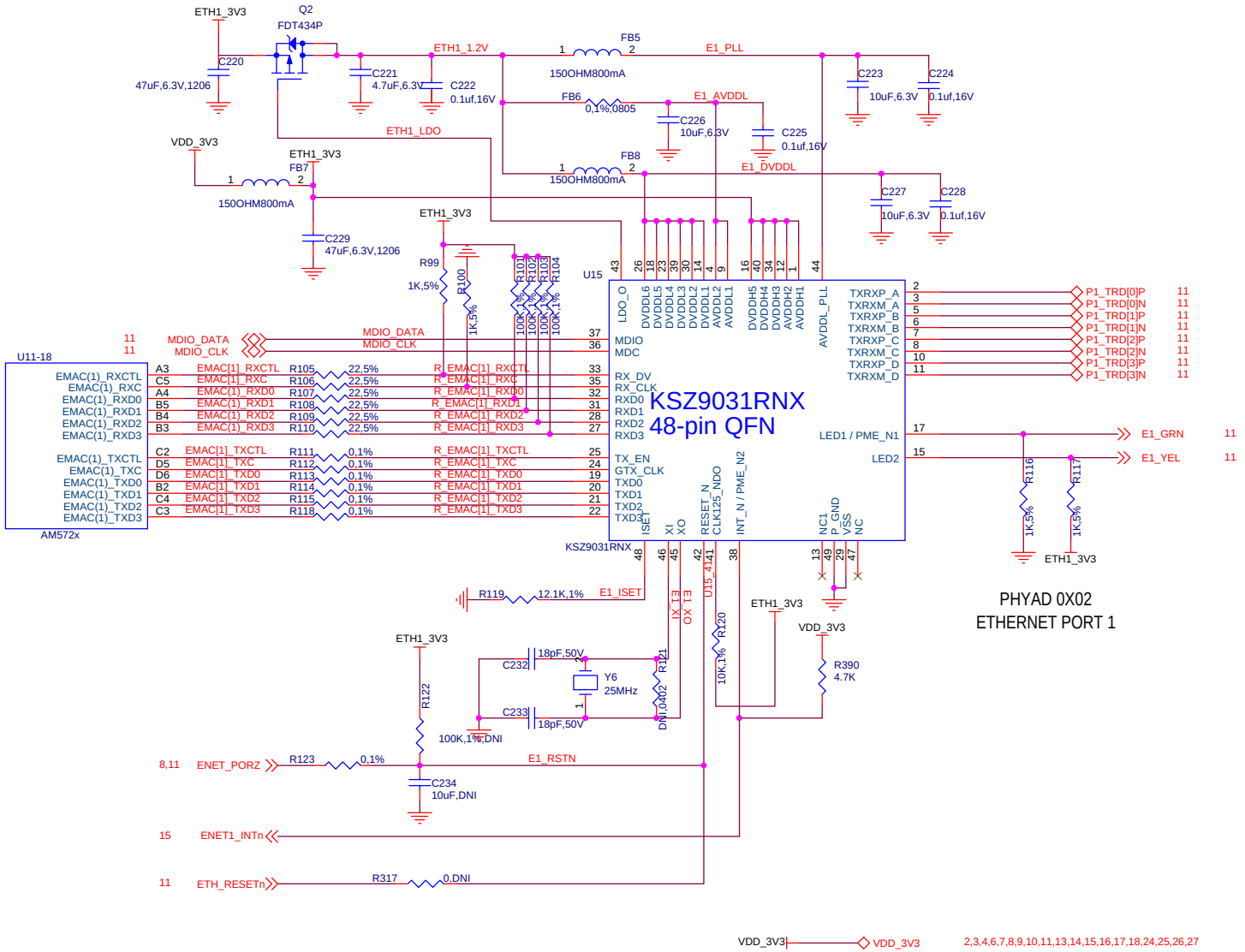

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AM572x General Purpose EVM Processor Module
Ethernet Port 1

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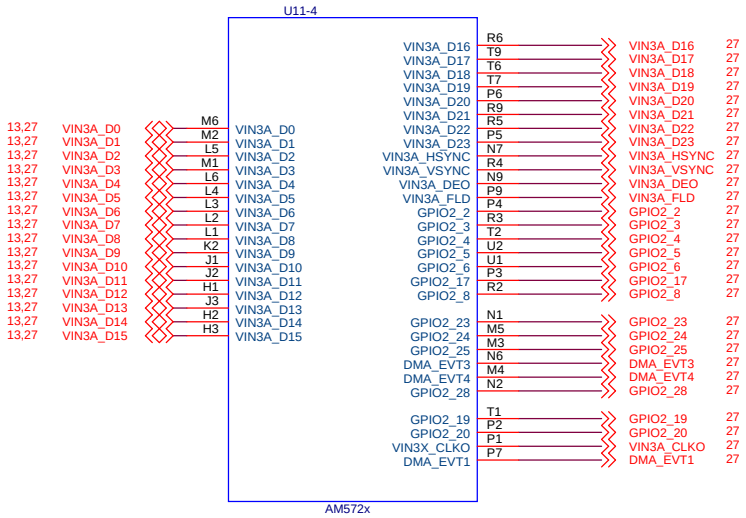
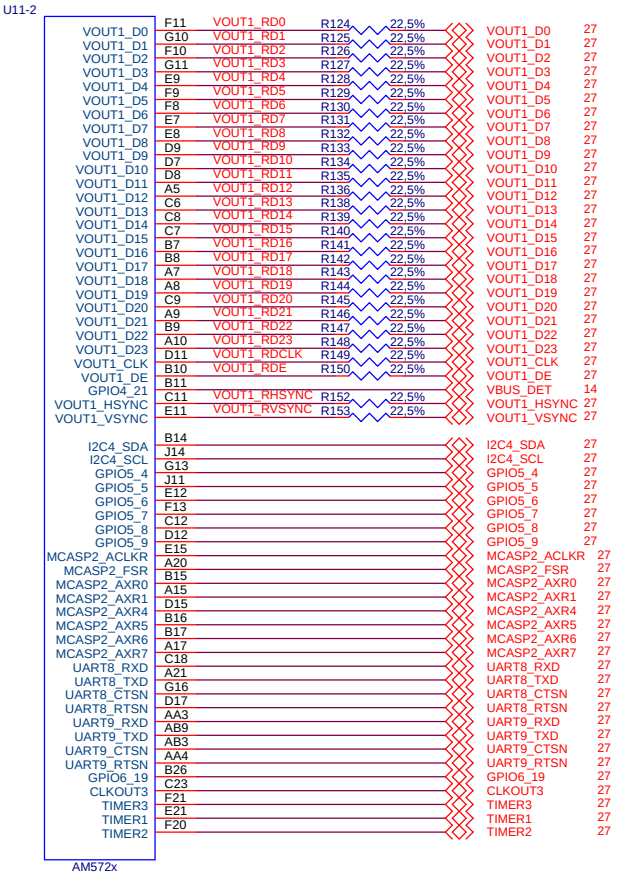
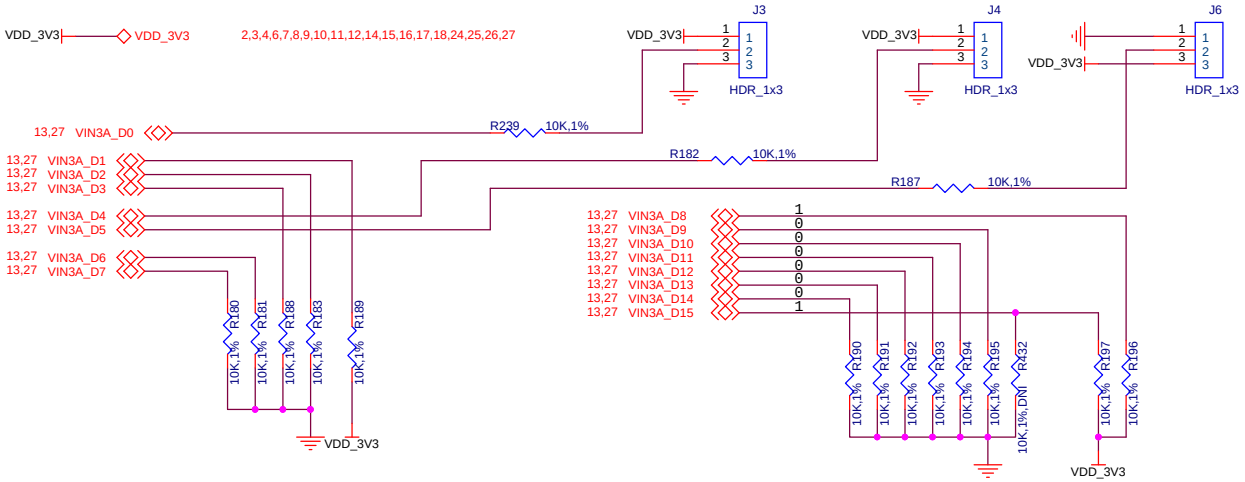


		1-2	2-3
BOOT from SD then eMMC	J3		X
	J4		X
	J6		X
BOOT from UART	J3	X	
	J4	X	
	J6	X	
BOOT from SATA then SD	J3	X	
	J4		X
	J6		X





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AM572x General Purpose EVM Processor Module

I/O LCD and Boot

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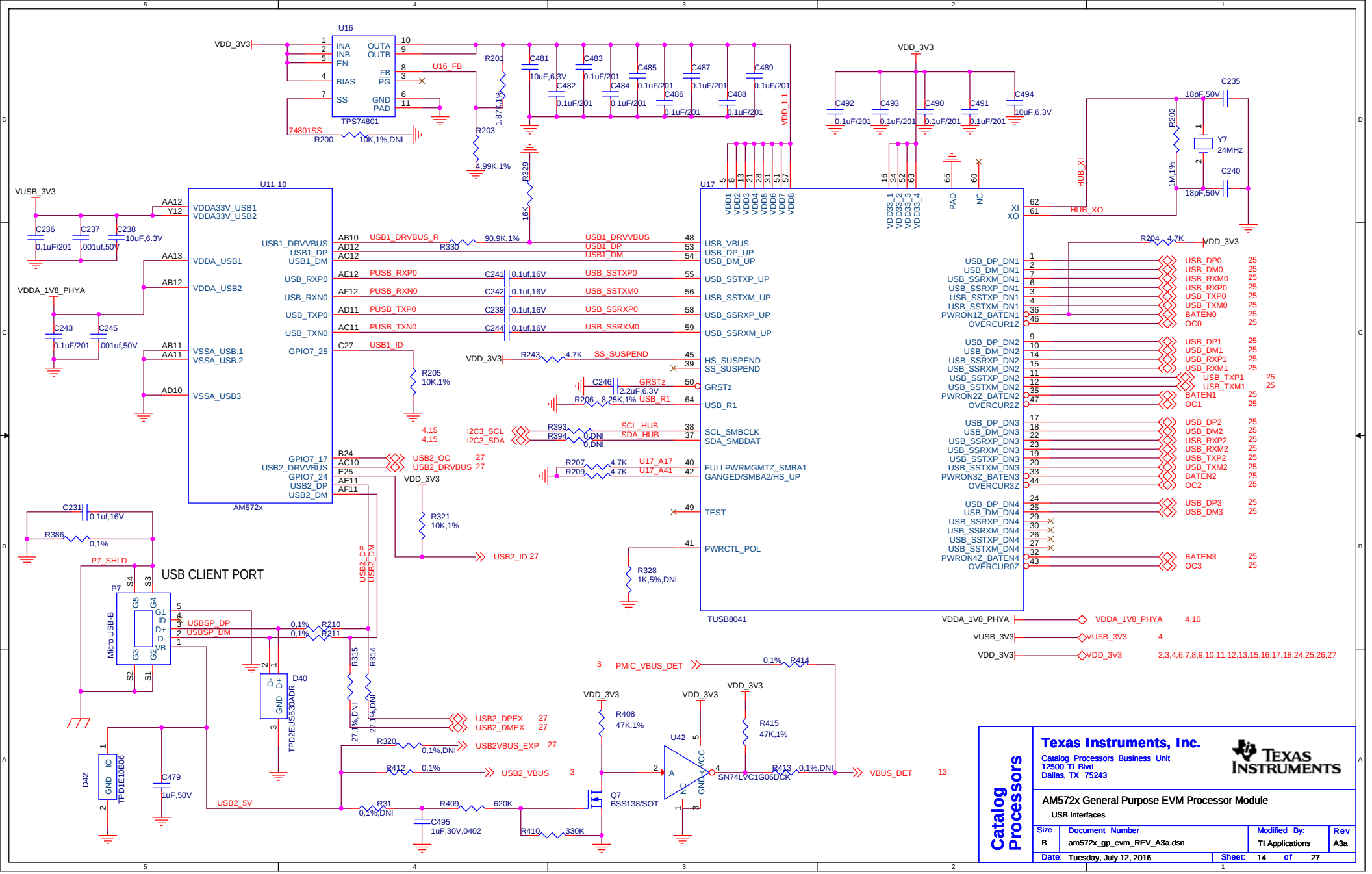
USB CLIENT PORT
TUSB8041
AM572x
TPS74801
SN74LVC1G06DCK
TPD2EUSB30ADR
TPD1E10B06
BSS138/SOT
Micro USB-B
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AM572x General Purpose EVM Processor Module
USB Interfaces
Size B
Document Number am572x_gp_evm_REV_A3a.dsn
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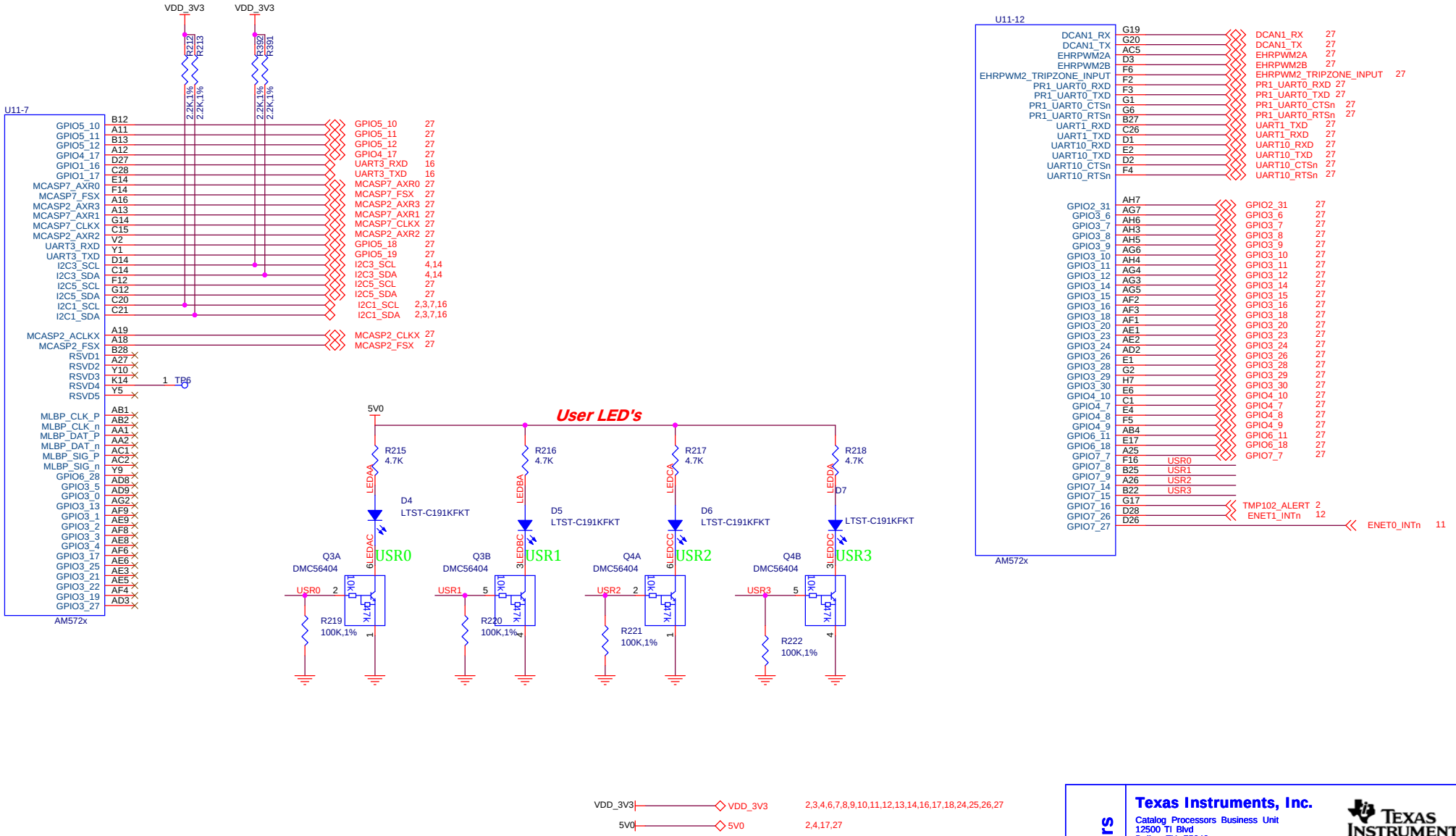
VDD_3V3
VDD_3V3
R212
R213
R392
R391
2.2K,1%
U11-7
GPIO5_10 B12
GPIO5_11 A11
GPIO5_12 B13
GPIO4_17 A12
GPIO1_16 D27
GPIO1_17 C28
MCASP7_AXR0 E14
MCASP7_FSX F14
MCASP2_AXR3 A16
MCASP7_AXR1 A13
MCASP7_CLKX G14
MCASP2_AXR2 C15
UART3_RXD V2
UART3_TXD Y1
I2C3_SCL D14
I2C3_SDA C14
I2C5_SCL F12
I2C5_SDA G12
I2C1_SCL C20
I2C1_SDA C21
MCASP2_ACLKX A19
MCASP2_FSX A18
RSVD1 B28
RSVD2 A27
RSVD3 Y10
RSVD4 K14
RSVD5 Y5
TP6
MLBP_CLK_P AB1
MLBP_CLK_n AB2
MLBP_DAT_P AA1
MLBP_DAT_n AA2
MLBP_SIG_P AC1
MLBP_SIG_n AC2
GPIO6_28 Y9
GPIO3_5 AD8
GPIO3_0 AD9
GPIO3_13 AG2
GPIO3_1 AF9
GPIO3_2 AE9
GPIO3_3 AF8
GPIO3_4 AE8
GPIO3_17 AF6
GPIO3_25 AE6
GPIO3_21 AE3
GPIO3_22 AE5
GPIO3_19 AF4
GPIO3_27 AD3
AM572x
GPIO5_10 27
GPIO5_11 27
GPIO5_12 27
GPIO4_17 27
UART3_RXD 16
UART3_TXD 16
MCASP7_AXR0 27
MCASP7_FSX 27
MCASP2_AXR3 27
MCASP7_AXR1 27
MCASP7_CLKX 27
MCASP2_AXR2 27
GPIO5_18 27
GPIO5_19 27
I2C3_SCL 4,14
I2C3_SDA 4,14
I2C5_SCL 27
I2C5_SDA 27
I2C1_SCL 2,3,7,16
I2C1_SDA 2,3,7,16
MCASP2_CLKX 27
MCASP2_FSX 27
User LED's
5V0
R215 4.7K
R216 4.7K
R217 4.7K
R218 4.7K
LEDAA
LEDABA
LEDACA
LEDADA
D4 LTST-C191KFKT
D5 LTST-C191KFKT
D6 LTST-C191KFKT
D7 LTST-C191KFKT
LEDAC
LEDABC
LEDACC
LEDADC
USR0
USR1
USR2
USR3
Q3A DMC56404
Q3B DMC56404
Q4A DMC56404
Q4B DMC56404
10K
47K
R219 100K,1%
R220 100K,1%
R221 100K,1%
R222 100K,1%
U11-12
DCAN1_RX G19
DCAN1_TX G20
EHRPWM2A AC5
EHRPWM2B D3
EHRPWM2_TRIPZONE_INPUT F6
PR1_UART0_RXD F2
PR1_UART0_TXD F3
PR1_UART0_CTSn G1
PR1_UART0_RTSn G6
UART1_RXD B27
UART1_TXD C26
UART10_RXD D1
UART10_TXD E2
UART10_CTSn D2
UART10_RTSn F4
GPIO2_31 AH7
GPIO3_6 AG7
GPIO3_7 AH6
GPIO3_8 AH3
GPIO3_9 AH5
GPIO3_10 AG6
GPIO3_11 AH4
GPIO3_12 AG4
GPIO3_14 AG3
GPIO3_15 AG5
GPIO3_16 AF2
GPIO3_18 AF3
GPIO3_20 AF1
GPIO3_23 AE1
GPIO3_24 AE2
GPIO3_26 AD2
GPIO3_28 E1
GPIO3_29 G2
GPIO3_30 H7
GPIO4_10 E6
GPIO4_7 C1
GPIO4_8 E4
GPIO4_9 F5
GPIO6_11 AB4
GPIO6_18 E17
GPIO7_7 A25
USR0 F16
USR1 B25
USR2 A26
USR3 B22
TMP102_ALERT G17
ENET1_INTn D28
D26
AM572x
DCAN1_RX 27
DCAN1_TX 27
EHRPWM2A 27
EHRPWM2B 27
EHRPWM2_TRIPZONE_INPUT 27
PR1_UART0_RXD 27
PR1_UART0_TXD 27
PR1_UART0_CTSn 27
PR1_UART0_RTSn 27
UART1_TXD 27
UART1_RXD 27
UART10_RXD 27
UART10_TXD 27
UART10_CTSn 27
UART10_RTSn 27
GPIO2_31 27
GPIO3_6 27
GPIO3_7 27
GPIO3_8 27
GPIO3_9 27
GPIO3_10 27
GPIO3_11 27
GPIO3_12 27
GPIO3_14 27
GPIO3_15 27
GPIO3_16 27
GPIO3_18 27
GPIO3_20 27
GPIO3_23 27
GPIO3_24 27
GPIO3_26 27
GPIO3_28 27
GPIO3_29 27
GPIO3_30 27
GPIO4_10 27
GPIO4_7 27
GPIO4_8 27
GPIO4_9 27
GPIO6_11 27
GPIO6_18 27
GPIO7_7 27
TMP102_ALERT 2
ENET1_INTn 12
ENET0_INTn 11
VDD_3V3 VDD_3V3 2,3,4,6,7,8,9,10,11,12,13,14,16,17,18,24,25,26,27
5V0 5V0 2,4,17,27
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I/O and LEDs
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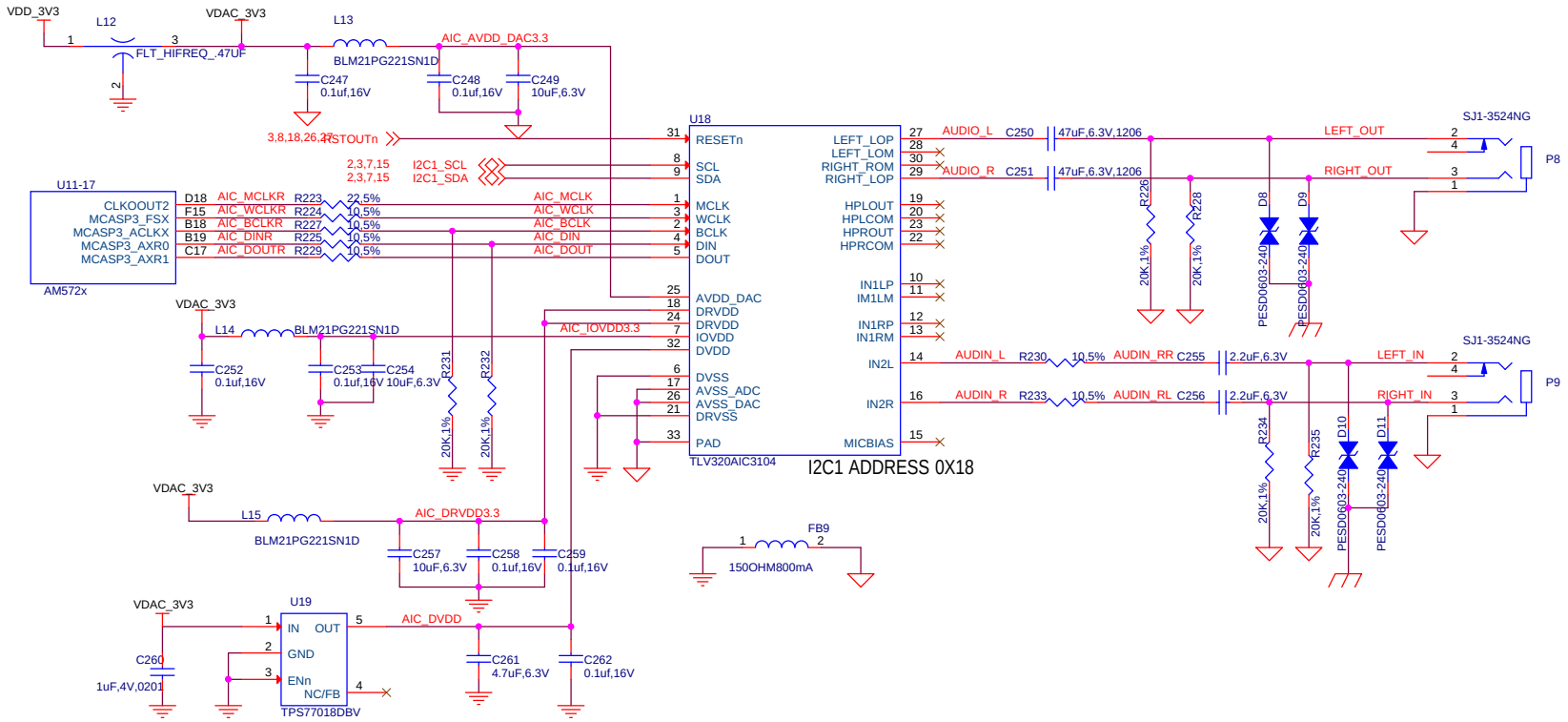

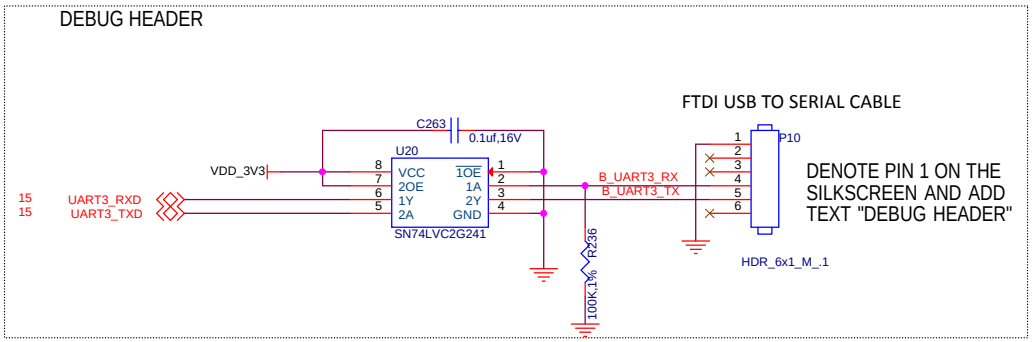

VDD_3V3 — VDD_3V3 2,3,4,6,7,8,9,10,11,12,13,14,15,17,18,24,25,26,27

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AM572x General Purpose EVM Processor Module

Audio CODEC and Debug Port

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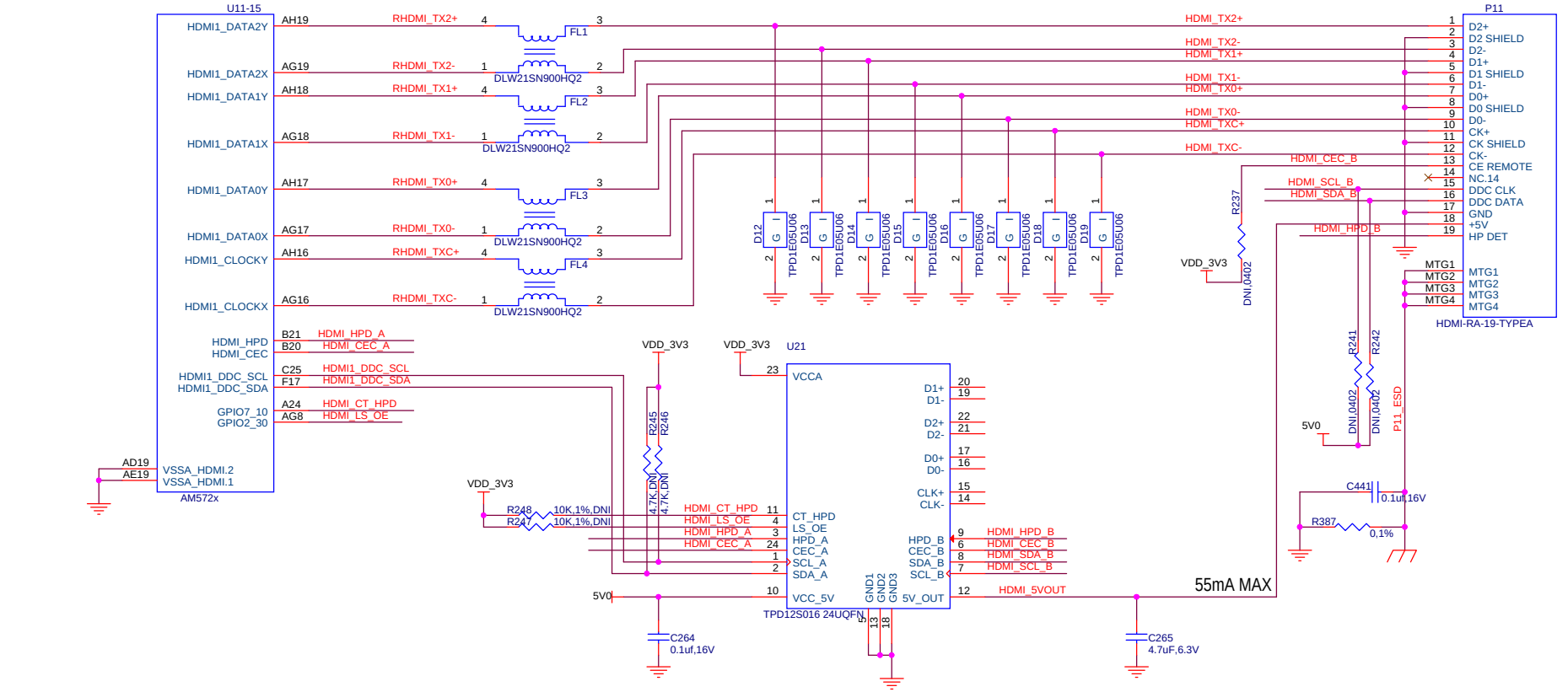

5V0 — 5V0 — 2,4,15,27
VDD_3V3 — VDD_3V3 — 2,3,4,6,7,8,9,10,11,12,13,14,15,16,18,24,25,26,27

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AM572x General Purpose EVM Processor Module
HDMI Port

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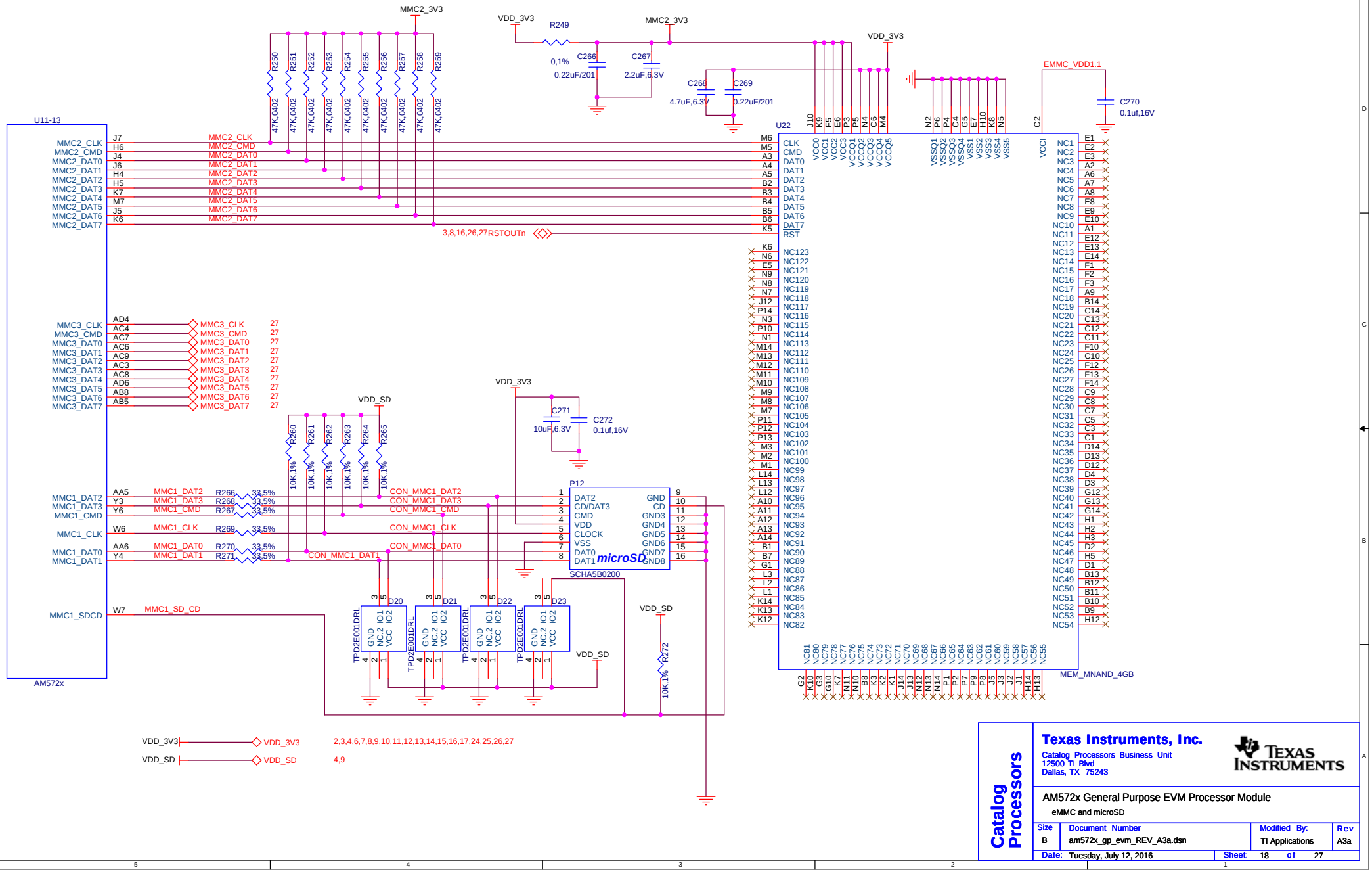

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AM572x General Purpose EVM Processor Module

eMMC and microSD

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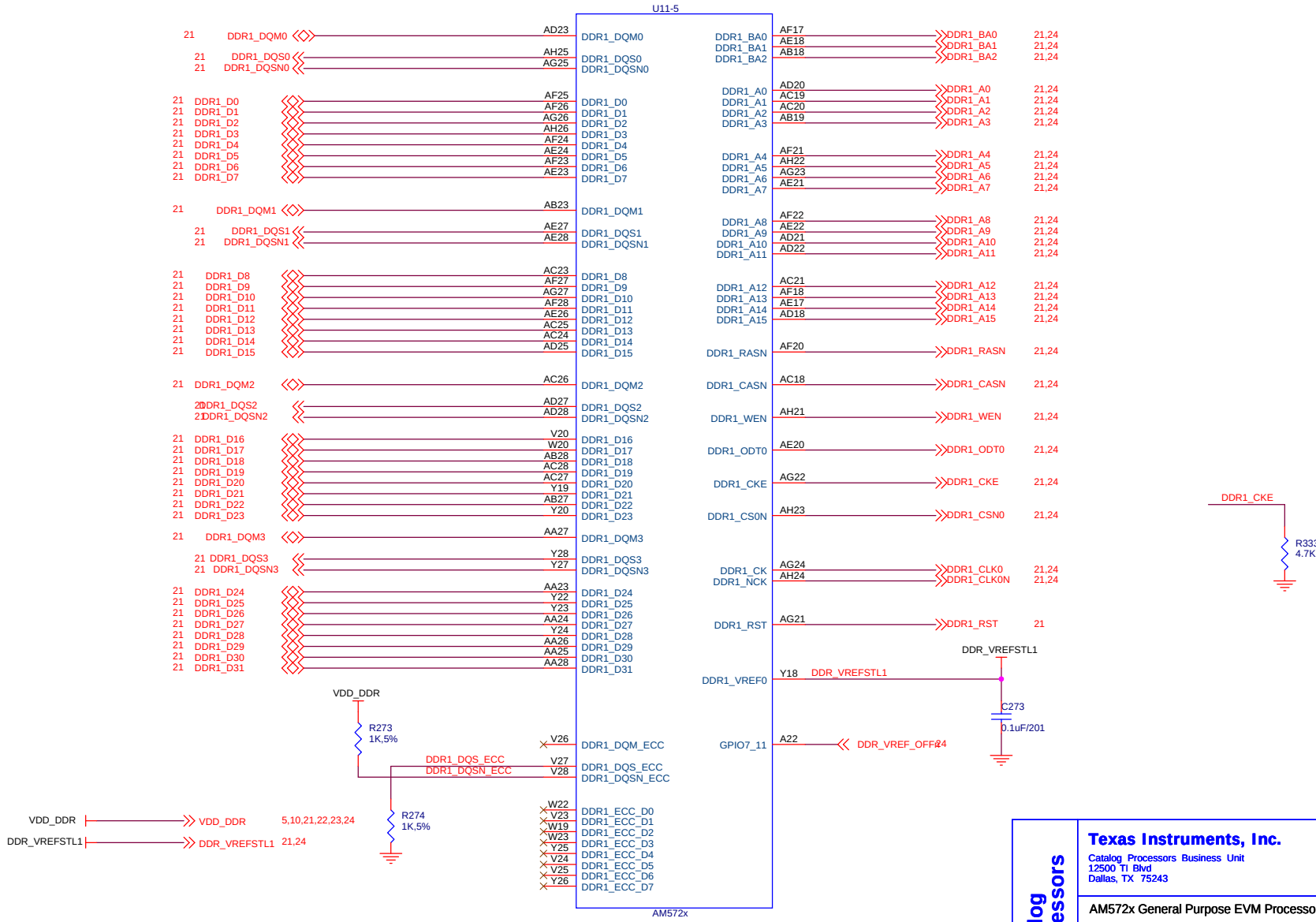
U11-5
DDR1_DQM0
DDR1_DQS0
DDR1_DQSN0
DDR1_D0
DDR1_D1
DDR1_D2
DDR1_D3
DDR1_D4
DDR1_D5
DDR1_D6
DDR1_D7
DDR1_DQM1
DDR1_DQS1
DDR1_DQSN1
DDR1_D8
DDR1_D9
DDR1_D10
DDR1_D11
DDR1_D12
DDR1_D13
DDR1_D14
DDR1_D15
DDR1_DQM2
DDR1_DQS2
DDR1_DQSN2
DDR1_D16
DDR1_D17
DDR1_D18
DDR1_D19
DDR1_D20
DDR1_D21
DDR1_D22
DDR1_D23
DDR1_DQM3
DDR1_DQS3
DDR1_DQSN3
DDR1_D24
DDR1_D25
DDR1_D26
DDR1_D27
DDR1_D28
DDR1_D29
DDR1_D30
DDR1_D31
DDR1_DQM_ECC
DDR1_DQS_ECC
DDR1_DQSN_ECC
DDR1_ECC_D0
DDR1_ECC_D1
DDR1_ECC_D2
DDR1_ECC_D3
DDR1_ECC_D4
DDR1_ECC_D5
DDR1_ECC_D6
DDR1_ECC_D7
AM572x
DDR1_BA0
DDR1_BA1
DDR1_BA2
DDR1_A0
DDR1_A1
DDR1_A2
DDR1_A3
DDR1_A4
DDR1_A5
DDR1_A6
DDR1_A7
DDR1_A8
DDR1_A9
DDR1_A10
DDR1_A11
DDR1_A12
DDR1_A13
DDR1_A14
DDR1_A15
DDR1_RASN
DDR1_CASN
DDR1_WEN
DDR1_ODT0
DDR1_CKE
DDR1_CS0N
DDR1_CK
DDR1_NCK
DDR1_RST
DDR1_VREF0
GPIO7_11
DDR_VREFSTL1
DDR_VREF_OFF
VDD_DDR
R273
1K,5%
R274
1K,5%
C273
0.1uF/201
R333
4.7K
5,10,21,22,23,24
21,24
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AM572x General Purpose EVM Processor Module
AM572x DDR3 Interface 1
Size
B
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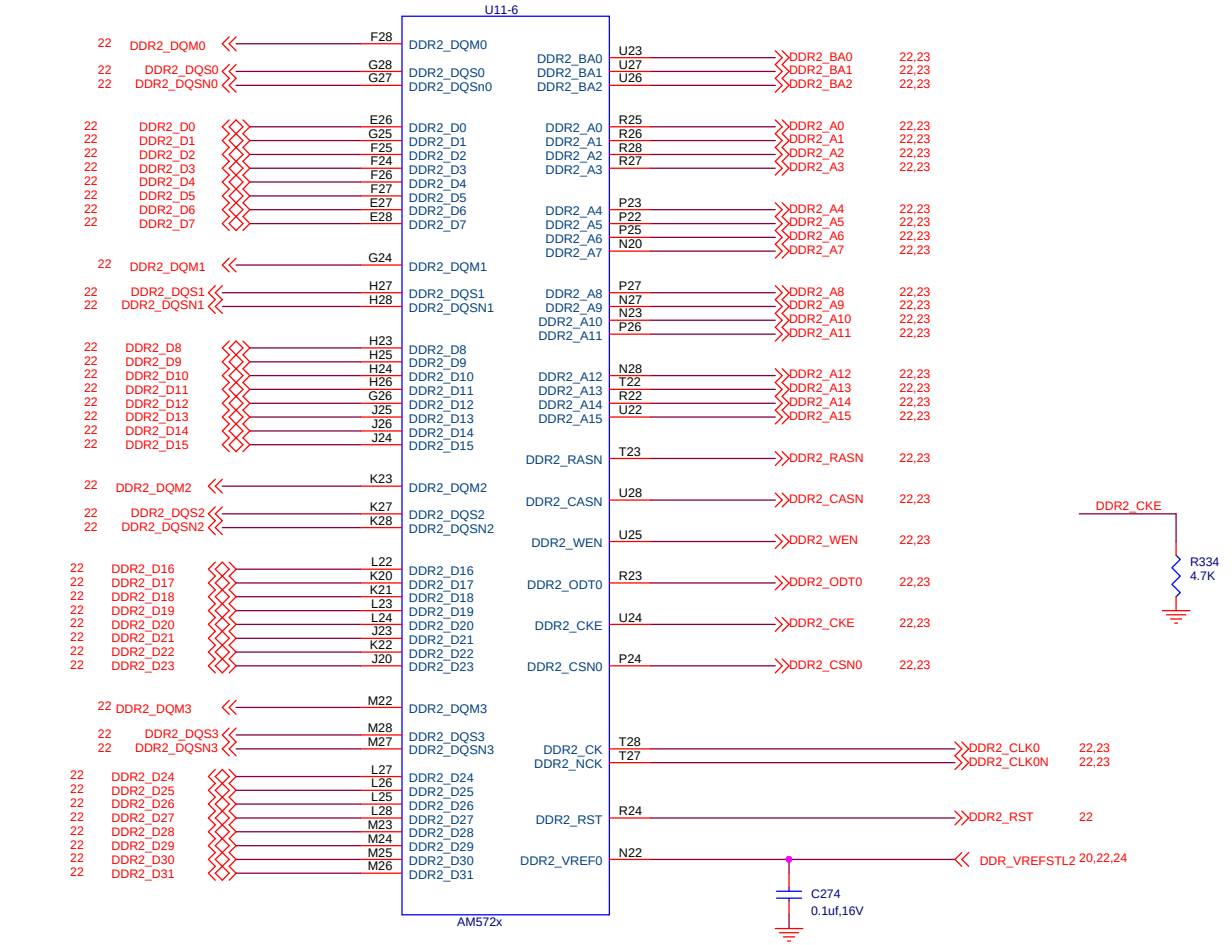

DDR_VREFSTL2 DDR_VREFSTL2 20,22,24

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AM572x General Purpose EVM Processor Module

AM572x DDR3 Interface 2

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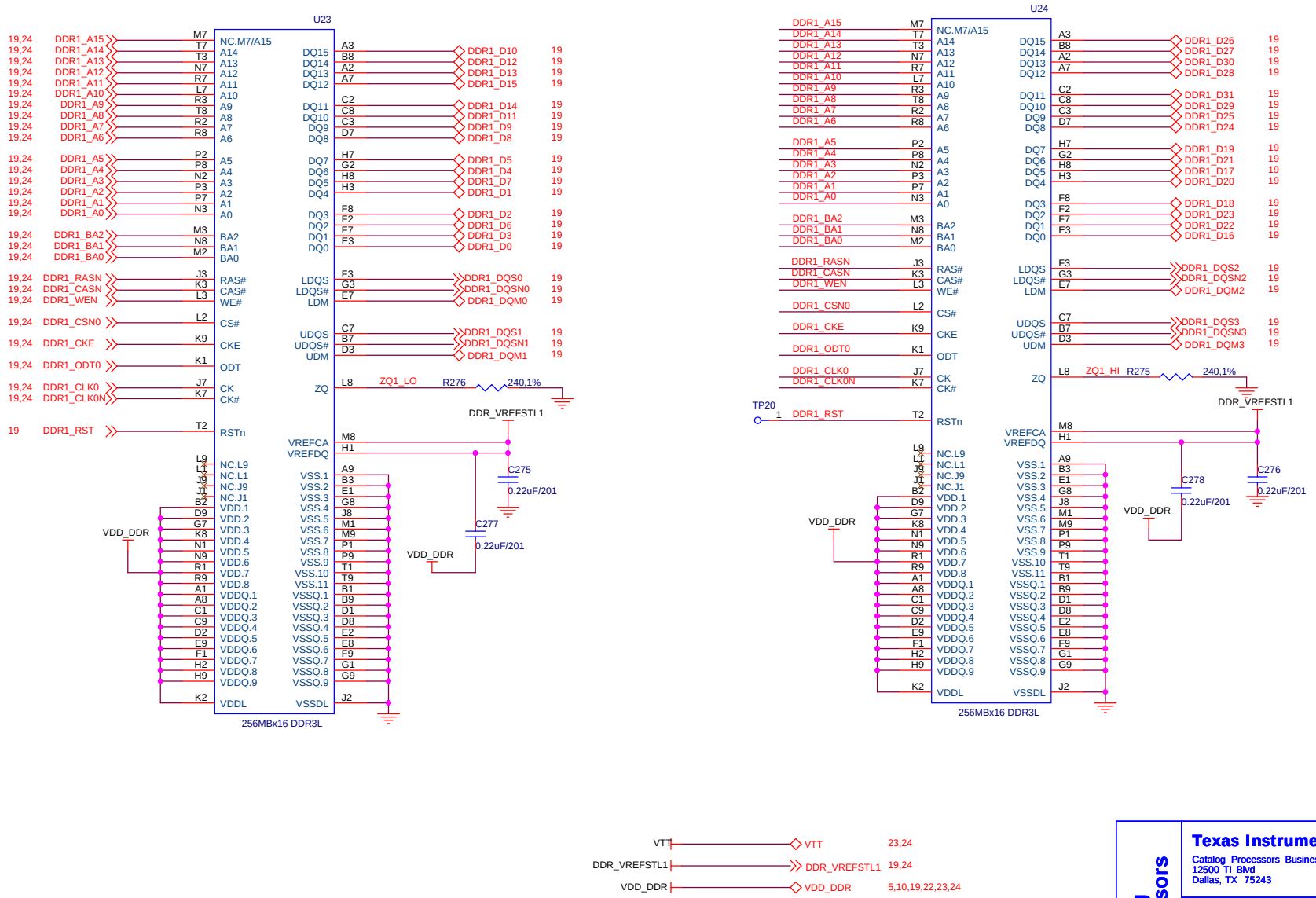

VTT 23,24

DDR_VREFSTL1 19,24

VDD_DDR 5,10,19,22,23,24

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AM572x General Purpose EVM Processor Module

DDR3 Bank 1

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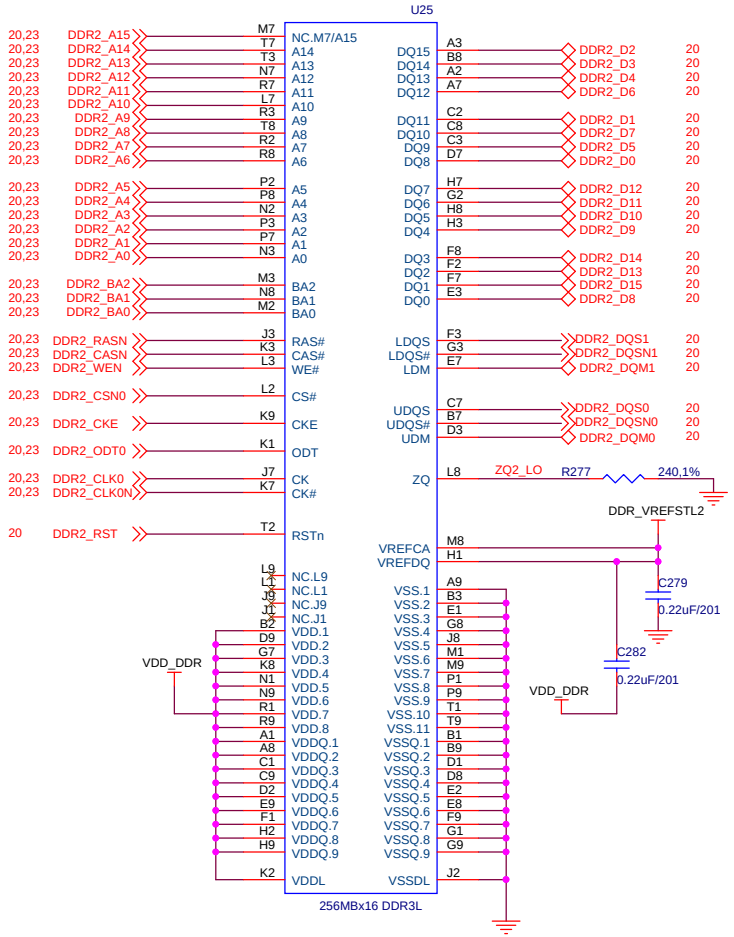

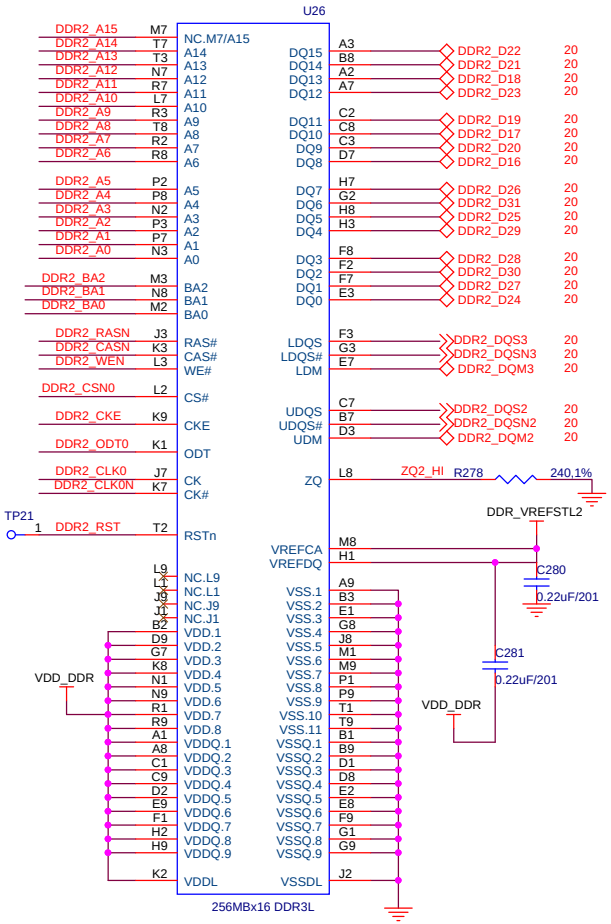

VDD_DDR — VDD_DDR 5,10,19,21,23,24

DDR_VREFSTL2 — DDR_VREFSTL2 20,24

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AM572x General Purpose EVM Processor Module
DDR3 Bank 2

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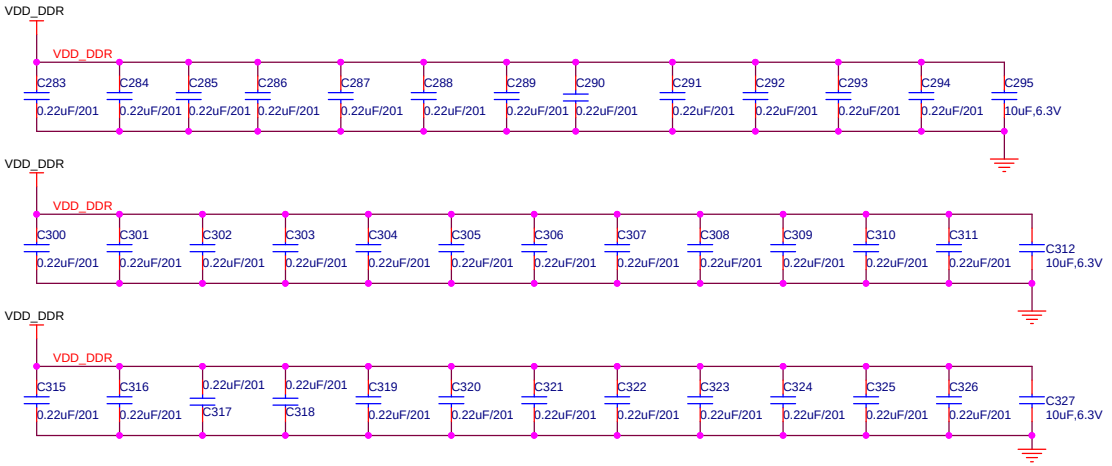

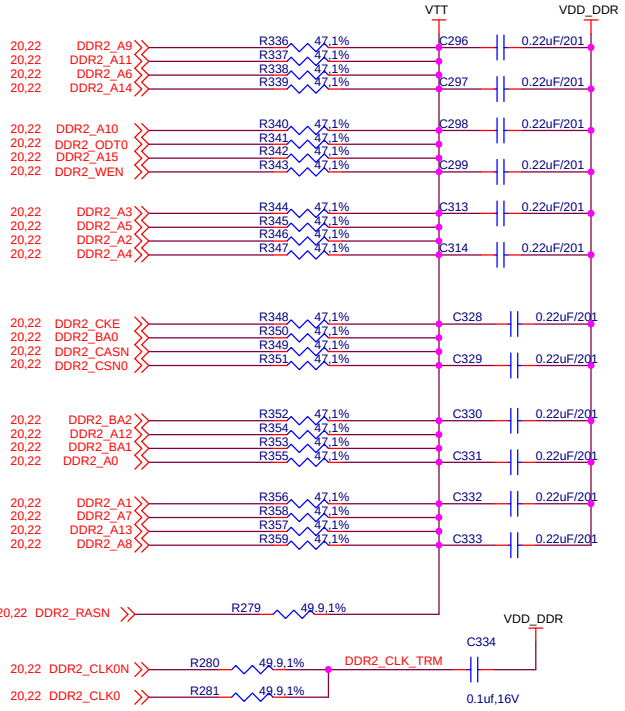

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AM572x General Purpose EVM Processor Module

DDR3 BANK 2 CAPS AND TERMINATIONS

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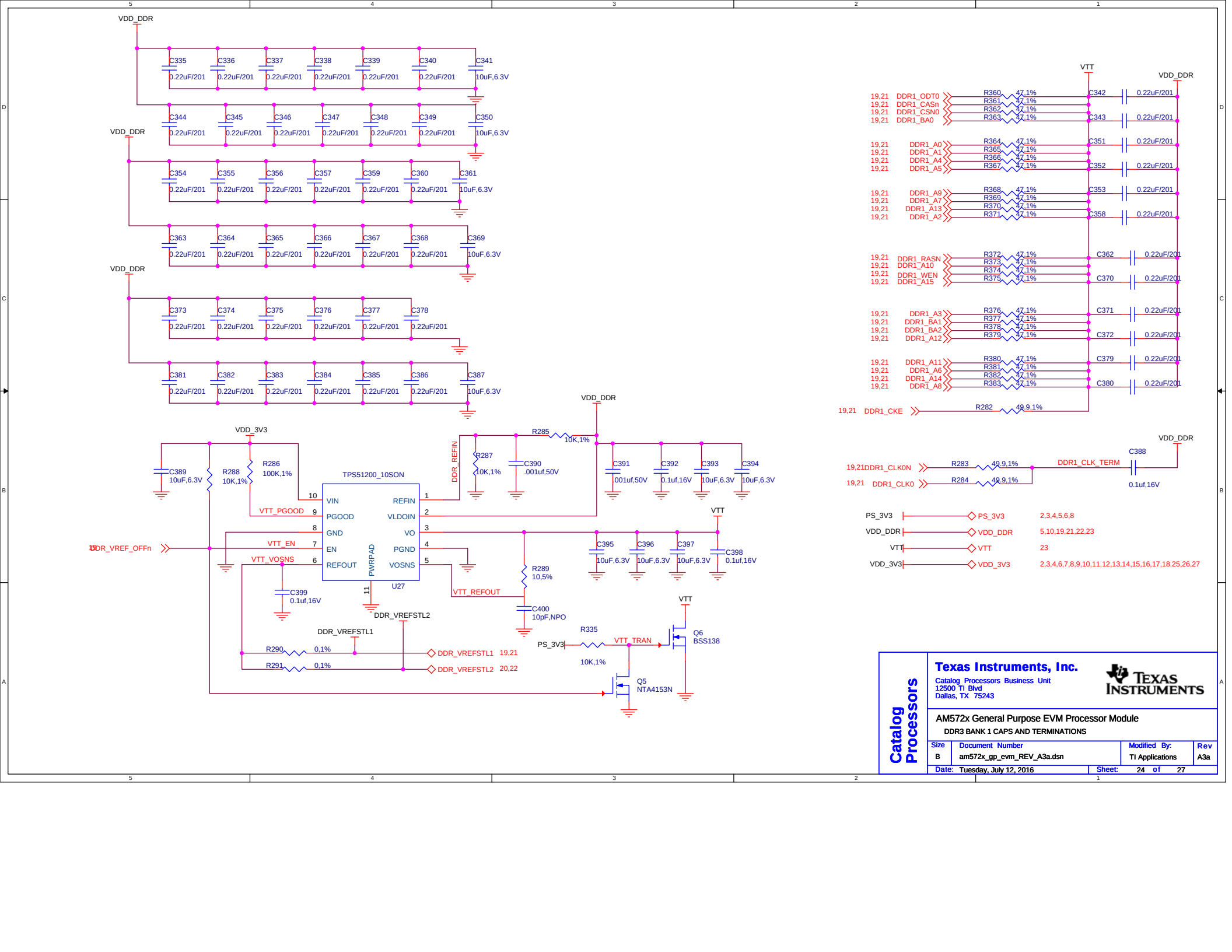
VDD_DDR
C335 0.22uF/201
C336 0.22uF/201
C337 0.22uF/201
C338 0.22uF/201
C339 0.22uF/201
C340 0.22uF/201
C341 10uF,6.3V
C344 0.22uF/201
C345 0.22uF/201
C346 0.22uF/201
C347 0.22uF/201
C348 0.22uF/201
C349 0.22uF/201
C350 10uF,6.3V
C354 0.22uF/201
C355 0.22uF/201
C356 0.22uF/201
C357 0.22uF/201
C359 0.22uF/201
C360 0.22uF/201
C361 10uF,6.3V
C363 0.22uF/201
C364 0.22uF/201
C365 0.22uF/201
C366 0.22uF/201
C367 0.22uF/201
C368 0.22uF/201
C369 10uF,6.3V
C373 0.22uF/201
C374 0.22uF/201
C375 0.22uF/201
C376 0.22uF/201
C377 0.22uF/201
C378 0.22uF/201
C381 0.22uF/201
C382 0.22uF/201
C383 0.22uF/201
C384 0.22uF/201
C385 0.22uF/201
C386 0.22uF/201
C387 10uF,6.3V
VTT
VDD_DDR
19,21 DDR1_ODT0
19,21 DDR1_CASn
19,21 DDR1_CSN0
19,21 DDR1_BA0
R360 47,1%
R361 47,1%
R362 47,1%
R363 47,1%
C342 0.22uF/201
C343 0.22uF/201
19,21 DDR1_A0
19,21 DDR1_A1
19,21 DDR1_A4
19,21 DDR1_A5
R364 47,1%
R365 47,1%
R366 47,1%
R367 47,1%
C351 0.22uF/201
C352 0.22uF/201
19,21 DDR1_A9
19,21 DDR1_A7
19,21 DDR1_A13
19,21 DDR1_A2
R368 47,1%
R369 47,1%
R370 47,1%
R371 47,1%
C353 0.22uF/201
C358 0.22uF/201
19,21 DDR1_RASN
19,21 DDR1_A10
19,21 DDR1_WEN
19,21 DDR1_A15
R372 47,1%
R373 47,1%
R374 47,1%
R375 47,1%
C362 0.22uF/201
C370 0.22uF/201
19,21 DDR1_A3
19,21 DDR1_BA1
19,21 DDR1_BA2
19,21 DDR1_A12
R376 47,1%
R377 47,1%
R378 47,1%
R379 47,1%
C371 0.22uF/201
C372 0.22uF/201
19,21 DDR1_A11
19,21 DDR1_A6
19,21 DDR1_A14
19,21 DDR1_A8
R380 47,1%
R381 47,1%
R382 47,1%
R383 47,1%
C379 0.22uF/201
C380 0.22uF/201
19,21 DDR1_CKE
R282 49,9,1%
VDD_3V3
C389 10uF,6.3V
R288 10K,1%
R286 100K,1%
TPS51200_10SON
VIN
PGOOD
GND
EN
REFOUT
REFIN
VLDOIN
VO
PGND
VOSNS
PWRPAD
U27
VTT_PGOOD
VTT_EN
VTT_VOSNS
DDR_REFIN
R287 10K,1%
C390 .001uf,50V
R285 10K,1%
C391 001uf,50V
C392 0.1uf,16V
C393 10uF,6.3V
C394 10uF,6.3V
19 DDR_VREF_OFFn
C395 10uF,6.3V
C396 10uF,6.3V
C397 10uF,6.3V
C398 0.1uf,16V
C399 0.1uf,16V
R289 10,5%
VTT_REFOUT
C400 10pF,NPO
DDR_VREFSTL2
DDR_VREFSTL1
R290 0,1%
R291 0,1%
DDR_VREFSTL1 19,21
DDR_VREFSTL2 20,22
R335 10K,1%
PS_3V3
VTT_TRAN
Q6 BSS138
Q5 NTA4153N
19,21 DDR1_CLK0N
19,21 DDR1_CLK0
R283 49,9,1%
R284 49,9,1%
DDR1_CLK_TERM
C388 0.1uf,16V
PS_3V3 PS_3V3 2,3,4,5,6,8
VDD_DDR VDD_DDR 5,10,19,21,22,23
VTT VTT 23
VDD_3V3 VDD_3V3 2,3,4,6,7,8,9,10,11,12,13,14,15,16,17,18,25,26,27
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DDR3 BANK 1 CAPS AND TERMINATIONS
Size B
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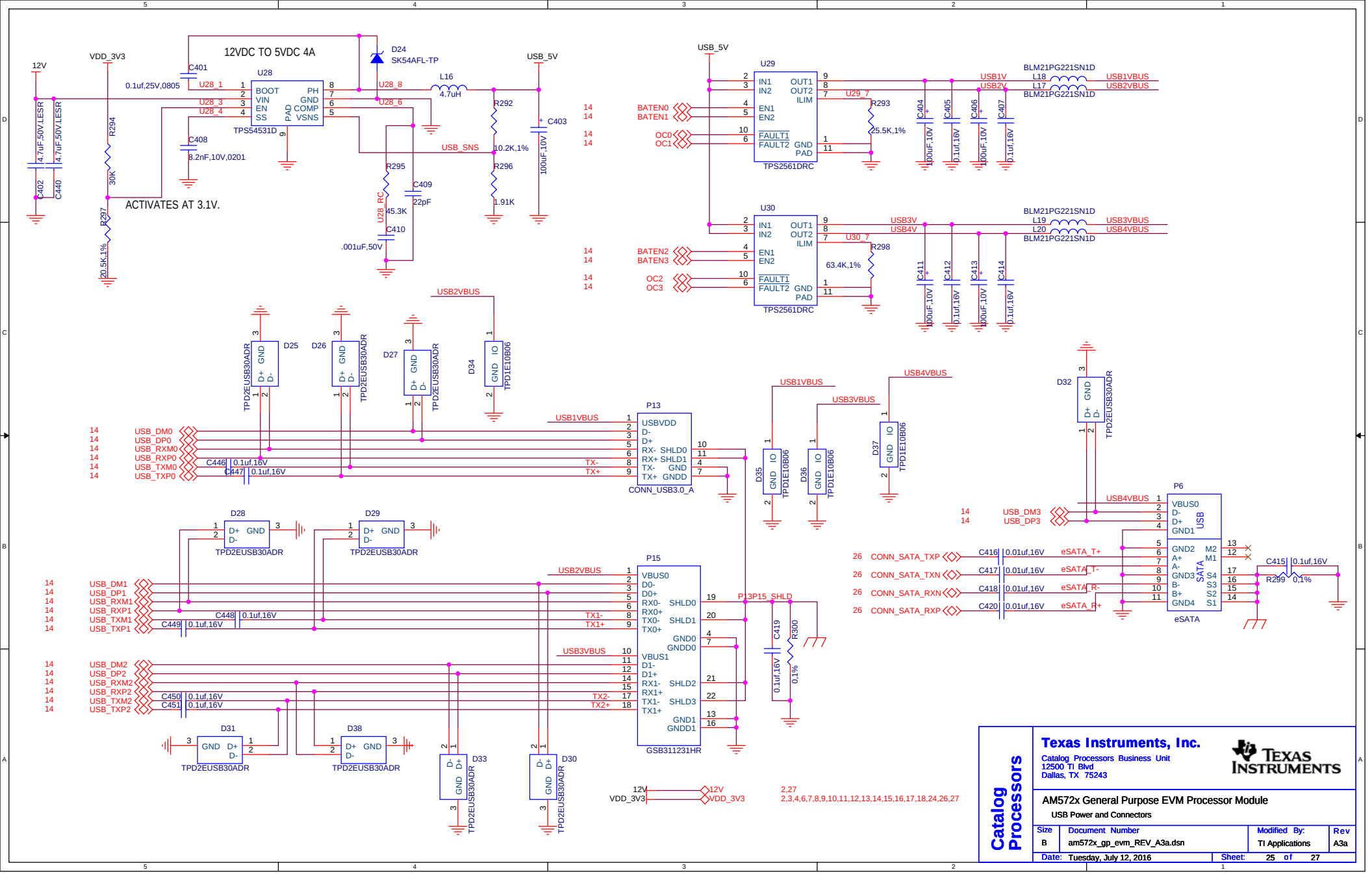
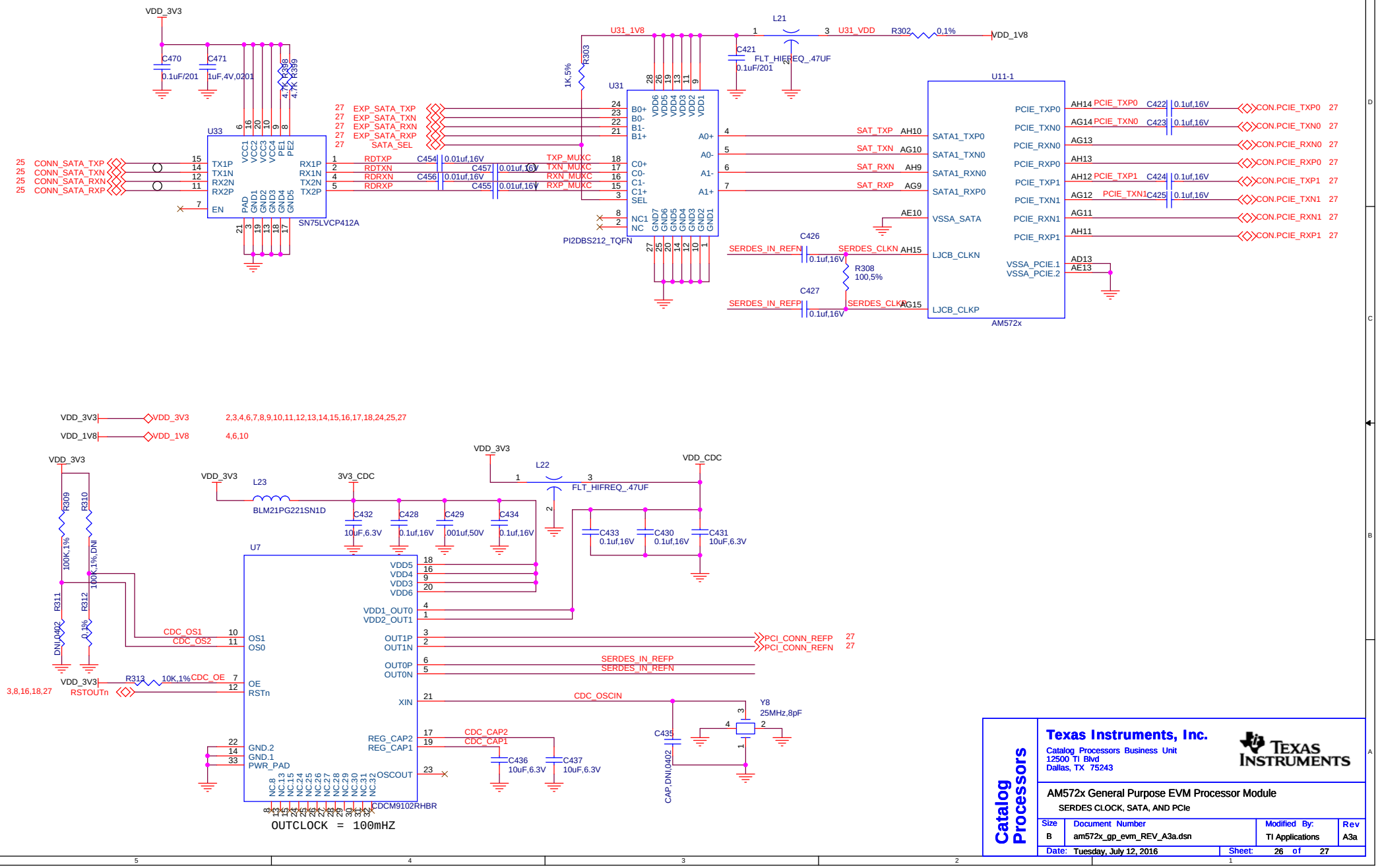


Preliminary Information - Subject to change



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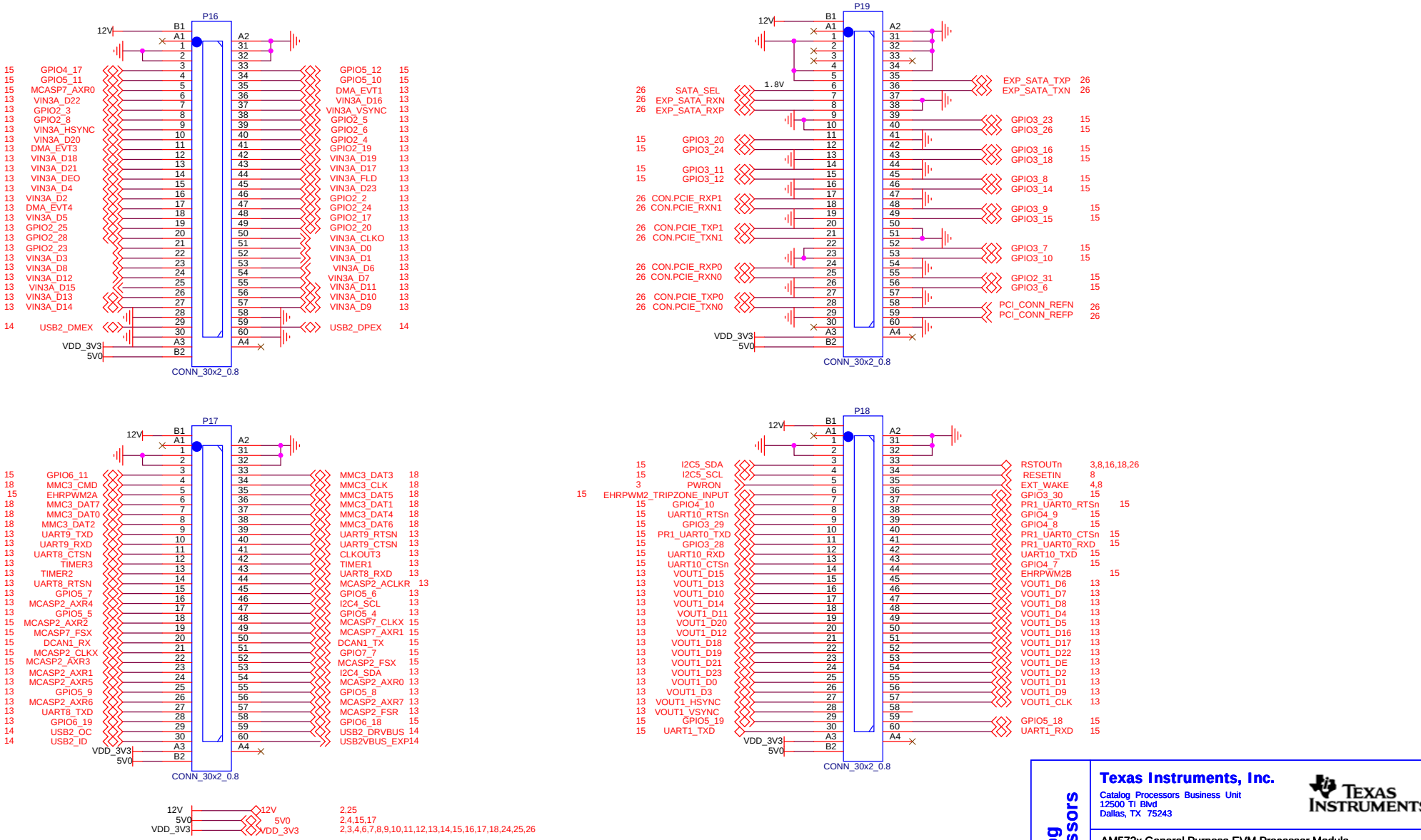
P16
CONN_30x2_0.8
P19
CONN_30x2_0.8
P17
CONN_30x2_0.8
P18
CONN_30x2_0.8
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