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Mini-Projet: COMPIL

2ème année Cycle Supérieur (2CS)

Option: Systèmes Informatiques (SQ)

Réalisation d'un traducteur pour le langage Mini-C

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1 Introduction

Le But de ce Mini-Projet est de développer un traducteur pour le langage Mini-C. Le compilateur doit produire un code intermédiaire sous forme de quadruplets correspondant au code source donné et pour cette dernière nous avons choisir le code assembleur comme code intermédiaire. Le présent document compris les différents tokens ou unités lexicaux, la grammaire LL(1) utilisé ainsi que le schéma de traduction.

2 Les Tokens

```
TOKEN : {
   <TYPE: <INT> | <FLOAT> >
   <IF: "if" >
   <ELSE: "else" >
   <#INT: "int" >
   <#FLOAT: "float" >
   <WHILE: "while" >
   <FOR: "for" >
}
TOKEN: {
   <ID: <LETTER> (<LETTER> | <DIGIT> | <ZERO>)* >
   <NUMBER: <DIGIT> (<DIGIT> | <ZERO>)* | <ZERO> >
   <#LETTER: ["a"-"z","A"-"Z"] >
   <#DIGIT: ["1"-"9"] >
   <#ZERO: "0" >
}
TOKEN: {
   <GPAR: "(" >
   <DPAR: ")" >
   <VIRGULE: "," > |
   <GACC: "{" >
   <DACC: "}" >
   <POINTVIRGULE: ";" > |
   <AFFECT: "=" > |
   <EQ:
   <LT:
             "<" > |
    <LTE:
             "<=" > |
```

```
<GT: ">" > |

<GTE: ">=" > |

<NEQ: "!=" > |

<ADD: "+" > |

<SUB: "-" > |

<MUL: "*" > |

<DIV: "/" >
}
```

3 La grammaire sous forme BNF

```
Start ::= (<Function>)* #
Function ::= <Type> identifier "(" <ArgList> ")" <StatementBlockDef>
ArgList ::= <Arg> ("," <Arg>)*
Arg ::= <Type> identifier
varDefineDef ::= <Type> identifier ( "=" <ExprAssign> )? ( "," <varList> )*
varList ::= identifier ( "=" <ExprAssign> )?
StatementBlockDef ::= "{" (<Stmt>)* "}"
Stmt ::= <varDefineDef> ";" | <Expr> ";" | <IfStmt> | <ForStmt> | <WhileStmt>
IfStmt ::= "if" "(" <ExprAssign> <Rvalue> ")" <StatementBlockDef> ( "else"
           <StatementBlockDef> | <Stmt> )
Expr ::= <ExprAssign> ( <Affec> )?
Affec ::= "=" <ExprAssign> | <Rvalue>
ExprAssign ::= ( identifier | number ) ( ( "+" | "-" | "*" | "/" )
               <ExprAssign> )? | "(" <ExprAssign> ")"
Rvalue ::= ( "<" | ">" | "==" | "!=" | " >=" | "<=" ) <ExprAssign>
WhileStmt ::= "while" "(" <ExprAssign> <Rvalue> ")" ( <StatementBlockDef>
              | <Stmt> )
```

```
ForStmt ::= "for" "(" ( <varDefineDef> )? ";" ( <ExprAssign> <Rvalue> )? ";" ( identifier "=" <ExprAssign> ) )? ")" <StatementBlockDef>
```

4 Le Schéma de traduction : Les Routines Sémantiques

4.1 La Boucle For

4.2 La Boucle While

```
""); quadC++;
```

4.3 La Structure Conditionnelle

4.4 Les Expressions Arithmétiques

```
ExprAssign ::= ( identifier M1| number M2 ) ( ( "+"M3 | "-" M4| "*" M5| "/" M6)
               <ExprAssign> )?| "(" <ExprAssign> ")"
M1 : GenQuad( "mov" , AX, VAL); quadC++;
     GenQuad( "mov" , ADDR, AX); quadC++;
M2 : GenQuad( "mov" , AX, VAL); quadC++;
     GenQuad( "mov" , ADDR, AX); quadC++;
M3 : GenQuad( "mov" , CX, ADDR1); quadC++;
     GenQuad( "add" , CX, ADDR2); quadC++;
     GenQuad( "mov" , ADDR1, CX); quadC++;
M4 : GenQuad( "mov" , CX, ADDR1); quadC++;
     GenQuad( "sub" , CX, ADDR2); quadC++;
     GenQuad( "mov" , ADDR1, CX); quadC++;
M5 : GenQuad( "mov" , CX, ADDR1); quadC++;
     GenQuad( "mov" , AL, ADDR2); quadC++;
     GenQuad( "mul" , CL, ""); quadC++;
     GenQuad( "mov" , ADDR1, AX); quadC++;
M6 : GenQuad( "mov" , CX, ADDR1); quadC++;
     GenQuad( "mov" , AL, ADDR2); quadC++;
```

```
GenQuad( "div" , CL, ""); quadC++;
GenQuad( "mov" , ADDR1, AX); quadC++;
```

4.5 La Comparaison

```
Rvalue ::= ( op = "<"M1 | ">"M2 | "=="M3 | "!="M4 | ">="M5 | "<="M6 )
            <ExprAssign>
M1 : GenQuad( "mov" , AX, ADDR ); quadC++;
     GenQuad( "mov" , BX, ADDR ); quadC++;
     GenQuad( "cmp" , AX, BX ); quadC++;
     GenQuad( "jge" , "ETQ"+quadC, "" ); quadC++;
M2 : GenQuad( "mov" , AX, ADDR ); quadC++;
     GenQuad( "mov" , BX, ADDR ); quadC++;
     GenQuad( "cmp" , AX, BX ); quadC++;
     GenQuad( "jle" , "ETQ"+quadC, "" ); quadC++;
M3 : GenQuad( "mov" , AX, ADDR ); quadC++;
     GenQuad( "mov" , BX, ADDR ); quadC++;
     GenQuad( "cmp" , AX, BX ); quadC++;
     GenQuad( "jne" , "ETQ"+quadC, "" ); quadC++;
M4 : GenQuad( "mov" , AX, ADDR ); quadC++;
     GenQuad( "mov" , BX, ADDR ); quadC++;
     GenQuad( "cmp" , AX, BX ); quadC++;
     GenQuad( "je" , "ETQ"+quadC, "" ); quadC++;
M5 : GenQuad( "mov" , AX, ADDR ); quadC++;
     GenQuad( "mov" , BX, ADDR ); quadC++;
     GenQuad( "cmp" , AX, BX ); quadC++;
     GenQuad( "jl" , "ETQ"+quadC, "" ); quadC++;
M6 : GenQuad( "mov" , AX, ADDR ); quadC++;
     GenQuad( "mov" , BX, ADDR ); quadC++;
     GenQuad( "cmp" , AX, BX ); quadC++;
     GenQuad( "jg" , "ETQ"+quadC, "" ); quadC++;
```

4.6 L'affectation

```
Affec ::= "=" <ExprAssign> M1 | <Rvalue>
M1 : GenQuad( "mov" , AX, VAL); quadC++;
    GenQuad( "mov" , ADDR, AX); quadC++;
```

5 Tests & Résultats

5.1 La Fonction Puissance

```
int pow(){int a=2; int b=9; int n; for(int i=0; i<b; i=i+1){a = a*b;}}
```

```
int pow(){int a=2; int b=9; int n; for(int i=0; i<b; i=i+1){a = a*b;}}
                   == Code généré =======
mov AX, 2
        mov BASE + 6, AX
        mov AX, 9
        mov BASE + 8, AX
        mov AX, 0
        mov BASE + 12, AX
        ETQpow6:
        save
        save
        mov BX, BASE + 8
        mov AX, BASE + 7
        cmp AX, BX
12
13
14
15
16
17
18
        jmp ETQpow6
        mov AX, 1
        mov BASE + 12, AX
        save
        jmp ETQpow8
        mov AX, BASE + 8
        mov BASE + 6, AX
        jmp ETQpow15
20
        ETQpow19:
        ETQpow21:
```

Figure 1: La Fonction Puissance

5.2 La Fonction Factorielle

```
int fact(){int a=9; int f = 1; for(int i=0; i<8; i=i+1){f=f*(a-i);}}
```

```
int fact(){int a=9; int f = 1; for(int i=0; i<8; i=i+1){f=f*(a-i);}}
   ======== Code généré =======
0
1
2
3
4
5
6
7
8
9
10
          mov AX, 9
          mov BASE + 0, AX
          mov AX, 1
          mov BASE + 2, AX
          jmp ETQfact2
mov BASE + 4, AX
ETQfact6:
          save
          save
          mov BX, BASE + 8
mov AX, BASE + 7
11
          cmp AX, BX
12
13
14
15
16
17
          jge ETQfact22
mov AX, 1
          mov BASE + 4, AX
          save
          jmp ETQfact8
          mov CX, BASE + 2
sub CX, BASE + 0
19
          mov BASE + 6, CX
20
21
22
23
24
          mov AX, BASE + 4
          mov BASE + 6,
jmp ETQfact15
          ETQfact22:
          ETQfact24:
```

Figure 2: La Fonction Factorielle