DAC Conversion Jitter Measurement System

Audio DA converters require very low conversion jitter to achieve high spurious free dynamic range and good signal-to-noise ratio. There are essentially no commercial measurement systems available which are optimized to verify this performance aspect. This master thesis (or semester thesis for two experienced students) comprises the implementation of such a measurement system. Possible approaches include a cross-correlating spectrum analyzer or the use of a very low jitter PLL. Carried out with an industry partner (Weiss Engineering Ltd.).

Character: 80% hardware implementation, 20% software implementation
Prerequisites: Interest in high-performance mixed signal circuit design; experience in analog circuit design beneficial