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Created on Thu May 3 15:41:32 2012

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3. component

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6. soldermask

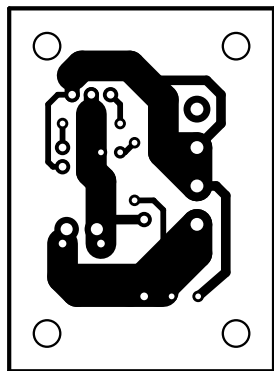
7. topsilk

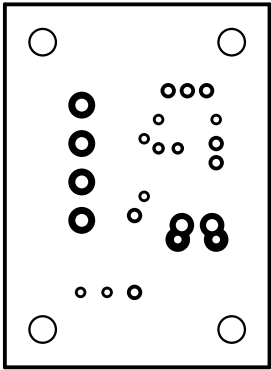
8. bottomsilk

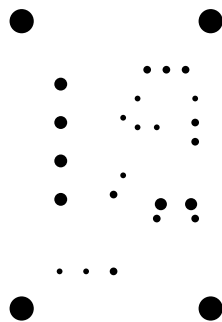
9. topassembly

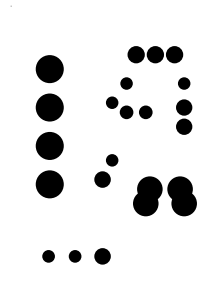
10. bottomassembly

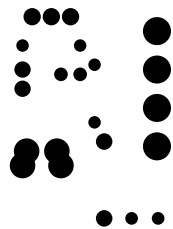
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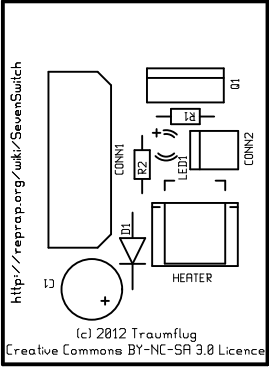




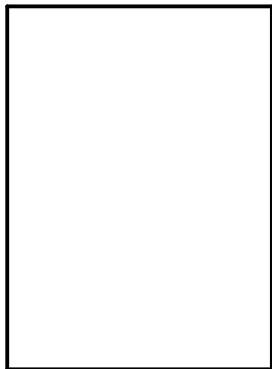


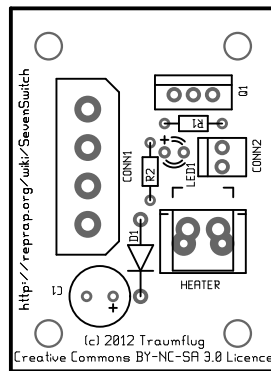




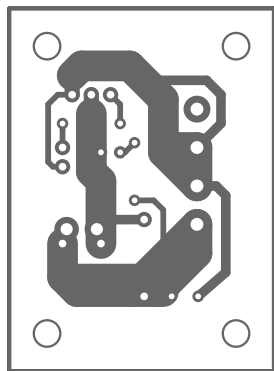


cs= extra loop to enhance voronai path generation



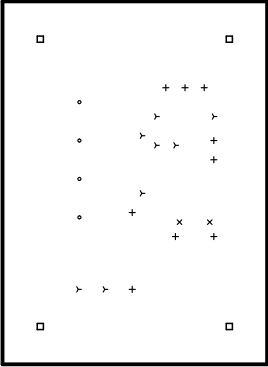


es=extra loop to enhance voronai path generation



There are 5 different drill sizes used in this layout, 27 holes total

Symbol	Diam. (Inch)	Count	Plated?	
y	0.030	8	YES	Title: (unknown) - Fabrication Drawing Author: Markus Hitter Date: Do 03 Mai 2012 13:41:32 GMT UTC
+	0.040	9	YES	
x	0.063	2	YES	
o	0.067	4	YES	
□	0.126	4	YES	



Board outline is the centerline of this path