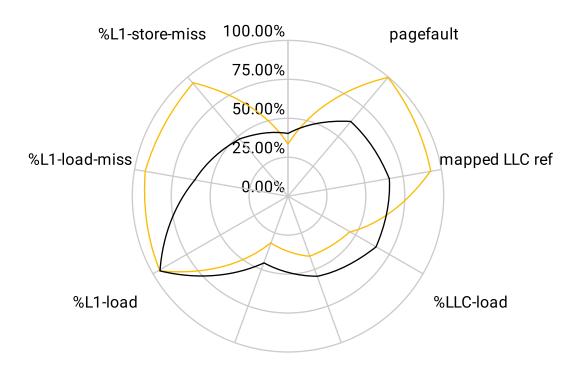
freqmine

stalled-cycles per instruction



%LLC-store-miss %LLC-load-miss