DIGITAL DESIGN & COMPUTER ARCHITECTURE (23EC1202)
Active Learning Methods (ALM's) - CO-3

Instructions:

1. CO-3 consists of one ALM and this ALM is evaluated for 25 marks.

2. We kindly request that faculty members take on the responsibility of conducting of ALM's in their allotted Lab/Theory hours and then promptly notifying the students to upload the soft copies (PDF File) of these assessments into LMS system.

3. Please make sure to complete these tasks on time and follow the guidelines provided above.

ALM-5: (Last Date of Submission is 05.04.2025) – Problem-Based Learning

1. Develop the model of RISC and CISC architectures, detailing its design philosophy, key features.

2. Design the operational flow for a pipelined processor with four stages, outlining each stage's function and how instructions progress through the pipeline.

Guidelines:

a) Ask students to take out a blank sheet of paper, pose the question.

b) Give them **five minutes to each question** to respond and they need to record their responses on paper.

c) Ask the students to upload the scanned copy of the same into LMS.

d) Conduct the activity in the Theory/lab hours before the given deadline.