BEX Minor Project Report – Guidelines / Contents

- ✓ **Report (2 Copies – Tape Binding) Submission Deadline – Shrawan 15, 2076 – (10 AM)**
- ✓ **Reports MUST have signature of supervisor on COVER page**
- ✓ **Groups MUST submit Student / Supervisor Form (10 Consultations) – Shrawan 15, 2076 – (10 AM)**

- ✓ **Reports WITHOUT supervisor signature will be NOT be accepted**

- ✓ **Reports WILL be checked by project coordinator**
- ✓ **Reports NOT following "BEX Project Report Template and Guidelines" MUST be RE-WRITTEN**

- ✓ **Reports meeting ALL required criteria will receive Department Stamp**
- ✓ **External Examiner contact info. will be released only after meeting ALL requirements**
- ✓ **Each Group MUST contact & meet their External Examiner and handover their Report**

[Final Defense Dates: Shrawan 21 & 22, 2076]

- **Cover Page**
- **Declaration**
- **Certificate of Approval (Name / Designation / Institute of External Examiner should be Generalized)**
- **Copyright**
- **Acknowledgement**
- **Abstract (Mention Keywords)**
- **Table of Contents**
- **List of Figures**
- **List of Tables**
- **List of Abbreviations**
- **Introduction**
  - **Background**
  - **Motivation**
  - **Problem Definition**
  - **Project Objectives**
  - **Project Scope and Applications**
  - **Report Organization**
- **Literature Review**
- **Requirement Analysis**
  - **Enumerate & describe why and where in your project the hardware components / instruments & software are required**
- **System Architecture and Methodology**
  - **Explain the system block diagram along with the purpose of each block & how the blocks interact with each other**
  - **Explain any flowcharts / algorithms / methods that your project follows**
- **Implementation Details**
  - **Describe how the hardware components / instruments & software function in your project**
  - **Describe the calibration process required for correct operation of each module**
  - **Describe the interfacing technicalities / protocol of each module used in your project**
  - **Explain in detail how all components are interconnected to make a functioning system**
- **Results and Analysis**
  - **Present the outputs of your project in the form of tables / graphs / charts / figures and explain their behavior**
  - **Perform error analysis, comparisons (theory, simulation, practical) and validate your output**
  - **Discuss the sources of errors in your project that caused your outputs to deviate from expected values**
- **Future Enhancements**
  - **Discuss the technical improvements that can be implemented in your project**
- **Conclusion**
- **Appendices (Use Separate pages for each Appendix)**
  - **Project Budget (Detailed Breakdown of Costs)**
  - **Project Timeline (Gantt Chart)**
  - **Circuit Diagrams (Should be Clear and Legible)**
  - **PCB Designs (Should be Clear and Legible)**
  - **Module Specifications (Should be brief - Keep only necessary tables and figures)**
- **References**
  - **Must follow IEEE guidelines**
  - **Left Justify the references**