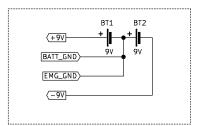
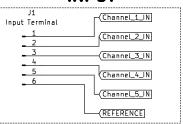
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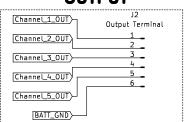
#### **POWER SUPPLY**



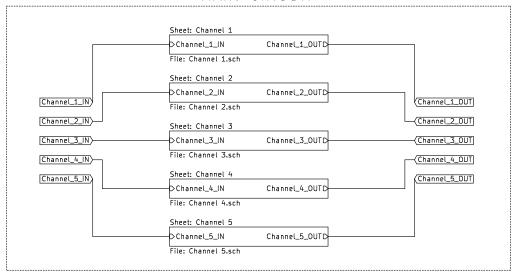
#### **INPUT**



#### OUTPUT



#### MAIN CIRCUIT



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Sheet:

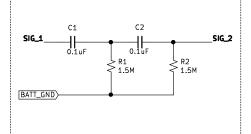
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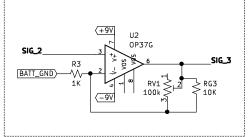
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# U1 ChanneL1\_JN D AD620 RG1 100 REFERENCE

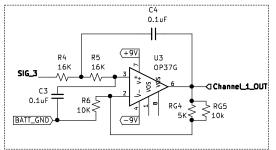
#### HIGH PASS FILTER



#### **AMPLIFIER**



# LOW PASS FILTER

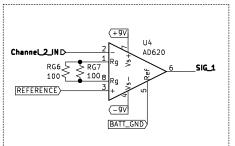


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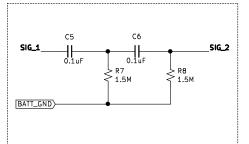
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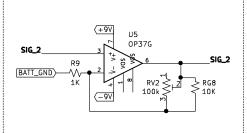
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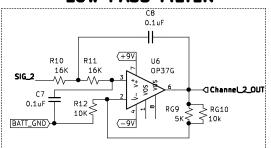
# HIGH PASS FILTER



#### **AMPLIFIER**



# **LOW PASS FILTER**



Upendra Subedi Sanjay Rijal Rhimesh Lwagun Rabin Nepal

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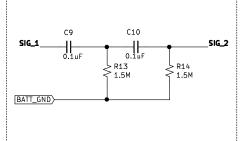
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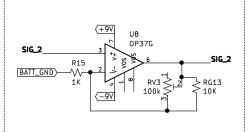
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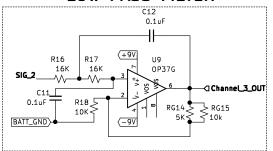
### HIGH PASS FILTER



### **AMPLIFIER**



#### LOW PASS FILTER



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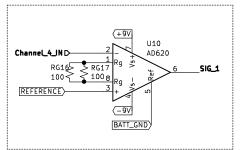
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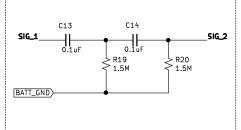
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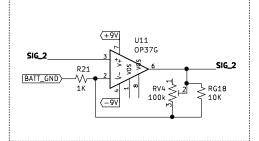
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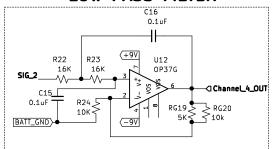
### HIGH PASS FILTER



#### **AMPLIFIER**



### **LOW PASS FILTER**

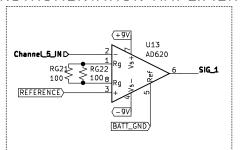


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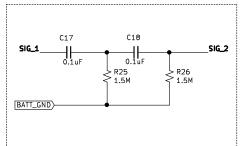
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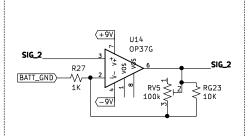
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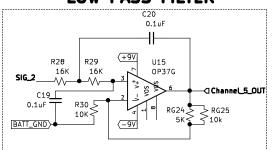
# HIGH PASS FILTER



#### **AMPLIFIER**



# **LOW PASS FILTER**



Upendra Subedi Sanjay Rijal Rhimesh Lwagun Rabin Nepal

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