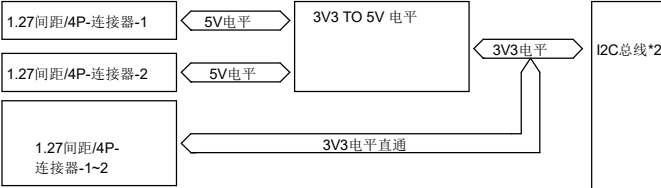
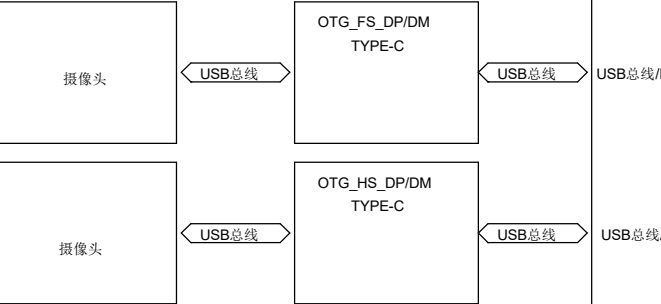
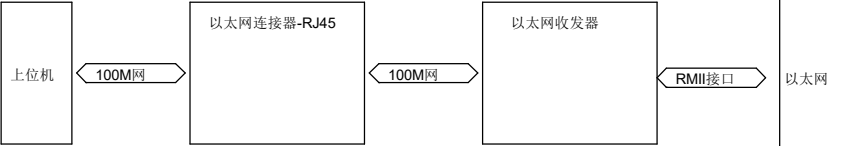


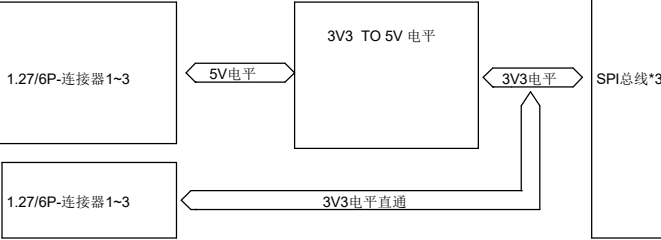
I2C
连接器数量-4个



连接器数量-2个

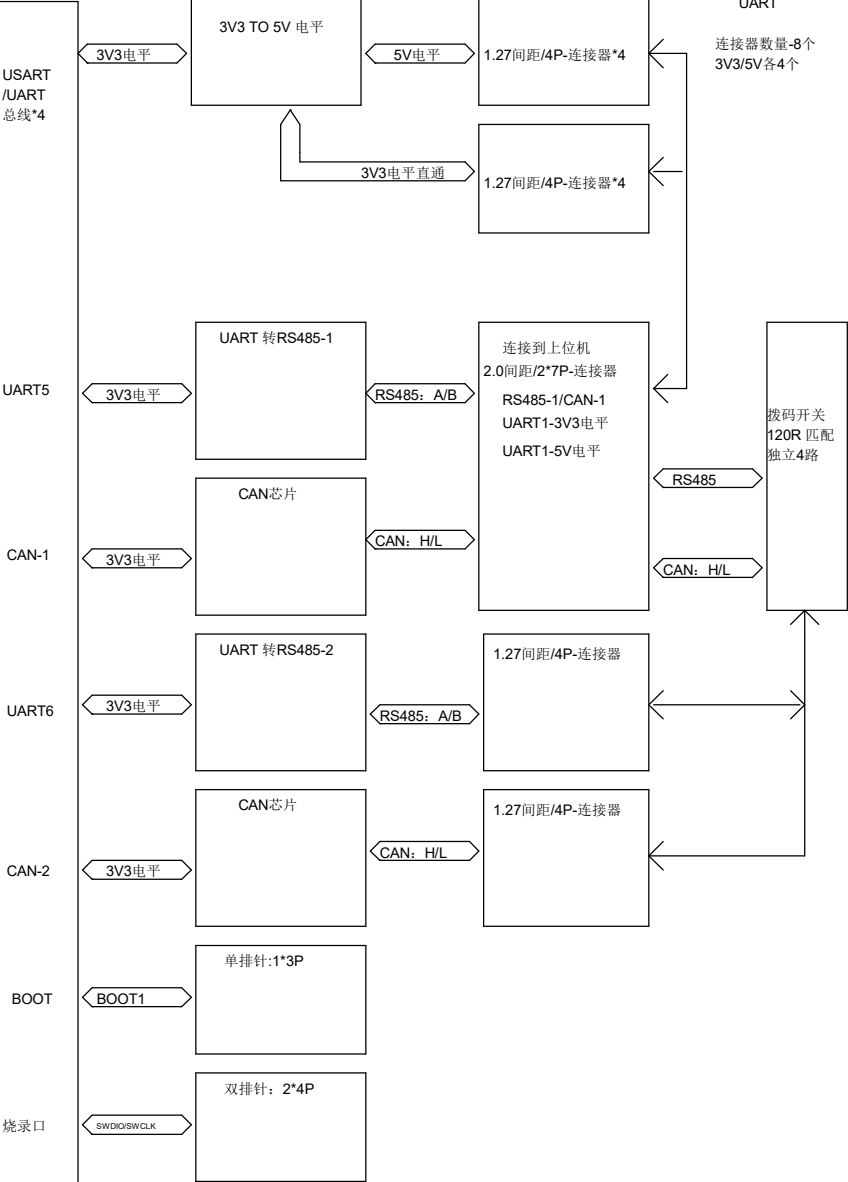


SPI
连接器-6个, 1.27间距

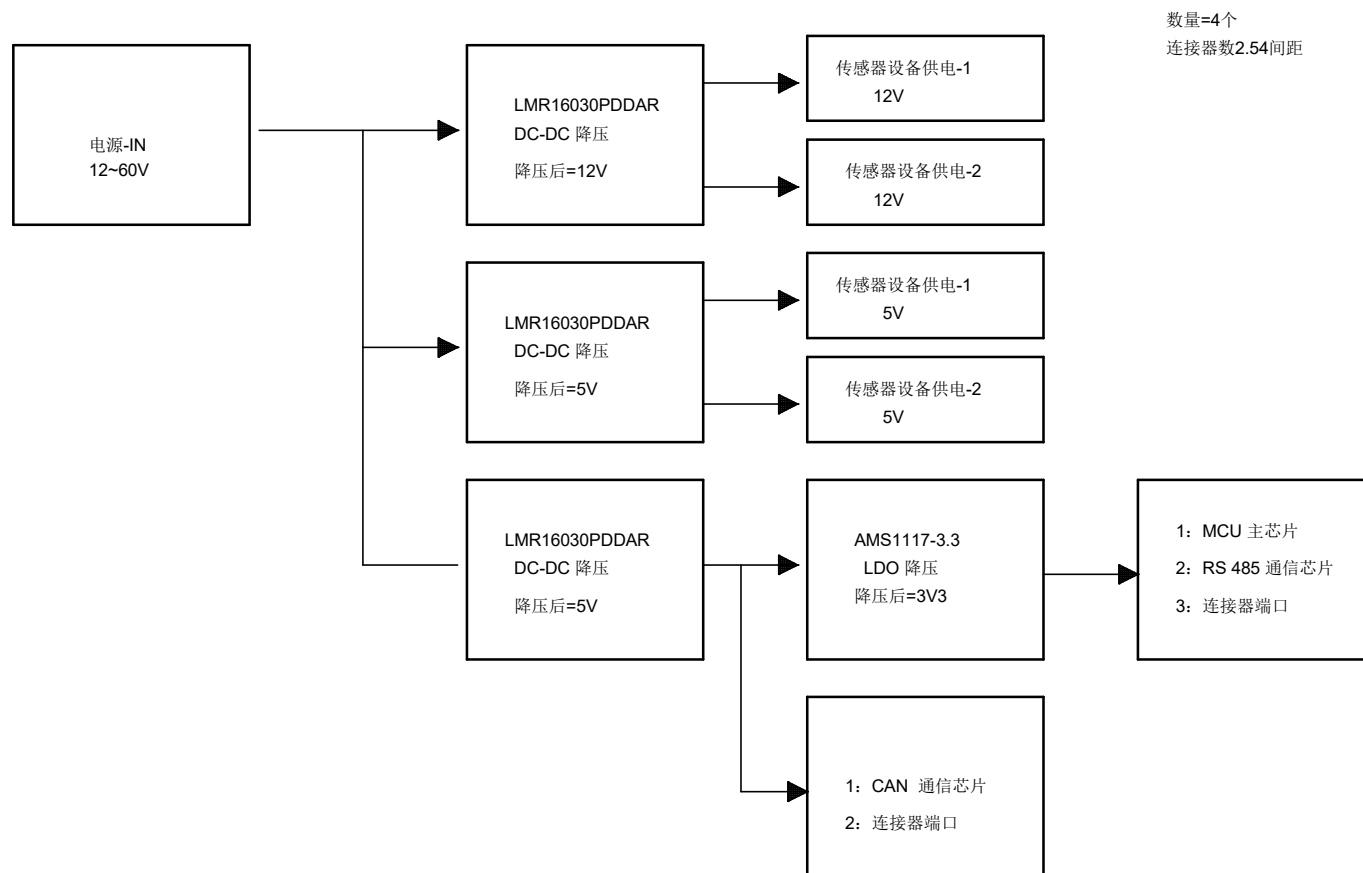


IO接口板信号框图

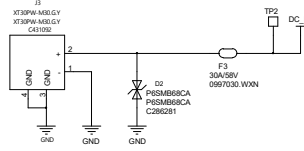
MCU
stm32f407VET6



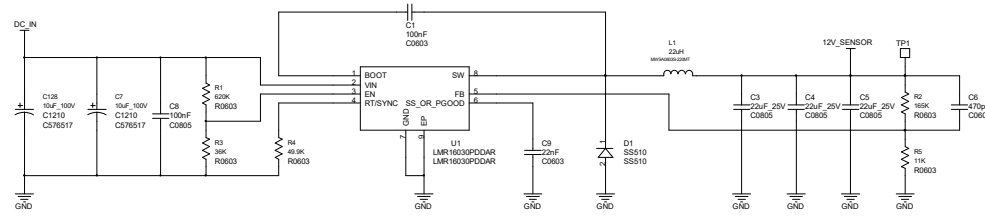
电源分配



- 1: DC_IN来自电源板, 12~60V IN
- 2: 电源端子采用XT30PW-M公头

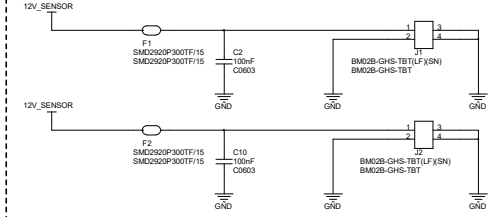


- 1: DC_IN TO 12V
- 2: 最大电流3A
- 3: 12V传感器电源。

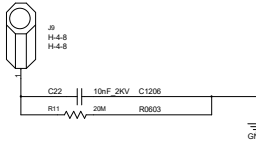
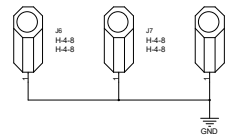


4: 计算公式

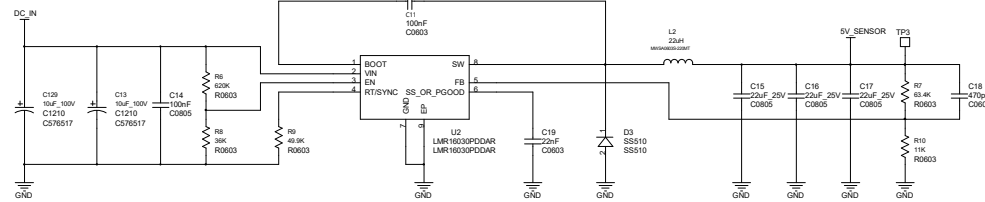
2路12V传感器电源



螺丝安装孔

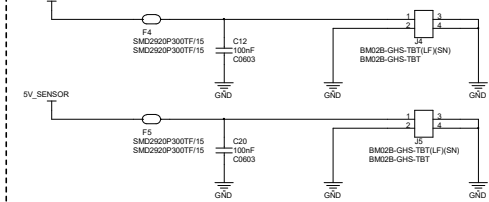


- 1: DC_IN TO 12V
- 2: 最大电流3A
- 3: 5V传感器电源。

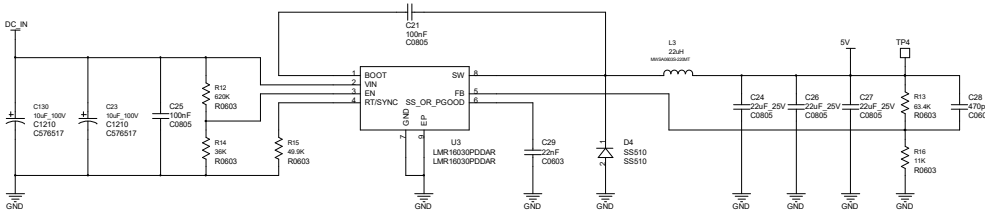


4: 计算公式

2路5V传感器电源

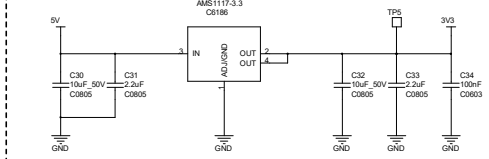


- 1: 12V TO 5V
- 2: 最大电流3A
- 3: 5V系统电源。。



3: 计算公式

- 1: 5V TO 3.3V
- 2: 最大电流1A
- 3: 3V3系统电源。。



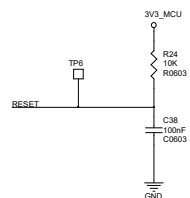
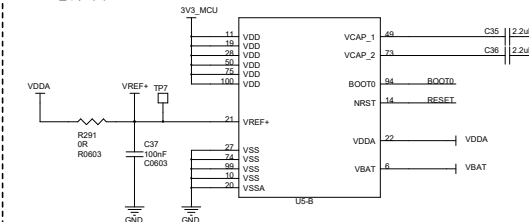
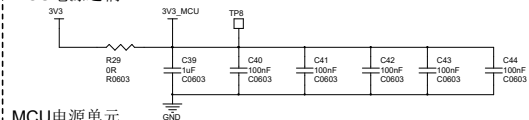
LED灯工作指示

页间连接符

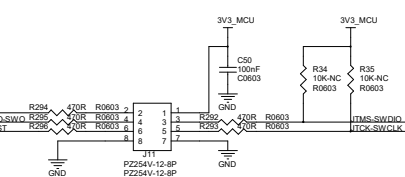
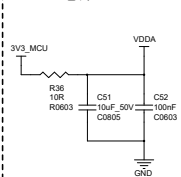
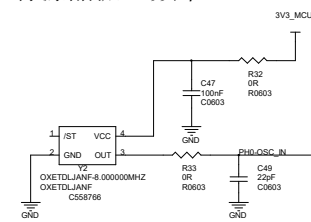
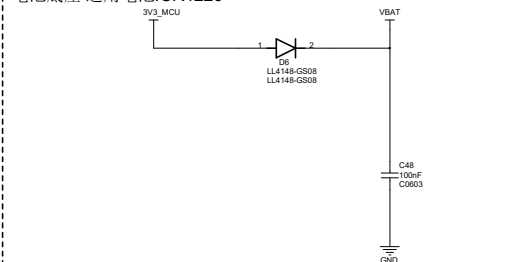
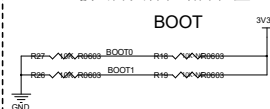


File	03_PWR
Size	Document Number
Date	Sunday, September 26, 2024
Sheet	3 of 11

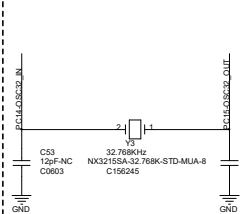
MCU电源单元



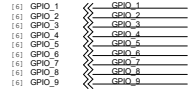
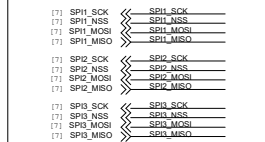
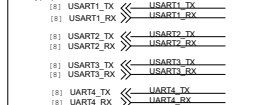
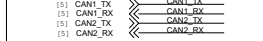
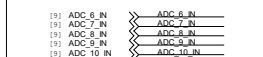
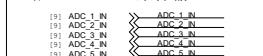
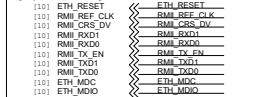
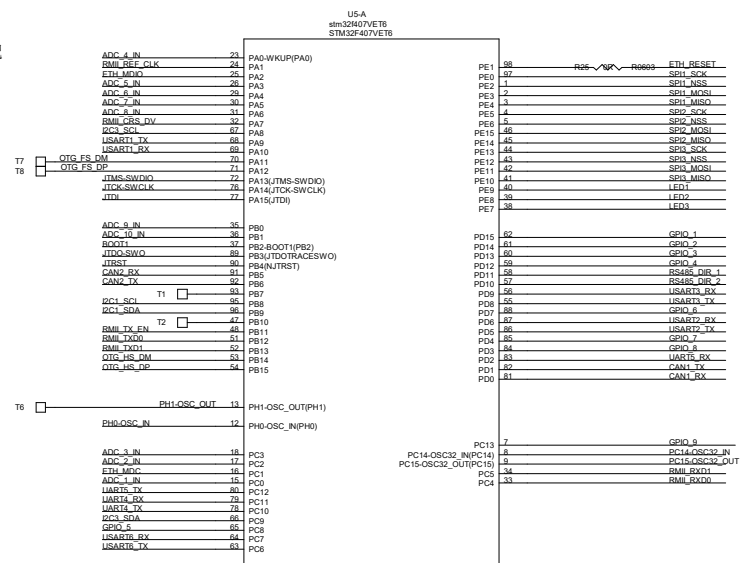
拨码开关打在断开位置




无源晶振: 32.768kHz。



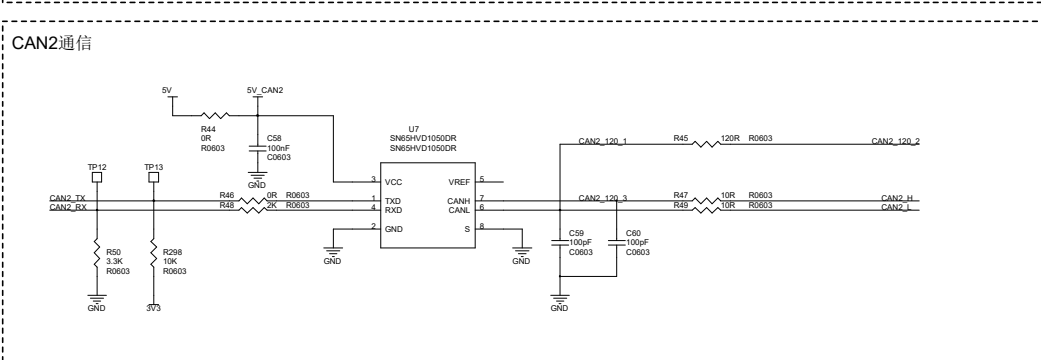
MCU管脚分配




CAN1通信



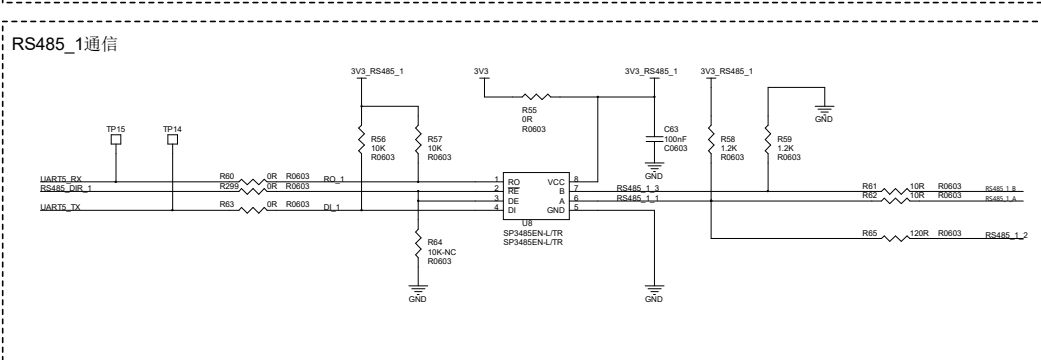
该原理图展示了CAN1通信模块的电路连接。核心器件为U8 SN65HVD1050DR，其VCC、VREF、TXD、RXD和GND引脚已正确连接。CAN1_TX和CAN1_RX信号线通过R43和R297（均为3.3K Ω）连接到TP11和TP10测试点。CAN1_120_1和CAN1_120_2信号线通过R38（120 Ω）连接到CAN1_120_3和CAN1_L信号线。CAN1_120_3和CAN1_L信号线通过R40（10 Ω）连接到CAN1_L和CAN1_1信号线。此外，电路中还包含C55（100nF）、C56（100nF）和C57（100nF）等电容，以及R37、R39、R41、R42、R43、R297、R38、R40、R0603等电阻。



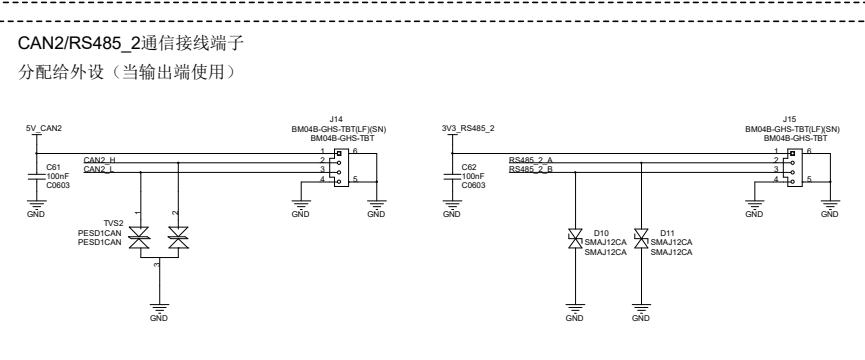
CAN2通信



该原理图展示了CAN2通信模块的电路设计。核心器件为U7 SN65HVD1050DR，其VCC、TXD、RXD和GND引脚已正确连接。电源部分由5V和5V CAN2提供，通过R44和R45进行滤波。信号部分通过R46、R47和R48进行匹配。终端电阻R50、R298、R45、R47和R48均设置为120Ω。电容C58、C59和C60用于滤波。图中还标注了TP12、TP13和R50、R298等测试点和电阻值。



RS485_1通信



CAN2/RS485_2通信接线端子
分配给外设（当输出端使用）

The diagrams illustrate the connection of CAN2 and RS485_2 signal lines to external components. The first diagram shows the CAN2_JL and CAN2_L lines connected to a 100nF capacitor C0603, a TVS2 diode, and a PESD1CAN diode. The second diagram shows the 3V3_RS485_2 and RS485_2_A/B lines connected to a 100nF capacitor C0603 and SMAJ12CA diodes D10 and D11. The third diagram shows the J15 RS485_2 lines connected to a 100nF capacitor C0603 and SMAJ12CA diodes D10 and D11.

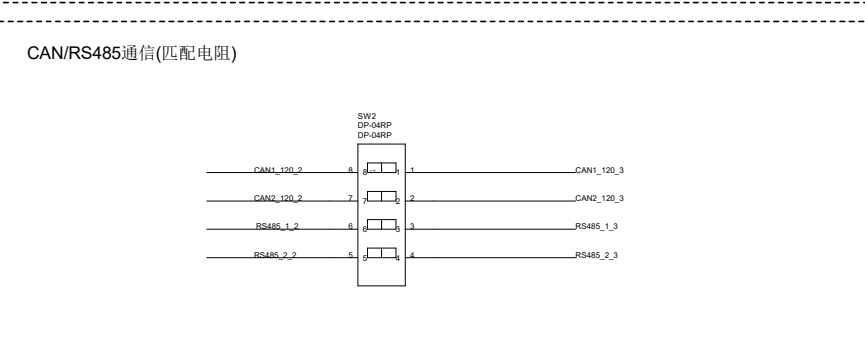
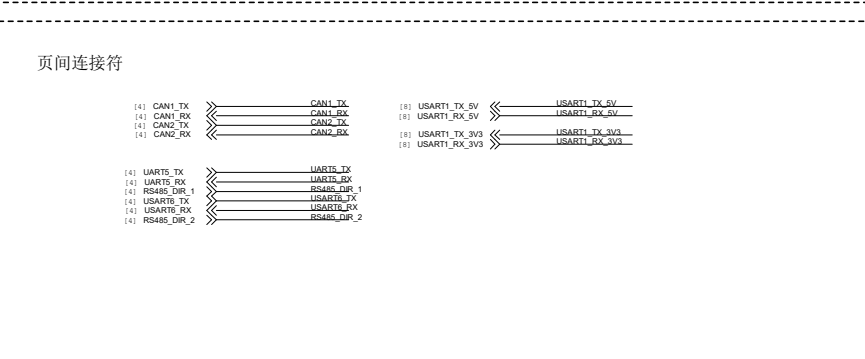


Diagram illustrating the connection of a CAN/RS485 communication module (SW2 DP-GMRP DP-04RP) to a CAN/RS485 network. The module has four pins on the left and four pins on the right. The connections are as follows:

- Pin 1 (Left) is connected to CAN1_120_2.
- Pin 2 (Left) is connected to CAN2_120_2.
- Pin 3 (Left) is connected to RS485_1_2.
- Pin 4 (Left) is connected to RS485_2_2.
- Pin 1 (Right) is connected to CAN1_120_3.
- Pin 2 (Right) is connected to CAN2_120_3.
- Pin 3 (Right) is connected to RS485_1_3.
- Pin 4 (Right) is connected to RS485_2_3.

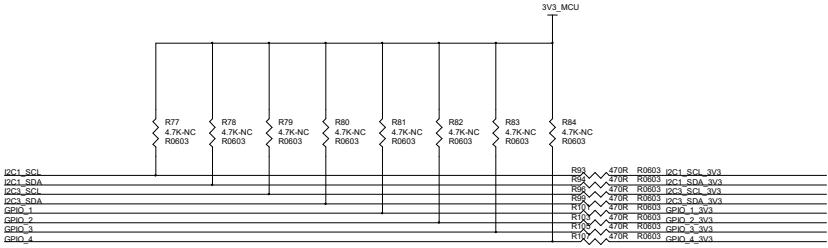


The diagram illustrates the connections for various modules in a system. The connections are as follows:

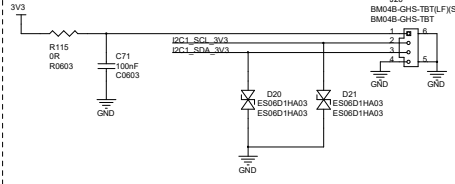
- CAN1 and CAN2:**
 - CAN1_TX is connected to CAN1_TX.
 - CAN1_RX is connected to CAN1_RX.
 - CAN2_TX is connected to CAN2_TX.
 - CAN2_RX is connected to CAN2_RX.
- UART5:**
 - UART5_TX is connected to UART5_TX.
 - UART5_RX is connected to UART5_RX.
- RS485:**
 - RS485_DFP_1 is connected to RS485_DFP_1.
 - RS485_DFP_2 is connected to RS485_DFP_2.
 - RS485_TX is connected to RS485_TX.
 - RS485_RX is connected to RS485_RX.
- USART1:**
 - USART1_TX_5V is connected to USART1_TX_5V.
 - USART1_RX_5V is connected to USART1_RX_5V.
 - USART1_TX_3V3 is connected to USART1_TX_3V3.
 - USART1_RX_3V3 is connected to USART1_RX_3V3.



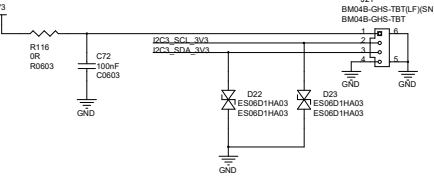
I2C 3V3 TO 5V 电平转换电路



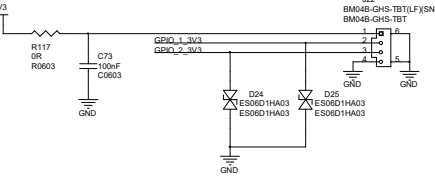
I2C1-3V3 电平-接口1



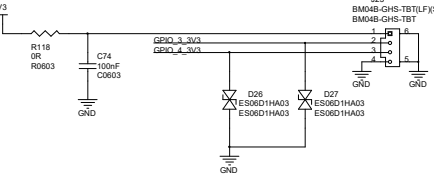
I2C3-3V3 电平-接口2



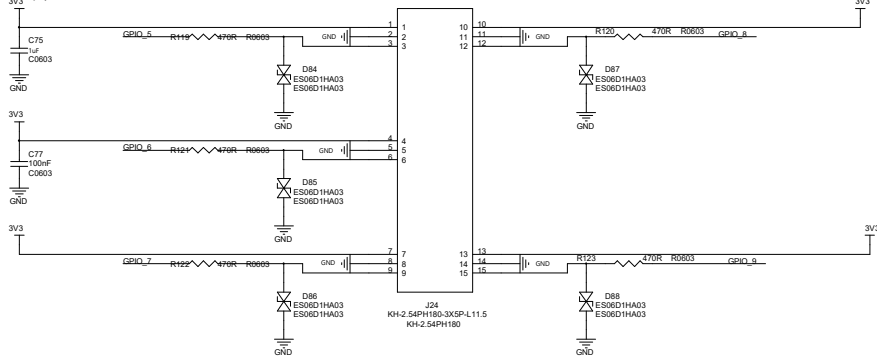
GPIO-3V3电平-接口1



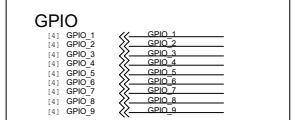
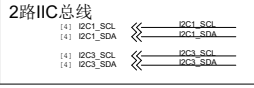
GPIO-3V3电平-接口2



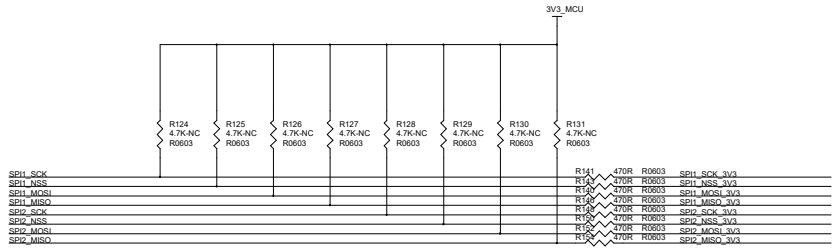
GPIO-3V3电平,3排针接口, 2.54间距



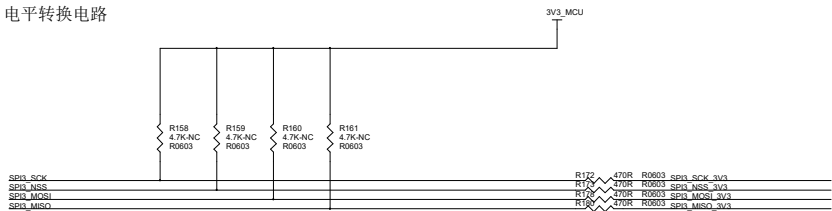
页面连接符



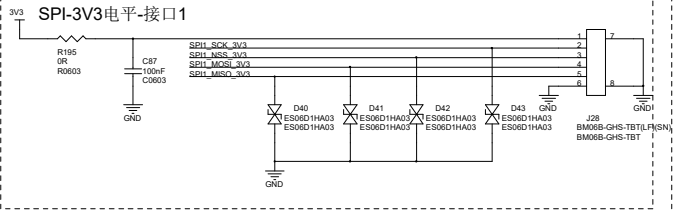
SPI 3V3 TO 5V 电平转换电路



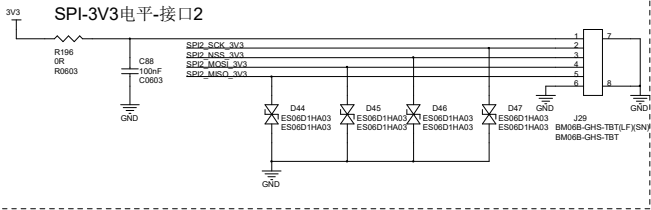
SPI 3V3 TO 5V 电平转换电路



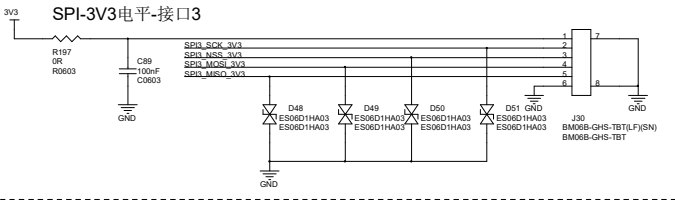
SPI-3V3电平-接口1



SPI-3V3电平-接口2



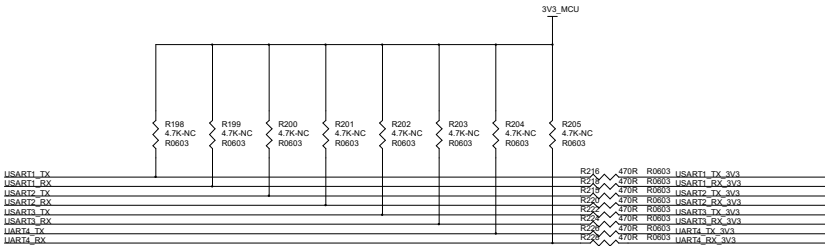
SPI-3V3电平-接口3



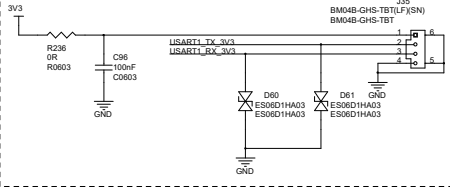
页间连接符

(4)	SPI1_SCK	《	SP1_SCK
(4)	SPI1_NSS	《	SP1_NSS
(4)	SPI1_MOSI	《	SP1_MOSI
(4)	SPI1_MISO	《	SP1_MISO
(4)	SPI2_SCK	《	SP2_SCK
(4)	SPI2_NSS	《	SP2_NSS
(4)	SPI2_MOSI	《	SP2_MOSI
(4)	SPI2_MISO	《	SP2_MISO
(4)	SPI3_SCK	《	SP3_SCK
(4)	SPI3_NSS	《	SP3_NSS
(4)	SPI3_MOSI	《	SP3_MOSI
(4)	SPI3_MISO	《	SP3_MISO

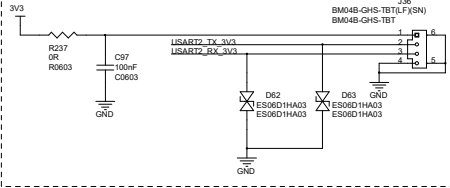
UART 3V3 TO 5V 电平转换电路



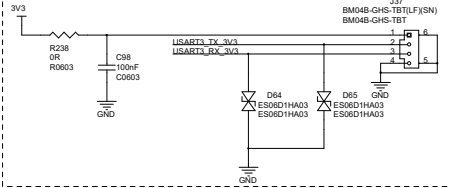
串口-3V3电平-接口1



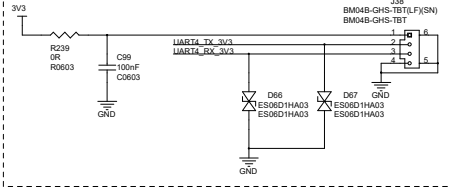
串口-3V3电平-接口2



串口-3V3电平-接口3

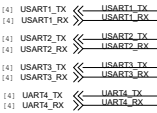


串口-3V3电平-接口4



页间连接符

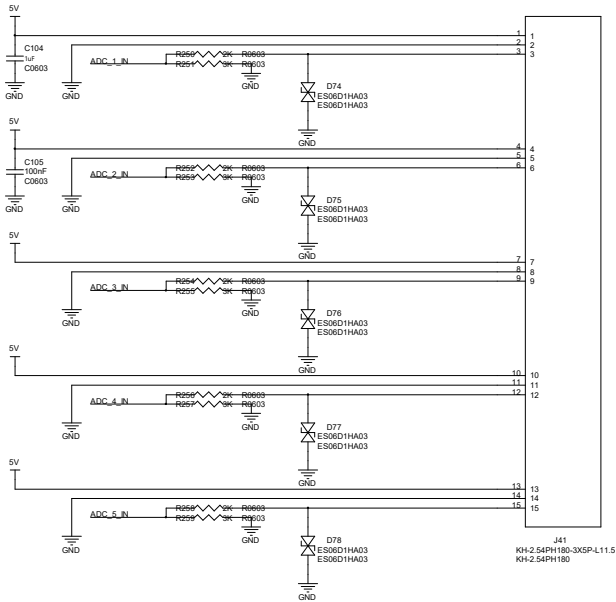
4路串口



页间连接符

[4]	ADC_1_IN	ADC_1_IN
[4]	ADC_2_IN	ADC_2_IN
[4]	ADC_3_IN	ADC_3_IN
[4]	ADC_4_IN	ADC_4_IN
[4]	ADC_5_IN	ADC_5_IN

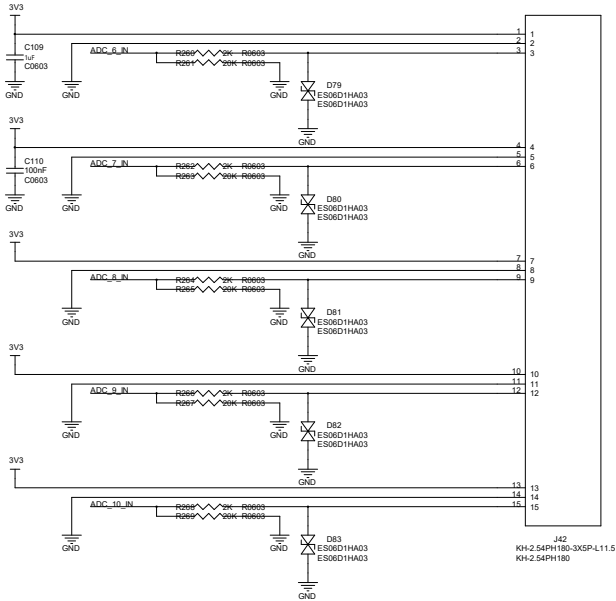
5路-5V ADC IN 采样输入



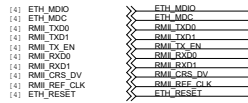
页间连接符

[4]	ADC_6_IN	ADC_6_IN
[4]	ADC_7_IN	ADC_7_IN
[4]	ADC_8_IN	ADC_8_IN
[4]	ADC_9_IN	ADC_9_IN
[4]	ADC_10_IN	ADC_10_IN

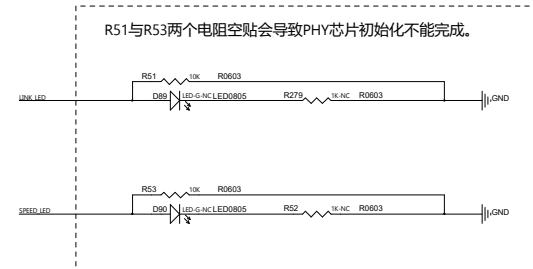
5路-3V3 ADC IN 采样输入



标准: IEEE 802.3, 10/100 Base-T/TX PHY。

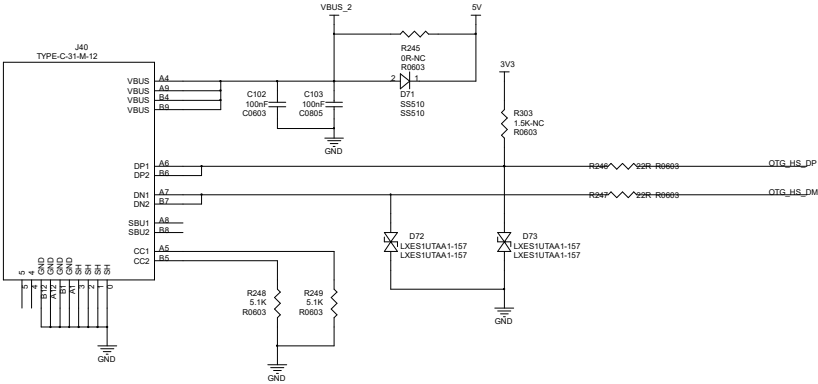


额定电流: 2.5A。



USB接口-1

USB接口-2



页间连接符

