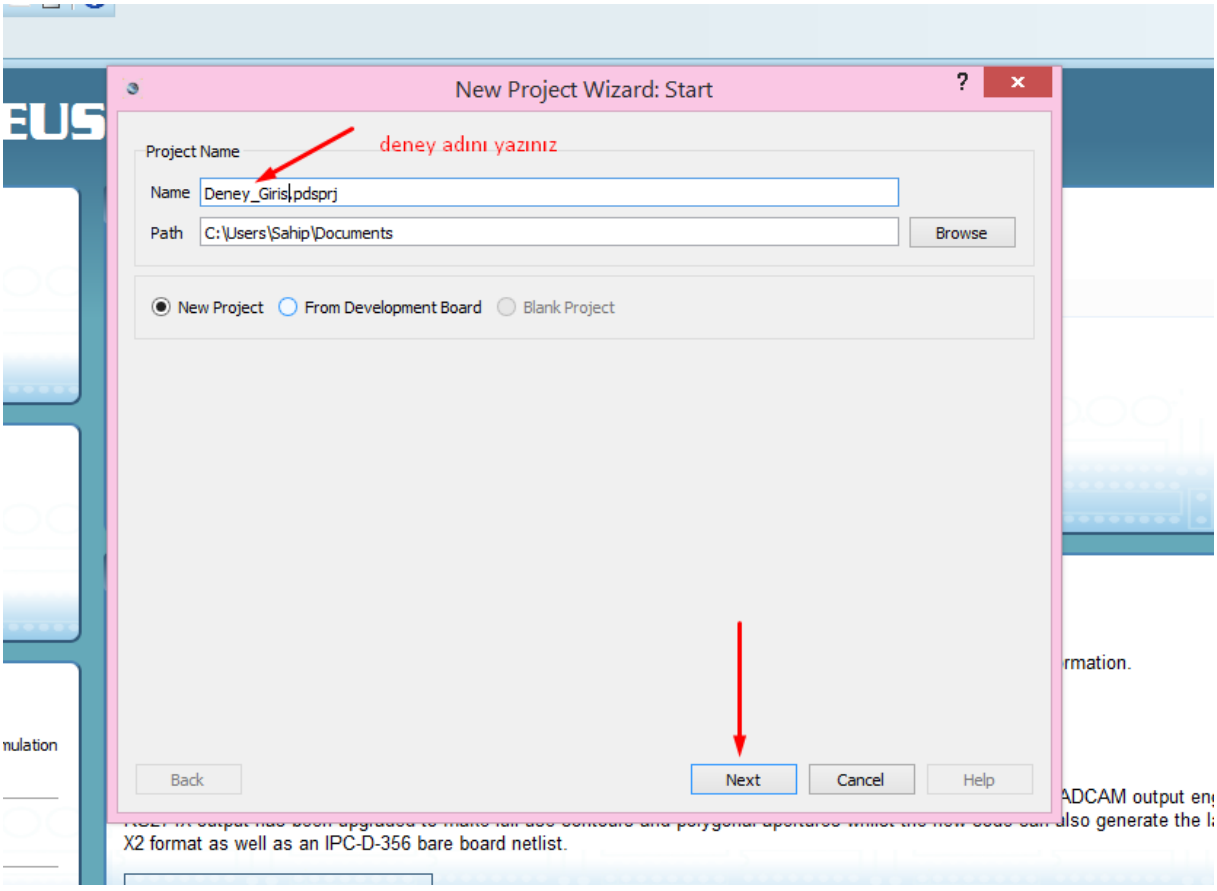
Proteus Design Suite 8.5

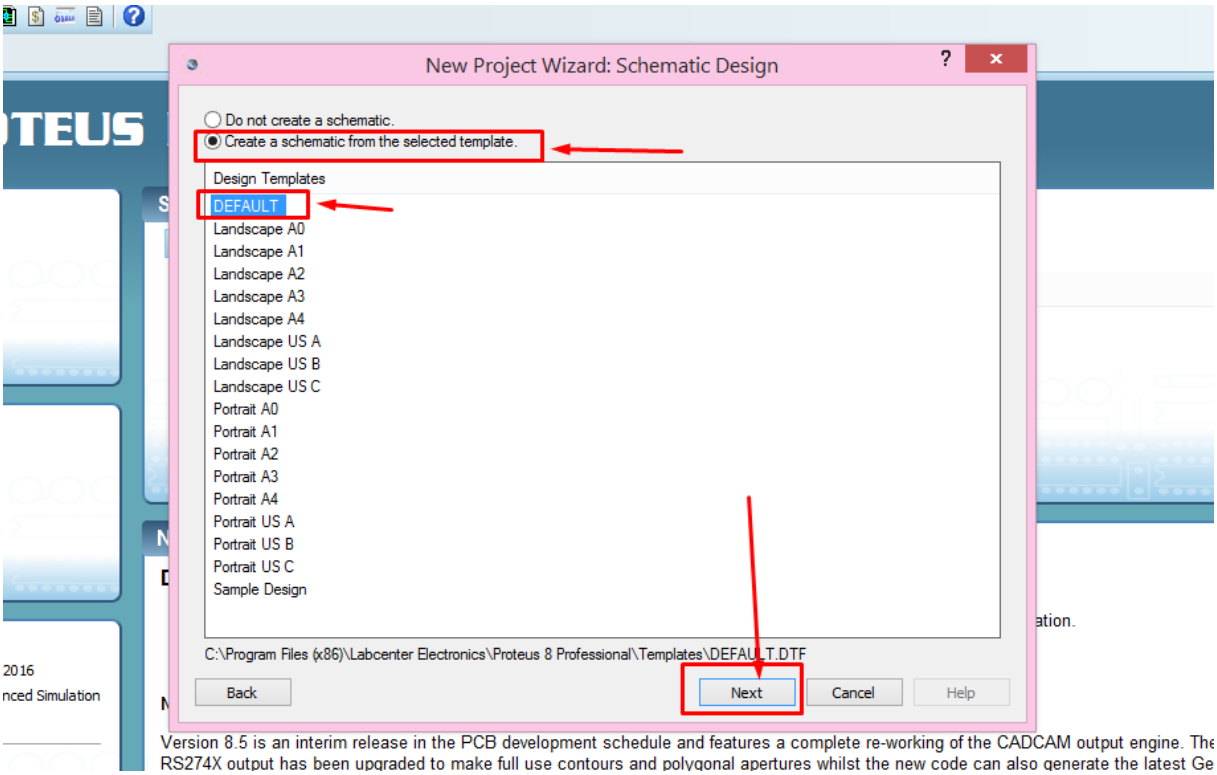
Proteus elektronik devre çizimi, benzetimi, baskı devre tasarımı yapabilen bir bilgisayar programıdır.

İsis ve Ares adlı 2 alt modülden oluşmaktadır.

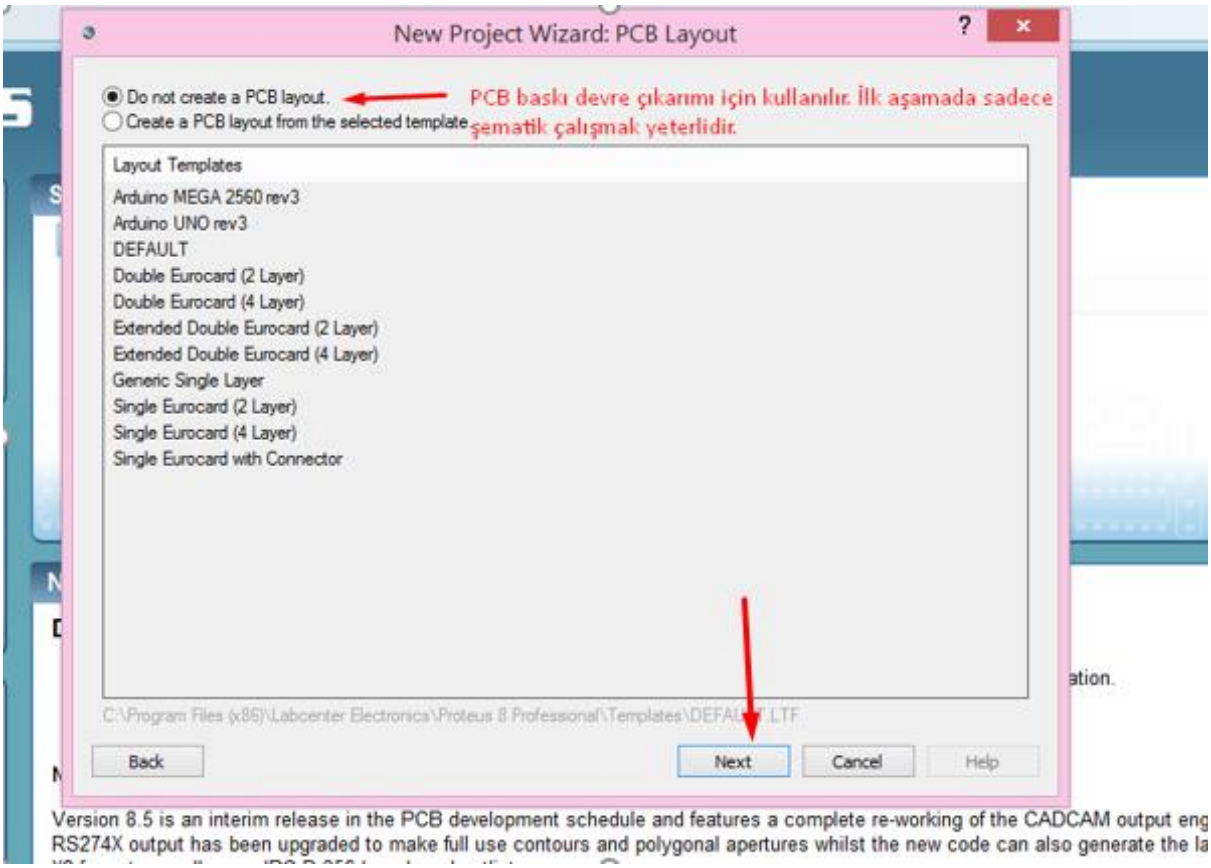
İsis, şematik devre çizimi ve benzetimi için kullanılırken, Ares, baskı devre hazırlama programı olarak kullanılmaktadır.

PROTEUS ile ilgili giriş bilgileri

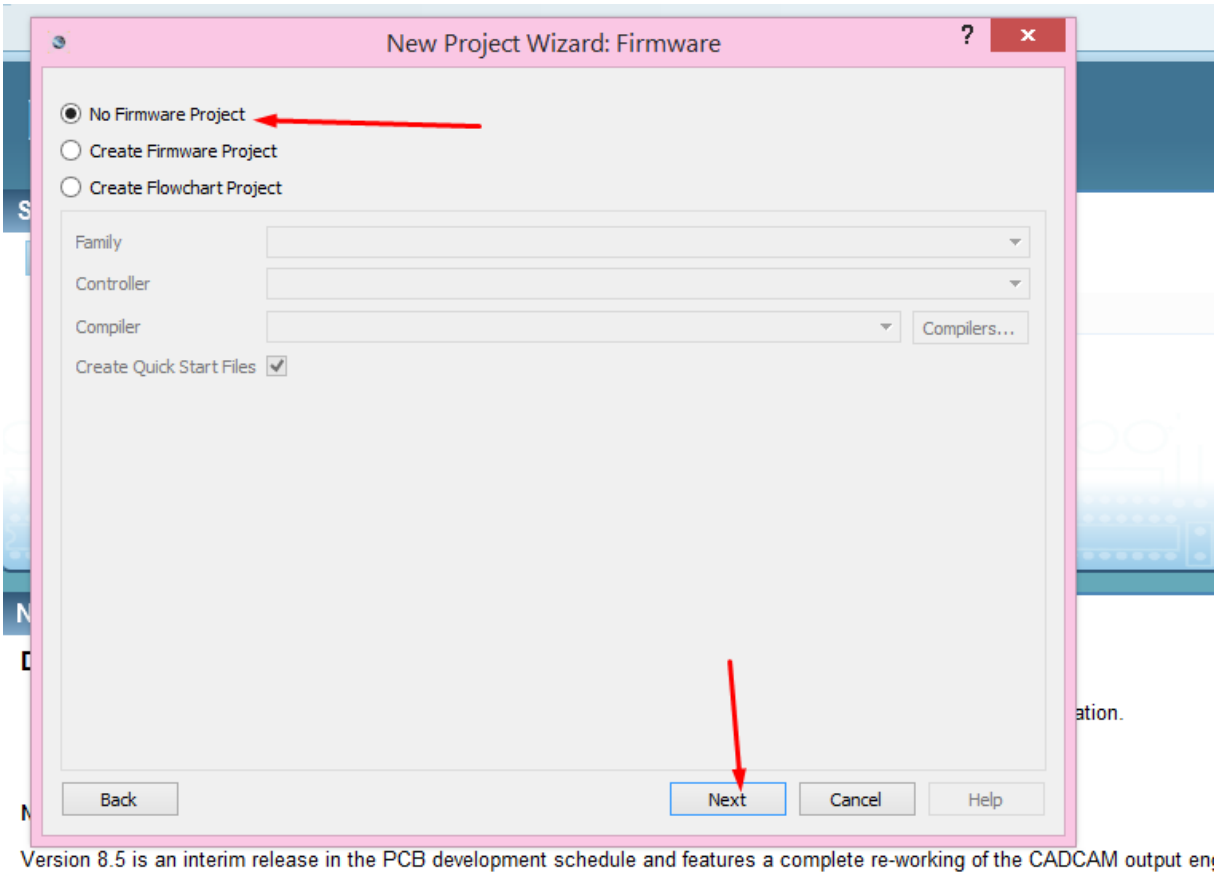




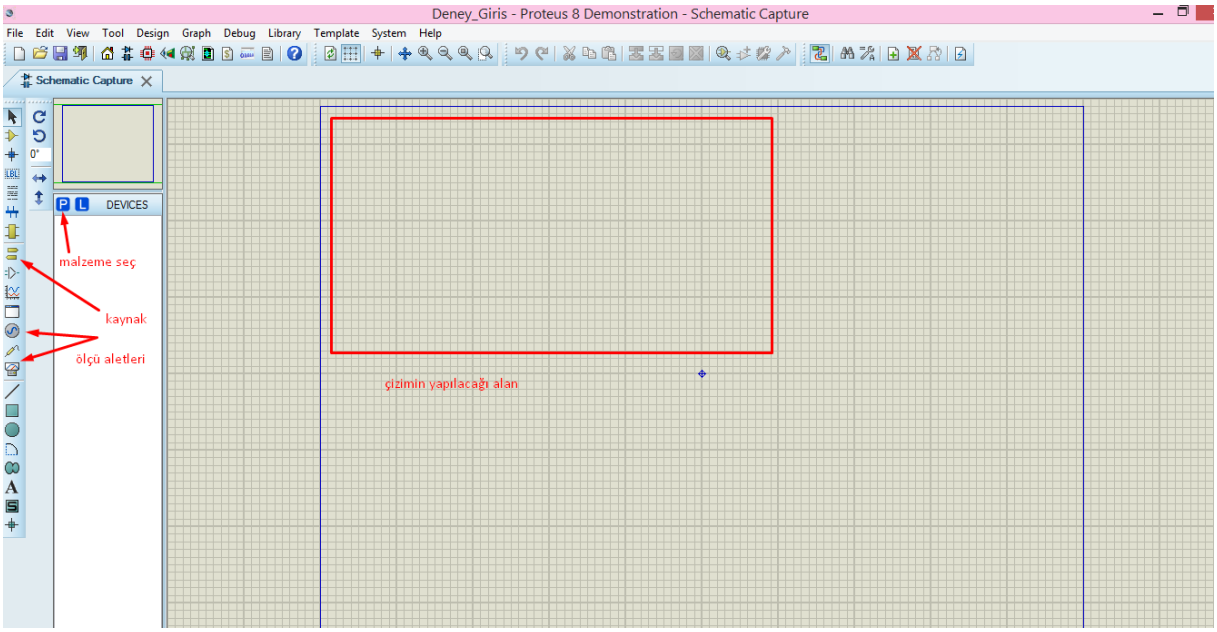
Version 8.5 is an interim release in the PCB development schedule and features a complete re-working of the CADCAM output engine. The RS274X output has been upgraded to make full use contours and polygonal apertures whilst the new code can also generate the latest Ge

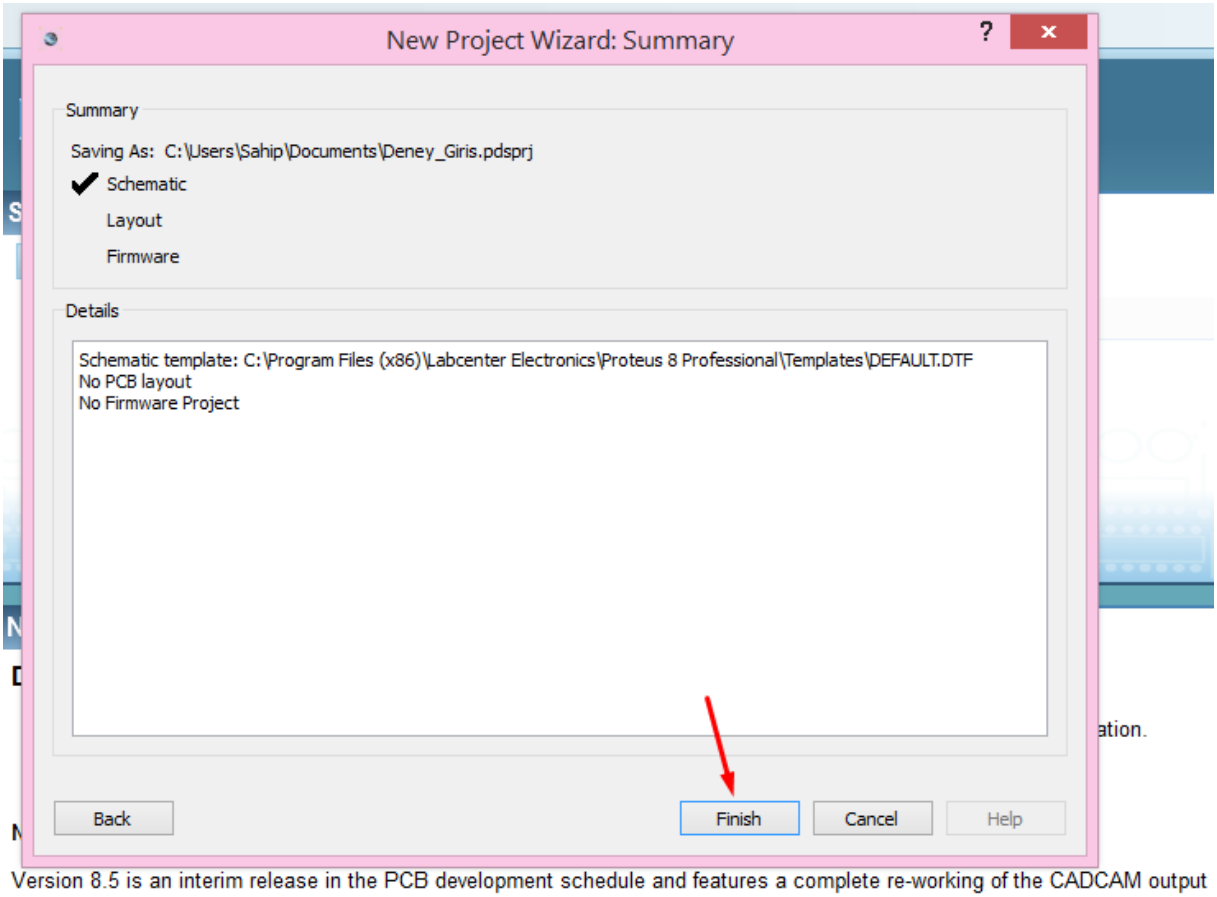


Version 8.5 is an interim release in the PCB development schedule and features a complete re-working of the CADCAM output eng
RS274X output has been upgraded to make full use contours and polygonal apertures whilst the new code can also generate the la

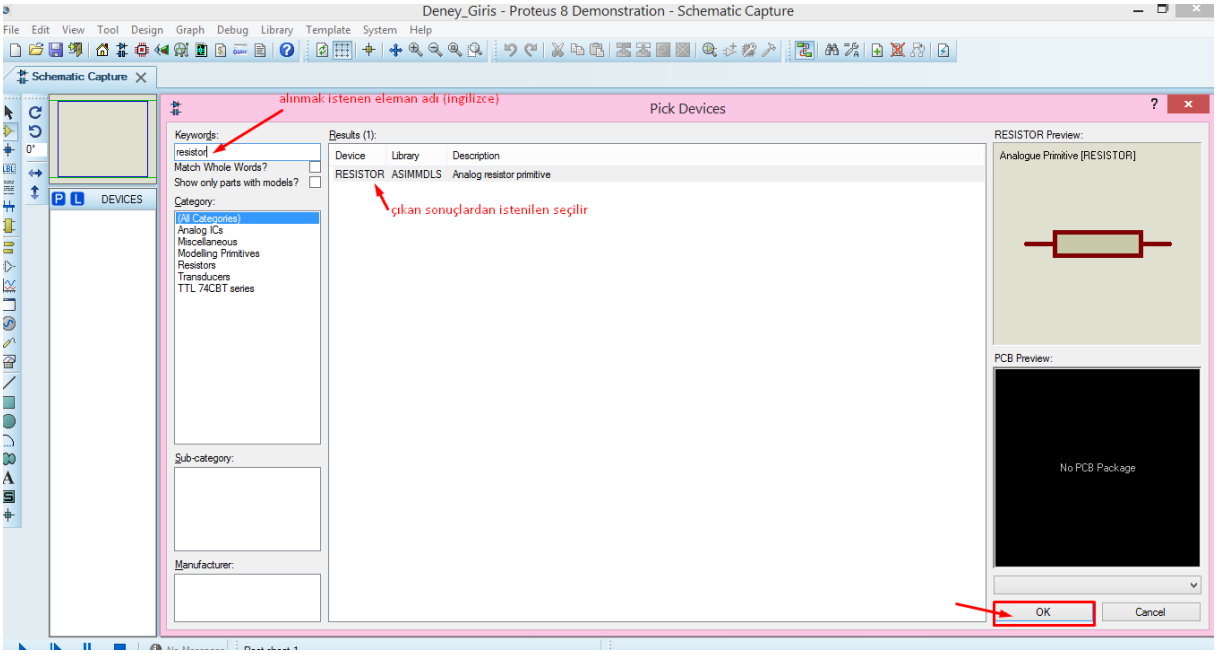


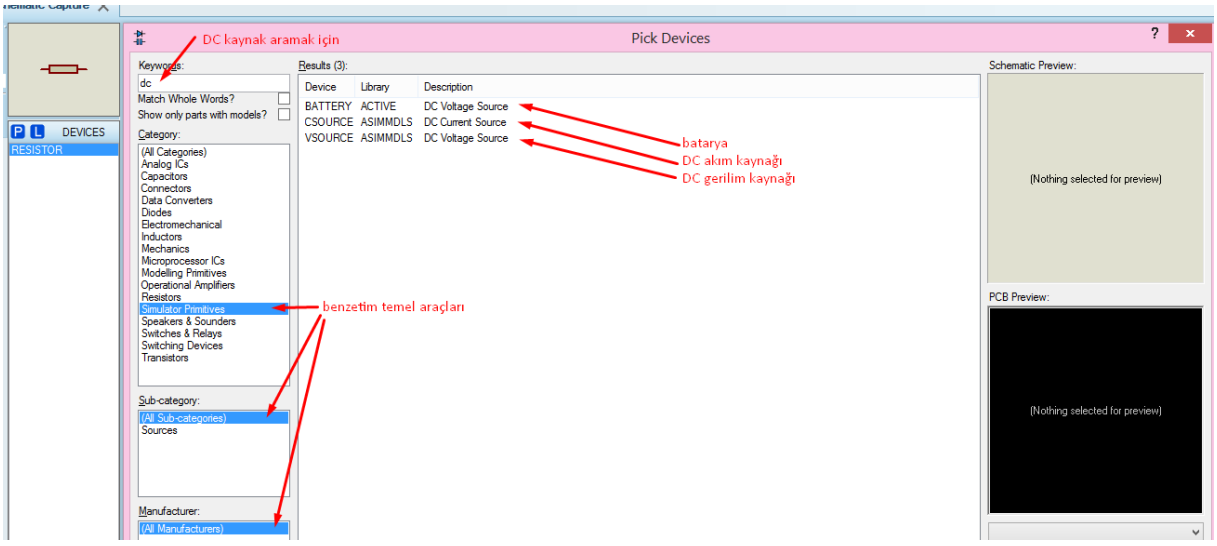
Version 8.5 is an interim release in the PCB development schedule and features a complete re-working of the CADCAM output eng
RS274X output has been upgraded to make full use contours and polygonal apertures whilst the new code can also generate the l

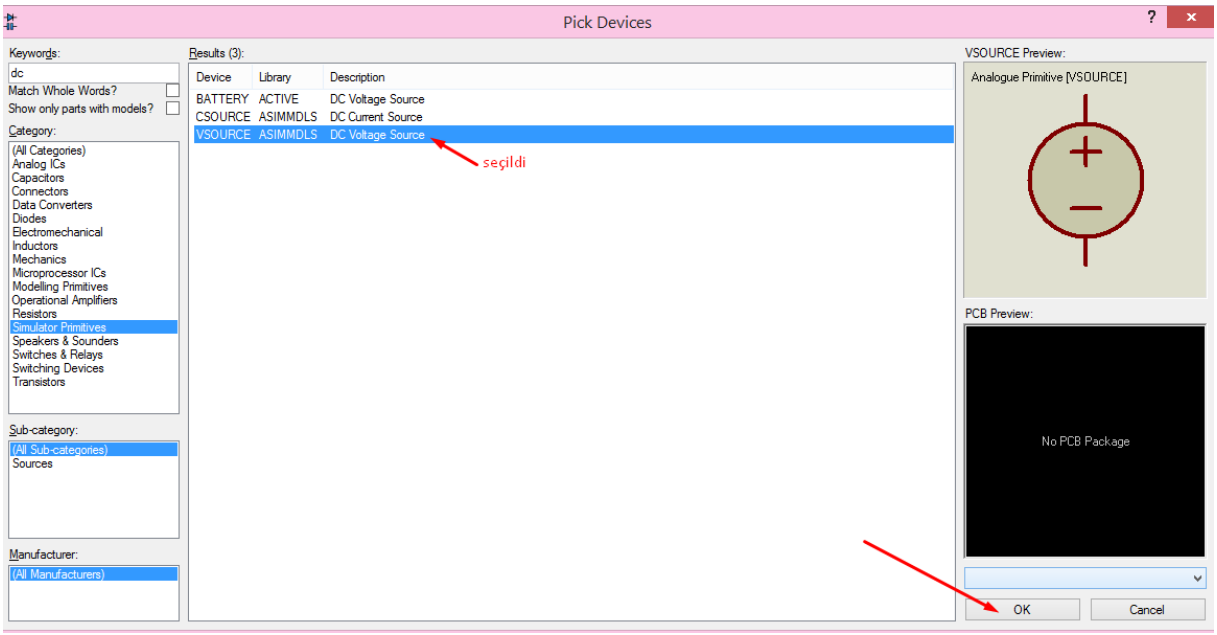


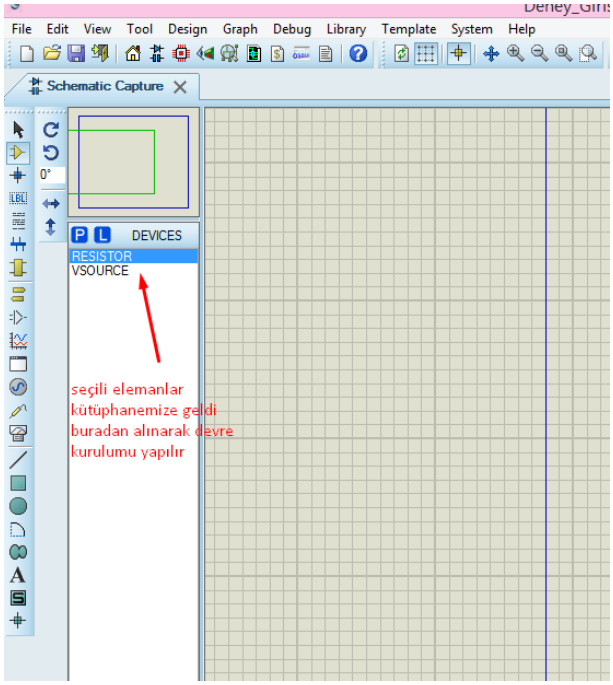


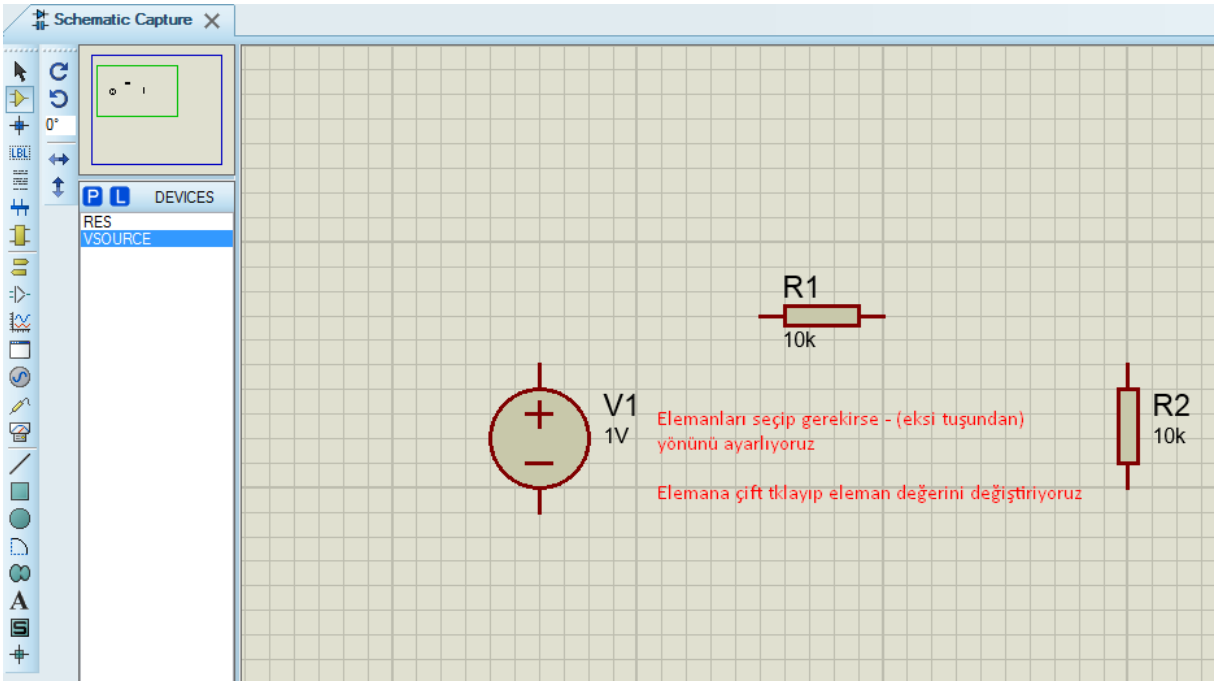
Version 8.5 is an interim release in the PCB development schedule and features a complete re-working of the CADCAM output RS274X output has been upgraded to make full use contours and polygonal apertures whilst the new code can also generate th

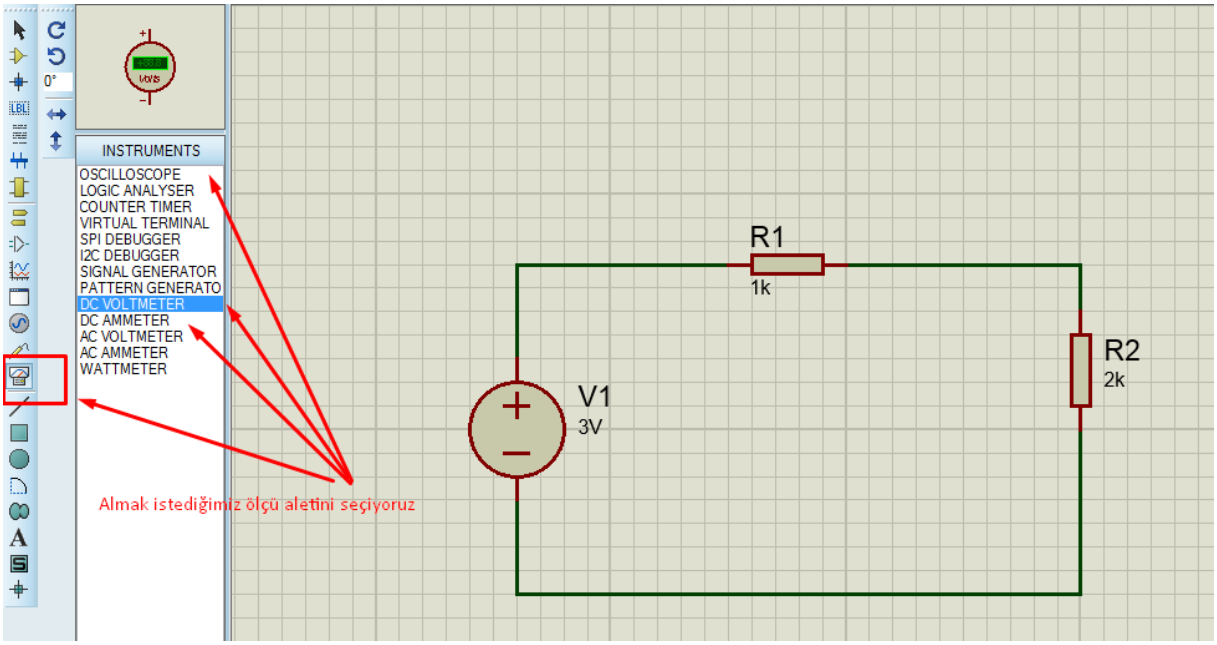

Deney_Giris - Proteus 8 Demonstration - Schematic Capture
File Edit View Tool Design Graph Debug Library Template System Help
Schematic Capture
alınmak istenen eleman adı (ingilizce)
Pick Devices
Keywords:
resistor
Match Whole Words?
Show only parts with models?
Category:
(All Categories)
Analog ICs
Miscellaneous
Modelling Primitives
Resistors
Transducers
TTL 74CBT series
Sub-category:
Manufacturer:
Results (1):
Device Library Description
RESISTOR ASIMMDLS Analog resistor primitive
çıkan sonuçlardan istenilen seçilir
RESISTOR Preview:
Analogue Primitive [RESISTOR]
PCB Preview:
No PCB Package
OK
Cancel
DEVICES

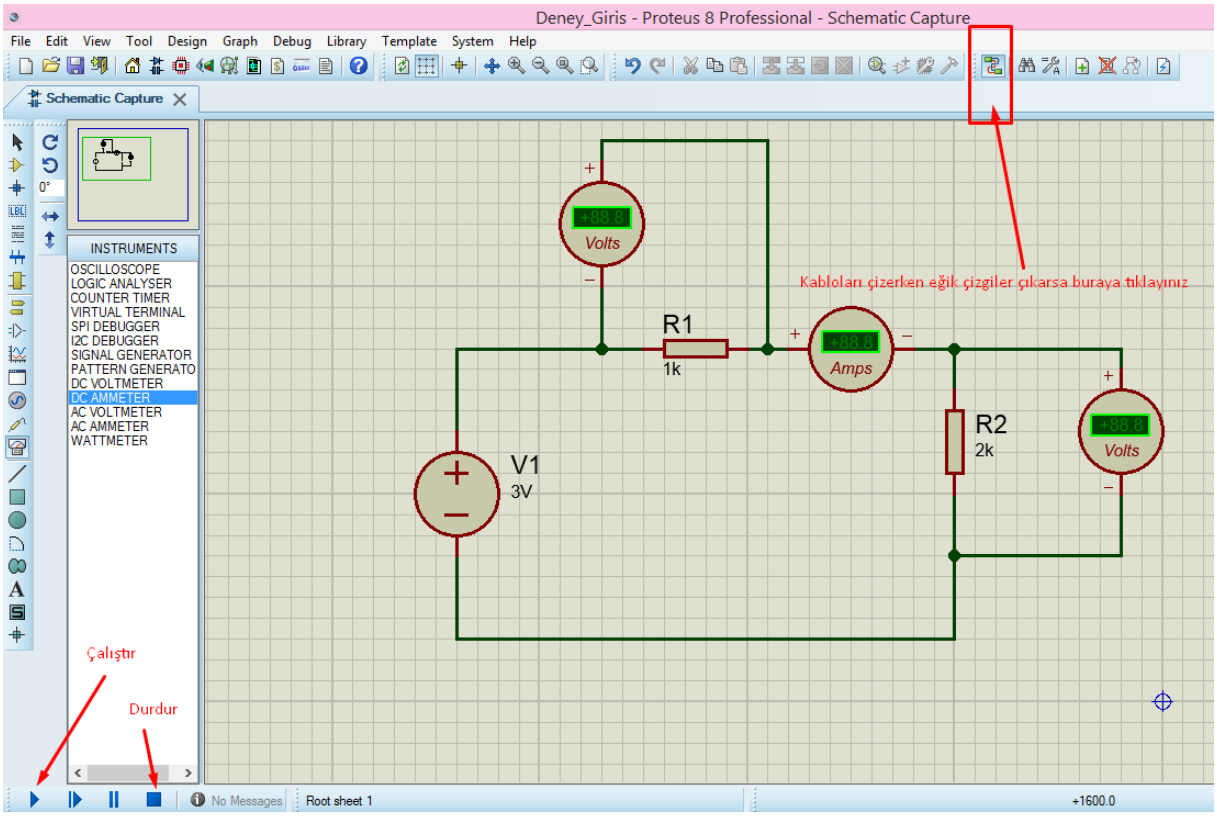

DC kaynak aramak için
Pick Devices
Keywords:
dc
Match Whole Words?
Show only parts with models?
Category:
(All Categories)
Analog ICs
Capacitors
Connectors
Data Converters
Diodes
Electromechanical
Inductors
Mechanics
Microprocessor ICs
Modelling Primitives
Operational Amplifiers
Resistors
Simulator Primitives
Speakers & Sounders
Switches & Relays
Switching Devices
Transistors
Sub-category:
(All Sub-categories)
Sources
Manufacturer:
(All Manufacturers)
Results (3):
Device Library Description
BATTERY ACTIVE DC Voltage Source
CSOURCE ASIMMDLS DC Current Source
VSOURCE ASIMMDLS DC Voltage Source
batarya
DC akım kaynağı
DC gerilim kaynağı
benzetim temel araçları
Schematic Preview:
(Nothing selected for preview)
PCB Preview:
(Nothing selected for preview)
DEVICES
RESISTOR

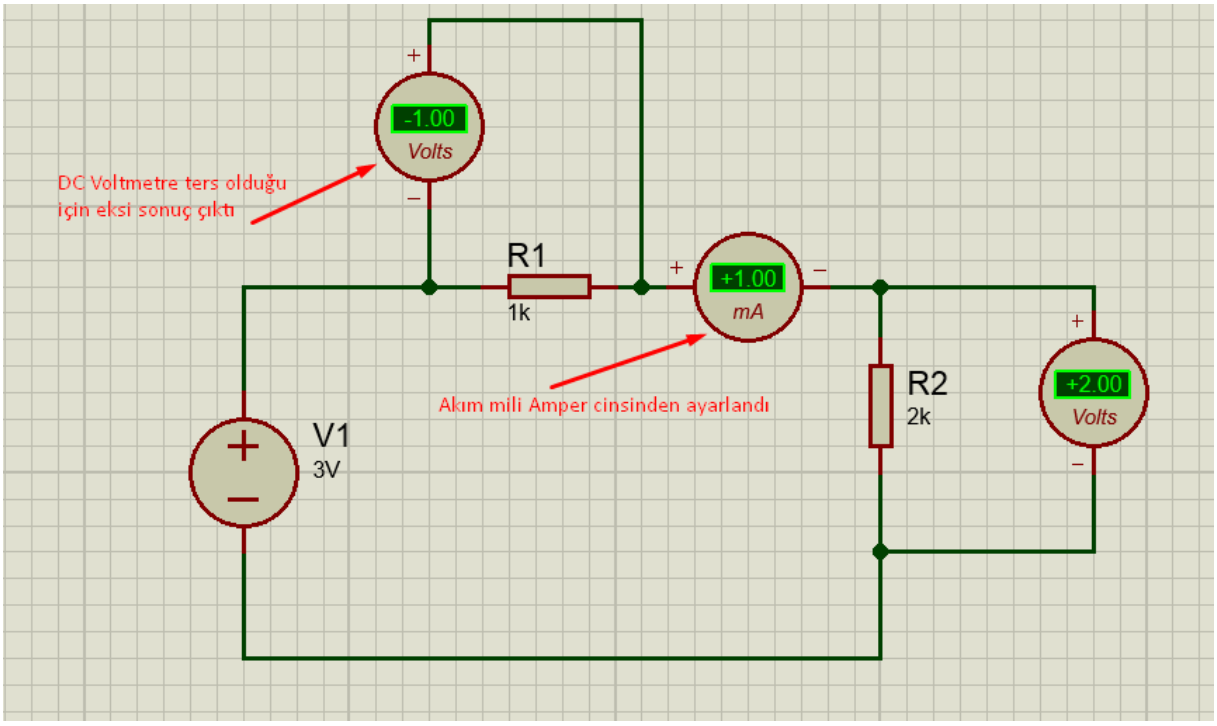

Pick Devices
Keywords:
dc
Match Whole Words?
Show only parts with models?
Category:
(All Categories)
Analog ICs
Capacitors
Connectors
Data Converters
Diodes
Electromechanical
Inductors
Mechanics
Microprocessor ICs
Modelling Primitives
Operational Amplifiers
Resistors
Simulator Primitives
Speakers & Sounders
Switches & Relays
Switching Devices
Transistors
Sub-category:
(All Sub-categories)
Sources
Manufacturer:
(All Manufacturers)
Results (3):
Device
Library
Description
BATTERY ACTIVE DC Voltage Source
CSOURCE ASIMMDLS DC Current Source
VSOURCE ASIMMDLS DC Voltage Source
seçildi
VSOURCE Preview:
Analogue Primitive [VSOURCE]
PCB Preview:
No PCB Package
OK
Cancel

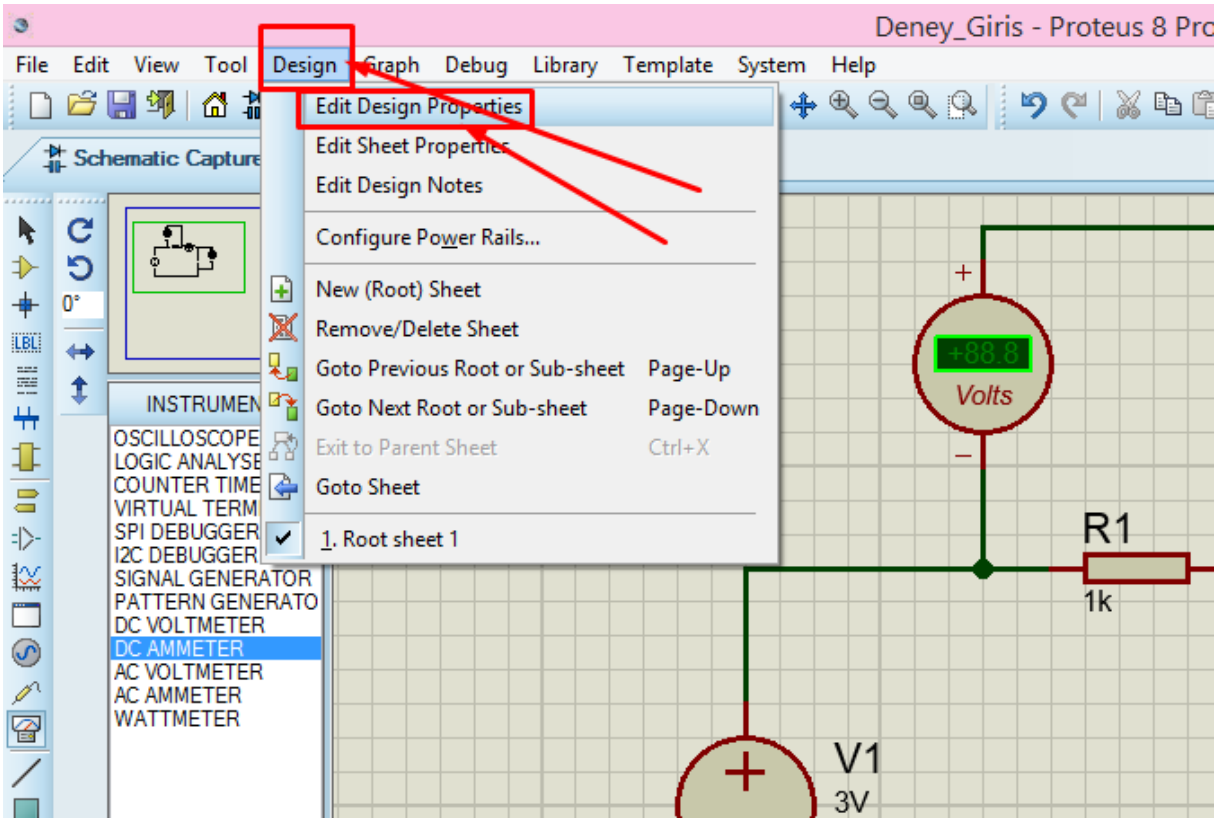

File Edit View Tool Design Graph Debug Library Template System Help
Schematic Capture
DEVICES
RESISTOR
VSOURCE
seçili elemanlar
kütüphanemize geldi
buradan alınarak devre
kurulumu yapılır

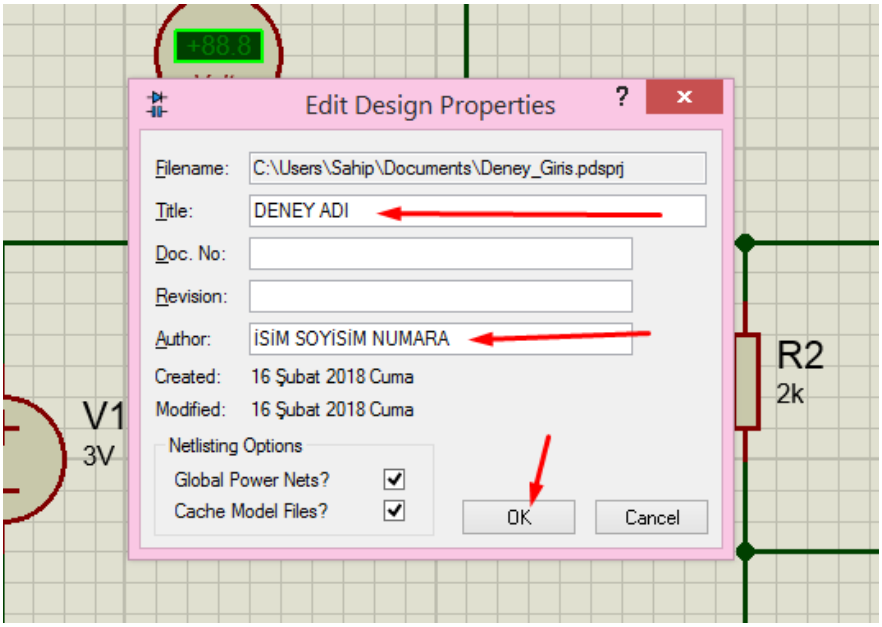

Schematic Capture
DEVICES
RES
VSOURCE
R1
10k
V1
1V
Elemanları seçip gerekirse - (eksi tuşundan)
yönünü ayarlıyoruz
Elemana çift tıklayıp eleman değerini değiştiriyoruz
R2
10k

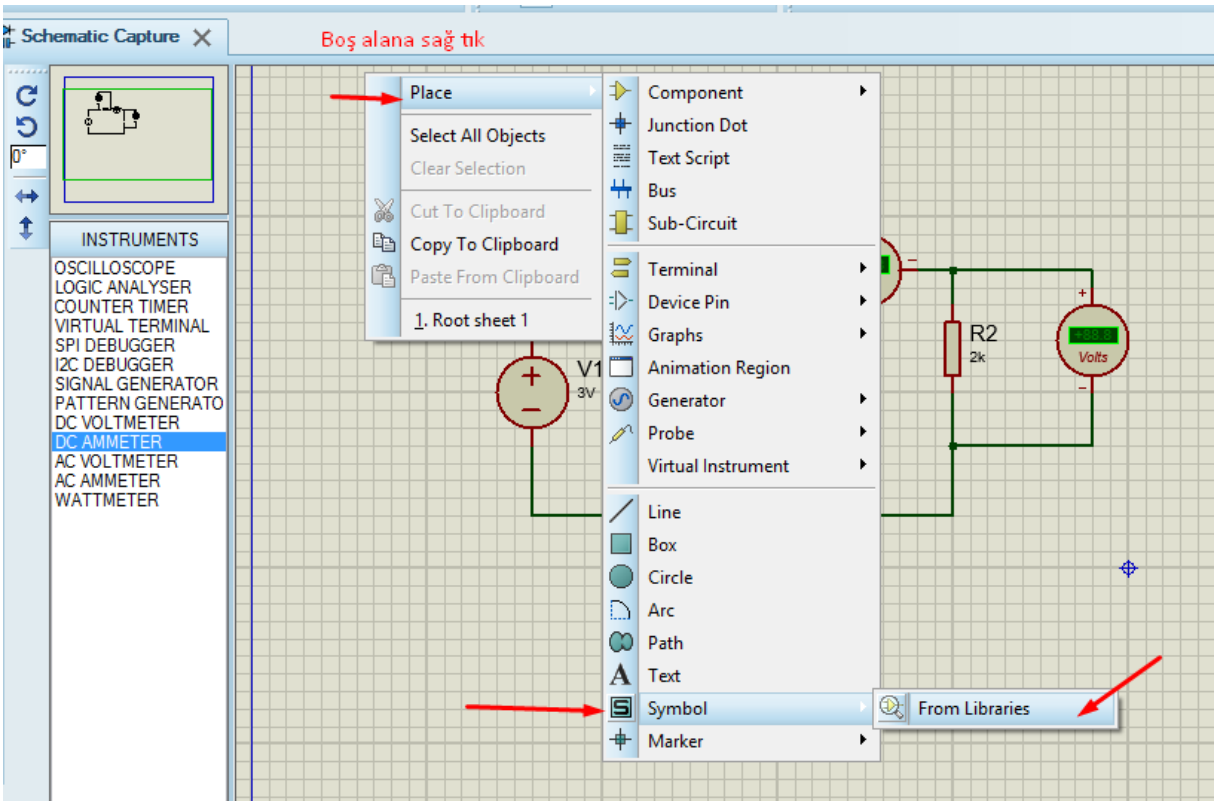

INSTRUMENTS
OSCILLOSCOPE
LOGIC ANALYSER
COUNTER TIMER
VIRTUAL TERMINAL
SPI DEBUGGER
I2C DEBUGGER
SIGNAL GENERATOR
PATTERN GENERATO
DC VOLTMETER
DC AMMETER
AC VOLTMETER
AC AMMETER
WATTMETER
Almak istediğimiz ölçü aletini seçiyoruz
R1
1k
R2
2k
V1
3V

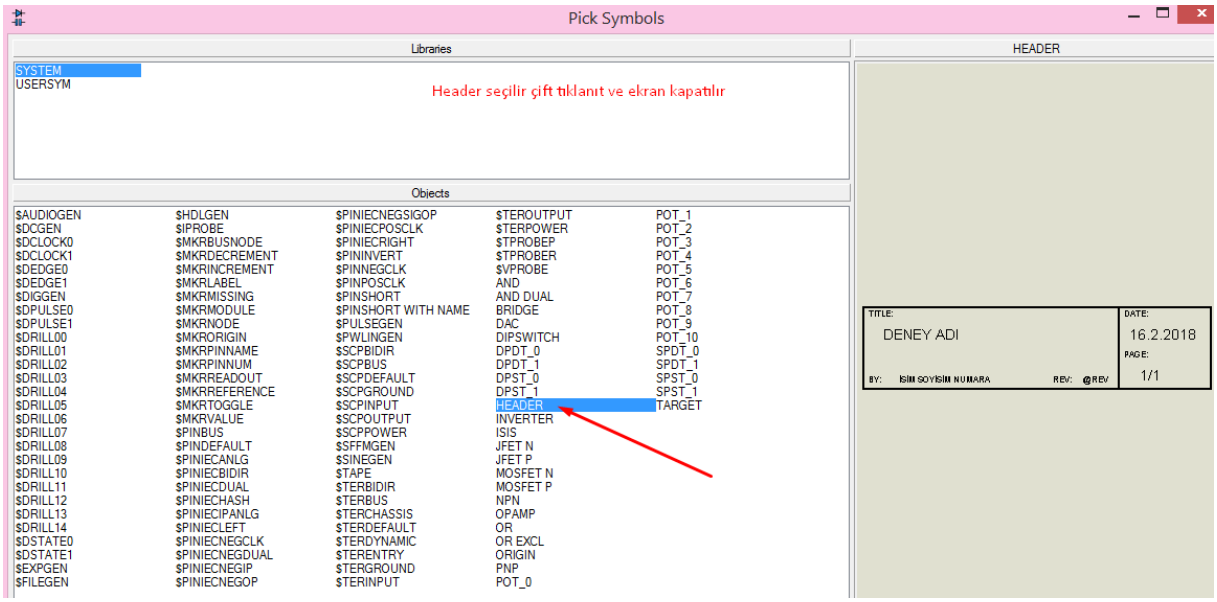

Deney_Giris - Proteus 8 Professional - Schematic Capture
File Edit View Tool Design Graph Debug Library Template System Help
Schematic Capture
INSTRUMENTS
OSCILLOSCOPE
LOGIC ANALYSER
COUNTER TIMER
VIRTUAL TERMINAL
SPI DEBUGGER
I2C DEBUGGER
SIGNAL GENERATOR
PATTERN GENERATO
DC VOLTMETER
DC AMMETER
AC VOLTMETER
AC AMMETER
WATTMETER
Kabloları çizerken eğik çizgiler çıkarsa buraya tıklayınız
Çalıştır
Durdur
Volts
Amps
R1
1k
R2
2k
V1
3V
No Messages
Root sheet 1
+1600.0

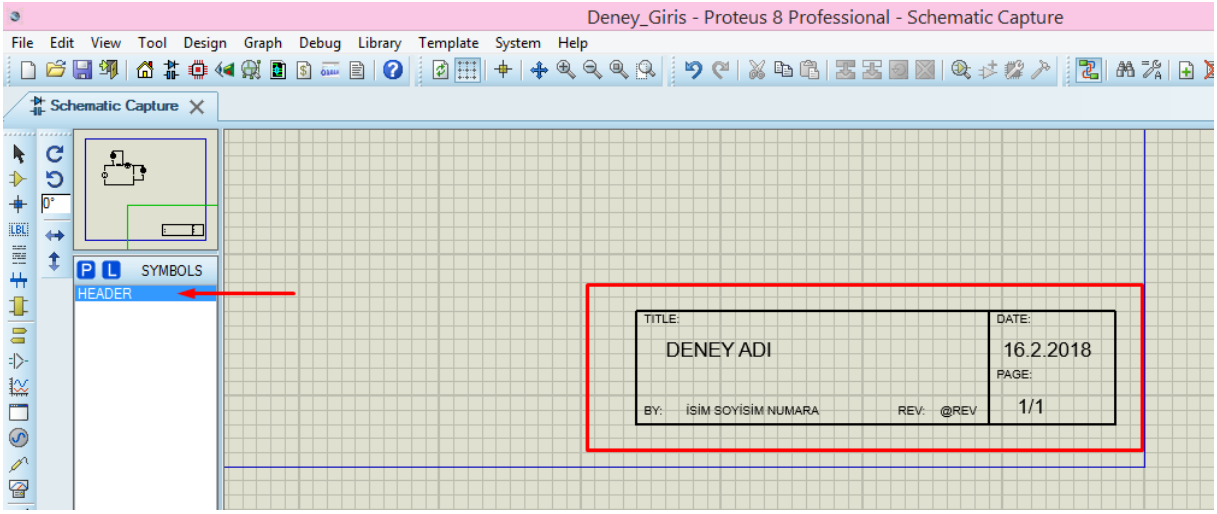

-1.00
Volts
DC Voltmetre ters olduğu
için eksi sonuç çıktı
R1
1k
+1.00
mA
Akım mili Amper cinsinden ayarlandı
R2
2k
+2.00
Volts
V1
3V

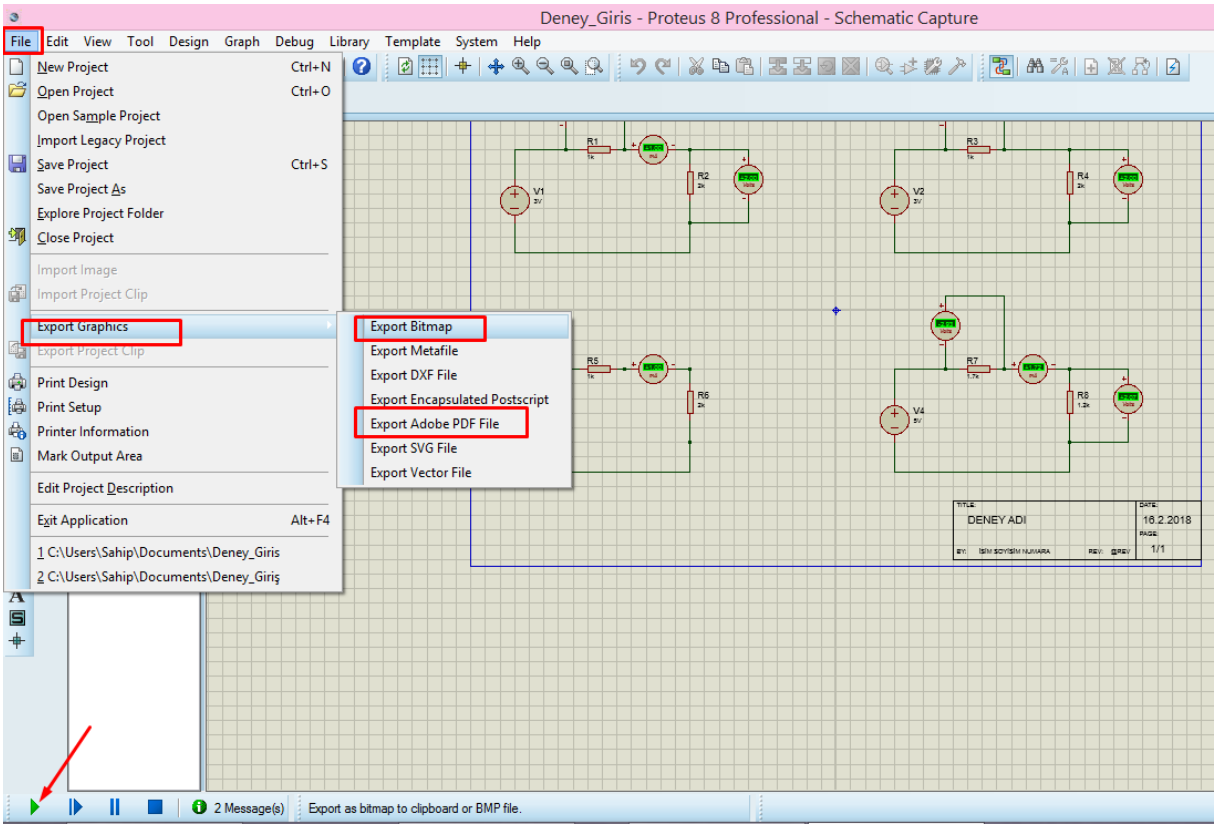

Deney_Giris - Proteus 8 Pro
File Edit View Tool Design Graph Debug Library Template System Help
Schematic Capture
Edit Design Properties
Edit Sheet Properties
Edit Design Notes
Configure Power Rails...
New (Root) Sheet
Remove/Delete Sheet
Goto Previous Root or Sub-sheet Page-Up
Goto Next Root or Sub-sheet Page-Down
Exit to Parent Sheet Ctrl+X
Goto Sheet
1. Root sheet 1
INSTRUMEN
OSCILLOSCOPE
LOGIC ANALYSE
COUNTER TIME
VIRTUAL TERM
SPI DEBUGGER
I2C DEBUGGER
SIGNAL GENERATOR
PATTERN GENERATO
DC VOLTMETER
DC AMMETER
AC VOLTMETER
AC AMMETER
WATTMETER
+88.8
Volts
R1
1k
V1
3V


Edit Design Properties
Filename: C:\Users\Sahip\Documents\Deney_Giris.pdsprj
Title: DENEY ADI
Doc. No:
Revision:
Author: İSİM SOYİSİM NUMARA
Created: 16 Şubat 2018 Cuma
Modified: 16 Şubat 2018 Cuma
Netlisting Options
Global Power Nets?
Cache Model Files?
OK
Cancel
R2
2k
V1
3V

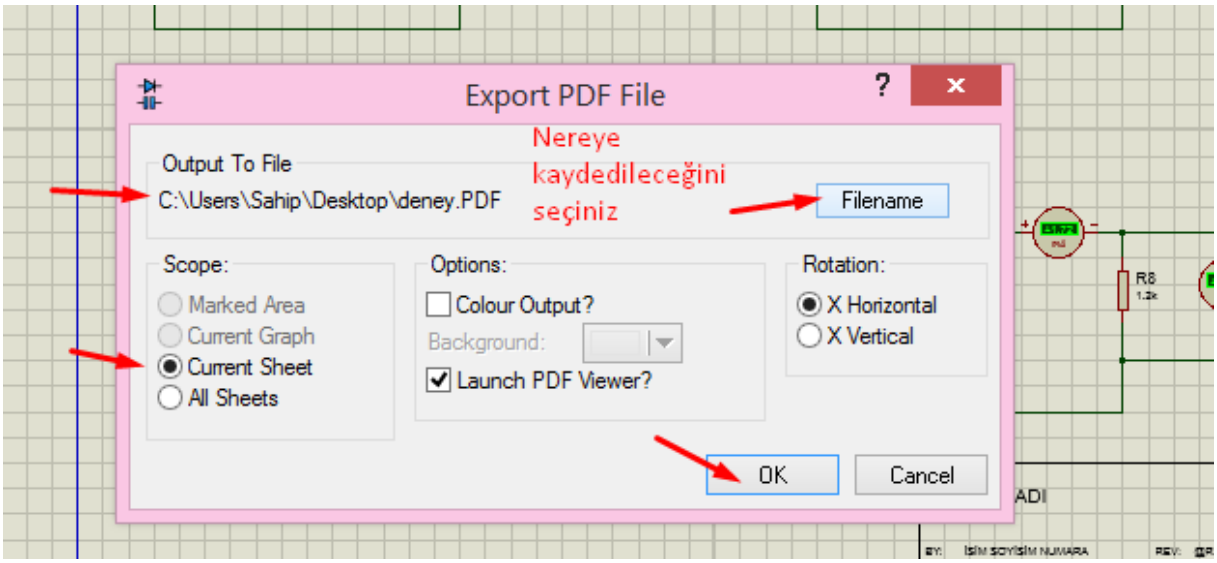

Schematic Capture
Boş alana sağ tık
INSTRUMENTS
OSCILLOSCOPE
LOGIC ANALYSER
COUNTER TIMER
VIRTUAL TERMINAL
SPI DEBUGGER
I2C DEBUGGER
SIGNAL GENERATOR
PATTERN GENERATO
DC VOLTMETER
DC AMMETER
AC VOLTMETER
AC AMMETER
WATTMETER
Place
Select All Objects
Clear Selection
Cut To Clipboard
Copy To Clipboard
Paste From Clipboard
1. Root sheet 1
Component
Junction Dot
Text Script
Bus
Sub-Circuit
Terminal
Device Pin
Graphs
Animation Region
Generator
Probe
Virtual Instrument
Line
Box
Circle
Arc
Path
Text
Symbol
Marker
From Libraries
R2
2k
V1
3V
Volts

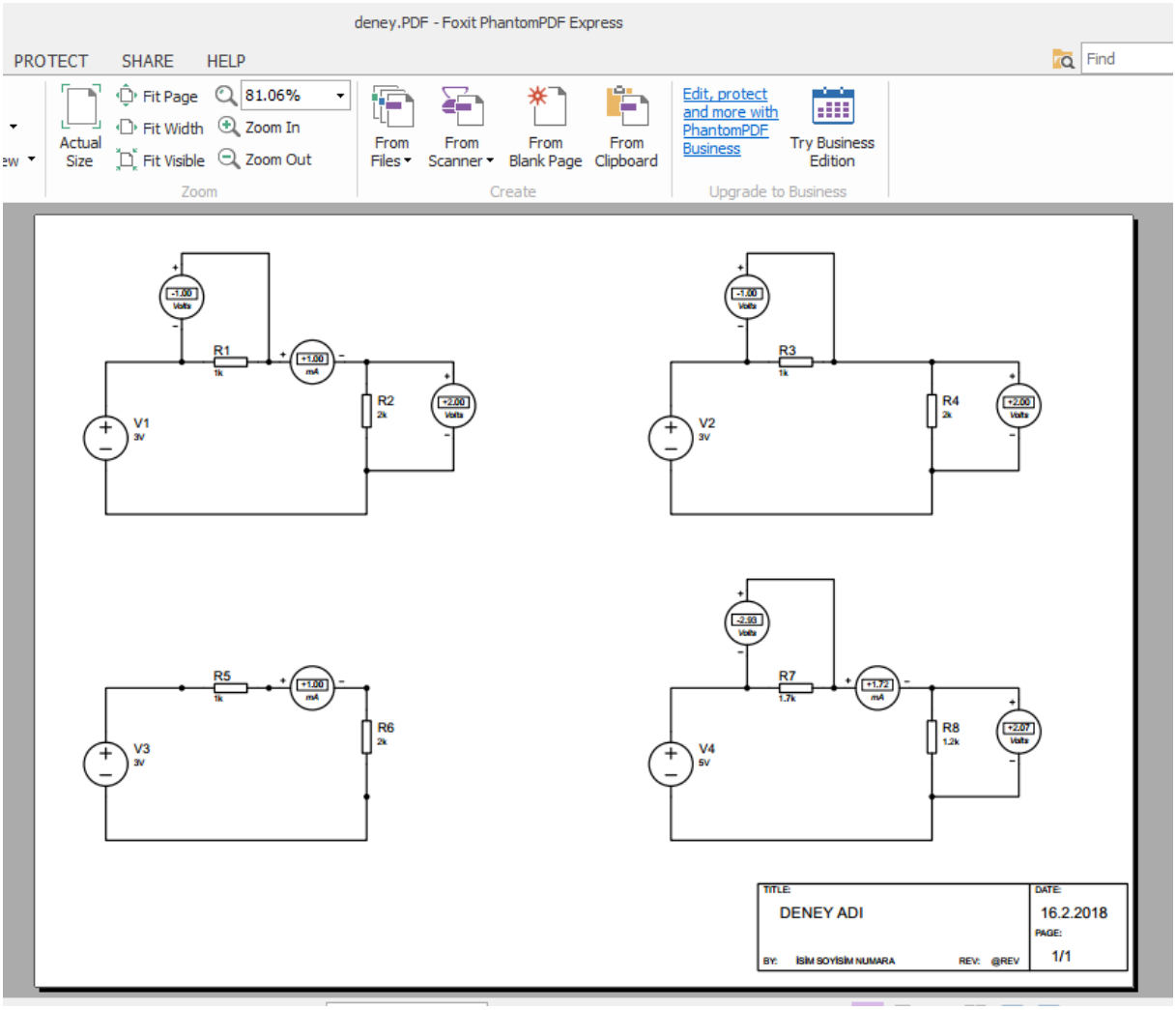

Pick Symbols
Libraries
SYSTEM
USERSYM
Header seçilir çift tıklanır ve ekran kapatılır
Objects
HEADER
TITLE:
DENEY ADI
DATE:
16.2.2018
PAGE:
1/1
BY: İSİM SOYİSİM NUMARA
REV: @REV


Deney_Giris - Proteus 8 Professional - Schematic Capture
File Edit View Tool Design Graph Debug Library Template System Help
Schematic Capture
SYMBOLS
HEADER
TITLE:
DENEY ADI
DATE:
16.2.2018
PAGE:
1/1
BY: İSİM SOYİSİM NUMARA
REV: @REV

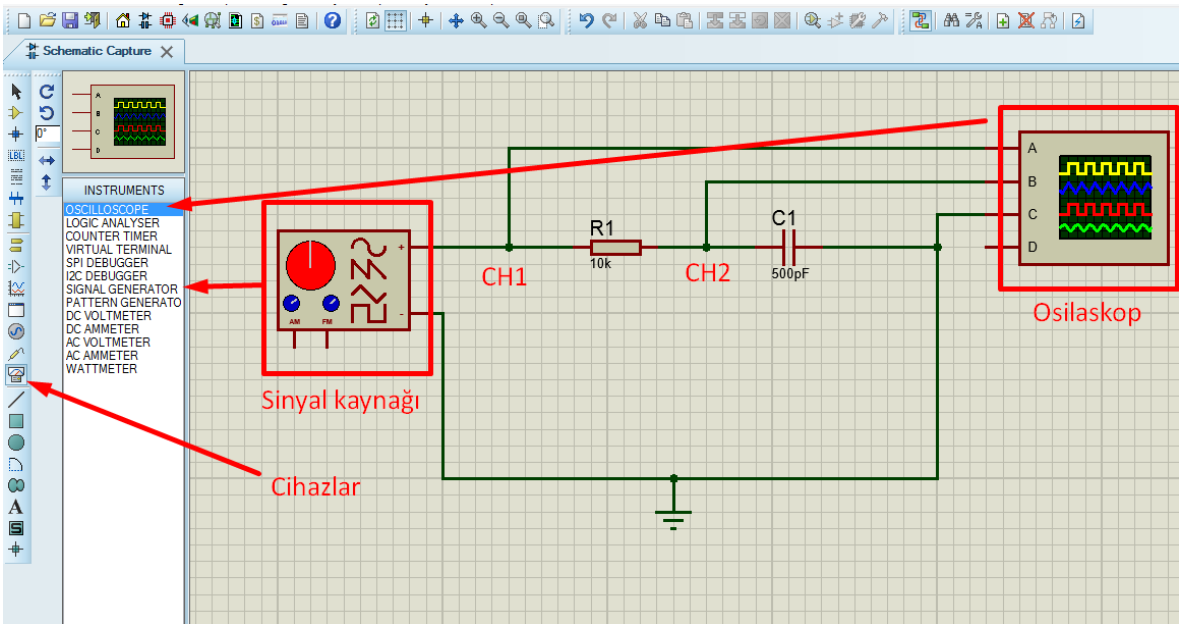


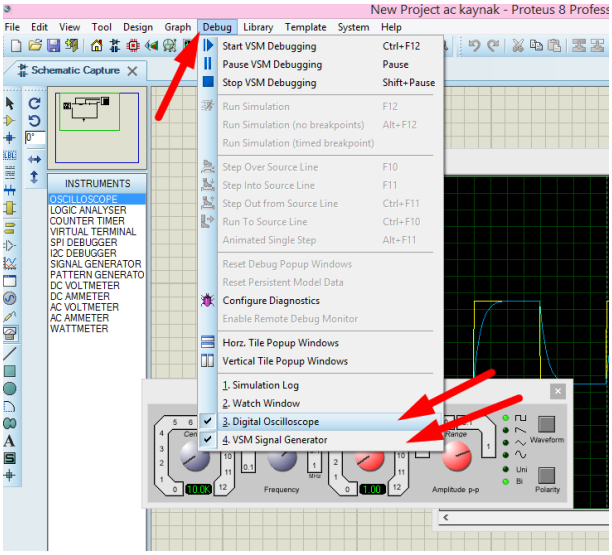
Export Graphics'den devre şemasını (Mavi çerçeve içini) bitmap (resim) veya pdf olarak alabilirsiniz.

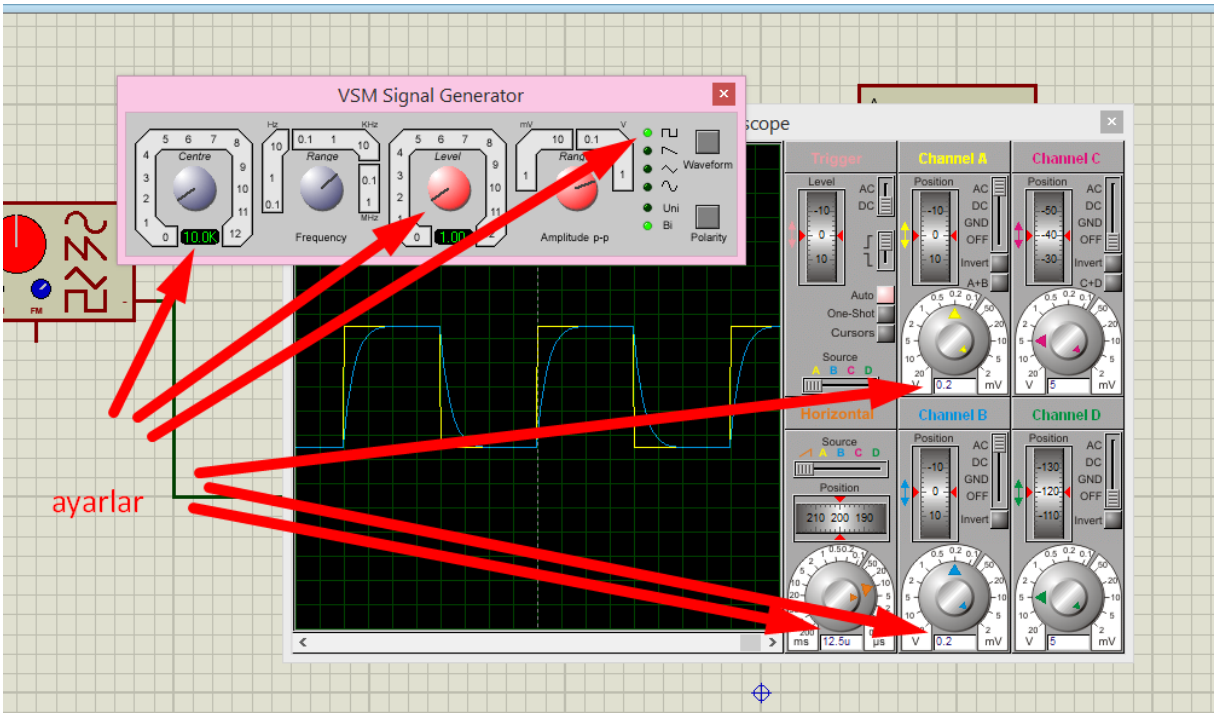




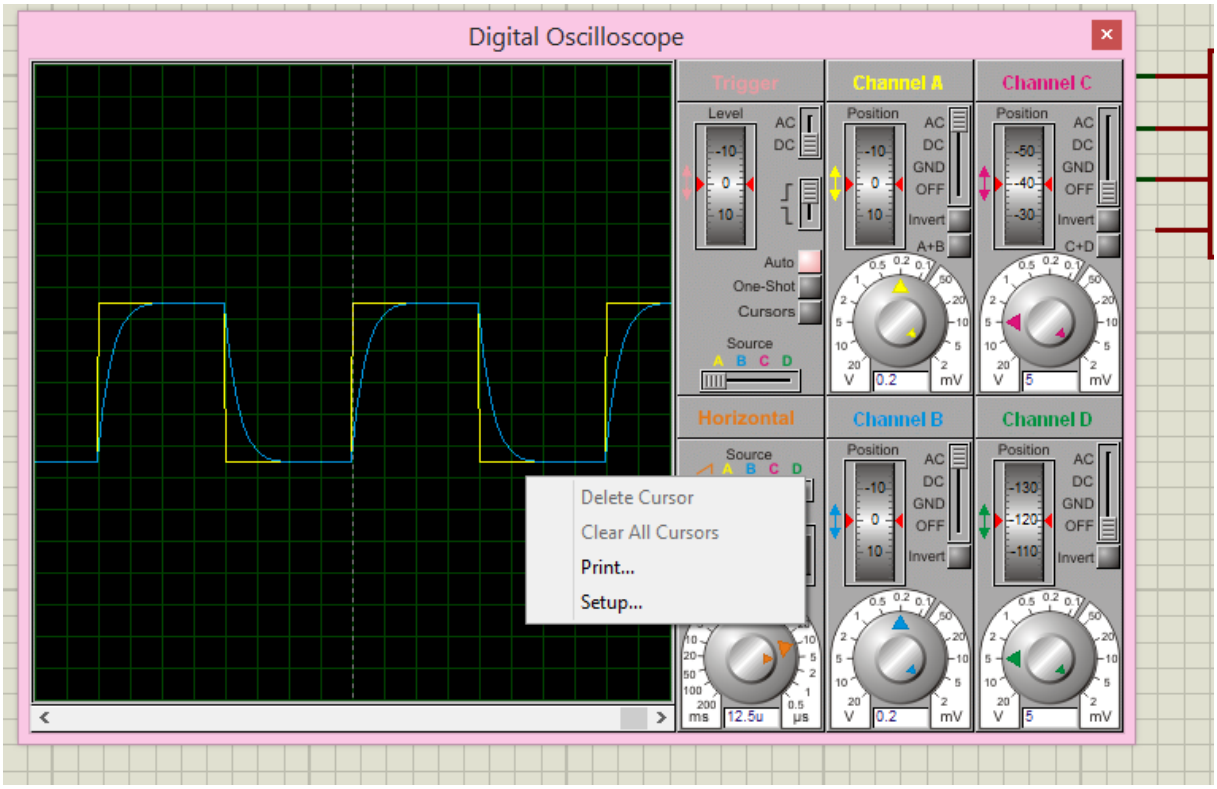
- pdf çıktısını alarak deney raporunuza ekleyiniz.
- Birden fazla deney şeması için uygun şekilde yerleştirerek tek bir sayfa çıktısı ekleyiniz.
- Deney şamalarında değerler okunaklı olmalıdır.


Schematic Capture
INSTRUMENTS
OSCILLOSCOPE
LOGIC ANALYSER
COUNTER TIMER
VIRTUAL TERMINAL
SPI DEBUGGER
I2C DEBUGGER
SIGNAL GENERATOR
PATTERN GENERATO
DC VOLTMETER
DC AMMETER
AC VOLTMETER
AC AMMETER
WATTMETER
R1
10k
C1
500pF
CH1
CH2
A
B
C
D
Osilaskop
Sinyal kaynağı
Cihazlar

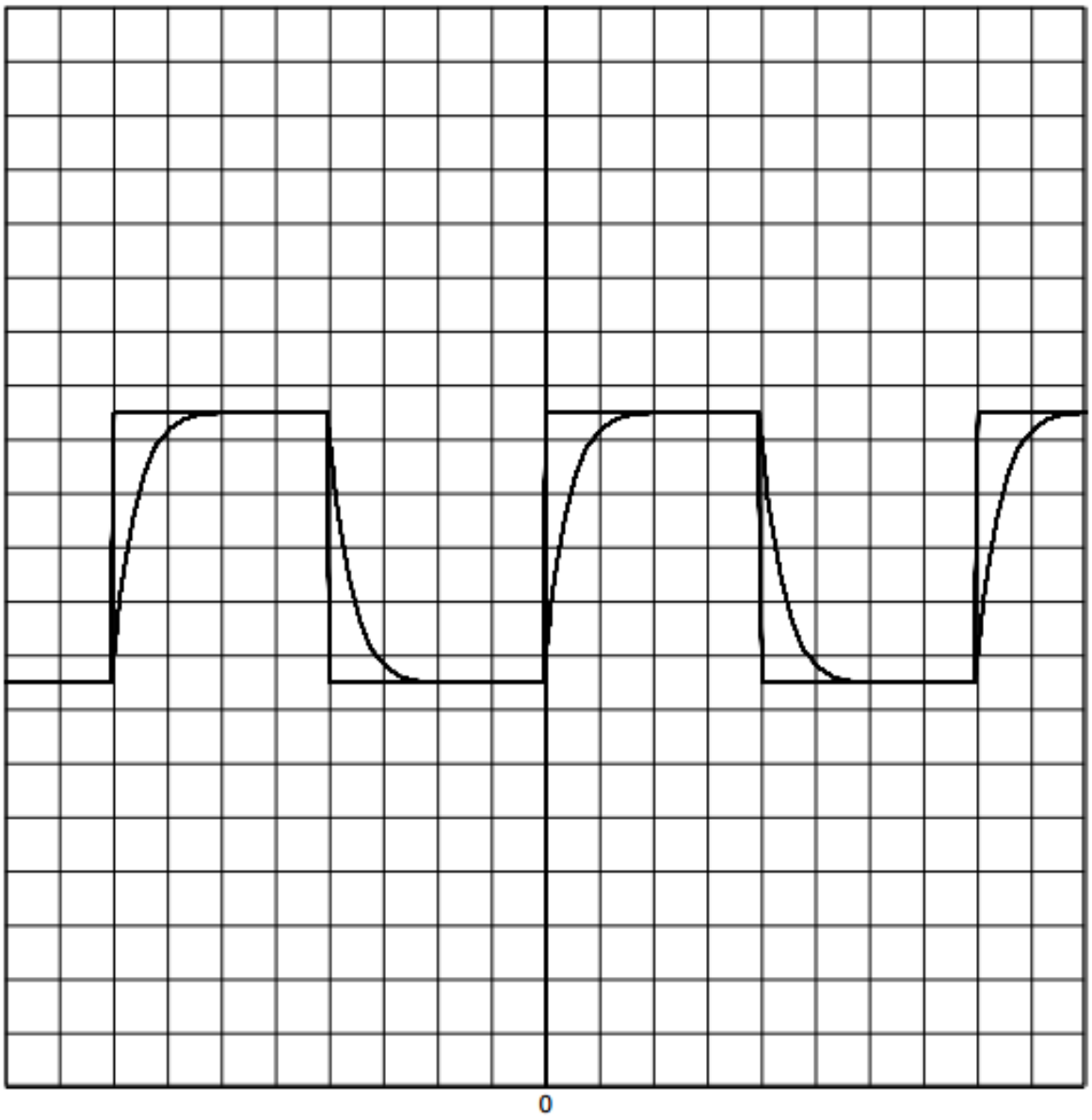

New Project ac kaynak - Proteus 8 Profes
File Edit View Tool Design Graph Debug Library Template System Help
Schematic Capture
INSTRUMENTS
OSCILLOSCOPE
LOGIC ANALYSER
COUNTER TIMER
VIRTUAL TERMINAL
SPI DEBUGGER
I2C DEBUGGER
SIGNAL GENERATOR
PATTERN GENERATO
DC VOLTMETER
DC AMMETER
AC VOLTMETER
AC AMMETER
WATTMETER
Start VSM Debugging Ctrl+F12
Pause VSM Debugging Pause
Stop VSM Debugging Shift+Pause
Run Simulation F12
Run Simulation (no breakpoints) Alt+F12
Run Simulation (timed breakpoint)
Step Over Source Line F10
Step Into Source Line F11
Step Out from Source Line Ctrl+F11
Run To Source Line Ctrl+F10
Animated Single Step Alt+F11
Reset Debug Popup Windows
Reset Persistent Model Data
Configure Diagnostics
Enable Remote Debug Monitor
Horz. Tile Popup Windows
Vertical Tile Popup Windows
1. Simulation Log
2. Watch Window
3. Digital Oscilloscope
4. VSM Signal Generator
Frequency
Amplitude p-p
Waveform
Polarity



Osilaskop ekranına sağ tıklayarak yazdır (print) seçeneğini açınız.



Yazıcılardan herhangi bir pdf yazıcı seçilerek sinyalin pdf çıktısını deney raporuna ekleyiniz.



	Channel A	Channel B	Channel C	Channel D
V/Div	200.00 mV	200.00 mV	5.00 V	5.00 V
Offset	0.00 V	0.00 V	-20.00 V	-60.00 V
Invert	Normal	Normal	Normal	Normal
Coupling	AC	AC	Off	Off

	Horizontal
Source	Trace
Position	125.00 uS
S/Div	12.50 uS

	Trigger
Source	Channel A
Level	0.00 V
Coupling	DC
Edge	Rising
Mode	Auto