

Yehowshua Immanuel

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OBJECTIVE	Self directed individual with more than 9 hobbyist software and hardware projects matured in the past 6 months. Currently seeking an internship in a competitive environment involving writing backend software, kernel level development, and or hardware level design.	
EDUCATION	Georgia Institute of Technology Atlanta, GA Masters in Computer Engineering	Jan 2019 – Present
	Georgia Institute of Technology Atlanta, GA Bachelor of Science in Computer Engineering	May 2017 – Dec 2018 GPA: 3.62 / 4.0
	Kennesaw State University Marietta, GA Transfer with 60 credit hours.	Jun 2016 – May 2017 GPA: 4.0 / 4.0
SKILLS	Computing Software Languages C(expert in C, proficient in rest), C++, Java, Python, Basic HTML, BASH(and most standard UNIX utils.) Hardware Languages VHDL, Verilog, Some Bluespec Software Tools Docker, Git, L ^A T _E X, VIM, Regular Expressions, some Apache Development Tools GNU/BSD Make, GCC, Sub tools(ie: ar, nm) APIs and Protocols REST, UART Hardware Raspberry Pi, FPGAs, Arduino, Embedded Arm, PCB Design Arts Music Piano, Bass, LMMS(Linux MultiMedia Studio) Film and Animation Blender, Synfig Studio, Inkscape Languages English, proficient in French	
EXPERIENCE	Synergy Lab, ECE Department, Georgia Tech Undergraduate Research Assistant, Implemented Verilog RAM controller Deployed and maintained documentation server	May 2018 – Aug 2018
	Electronic Systems Warfare Lab, Georgia Tech Research Institute Student Assistant Scripted and deployed Git version control commit rules Built BSD POSIX Libraries for VXWorks RTOS and wrote accompanying drivers.	May 2018 – Aug 2018
	RetroTech, Georgia Tech Student Assistant Diagnosed and restored legacy computers such as the Macintosh SE 1987. Deployed Verilog logic analyzer in FPGA to facilitate this process	Aug 2017 – May 2018
	Sensors and Electromagnetic Applications Laboratory, Georgia Tech Research Institute Student Assistant Designed a printed circuit power board for an airborne radar.	May 2017 – Aug 2017
PROJECTS	Hobby Wiki Server Maintain my own wiki server that allows students to contribute their mathematical findings and explanations for certain challenging questions.	Feb 2018 – Present
	VHDL Toolchain, ECE Department, Georgia Tech Published my GHDL toolchain workflow installer to help fellow Computer Architecture students.	Apr 2018 May 2018
	Instruction Wrapper, ECE Department, Georgia Tech Wrote instruction parser driver for instructional FPGA to be adopted by ECE department at Georgia Tech	Nov 2017

RELEVANT COURSEWORK	Digital Signal Processing	
	Applied Discrete Fourier transform in software and searched for waveform patterns.	
	Computer Architecture and Concurrency	
	Simulated designed and simulated various MIPS CPU implementations	
LEADERSHIP	Chi Alpha Worship Band	Aug 2018 – Present
	Played bass for worship team.	
	FutureTech Middle School Outreach	Mar 2018
	Lead FutureTech Exhibit at Georgia Tech to introduce middle schoolers to Computer Engineering	
	BCM Worship Band	Aug 2016 – May 2017
	Played piano for worship team.	