Yehowshua Immanuel

yimmanuel3@gatech.edu   +1 (678) 216-6333   yehowshuaimmanuel.com/about

OBJECTIVE	Self directed individual with more than 9 hobbyist software and hardware projects matured in the past 6 months. Currently seeking an internship in a competitive environment involving writing backend software, kernel level development, and or hardware level design.

EDUCATION

Georgia Institute of Technology | Atlanta, GA — Jan 2019 – Present
Masters in Computer Engineering

Georgia Institute of Technology | Atlanta, GA — May 2017 – Dec 2018
Bachelor of Science in Computer Engineering — GPA: 3.62 / 4.0

Kennesaw State University | Marietta, GA — Jun 2016 – May 2017
Transfer with 60 credit hours. — GPA: 4.0 / 4.0

SKILLS

Computing

Software Languages
C(expert in C, proficient in rest), C++, Java, Python, Basic HTML, BASH(and most standard UNIX utils.)

Hardware Languages
VHDL, Verilog, Some Bluespec

Software Tools
Docker, Git, LaTeX, VIM, Regular Expressions, some Apache

Development Tools
GNU/BSD Make, GCC, Sub tools(ie: ar, nm)

APIs and Protocols
REST, UART

Hardware
Raspberry Pi, FPGAs, Arduino, Embedded Arm, PCB Design

Arts

Music
Piano, Bass, LMMS(Linux MultiMedia Studio)

Film and Animation
Blender, Synfig Studio, Inkscape

Languages
English, proficient in French

EXPERIENCE

Synergy Lab, ECE Department, Georgia Tech
Undergraduate Research Assistant, — May 2018 – Aug 2018
 Implemented Verilog RAM controller
 Deployed and maintained documentation server

Electronic Systems Warfare Lab, Georgia Tech Research Institute
Student Assistant — May 2018 – Aug 2018
 Scripted and deployed Git version control commit rules
 Built BSD POSIX Libraries for VXWorks RTOS and wrote accompanying drivers.

RetroTech, Georgia Tech
Student Assistant — Aug 2017 – May 2018
 Diagnosed and restored legacy computers such as the Macintosh SE 1987. Deployed Verilog logic analyzer in FPGA to facilitate this process

Sensors and Electromagnetic Applications Laboratory, Georgia Tech Research Institute
Student Assistant — May 2017 – Aug 2017
 Designed a printed circuit power board for an airborne radar.

PROJECTS

Hobby Wiki Server — Feb 2018 – Present
Maintain my own wiki server that allows students to contribute their mathematical findings and explanations for certain challenging questions.

VHDL Toolchain, ECE Department, Georgia Tech — Apr 2018 May 2018
Published my GHDL toolchain workflow installer to help fellow Computer Architecture students.

Instruction Wrapper, ECE Department, Georgia Tech — Nov 2017
Wrote instruction parser driver for instructional FPGA to be adopted by ECE department at Georgia Tech

RELEVANT COURSEWORK	**Digital Signal Processing**
	Applied Discrete Fourier transform in software and searched for waveform patterns.
	Computer Architecture and Concurrency
	Simulated designed and simulated various MIPS CPU implementations

LEADERSHIP	**Chi Alpha Worship Band**	Aug 2018 – Present
	Played bass for worship team.	
	FutureTech Middle School Outreach	Mar 2018
	Lead FutureTech Exhibit at Georgia Tech to introduce middle schoolers to Computer Engineering	
	BCM Worship Band	Aug 2016 – May 2017
	Played piano for worship team.	