

Branch Field B0

BO RANGE INCLUSIVE	BO RANGE (DECIMAL) INCLUSIVE	POWER ISA BITPAT	DECREMENT CTR BEFORE BRANCH CONDITION CHECK?	BRANCH CONDITION	BRANCH TAKEN LIKELY
[0b00000 : 0b00001]	[0:1]	0000Z	Y	(CTR != 0) and (CR[Bi] == 0)	
[0b00010 : 0b00011]	[2:3]	0001Z	Y	(CTR == 0) and (CR[Bi] == 0)	
[0b00100 : 0b00101]	[4:5]	001AT		CR[Bi] == 0	
0b00110	6	001AT		CR[Bi] == 0	N
0b00111	7	001AT		CR[Bi] == 0	Y
[0b01000 : 0b01001]	[8:9]	0100Z	Y	(CTR != 0) and (CR[Bi] == 1)	
[0b01010 : 0b01011]	[10:11]	0101Z	Y	(CTR == 0) and (CR[Bi] == 1)	
[0b01100 : 0b01101]	[12:13]	011AT		CR[Bi] == 1	
0b01110	14	011AT		CR[Bi] == 1	N
0b01111	15	011AT		CR[Bi] == 1	Y
[0b10000 : 0b10001]	[16:17]	1A00T	Y	CTR != 0	
[0b10010 : 0b10011]	[18:19]	1A01T	Y	CTR == 0	
[0b10100 : 0b10111]	[20:23]	1Z1ZZ		ALWAYS BRANCH	
0b11000	24	1A00T	Y	CTR != 0	N
0b11001	25	1A00T	Y	CTR != 0	Y
0b11010	26	1A01T	Y	CTR == 0	N
0b11011	27	1A01T	Y	CTR == 0	Y
[0b11100 : 0b11111]	[28:31]	1Z1ZZ		ALWAYS BRANCH	