

BRAGADEESHAN SUPPUSAMY

COMPUTER ENGINEERING GRADUATE

CONTACT

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<u>portfolio.netlify.app/</u>

Linked in : bragadeeshansuppusamy

Github: Bragadeeshan

TECHNICAL SKILLS

PROGRAMMING LANGUAGES | C.

Java, Python, MATLAB , Go

HARDWARE PROGRAMMING |

Verilog, AVR C,ARM Assembly,

Raspberry Pi

WEB/APP DEVELOPMENT

Django, Django REST, HTML, PHP,

Dart, React, NodeJS

PCB DESIGN | Proteas, Fritzing

3D MODELING | Fusion 360 , solid

works, AUTOCAD

OTHER SOFTWARES | ADOBE

Photoshop, Premier Pro,

Lightroom, Canva, Slack, Click up,

Google Sheets / Slides / Docs

PERSONAL SKILLS

Critical & Analytical Thinking Decision Making Project Management Growth Mindset Innovative Thinking Time management Team player Fast learner Motivated

INTERESTS

IOT

Cloud Computing Machine Learning Graphic Designing Video Production Video Editing Mobile application Digital Marketing

LANGUAGES

Badminton

TAMIL - NATIVE ENGLISH -FLUENT SINHALA - FLUENT

PROFILE

An Ambitious Computer engineering undergraduate who is in a way of becoming a future leader and a hard-working and dedicated student. Extremely motivated to constantly develop my skills and grow professionally. I am confident in my ability to come up with interesting answers to the problems laid in front of me

EDUCATION

BSC IN COMPUTER ENGINEERING - UNIVERSITY OF PERADENIYA

Nov 2017 - 2023 | Sri Lanka

GPA - 3.0/4.0

GCE ADVANCED LEVEL - HIGHLANDS COLLEGE HATTON

2016

Maths - A , Chemistry - A , Physics - B

z-score 1.86

WORK EXPERIENCE

SOFTWARE SUPPORT ENGINEER - SPADES

April 2022 - December 2022 | Sri Lanka

DEV-OPS ENGINEERING INTERN - ARIMAC

Jan 2022 - April 2022 | Sri Lanka

PROJECTS

ENHANCEMENT OF THE EFFICIENCY OF VEHICULAR COMMUNICATION USING SDN

Research on Vehicular communication to enhance mobility using software defined networking and improvement of SDN VANETS link - https://github.com/cepdnaclk/e16-4yp-Enhancement-of-Efficiency-of-Vehicular-Communication-using-SDN

SMART VENDING MACHINE

Smart vending machine controlled by a web application with real-time transaction tracking and updating in the cloud.

Technologies - Python Django, Django REST, HTML ,PHP, Proteas, Raspberry Pi

link - https://github.com/cepdnaclk/e16-3yp-smart-vending-machine

IMSHARE

Social media mockup software where you can share, post, comment pictures

Technologies - React, NodeJS ,Netify

link - https://github.com/Bragadeeshan/imsocial

DEEP LEARNING BASED SIDE CHANNEL ANALYSIS

Research project on side channel analysis using deep learning to prevent the information leakage through one of the side channel power leakage.

Technologies - Python, MATLAB

(Still on progress)

CLUBS & SOCIETIES

AIESEC Gavel club Engineering Students Union Hindu Students Union

ACHEIVEMENTS

BEST EMERGING YOUNG LEADER -

AIESEC in University of Peradeniya

ACES CODERS V8.0 - PARTICIPATION

12-hour algorithmic coding nationwide competition, nationwide competition over 150+ teams

DEEP SPEED - SEMIFINALISTS

Line following robot competition Top 5 out of 80 teams participated.

DART - BEGINNERS COURSE

Udemy Certification

PYTHON BOOTCAMP

Udemy Certification.

A-Z AZURE FUNDEMENTALS

Udemy Certification

CI/CD INTRODUCTION

Udemy Certification

REFEREES

DR. DAMAYANTHI HERATH

Senior Lecturer, Dept. of Computer Engineering University of Peradeniya damayanthiherath@eng.pdn.ac.ll +9481 239 3920

DR. ISURU NAWINNE

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Dept. of Computer Engineering
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DR. JANAKA ALAWATUGODA,

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FUZZY BASED RECOMMENDATION SYSTEM FOR FINAL YEAR RESEARCH TOPIC SELECTION

Fuzzy logic based recommendation system using student's basic skills as a computer engineering undergraduate and the output is percentage of each research topic recommendation

Technologies - Matlab

LIVER DISEASE PREDICTION WITH MACHINE LEARNING

Liver disease prediction system using the datasets from UCI machine learning repositories.

Technologies - Python

CAR RENTAL SYSTEM

A web application that can be used to manage rental activities. Technologies - HTML, PHP, CSS

BIDDING SYSTEM

By using this Client-Server architecture application clients can bid on whatever they like. Real-time updates will be shown on the client-side.

Technologies - Java 8-BIT SINGLE-CYCLE CPU

8-bit single-cycle CPU based on Harvard architecture which uses a 32-bit instruction word. This CPU can execute most algorithms
Technologies - Verilog, Harvard architecture, pipelining mechanisms, memory hierarchy(direct-mapped cache), custom assembler link - https://github.com/Bragadeeshan/8-bit-CPU

LEADERSHIP EXPERIENCES

LOCAL COMMITEE VICE PRESIDENT - DIGITAL EXPERIENCE

AIESEC in University of Peradeniya | 2020 | Sri Lanka

STUDENT AMBASSADOR ARIMAC (ARIMAC HEROES)

Arimac Lanka | 2020/2021 | Sri Lanka

ICOMM MANAGER - ENTITY CONTROL BOARD AUSTRIA

AIESEC in Austria | 2021 | Austria

VIDEO EDITOR - NATIONAL SHOWCASING TEAM

AIESEC in Sri Lanka |2019 | Sri Lanka

GLOBAL VOLUNTEER - POLAND

AIESEC in Poland |2019 | Poland

MODERATOR - ACES HACKATHON 2019

2019 | Sri lanka

HEAD PREFECT - HIGHLANDS COLLEGE HATTON

2016 | Sri lanka