

2.6.5 GD32F350xx pin alternate functions

Table 2-7. Port A alternate functions summary

Pin Name	AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7
PA0		USART0_CTS ⁽¹⁾ USART1_CTS ⁽²⁾	TIMER1_CH0, TIMER1_ETI	TSI_G0 _IO0	I2C1_SCL ⁽³⁾			CMP0 _OUT
PA1	EVENTOUT	USART0_RTS ⁽¹⁾ USART1_RTS ⁽²⁾	TIMER1_CH1	TSI_G0 _IO1	I2C1_SDA ⁽³⁾)			
PA2	TIMER14_ CH0	USART0_TX ⁽¹⁾ USART1_TX ⁽²⁾	TIMER1_CH2	TSI_G0 _IO2				CMP1 _OUT
PA3	TIMER14_ CH1	USART0_RX ⁽¹⁾ USART1_RX ⁽²⁾	TIMER1_CH3	TSI_G0 _IO3				
PA4	SPI0_NSS/ I2S0_WS	USART0_CK ⁽¹⁾ USART1_CK ⁽²⁾		TSI_G1 _IO0	TIMER13_ CH0		SPI1_N SS ⁽³⁾	
PA5	SPI0_SCK/ I2S0_CK	CEC	TIMER1_CH0, TIMER1_ETI	TSI_G1 _IO1				
PA6	SPI0_MISO/ I2S0_MCK	TIMER2_CH0	TIMER0_BKIN	TSI_G1 _IO2		TIMER15_ CH0	EVENT OUT	CMP0 _OUT
PA7	SPI0_MOSI/ I2S0_SD	TIMER2_CH1	TIMER0_CH0_ ON	TSI_G1 _IO3	TIMER13_ CH0	TIMER16_ CH0	EVENT OUT	CMP1 _OUT
PA8	CK_OUT	USART0_CK	TIMER0_CH0	EVENT OUT	USART1_TX ⁽²⁾	USBFS_S OF	CTC_S YNC	
PA9	TIMER14_ BKIN	USART0_TX	TIMER0_CH1	TSI_G3 _IO0	I2C0_SCL	USBFS_V BUS		
PA10	TIMER16_ BKIN	USART0_RX	TIMER0_CH2	TSI_G3 _IO1	I2C0_SDA	USBFS_I D		
PA11	EVENTOUT	USART0_CTS	TIMER0_CH3	TSI_G3 _IO2			SPI1_IO 2 ⁽³⁾	CMP0 _OUT
PA12	EVENTOUT	USART0_RTS	TIMER0_ETI	TSI_G3 _IO3			SPI1_IO 3 ⁽³⁾	CMP1 _OUT
PA13	SWDIO	IFRP_OUT					SPI1_MISO ⁽³⁾	
PA14	SWCLK	USART0_TX ⁽¹⁾ USART1_TX ⁽²⁾					SPI1_MOSI ⁽³⁾	
PA15	SPI0_NSS/ I2S0_WS	USART0_RX ⁽¹⁾ USART1_RX ⁽²⁾	TIMER1_CH0, TIMER1_ETI	EVENT OUT			SPI1_N SS ⁽³⁾	

Table 2-8. Port B alternate functions summary

Pin Name	AF0	AF1	AF2	AF3	AF4	AF5	AF6
PB0	EVENTOUT	TIMER2_CH2	TIMER0_CH1_ON	TSI_G2_IO1	USART1_RX		
PB1	TIMER13_CH0	TIMER2_CH3	TIMER0_CH2_ON	TSI_G2_IO2			SPI1_SCK ⁽³⁾
PB2				TSI_G2_IO3			
PB3	SPI0_SCK / I2S0_CK	EVENTOUT	TIMER1_CH1	TSI_G4_IO0			
PB4	SPI0_MISO / I2S0_MCK	TIMER2_CH0	EVENTOUT	TSI_G4_IO1			
PB5	SPI0_MOSI / I2S0_SD	TIMER2_CH1	TIMER15_BKIN	I2C0_SMBA			
PB6	USART0_TX	I2C0_SCL	TIMER15_CH0_ON	TSI_G4_IO2			
PB7	USART0_RX	I2C0_SDA	TIMER16_CH0_ON	TSI_G4_IO3			
PB8	CEC	I2C0_SCL	TIMER15_CH0	TSITG			
PB9	IFRP_OUT	I2C0_SDA	TIMER16_CH0	EVENTOUT		I2S0_MCK	
PB10	CEC	I2C0_SCL ⁽¹⁾ , I2C1_SCL ⁽³⁾	TIMER1_CH2	TSITG			SPI1_IO2 ⁽³⁾
PB11	EVENTOUT	I2C0_SDA ⁽¹⁾ , I2C1_SDA ⁽³⁾	TIMER1_CH3	TSI_G5_IO0			SPI1_IO3 ⁽³⁾
PB12	SPI0_NSS ⁽¹⁾ , SPI1_NSS ⁽³⁾	EVENTOUT	TIMER0_BKIN	TSI_G5_IO1	I2C1_SMB_A ⁽³⁾		
PB13	SPI0_SCK ⁽¹⁾ , SPI1_SCK ⁽³⁾		TIMER0_CH0_ON	TSI_G5_IO2			
PB14	SPI0_MISO ⁽¹⁾ , SPI1_MISO ⁽³⁾	TIMER14_CH0	TIMER0_CH1_ON	TSI_G5_IO3			
PB15	SPI0_MOSI ⁽¹⁾ , SPI1_MOSI ⁽³⁾	TIMER14_CH1	TIMER0_CH2_ON	TIMER14_CH0_ON			

Table 2-9. Port C alternate functions summary

Pin Name	AF0	AF1	AF2	AF3	AF4	AF5	AF6
PC0	EVENTOUT						
PC1	EVENTOUT						
PC2	EVENTOUT						
PC3	EVENTOUT						
PC4	EVENTOUT						
PC5	TSI_G2_IO0						
PC6	TIMER2_CH0		I2S0_MCK				
PC7	TIMER2_CH1						
PC8	TIMER2_CH2						
PC9	TIMER2_CH3						
PC10							
PC11							
PC12							
PC13							
PC14							
PC15							

Table 2-10. Port D alternate functions summary

Pin Name	AF0	AF1	AF2	AF3	AF4	AF5	AF6
PD0							
PD1							
PD2	TIMER2_ETI						
PD3							
PD4							
PD5							
PD6							
PD7							
PD8							
PD9							
PD10							
PD11							
PD12							
PD13							
PD14							
PD15							

Table 2-11. Port F alternate functions summary

Pin Name	AF0	AF1	AF2	AF3	AF4	AF5	AF6
PF0	CTC_SYNC						
PF1							
PF2							
PF3							
PF4	EVENTOUT						
PF5	EVENTOUT						
PF6	I2C0_SCL ⁽¹⁾ I2C1_SCL ⁽³⁾						
PF7	I2C0_SDA ⁽¹⁾ I2C1_SDA ⁽³⁾						
PF8							
PF9							
PF10							
PF11							
PF12							
PF13							
PF14							
PF15							

Notes:

- (1) Functions are available on GD32F350x4 devices only.
- (2) Functions are available on GD32F350xB/8/6 devices.
- (3) Functions are available on GD32F350xB/8 devices.