

Labo 2 operational amplifiers

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1. Introduction

Operational amplifiers, or op-amps, are versatile analogue components widely used in electronics. These high-gain, differential amplifiers play a crucial role in signal processing, amplification, and waveform generation. In square and triangle wave generators, op-amps contribute to precision and stability. By exploiting the op-amp's inherent properties and integrating feedback networks, engineers design circuits that reliably produce square and triangle waveforms for applications ranging from signal testing to modulation. Understanding op-amps is fundamental to mastering the intricacies of waveform generation, enabling engineers to create precise and efficient electronic systems.

2. Understanding and design for both subcircuits

2.1. Subcircuit 1

- 2.1.1. Based on the configuration of the operational amplifier and on the power supply voltage (VDD as positive voltage, and VSS = -VDD as negative voltage), how much possible output values are there for V₂? What are these values?

The possible output values are all the values between the maximum and minimum the OPAMP can output. With rail-to-rail OPAMPS this will be V_{DD} and V_{SS}, if the OPAMPS are not rail-to-rail the exact values can be found in the datasheet and are usually a constant offset from the input voltages.

- 2.1.2. Choose one possible value for V₂: 3.3V

- 2.1.2.a. What will be the value on V₊?

The value will be equal to: $\frac{R_{b2}}{R_{b1} + R_{b2}} V_2$

- 2.1.2.b. How is the voltage V₁ going to change if it was previously at 0V

The voltage will increase as the capacitor charges according to RC circuit charge equation. However the charging will stop once the voltage is higher than the voltage at V₊ then it will start to discharge again

- 2.1.2.c. After some time, the value of V₂ is going to change, why and next value of V₂

As mentioned before, the output voltage will change, this is due the voltage at the inverting input to be higher than the voltage at the non-inverting input. The value at V₂ will then change to V_{SS}.

- 2.1.2.d. How is it going to evolve over time? Draw a schematic of V₁ and V₂ in time.

The circuit will start to oscillate with the capacitor sequentially charging and discharging. The output will follow and will make a square waveform.

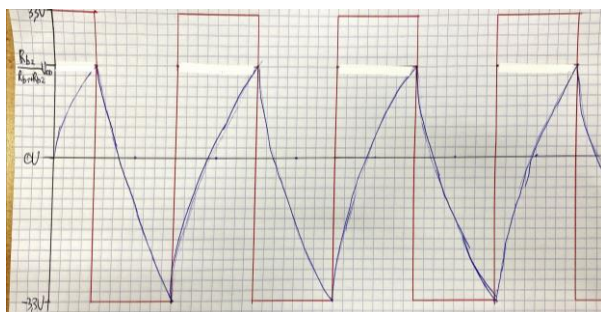
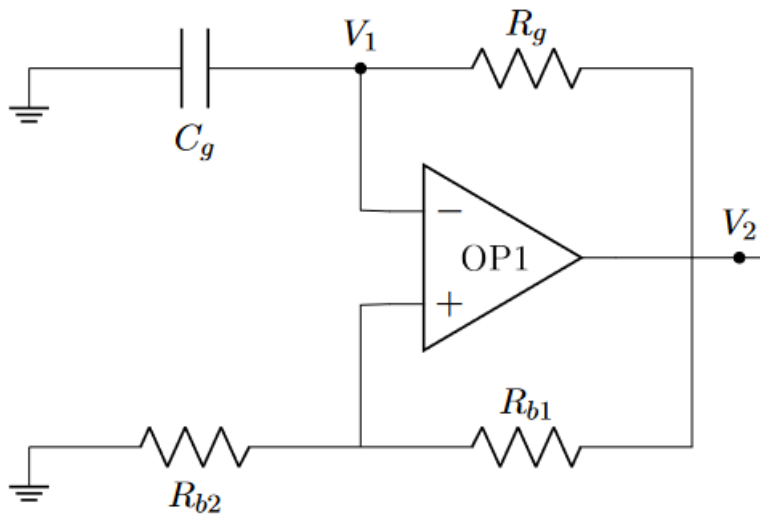


Figure 1: oscillation in circuit with: capacitor voltage blue, and output voltage red

2.1.3. Calculate the values of the components

Subcircuit 1



$$f = \left[2R_g C_g \cdot \ln \left(\frac{1+\beta}{1-\beta} \right) \right]^{-1}$$

$$\beta = \frac{R_{b2}}{R_{b2} + R_{b1}}$$

Assume $\beta = 0.462$ for easier calculation as in this case $\ln \left(\frac{1+\beta}{1-\beta} \right) = 1$

Calculator $\Rightarrow \text{solve} \left(\frac{b}{b+a} = 0.462, a, b \right) \Rightarrow b = 0.858736 * a$

$R_{b1} = 10\text{k}\Omega$; $R_{b2} = 8.587\text{k}\Omega$.

$f = (2 * R_g * C_g)^{-1} \Rightarrow \text{take } R = 1\text{k}\Omega \Rightarrow \text{solve}((2 * 1000 * C)^{-1} = 10000, C) \Rightarrow C = 50\text{nF}$

2.2. Subcircuit 2

2.2.1. What kind of mathematical function is achieved by this circuit

An integrator

2.2.2. How does that mathematical function work in practice, i.e. how do the different elements (opamp, resistor, capacitor) enable to achieve it?

The main circuit is the RC network consisting of The resistor and the capacitor. When a voltage is applied the capacitor starts to charge with the current allowed through the resistor. This current through the resistor causes a voltage, which is integrated on the capacitor. The OPAMP causes a negative feedback to make the charging waveform identical to an integral.

2.2.3. Design subcircuit 2 such that the output V3 has an appropriate shape, and the same amplitude and frequency as V2.

Assuming the capacitor charges in 5τ to V_{in} , from the RC constant. And each charge is half a period, τ needs to be 1/10 the period.

The period = 0.1ms , $\tau = 0.01\text{ms} = R * C$

If we take R to be $1\text{M}\Omega$ as it shouldn't be high to not influence subcircuit 1, then:

$$\text{solve}(1000000 * C = 0.00001, C) \Rightarrow C = 1\text{nF}$$

3. Simulation detail and simulations

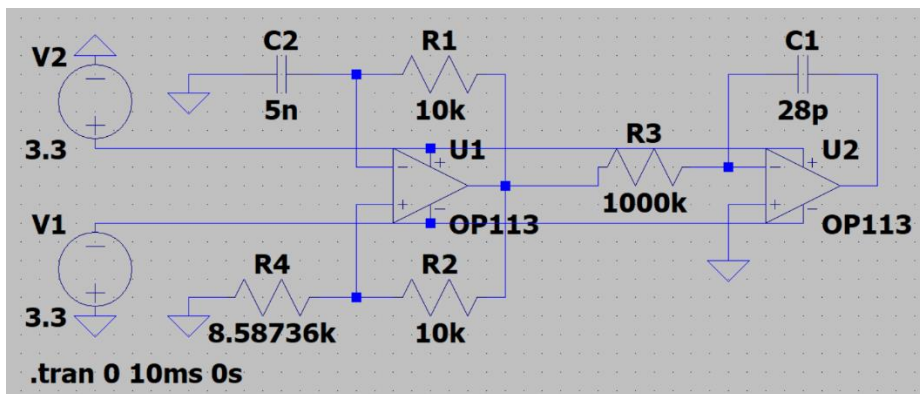


Figure 2: complete circuit with square wave oscillator and triangle oscillator

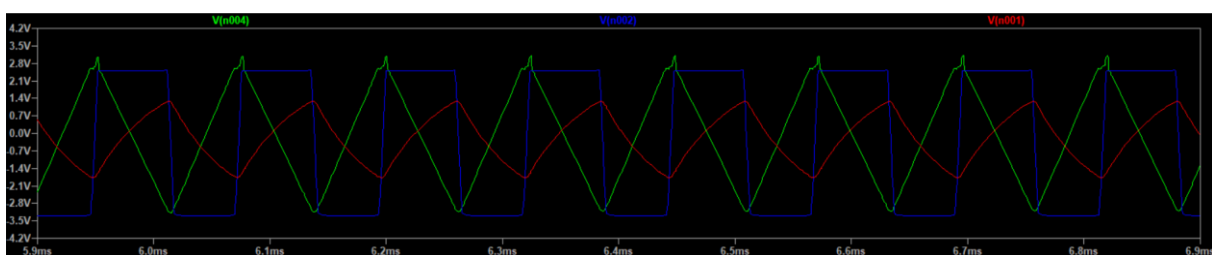


Figure 3: relevant signals to the circuit: triangle output in green, square-wave in blue, and C_g voltage in red

The square wave created as mentioned above, it is however not a perfect square wave as the edges are rounded, this can be due to OPAMP imperfections either in rise/fall times or in the comparative ability around the switching values. Not the square wave is between -3.3V and 2.5V, this will be discussed later. However the frequency seems to deviate as well, being a bit lower

The capacitor waveform is as expected as well, it shows the charge curve and the discharge curve in function of the square wave.

The triangle wave is rather well, it does show deviations on the top, but this due to the choice of components. The components were chosen by means of trial and error, as the ones calculated above did not result in a usable triangle wave. These values are chosen as optimal as possible while still staying stable. This is probably due to the frequency mis-matching

To fix the output voltage to the desired value an inverting amplifier can be used. Because the output voltage is 1V and the input voltage is 3.3V, the amplification needs to be about $1/3.03$. The feedback resistor can be chosen as 1k Ω and the feedback resistor 3k Ω

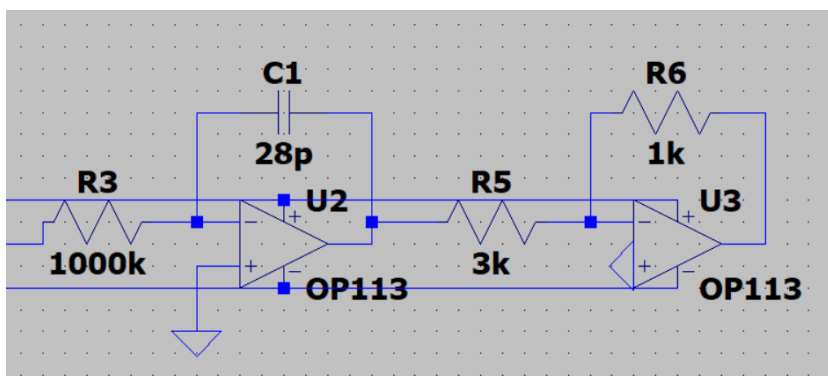


Figure 4: additional inverting amplifier added to the output

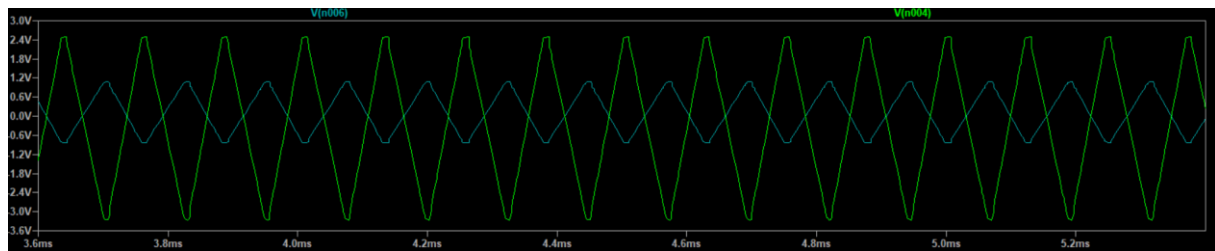


Figure 5: output waveforms showing the original triangle green and diminished output in cyan

4. Conclusion

The circuit oscillated the correct waveforms, however a bunch of the calculations did not work, after close inspection the frequency of oscillation did not match the calculated one, as this one is around 8kHz instead of the desired 10kHz. This caused problems down the line at the triangle wave generator, where the values had to be chosen experimentally to get a good waveform. This might be an interesting topic to explore in a future lab.

Another observed topic is the use of large resistors between OPAMP subcircuits. When experimentally choosing the values for the triangle generator, it was discovered that a large input resistor greatly improved the performance of the signal, even at the same RC constant value. The recommended values here are $1\text{M}\Omega$, however this value comes close to the input impedances of the OPAMPs so a lower value of $10\text{-}100\text{k}\Omega$ is probably the best.

Another observed phenomenon is that the square wave does not reach all the way to 3.3V but stays at a constant value below it, a value of about 0.7V. This value is suspiciously close to the bias voltage of a diode. And this is probably true as this OPAMP is not a rail-to-rail OPAMP and has probably a diode in the package. And this is furthermore another factor which could have influenced the deviation in frequency as the expected charging times are proportional to the voltage. The solution to this would be to use a rail-to-rail OPAMP.

Finally the inverting amplifier at the end of the chain brought down the voltage to around $\pm 1\text{V}$ however, the measured values proved to be different. This is probably also due to the abovementioned deviations.

All in all the circuit did function in its rough specifications and this Labo was successful. However it does have a lot of room for another one with the objective to explore the deviations.