

Problem 14: [6 points]  
Filename: hw0prob14.sv

```
1 `default_nettype none
2 module hw0prob14
3     (input logic a,
4      input logic b,
5      input logic c,
6      output logic p );
7     logic n_b, f1;
8     not g1(n_b, b);
9     and #2 g2(f1, a, n_b);
10    or #3 g3(p, c, f1);
11 endmodule: hw0prob14
```