

# YIFAN YANG

✉ 2197209675@qq.com · ☎ 18053228579

## EDUCATION BACKGROUND

---

**Huazhong University of Science and Technology**

Wuhan China |2020/09–2024.06

- **Bachelor of Integrated Circuit Design and System**
- **GPA:** 3.87/4.0      **Average Score:**87.88/100
- **CET4:** 601      **CET6:**542      **IELTS:**6.5(L:7.5|R:7.5|W:6.0|S:5.5)

## RELEVANT COURSES

---

### Mathematics courses

- Calculus I (94), Complex Function and Integral Transform (92), Mathematical Physics Equation and Special Function (93), Calculus II (91).

### Professional courses

- Signals and Linear Systems(93), HDL and Design of Digital System(92), Semiconductor Physics(II)(91), Analog Circuit and Digital System (II)(89), Circuit Theory (III)(89), CMOS Analog Circuit Design(88).

## RESEARCH EXPERIENCE

---

### Clinician assistant applet

2022/05 – 2022/08

*Supervisor:Associate Professor Hei Xiaojun*

- Use front-end programming language to build a doctor-patient communication platform
- Complete the information deployment on the doctor side and the patient side
- Awarded as a school-level innovation project

### FPGA embedded project

2022/08 – 2022/11

*Supervisor:Senior Engineer Wu Jianxin*

- We designed a fingerprint identification module to ensure security, and connected it to the display screen of the computer to realize the design of VGA display module
- We used xilinx series FPGA development board to design a bank payment system, which has the functions of deposit, withdrawal and balance inquiry
- Our project was based on the vivado and vitas platforms to achieve the above functions

## SKILLS AND INTERESTS

---

- **Programming Languages:** Matlab,C,Verilog HDL.
- **Development tools/platforms:** Quartus II,Vivado,Cadence Virtuoso,Modelsim.

## HONORS AND AWARDS

---

- Self Improvement Scholarship(2021)
- Honorable Mention of The Mathematics Contest in Modeling of America(2022)
- Excellent Award of National Undergraduate Embedded Chip Design Competition(2022)
- Scholarship for outstanding student cadres(2022)