

Atmega 328 Pins

IC Pin	Pro Mini Pin	Port	Logic Analyzer	Function	Type	Ownership
1	18	PD3 / OC2B	1	TICK (debug)	Out	lin_decoder
2	19	PD4		LIN2_TX	Out	lin_decoder
3	11, 16	GND		GND	Power	hardware
4	9	VCC		+5V	Power	hardware
5	11, 16	GND		GND	Power	hardware
6	9	VCC		+5V	Power	hardware
7		PB6				
8		PB7				
9	20	PD5		BUZZER	Out	main
10	21	PD6	7	GP DEBUG	Out	lin_decoder
11	22	PD7		STATUS LED	Out	main
12	23	PB0		FRAME LED	Out	main
13	24	PB1		ERROR LED	Out	main
14	1	PB2 / OC1B				
15	2	PB3 / MOSI	5	MOSI / LIN_ERROR (debug)	Out	lin_decoder
16	3	PB4 / MISO	4	MISO / LIN_SAMPLE (debug)	Out	lin_decoder
17	4	PB5 / SCK		SCK	In	
18		AVCC				
19		ADC6				
20		AREF				
21	11, 16	GND		GND	Power	hardware
22		ADC7				
23	5	PC0	3	LIN_BREAK (debug)	Out	lin_decoder
24	6	PC1	6	LIN2_RX	In	lin_decoder
25	7	PC2		LIN1_TX	Out	lin_decoder
26	8	PC3	2	LIN_ISR (debug)	Out	lin_decoder

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27		PC4				
28		PC5				
29	10, 15	PC6 / RESET		RESET	In	hardware
30	14	PD0 / RXI		SERIAL RX (115,200)	In	passive_serial
31	13	PD1 / TXO		SERIAL TX (115,200)	Out	passive_serial
32	17	PD2	0	LIN1_RX	In	lin_decoder