LIN BUS

JP1
1
2 — LIN1
3

JP3
1
2 — LIN2
3

F1
500ma Slow

MBR0540
D6

C2 22uF 40V
C1 0.1uF 40V

+12V

LIN 1 RX/TX + LDO

ATA6631
U2

+12V

1 VS
7 NRES
4 LIN — LIN1
6 TXI — LIN1_TX
5 RXO — LIN1_RX
GND 3

+5V
ENI 8
2 — +5V

C5 10uF
C4 0.1uF

C9 220pF

LIN 2 RX/TX

MBR0540
D9

+12V

R9 1K

ATA6631
U3

1 VS
7 NRES
4 LIN — LIN2
6 TXI — LIN2_TX
5 RXO — LIN2_RX
GND 3

+5V
ENI 8
2

C8 0.1uF

C3 220pF

SERIAL I/O (Optional)

D2 MBR0540 → +12V
D3 MBR0540 → +5V

0.1uF C7 — RESET

J3
6 DTR
5 RXI
4 TXO
3 S-5V
2 GND
1 GND

10K R1 — SER_TX
10K R4 — SER_RX

MCU

+5V

0.1uF C6

U1

18 AVCC
4 VCC
6 VCC

20 AREF

16MHz X1
7 PB6(XTAL1/TOSC1)
8 PB7(XTAL2/TOSC2)

10k R5
+5V

29 PC6(/RESET)
RESET

5 AGND
3 GND
21 GND

PC0(ADC0) 23 — LIN_BREAK
PC1(ADC1) 24 — LIN2_RX
PC2(ADC2) 25 — LIN1_TX
PC3(ADC3) 26 — LIN_ISR
PC4(ADC4/SDA) 27
PC5(ADC5/SCL) 28
ADC6 19
ADC7 22

PD0(RXD) 30 — SER_RX
PD1(TXD) 31 — SER_TX
PD2(INT0) 32 — LIN1_RX
PD3(INT1) 1 — LIN_TICK
PD4(XCK/T0) 2 — LIN2_TX
PD5(T1) 9
PD6(AIN0) 10 — GP_DEBUG
PD7(AIN1) 11

PB0(ICP) 12 — FRAME_LED
PB1(OC1A) 13 — ERROR_LED
PB2(SS/OC1B) 14
PB3(MOSI/OC2) 15 — MOSI/LIN_ERR
PB4(MISO) 16 — MISO/LIN_DATA
PB5(SCK) 17 — SCK

ATMEGA328

STATUS LEDS

FRAME_LED
ERROR_LED

R2 3.3K
R3 3.3K
LED1
LED2

ICSP

JP2   ICSP
MISO/LIN_DATA 1 — 2 +5V
SCK 3 — 4 MOSI/LIN_ERR
RESET 5 — 6

TITLE: proxy

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