Atmega 328 Pins

IC Pin	Pro Mini Pin	Port	Logic Analyzer	Function	Ownership
1	18	PD3 / OC2B	2	TICK output (debug)	lin_decoder
2	19	PD4	6	LIN2_TX	lin_decoder
3	11, 16	GND		GND	hardware
4	9	VCC		+5V	hardware
5	11, 16	GND		GND	hardware
6	9	VCC		+5V	hardware
7		PB6			
8		PB7			
9	20	PD5		BUZZER output	main
10	21	PD6			
11	22	PD7		STATUS LED output	main
12	23	PB0		FRAME LED output	main
13	24	PB1		ERROR LED output	main
14	1	PB2 / OC1B			
15	2	PB3 / MOSI	6	MOSI / LIN_ERROR (debug)	lin_decoder
16	3	PB4 / MISO	5	MISO / LIN_SAMPLE (debug)	lin_decoder
17	4	PB5 / SCK		SCK / LED	
18		AVCC			
19		ADC6			
20		AREF			
21	11, 16	GND		GND	hardware
22		ADC7			
23	5	PC0	4	LIN_BREAK output (debug)	lin_decoder
24	6	PC1			

Atmega 328 Pins

IC Pin	Pro Mini Pin	Port	Logic Analyzer	Function	Ownership
25	7	PC2			
26	8	PC3	3	LIN_ISR output (debug)	lin_decoder
27		PC4			
28		PC5			
29	10, 15	PC6 / RESET		RESET	hardware
30	14	PD0 / RXI		SERIAL RX Input (115,200)	passive_serial
31	13	PD1 / TXO		SERIAL TX output (115,200)	passive_serial
32	17	PD2	1	LIN1_IN	lin_decoder