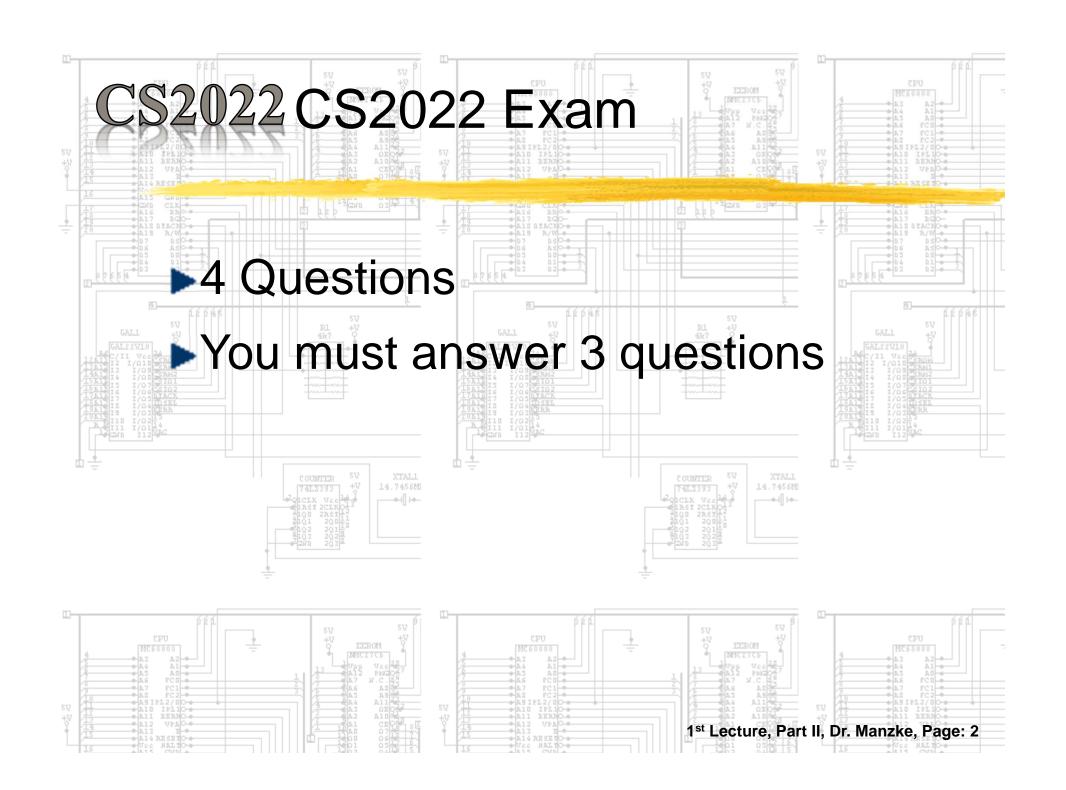
CS2022

Computer Architecture I

Computer Architecture

- Lecturer: Dr. Michael Manzke
 - ► Office: Stack B https://goo.gl/maps/CrMbKSt5kpS2
 - ► Ext: 2400
 - ► Email: michael.manzke@cs.tcd.ie
 - ► HTML:

http://www.cs.tcd.ie/Michael.Manzke/index.php/mm-teaching/undergraduate/cs2022



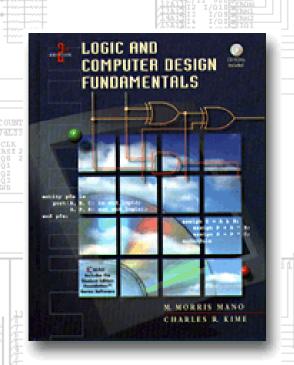


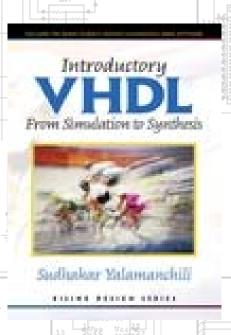
- Questions will seek to establish that you have a good grasp of the following concepts:
 - Microoperations
 - Datapath
 - Busses
 - ► Alu/shift design
 - Status bit generation and use
 - Control unit design and operation

CS2022

Course Text:

- "Introductory VHDL: From Simulation to Synthesis"
- "Logic and Computer Design Fundamentals" 2nd Edition updated, Mano (includes Xilinx Student Edition 4.2i software)

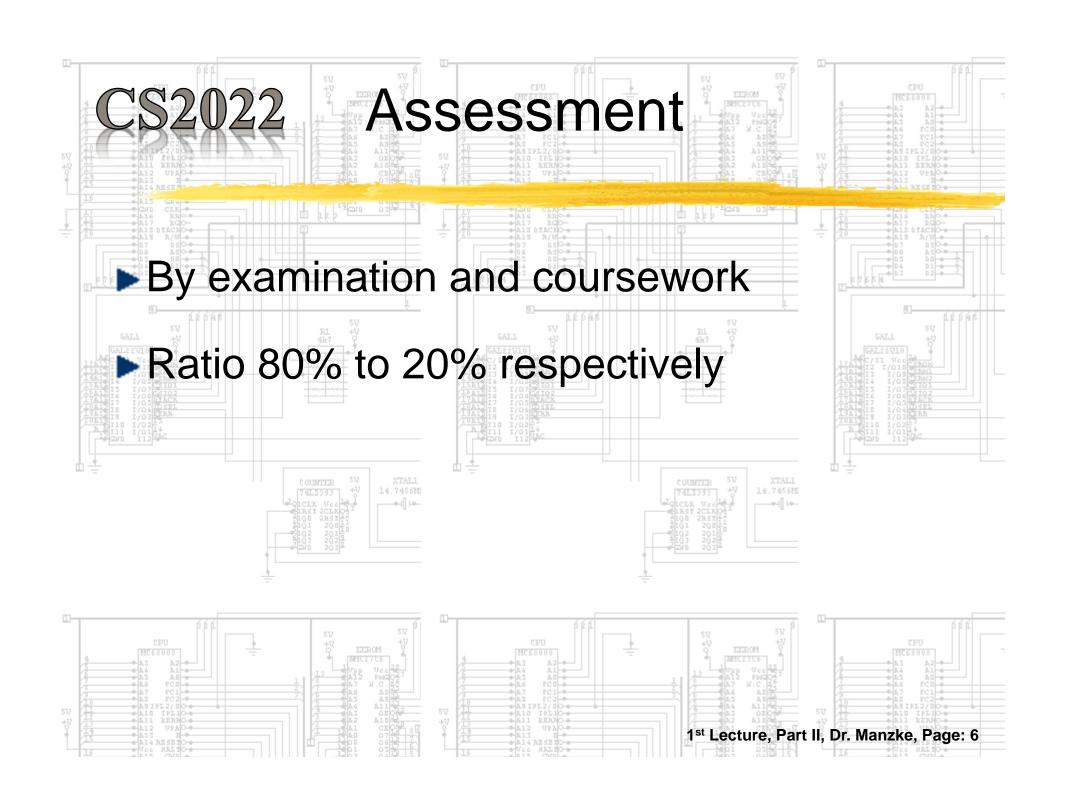


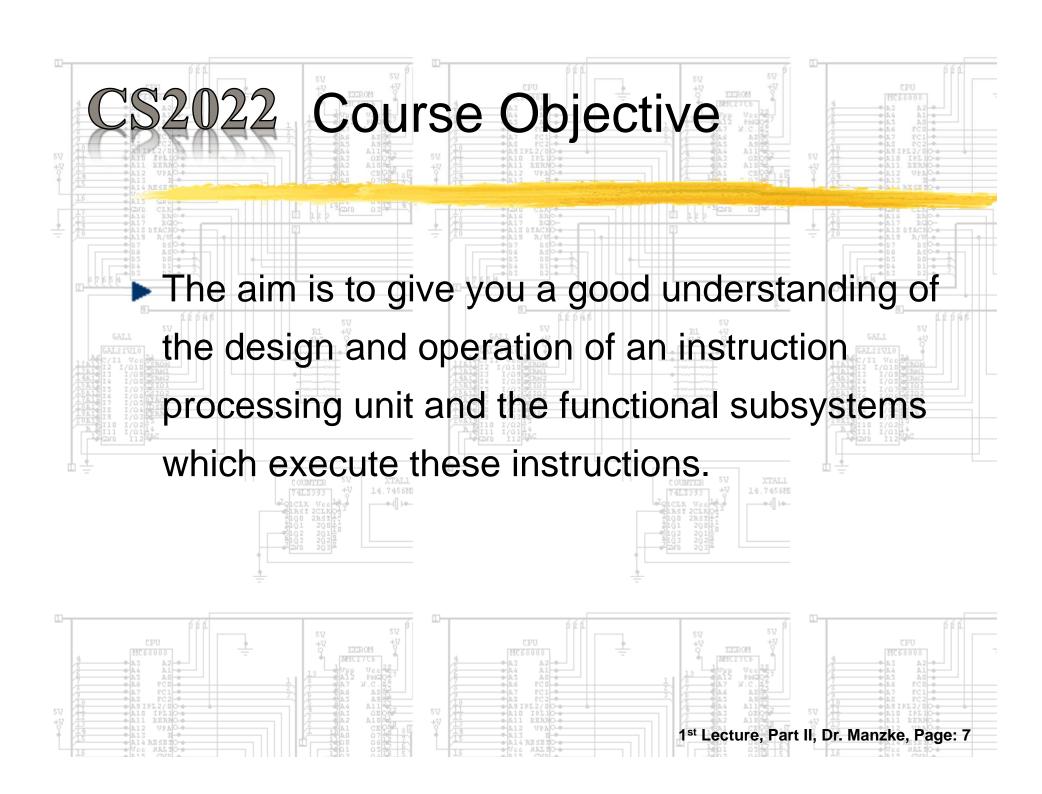


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CS2022 Organisation of the Course

- Lectures:
 - ▶ Relate to chapter 7 and 8 of Mano & Kime textbook
 - Prerequisite:
 - ► Chapter 1-6 of Mano & Kime textbook
- Tutorials:
 - ► Will largely be problems from Mano & Kime textbook and the two projects.
- ▶ Projects:
 - There will be two in which you will be asked to design and simulate key elements of a processor.





CS2022 Von Neumann Architecture

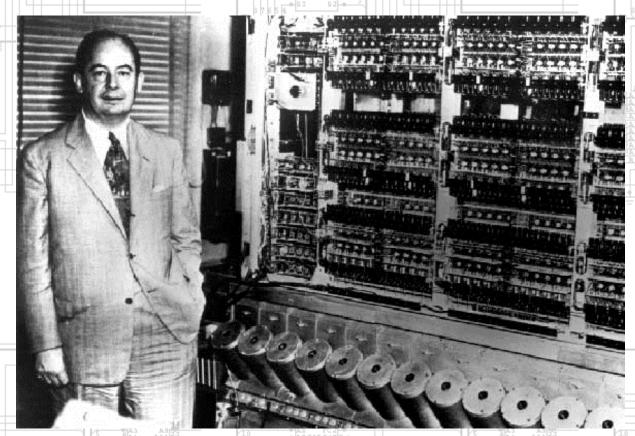
- This design was first specified by John von Newmann in 1940 and so the resulting architecture is often called the 'von Neumann' architecture.
- Please see the following web page:
 - http://www-gap.dcs.stand.ac.uk/~history/Mathematicians/Von_Neumann.html



John von Neumann

Born: 28 Dec 1903 in Budapest, Hungary

Died: 8 Feb 1957 in Washington D.C., USA



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S2022 Datapath and Control Unit **Determines Performs** Sequence Data-processing of Operations Operations Control signals Control Data Control inputs outputs Status signals Datapath Unit Control Data outputs inputs 1st Lecture, Part II, Dr. Manzke, Page: 10