WHEN TETING OUR MIDPOINT.V ON AN FPGA BOARD:

- The LED lights will initially be set to 0101. (We are truncating the given input to the four least significant bits.)
- By setting Switch0 to high or low, the test engineer is determining whether the new least significant bit will be high or low.
- By setting Swtich1 to high, the test engineer is shifting the bits
- For example:
    - Lights initially 0101, Switch0 is low.
    - When Switch1 is set to high, lights will display 1010.
    - If Switch0 remains low, if Switch1 is set low then high, lights will display 0100.
    - Then, if Switch0 is high, once Switch one is set low then high again, lights will display 1001.
- Button0 can be used to reset the board to its initial state: 0101.