

```
on stdcore[3]:port in p_aud_bclk           = XS1_PORT_1C;  
on stdcore[3]:port in p_aud_lrcin         = XS1_PORT_1D;  
on stdcore[3]:port in p_aud_lrcout        = XS1_PORT_1H;  
on stdcore[3]:buffered in port:8 p_aud_din = XS1_PORT_1J;  
on stdcore[3]:buffered out port:8 p_aud_dout = XS1_PORT_1I;
```

```
par  
{
```

```
...
```

```
    audio_i2s_dac_slave(p_aud_bclk,  
                        p_aud_lrcout,  
                        p_aud_dout,  
                        aud_bit_clock,  
                        clk_ctl,  
                        audio_data);}
```

```
...  
}
```