## <u>Prelab</u>

2  $(\overline{A+B})(\overline{CD})$ 

3

NAND:

Α	В	out			
0	0	1			
0	1	1			
1	0	1			
1	1	0			

NOR:

Α	В	out
0	0	1
0	1	0
1	0	0
1	1	1

,

Α	В	С	D	out
0	0	0	0	1
1	0	0	0	0
0	1	0	0	0
1	1	0	0	0
0	0	1	0	1
1	0	1	0	0
0	1	1	0	0
1	1	1	0	0
0	0	0	1	1
1	0	0	1	0
0	1	0	1	0
1	1	0	1	0
0	0	1	1	0
1	0	1	1	0
0	1	1	1	0
1	1	1	1	0