xv6 is a re-implementation of Dennis Ritchie's and Ken Thompson's Unix Version 6 (v6). xv6 loosely follows the structure and style of v6, but is implemented for a modern x86-based multiprocessor using ANSI C.

## ACKNOWLEDGMENTS

xv6 is inspired by John Lions's Commentary on UNIX 6th Edition (Peer to Peer Communications; ISBN: 1-57398-013-7; 1st edition (June 14, 2000)). See also http://pdos.csail.mit.edu/6.828/2014/xv6.html, which provides pointers to on-line resources for v6.

xv6 borrows code from the following sources:
 JOS (asm.h, elf.h, mmu.h, bootasm.S, ide.c, console.c, and others)
 Plan 9 (entryother.S, mp.h, mp.c, lapic.c)
 FreeBSD (ioapic.c)
 NetBSD (console.c)

The following people have made contributions:
 Russ Cox (context switching, locking)
 Cliff Frey (MP)
 Xiao Yu (MP)
 Nickolai Zeldovich
 Austin Clements

In addition, we are grateful for the bug reports and patches contributed by Silas Boyd-Wickizer, Peter Froehlich, Shivam Handa, Anders Kaseorg, Eddie Kohler, Yandong Mao, Hitoshi Mitake, Carmi Merimovich, Joel Nider, Greg Price, Eldar Sehayek, Yongming Shen, Stephen Tu, and Zouchangwei.

The code in the files that constitute xv6 is Copyright 2006-2014 Frans Kaashoek, Robert Morris, and Russ Cox.

## ERROR REPORTS

If you spot errors or have suggestions for improvement, please send email to Frans Kaashoek and Robert Morris (kaashoek.rtm@csail.mit.edu).

## BUILDING AND RUNNING XV6

To build xv6 on an x86 ELF machine (like Linux or FreeBSD), run "make". On non-x86 or non-ELF machines (like OS X, even on x86), you will need to install a cross-compiler gcc suite capable of producing x86 ELF binaries. See http://pdos.csail.mit.edu/6.828/2014/tools.html. Then run "make TOOLPREFIX=i386-jos-elf-".

To run xv6, install the OEMU PC simulators. To run in OEMU, run "make gemu".

To create a typeset version of the code, run "make xv6.pdf". This requires the "mpage" utility. See http://www.mesa.nl/pub/mpage/.

The numbers to the left of the file names in the table are sheet numbers. The source code has been printed in a double column format with fifty lines per column, giving one hundred lines per sheet (or page). Thus there is a convenient relationship between line numbers and sheet numbers.

# My additions	33 swtch.S	
01 BC_ps.c	33 kalloc.c	<pre># string operations</pre>
01 BC_ps.h		71 string.c
02 BCdate.c	# system calls	
02 BC_testgiduid.c	35 traps.h	<pre># low-level hardware</pre>
03 BCtime.c	35 vectors.pl	73 mp.h
	36 trapasm.S	74 mp.c
# basic headers	36 trap.c	76 lapic.c
03 types.h	38 syscall.h	79 ioapic.c
04 param.h	38 syscall.c	80 picirq.c
04 memlayout.h	40 sysproc.c	81 kbd.h
05 defs.h	42 halt.c	83 kbd.c
07 x86.h		83 console.c
09 asm.h	<pre># file system</pre>	87 timer.c
09 mmu.h	43 buf.h	87 uart.c
12 elf.h	43 fcntl.h	
	44 stat.h	<pre># user-level</pre>
<pre># entering xv6</pre>	44 fs.h	88 initcode.S
12 entry.S	45 file.h	89 usys.S
13 entryother.S	46 ide.c	89 init.c
14 main.c	48 bio.c	90 sh.c
	50 log.c	
# locks	53 fs.c	<pre># bootloader</pre>
17 spinlock.h	61 file.c	97 bootasm.S
18 spinlock.c	63 sysfile.c	98 bootmain.c
	68 exec.c	
# processes		
19 vm.c	# pipes	
25 proc.h	70 pipe.c	<pre># add student files her</pre>
26 proc.c		

The source listing is preceded by a cross-reference that lists every defined constant, struct, global variable, and function in xv6. Each entry gives, on the same line as the name, the line number (or, in a few cases, numbers) where the name is defined. Successive lines in an entry list the line numbers where the name is used. For example, this entry:

```
swtch 2658
0374 2428 2466 2657 2658
```

indicates that swtch is defined on line 2658 and is mentioned on five lines on sheets 03, 24, and 26.

acquire 1824	B_DIRTY 4311	9141 9142 9255 9267 9269	CMOS_RETURN 7836
0627 1824 1828 2697 2760	4311 4743 4766 4771 4810	9272 9273 9274 9277 9278	7836 7891
2910 2942 2973 3067 3124	4828 4940 4969 5289	9282	CMOS_STATA 7875
3168 3183 3216 3229 3426	begin_op 5178	B_VALID 4310	7875 7923
3443 3716 4122 4142 4757	0585 2937 5178 6233 6324	4310 4770 4810 4828 4957	CMOS_STATB 7876
4815 4920 4981 5180 5207	6471 6561 6661 6706 6724	bwrite 4965	7876 7916
5224 5281 5558 5591 5611	6756 6870	0515 4965 4968 5130 5163	CMOS_UIP 7877
5640 5660 5670 6179 6204	bfree 5379	5241	7877 7923
6218 7063 7084 7105 8410	5379 5764 5774 5777	bzero 5339	COM1 8763
8581 8627 8663	bget 4916	5339 5368	8763 8773 8776 8777 8778
allocproc 2755	4916 4948 4956	C 8181 8574	8779 8780 8781 8784 8790
2755 2816 2880	binit 4889	8181 8229 8254 8255 8256	8791 8807 8809 8817 8819
allocuvm 2203	0512 1481 4889	8257 8258 8260 8574 8584	commit 5251
0672 2203 2217 2859 6896	bmap 5710	8587 8594 8605 8638	5103 5223 5251
6908	5472 5710 5736 5819 5869	CAPSLOCK 8162	CONSOLE 4587
alltraps 3604	bootmain 9817	8162 8195 8336	4587 8677 8678
3559 3567 3580 3585 3603	9768 9817	cgaputc 8505	consoleinit 8673
3604	BPB 4507	8505 8563	0518 1477 8673
ALT 8160	4507 4510 5360 5362 5386	clearpteu 2279	consoleintr 8577
8160 8188 8190	bread 4952	0681 2279 2285 6910	0520 8348 8577 8825
argfd 6369	0513 4952 5127 5128 5140	cli 0807	consoleread 8620
6369 6406 6421 6433 6444	5156 5238 5239 5332 5343	0807 0809 1376 1910 8460	8620 8678
6456	5361 5385 5510 5531 5618	8554 9712	consolewrite 8658
argint 3895	5726 5770 5819 5869	cmd 9016	8658 8677
0645 3895 3908 3924 4083	brelse 4976	9016 9028 9037 9038 9043	conspute 8551
4106 4120 4193 4205 4220	0514 4976 4979 5131 5132	9044 9052 9057 9061 9070	8366 8397 8418 8436 8439
6374 6421 6433 6658 6726	5147 5164 5242 5243 5334	9073 9078 9086 9092 9096	8443 8444 8551 8591 8597
6727 6781	5346 5367 5372 5392 5516	9104 9128 9130 9219 9231	8604 8665
argptr 3904	5519 5540 5626 5/32 5//6	9235 9236 9352 9355 9357	CONTEXT 2610
0040 3904 4102 4224 0421	5822 5873 DCTZE 44EE	9358 9359 9360 9363 9364	2000 2001 2002 2010 2032
0433 0430 000/	MONT AAEE AATO AEN1 AENT	0272 0272 0274 0275 0276	2000 2001 2002 2003 3070
0647 2021 6460 6550 6650	430/ 4433 44/3 4301 430/ 4731 4745 4767 5100 5130	0270 0200 0202 0204 0205	3110 3270 COM7 7022
6707 6725 6757 6781	5240 5344 5810 5820 5821 4731 4743 4707 3100 3129	0396 0397 0399 0390 0400	7022 7022 7024 7025 7026
2+tribute 1560	5240 5344 5019 5020 5021	9300 9307 9300 9309 9400	7027 7020 7020
acciibuce i300 0521 0615 1459 1560	huf 4300	9401 9403 9403 9400 9407	gonvout 2368
BACK 9012	0500 0513 0514 0515 0557	9416 9418 9419 9420 9421	0680 2368 6918 6929
9012 9127 9420 9689	0500 0515 0511 0515 0557	9422 9512 9513 9514 9515	CODYINT 2303
hackcmd 9050 9414	4300 4304 4305 4306 4662	9517 9521 9524 9530 9531	0677 2303 2314 2316 2884
9050 9064 9128 9414 9416	4678 4681 4725 4754 4804	9534 9537 9539 9542 9546	cprintf 8402
9542 9655 9690	4806 4809 4877 4881 4885	9548 9550 9553 9555 9558	0519 1474 1514 2217 3276
BACKSPACE 8500	4891 4903 4915 4918 4951	9560 9563 9564 9575 9578	3280 3282 3740 3753 3758
8500 8517 8559 8591 8597	4954 4965 4976 5055 5127	9581 9585 9600 9603 9608	4038 4152 5472 7569 7589
balloc 5354	5128 5140 5141 5147 5156	9612 9613 9616 9621 9622	7811 8012 8402 8462 8463
5354 5374 5717 5725 5729	5157 5163 5164 5238 5239	9628 9637 9638 9644 9645	8464 8467
BBLOCK 4510	5272 5319 5330 5341 5357	9651 9652 9661 9664 9666	cpu 2558
4510 5361 5385	5381 5506 5528 5605 5713	9672 9673 9678 9684 9690	0560 1474 1514 1516 1528
B_BUSY 4309	5759 5805 5855 8379 8390	9691 9694	1756 1816 1837 1858 1896
4309 4808 4926 4927 4940	8394 8397 8568 8589 8603	CMOS_PORT 7835	1911 1912 1920 1922 1968
4943 4967 4978 4990	8637 8658 8665 9137 9140	9141 9142 9255 9267 9269 9272 9273 9274 9277 9278 9282  B_VALID 4310 4310 4770 4810 4828 4957  bwrite 4965 0515 4965 4968 5130 5163 5241  bzero 5339 5339 5368 C 8181 8574 8181 8229 8254 8255 8256 8257 8258 8260 8574 8584 8587 8594 8605 8638  CAPSLOCK 8162 8162 8195 8336 cgaputc 8505 8505 8563 clearpteu 2279 0681 2279 2285 6910 cli 0807 0807 0809 1376 1910 8460 8554 9712 cmd 9016 9016 9028 9037 9038 9043 9044 9052 9057 9061 9070 9073 9078 9086 9092 9096 9104 9128 9130 9219 9231 9235 9236 9352 9355 9357 9388 9359 9360 9363 9364 9366 9368 9369 9370 9371 9372 9373 9374 9375 9376 9379 9380 9382 9384 9385 9386 9387 9388 9389 9400 9401 9403 9405 9406 9407 9408 9409 9410 9413 9414 9416 9418 9419 9420 9421 9422 9512 9513 9514 9515 9517 9521 9524 9530 9531 9534 9537 9539 9542 9546 9548 9550 9553 9555 9558 9560 9563 9564 9575 9578 9581 9585 9600 9603 9608 9612 9673 9678 9684 9690 9691 9694  CMOS_PORT 7835 7835 7849 7850 7888	1981 1987 2126 2127 2128

2129 2558 2568 2572 2583	1029 1977 1978 2823 2824	fatahatr 3879	0773 0776 1151 3661
3078 3109 3115 3116 3117	3673 3768 3777	0649 3879 3926 6794	gethuiltin 9201
3715 3740 3741 3753 3754	F0F9C 8166	file 4550	9201 9226
3758 3760 7463 7464 7811	8166 8320 8324 8325 8327	0502 0527 0528 0529 0531	getgallerneg 1876
8462	8330	0502 0527 0520 0527 0551	0628 1838 1876 3278 8465
anunum 7901	olfhdr 1205	5320 6150 6164 6174 6177	go+amd 0127
0575 1538 1974 7801 8023	1205 6865 9819 9824	6180 6201 6202 6214 6216	9127 9267
8032	FI.F MACIC 1202	6252 6265 6302 6363 6369	gettoken 9456
CDN DF 0077	1202 6881 0830	6372 6388 6403 6417 6420	0/156 05/1 05/5 0557 0570
0977 1385 1421 9743	FI.F DPOC LOAD 1936	6442 6453 6655 6804 7006	9571 9607 9611 9633
CPN DC 1987	1236 6892	7021 8360 8758 9029 9088	grownrog 2853
0987 1300 1421	end on 5203	9089 9364 9372 9572	0611 2853 4109
CDU MD U083	0586 2030 5203 6235 6320	fileallog 6175	havedick1 4680
0003 1200 1421	6472 6480 6408 6507 6562	0527 6175 6682 7027	1680 4714 4812
CD4 DGE 1989	6597 6602 6666 6671 6677	fileclose 6214	holding 1894
0000 1202 1/1/	6686 6600 6708 6712 6720	0528 2022 6214 6220 6447	0620 1827 1854 1804 2107
greate 6607	6732 6758 6764 6760 6872	6684 6815 6816 7054 7056	UNTIDE 7001
6607 6627 6640 6644 6664	6002 6055	filedum 6202	7881 7001
6707 6720	ontry 1200	0520 2002 6202 6206 6410	1001 7904 121100 EE02
0707 0720 CDTDODT 9501	1211 1286 1280 1200 2552	0329 2902 0202 0200 0410 filoinit 6160	0520 5503 5521 6626 6627
QEN1 QE10 QE11 QE12 QE12	2552 6042 7221 0021 0045	0530 1482 6168	TDI OCK 1501
0501 0510 0511 0512 0513	0046	filoroad 6265	15LOCK 4504 4E04 EE10 EE21 E610
0001 0002 0000 0004	7040 FOT 7665	0521 6265 6200 6422	4504 5510 5531 5016 T DITCY 4676
01E0 010E 0100 033E	7665 779/ 7925	filestat 6252	1_BUS1 4373 4575 5612 5614 5637 5641
0100 0100 0100 0330	7000 7704 7020 FDDOD 7606	0522 6252 6450	4575 5012 5014 5037 5041
7000 700E	7606 7777	0332 0232 0430	7003 3003 TCDUT 7670
doallogum 2222	7000 //// FCD 7660	1116ML1CG 0207	1CRH1 /0/9
0672 2210 2222 2266 2062	ESK 7000 7660 7700 7701	0000 0000 0004 0000 0400	7079 7767 7637 7609
00/3 ZZIO ZZ3Z ZZ00 Z00Z	7000 7700 7701	FIRSI_PROC_GID 2000	7660 7700 7700 70E0 7060
DEVSPACE 0434	0222 0222 0524 4007 6707	2000 DDOG 111D 0EE4	7070
0454 2002 2095	0322 0323 0324 4007 0797	FIRSI_PROC_UID 2004	7070
4500 4505 5000 5010 5050	0000 0310 0313 0300 3001	2004 2772	TD /002
458U 4585 58U8 581U 5858	9U8Z	FL_IF U90U 0060 1010 1010 2027 2112	/002 /098 /810
3000 0101 00// 00/0	DABC 9000	7000	TDE_BS1 4005
4477 4F01 FF07 FF11 FF00	9000 9077 9359 9005	/000 famile 2074	4000 4000 TDE CMD DEAD 4670
44// 4501 550/ 5511 5529	0000 000F 0070 00F2 00FF	OTK 28/4	1DE_CMD_READ 4670
3534 3000 3019	9020 9005 9078 9353 9355	0321 0010 2874 4001 4002	40/U 4/4/
dirent 4515	9021 9027 9028 9050 9000	8910 8973 8975 9305 9307	IDE_CMD_WRITE 40/I
4515 5914 5955 6516 6554	exit 2921	IOTKI 93UI	40/1 4/44
01711NK 5952	0129 0212 0219 0276 0313	9055 9097 9107 9114 9129	IDE_DF 4667
053/ 5921 5952 596/ 59/5	0324 0337 0009 2921 2902	9281 9301	400 / 4091
6491 6639 6643 6644	3705 3709 3769 3778 4002	forkret 3133	IDE_DRDY 4666
dirlookup 5911	4068 8866 8869 8911 8976	2668 2803 3133	4666 4689
0538 5911 5917 5959 6075	8981 9071 9080 9090 9133	ireerange 3401	IDE_ERR 4008
05/3 001/	9285 9292	3361 3384 3390 3401	4668 4691
DIRSIZ 4513	EXTMEM 0452	ireevm 2260	1deinit 4/UI
4513 4517 5905 5972 6028	0452 0458 2079	0674 2260 2265 2328 2986	0555 1483 4701
6029 6092 6465 6555 6611	IQALLOC 6388	6945 6952	1deintr 4/52
dopuiltin 9231	6388 6408 6682 6812	FSSIZE U412	U556 3724 4752
9231 9278	retchint 386/	U412 4729	idelock 46//
DAT-OSEK 107A	0048 3867 3897 6788	fetchstr 3879     0649 3879 3926 6794  file 4550     0502 0527 0528 0529 0531     0532 0533 0601 2635 4550     5320 6158 6164 6174 6177     6180 6201 6202 6214 6216     6252 6265 6302 6363 6369     6372 6388 6403 6417 6429     6442 6453 6655 6804 7006     7021 8360 8758 9029 9088     9089 9364 9372 9572  filealloc 6175     0527 6175 6682 7027  fileclose 6214     0528 2932 6214 6220 6447     6684 6815 6816 7054 7056  filedup 6202     0529 2902 6202 6206 6410  fileinit 6168     0530 1482 6168  fileread 6265     0531 6265 6280 6423  filestat 6252     0532 6252 6458  filewrite 6302     0533 6302 6334 6339 6435  FIRST_PROC_GID 2553     2553 2771  FIRST_PROC_UID 2554     2554 2772  FL_IF 0960     0960 1912 1918 2827 3113     7808  fork 2874     0321 0610 2874 4001 4062     8910 8973 8975 9305 9307  fork1 9301     9055 9097 9107 9114 9129     9281 9301  forkret 3133     2668 2803 3133  freerange 3401     3361 3384 3390 3401  freevm 2260     0674 2260 2265 2328 2986     6945 6952  FSSIZE 0412     0412 4729  gatedesc 1151	40// 4/05 4/5/ 4/59 4//8

4815 4829 4832	8658	3536 3739 7757	8170 8218 8240 8264
iderw 4804	INDIT RIF 8566	TRO TIMER 3531	KEV HOME 8169
0557 4804 4809 4811 4813	8566 8568 8589 8601 8603	3531 3714 3773 7764 8730	8169 8218 8240 8264
4958 4970	8605 8637	igdirempty 6513	KEV TMC 8177
idestart 4725	ingl 0712	6513 6520 6579	8177 8219 8241 8265
4681 4725 4728 4734 4776	0712 0714 4767 9873	ismn 7465	KEV I.E 8173
4825	install trans 5122	0589 1484 7465 7562 7570	8173 8217 8239 8263
idewait 4685	5122 5171 5256	7590 7593 8005 8025	KEY PGDN 8176
4685 4708 4736 4766	INT DISABLED 7969	itrunc 5756	8176 8216 8238 8262
idtinit 3679	7969 8017	5323 5667 5756	KEY PGIIP 8175
0656 1515 3679	ioapic 7977	iunlock 5635	8175 8216 8238 8262
idup 5589	7557 7579 7580 7974 7977	0544 5635 5638 5682 6072	KEY RT 8174
0540 2903 5589 6062	7986 7987 7993 7994 8008	6257 6277 6328 6486 6689	8174 8217 8239 8263
iget 5554	IOAPIC 7958	6767 8625 8662	KEY UP 8171
5476 5517 5554 5574 5929	7958 8008	iunlockput 5680	8171 8215 8237 8261
6060	ioapicenable 8023	0545 5680 6067 6076 6079	kfree 3415
iinit 5468	0560 4707 8023 8682 8793	6479 6492 6495 6506 6580	0566 2248 2250 2270 2273
0541 3144 5468	ioapicid 7467	6591 6595 6601 6618 6622	2885 2984 3406 3415 3420
ilock 5603	0561 7467 7580 7597 8011	6646 6676 6685 6711 6732	7052 7073
0542 5603 5609 5629 6065	8012	6763 6901 6954	kill 3225
6255 6274 6325 6477 6490	ioapicinit 8001	iupdate 5526	0612 3225 3759 4006 4085
6503 6567 6575 6615 6619	0562 1476 8001 8012	0546 5526 5669 5782 5878	8917
6629 6674 6761 6875 8632	ioapicread 7984	6485 6505 6589 6594 6633	kinit1 3380
8652 8667	7984 8009 8010	6637	0567 1469 3380
inb 0703	ioapicwrite 7991	I_VALID 4576	kinit2 3388
0703 4689 4713 7604 7891	7991 8017 8018 8031 8032	4576 5617 5627 5661	0568 1487 3388
8314 8317 8511 8513 8784	IO_PIC1 8057	kalloc 3438	KSTACKSIZE 0401
8790 8791 8807 8817 8819	8057 8070 8085 8094 8097	0565 1544 2013 2092 2159	0401 1304 1313 1545 2129
9723 9731 9854	8102 8112 8126 8127	2215 2319 2784 3438 7029	2788
initlock 1812	IO_PIC2 8058	KBDATAP 8154	kvmalloc 2107
0630 1812 2677 3382 3675	8058 8071 8086 8115 8116	8154 8317	0668 1470 2107
4705 4893 5112 5470 6170	8117 8120 8129 8130	kbdgetc 8306	lapiceoi 7822
7035 8675	IO_TIMER1 8709	8306 8348	0577 3721 3725 3732 3736
initlog 5106	8709 8718 8728 8729	kbdintr 8346	3742 7822
0583 3145 5106 5109	IPB 4501	0571 3731 8346	lapicinit 7751
inituvm 2153	4501 4504 5511 5532 5619	KBS_DIB 8153	0578 1472 1506 7751
0675 2153 2158 2820	iput 5658	8153 8315	lapicstartap 7841
inode 4562	0543 2938 5658 5664 5683	KBSTATP 8152	0579 1549 7841
0503 0537 0538 0539 0540	5960 6083 6234 6496 6768	8152 8314	lapicw 7695
0542 0543 0544 0545 0546	IRQ_COM1 3533	KERNBASE 0457	7695 7757 7763 7764 7765
0548 0549 0550 0551 0552	3533 3734 8792 8793	0457 0458 0462 0463 0467	7768 7769 7774 7777 7780
0676 2168 2636 4556 4562	IRQ_ERROR 3535	0468 0470 0471 1565 1883	7781 7784 7787 7788 7793
4581 4582 5323 5464 5476	3535 7777	2079 2208 2266	7825 7857 7858 7860 7869
5502 5526 5553 5556 5562	IRQ_IDE 3534	KERNLINK 0458	7870
5588 5589 5603 5635 5658	3534 3723 3727 4706 4707	0458 2080	lcr3 0840
5680 5710 5756 5787 5802	IRQ_KBD 3532	KEY_DEL 8178	0840 2118 2133
5852 5910 5911 5952 5956	3532 3730 8681 8682	8178 8219 8241 8265	Igdt 0762
6054 6057 6089 6100 6466	IRQ_SLAVE 8060	KEY_DN 8172	0762 0770 1383 1983 9741
6513 6553 6606 6610 6656	8060 8064 8102 8117	8172 8215 8237 8261	11dt 0776
6704 6719 6754 6866 8620	IRQ_SPURIOUS 3536	3536 3739 7757 IRQ_TIMER 3531     3531 3714 3773 7764 8730 isdirempty 6513     6513 6520 6579 ismp 7465     0589 1484 7465 7562 7570     7590 7593 8005 8025 itrunc 5756     5323 5667 5756 iunlock 5635     0544 5635 5638 5682 6072     6257 6277 6328 6486 6689     6767 8625 8662 iunlockput 5680     0545 5680 6067 6076 6079     6479 6492 6495 6506 6580     6591 6595 6601 6618 6622     6646 6676 6685 6711 6732     6763 6901 6954 iupdate 5526     0546 5526 5669 5782 5878     6485 6505 6589 6594 6633     6637 I_VALID 4576     4576 5617 5627 5661 kalloc 3438     0565 1544 2013 2092 2159     2215 2319 2784 3438 7029 KBDATAP 8154     8154 8317 kbdgetc 8306     8306 8348 kbdintr 8346     0571 3731 8346 KBS_DIB 8153     8153 8315 KBSTATP 8152     8152 8314 KERNBASE 0457     0458 0462 0463 0467     0468 0470 0471 1565 1883     2079 2208 2266 KERNLINK 0458     0458 2080 KEY_DEL 8178     819 8241 8265 KEY_DN 8172     8172 8215 8237 8261 KEY_END 8170	0776 0784 3681

LINTO 7684	0636 7165 7495 7538 7926 memmove 7181 0637 1535 2162 2321 2382 5129 5240 5333 5538 5625 5821 5871 6029 6031 7181 7204 8526 memset 7154 0638 2016 2094 2160 2221 2802 2822 3423 5344 5513 6584 6784 7154 8528 9140 9358 9369 9385 9406 9419 microdelay 7831 0580 7831 7859 7861 7871 7889 8808 min 5322 0345 0346 5322 5820 5870 MINS 7880 7880 7903 MONTH 7883 7883 7906 mp 7302 7302 7458 7487 7494 7495 7496 7505 7510 7514 7515 7518 7519 7530 7533 7535	7251 7566	6202
7694 7769	0030 /103 /433 /330 /320 mommosso 7181	7551 7500 mpgeargh 7506	0392 NDDFNTDTFC 1071
LINT1 7685	0627 1535 2162 2221 2282	7506 7535	1071 1561 2267
7685 7769	5129 5240 5333 5538 5625	mngearch1 7488	NDDOC 0400
LIST 9011	5821 5871 6029 6031 7181	7488 7514 7518 7521	0400 2662 2702 2761 2951
0011 0005 0407 0683	7204 8526	multiboot header 1275	2977 3068 3207 3230 3269
listcmd 9041 9401	memget 7154	1274 1275	NDTENTES 1072
9041 9066 9096 9401 9403	0638 2016 2094 2160 2221	namecumo 5903	1072 2244
9546 9657 9684	2802 2822 3423 5344 5513	0547 5903 5924 6570	NSEGS 2552
loadgs 0801	6584 6784 7154 8528 9140	namei 6090	1961 2552 2562
0801 1984	9358 9369 9385 9406 9419	0548 2832 6090 6472 6670	nulterminate 9652
loaduvm 2168	microdelay 7831	6757 6871	9515 9530 9652 9673 9679
0676 2168 2174 2177 6898	0580 7831 7859 7861 7871	nameiparent 6101	9680 9685 9686 9691
log 5087 5100	7889 8808	0549 6055 6070 6082 6101	NUMLOCK 8163
5087 5100 5112 5114 5115	min 5322	6488 6562 6613	8163 8196
5116 5126 5127 5128 5140	0345 0346 5322 5820 5870	namex 6055	O CREATE 4353
5143 5144 5145 5156 5159	MINS 7880	6055 6093 6103	4353 6663 9578 9581
5160 5161 5172 5180 5182	7880 7903	NBUF 0411	O_RDONLY 4350
5183 5184 5186 5188 5189	MONTH 7883	0411 4881 4903	4350 6675 9575
5207 5208 5209 5210 5211	7883 7906	ncpu 7466	O_RDWR 4352
5213 5216 5218 5224 5225	mp 7302	1474 1537 2573 4707 7466	4352 6696 8964 8966 9259
5226 5227 5237 5238 5239	7302 7458 7487 7494 7495	7568 7569 7573 7574 7575	outb 0721
5253 5257 5276 5278 5281	7496 7505 7510 7514 7515	7595	0721 4711 4720 4737 4738
5282 5283 5286 5287 5288	7518 7519 7530 7533 7535	NCPU 0402	4739 4740 4741 4742 4744
5290	7537 7544 7554 7560 7600	0402 2572 7463	4747 7603 7604 7849 7850
logheader 5082	mpbcpu 7470	NDEV 0406	7888 8070 8071 8085 8086
5082 5094 5108 5109 5141	0590 7470	0406 5808 5858 6161	8094 8097 8102 8112 8115
5157	MPBUS 7352	NDIRECT 4472	8116 8117 8120 8126 8127
LOGSIZE 0410	7352 7583	4472 4474 4483 4573 5715	8129 8130 8510 8512 8531
0410 5084 5184 5276 6317	mpconf 7313	5720 5724 5725 5762 5769	8532 8533 8534 8727 8728
log_write 5272	7313 7529 7532 7537 7555	5770 5777 5778	8729 8773 8776 8777 8778
0584 5272 5279 5345 5366	mpconfig 7530	NELEM 0684	8779 8780 8781 8809 9728
5391 5515 5539 5730 5872	7530 7560	0684 2097 2721 3272 4032	9736 9864 9865 9866 9867
ltr 0788	mpenter 1502	6786	9868 9869
0788 0790 2130	1502 1546	nextpid 2667	outsl 0733
makeint 9163	mpinit 7551	2667 2769 2770 2780	0733 0735 4745
9163 9184 9190	0591 1471 7551 7569 7589	NFILE 0404	outw 0727
mappages 2029	mpioapic 7339	0404 6164 6180	0727 1431 1433 4153 9774
2029 2098 2161 2222 2322	7339 7557 7579 7581	NINDIRECT 4473	9776
MAXARG 0408	MPIOAPIC 7353	44/3 44/4 5/22 5//2	O_WRONLY 4351
0408 6777 6864 6915	7353 7578 MDTOTNED 7354	NINODE 0405	4351 6695 6696 9578 9581
MAXARGS 9014	MPIOINTR 7354	0405 5464 5562	P2V U468
9014 9022 9023 9640	/354 /584	NO 8156	0468 1469 1487 7512 7851
MAXFILE 44/4	MPLINTR /355	8156 8202 8205 8207 8208	8502
44/4 5805	/355 /585	8209 8210 8212 8224 8227	panic 8455 9289
MAXUPBLUCKS U4U9	0345 0346 5322 5820 5870 MINS 7880 7880 7903 MONTH 7883 7883 7906 mp 7302 7302 7458 7487 7494 7495 7496 7505 7510 7514 7515 7518 7519 7530 7533 7535 7537 7544 7554 7560 7600 mpbcpu 7470 0590 7470 MPBUS 7352 7352 7583 mpconf 7313 7313 7529 7532 7537 7555 mpconfig 7530 7530 7560 mpenter 1502 1502 1546 mpinit 7551 0591 1471 7551 7569 7589 mpioapic 7339 7339 7557 7579 7581 MPIOAPIC 7353 7353 7578 MPIOINTR 7354 7354 7584 MPLINTR 7355 7355 7585 mpmain 1512 1459 1490 1507 1512 mpproc 7328 7328 7556 7567 7576 MPPROC 7351	8229 823U 823I 8232 8234	0521 1828 1855 1919 1921
MAY DDOCECC TADE 0104	1409 1490 1007 1012	0252 0253 0255 0256 025/	2040 2090 2132 2138 21/4
0104 0100 0104 0100	MPPLOC /320	0430 MOETIE 0403	2111 2240 2200 2200 2314
memamo 7165	1320 1330 1301 1310 MDDDOC 7351	UVUZ 263E 20UU 203U 627E	2110 2110 2114 21E6 21E0
memomp /100	MEEROC /JJI	0103 2033 2500 2530 03/0	2110 2117 2114 2120 3123

3420 3755 4728 4730 4734	2214 2221 2222 2241 2244 2312 2321 2322 2379 2385 2821 2828 3405 3419 3423 6908 6910 PHYSTOP 0453 0453 1487 2081 2095 2096 3419 picenable 8075 0595 4706 8075 8681 8730 8792 picinit 8082 0596 1475 8082 picsetmask 8067 8067 8077 8133 pinit 2675 0613 1479 2675 pipe 7011 0504 0602 0603 0604 4004 4555 6231 6272 6309 7011 7023 7029 7035 7039 7043 7061 7080 7101 8913 9105 9106 PIPE 9010 9010 9103 9386 9677 pipealloc 7021 0601 6809 7021 pipeclose 7061 0602 6231 7061 pipecmd 9035 9380	2901 2902 2903 2905 2923	0632 1826 1905 2125
4809 4811 4813 4948 4968	2312 2321 2322 2379 2385	2926 2931 2932 2933 2938	rcr2 0832
4979 5109 5210 5277 5279	2821 2828 3405 3419 3423	2940 2945 2951 2952 2960	0832 3754 3761
5374 5389 5521 5574 5609	6908 6910	2970 2977 2978 3001 3007	readeflags 0794
5629 5638 5664 5736 5917	PHYSTOP 0453	3060 3068 3075 3078 3083	0794 1909 1918 3113 7808
5921 5967 5975 6206 6220	0453 1487 2081 2095 2096	3111 3116 3125 3155 3173	read_head 5138
6280 6334 6339 6520 6578	3419	3174 3178 3205 3207 3227	5138 5170
6586 6627 6640 6644 8413	picenable 8075	3230 3265 3269 3655 3704	readi 5802
8455 8462 8523 9056 9075	0595 4706 8075 8681 8730	3706 3708 3751 3759 3760	0550 2183 5802 5920 5966
9106 9289 9307 9528 9572	8792	3762 3768 3773 3777 3855	6275 6519 6520 6879 6890
9606 9610 9636 9641	picinit 8082	3869 3883 3886 3897 3910	readsb 5328
panicked 8368	0596 1475 8082	4031 4033 4039 4040 4057	0536 5113 5328 5384 5471
8368 8468 8553	picsetmask 8067	4091 4108 4125 4173 4179	readsect 9860
parseblock 9601	8067 8077 8133	4185 4196 4208 4657 5316	9860 9895
9601 9606 9625	pinit 2675	6062 6361 6376 6393 6394	readseg 9879
parsecmd 9518	0613 1479 2675	6446 6768 6770 6814 6854	9814 9827 9838 9879
9057 9282 9518	pipe 7011	6936 6939 6940 6941 6942 6943 6944 7004 7087 7107	recover_from_log 5168
parseexec 9617	0504 0602 0603 0604 4004	6943 6944 7004 7087 7107	5102 5117 5168
9514 9555 9617	4555 6231 6272 6309 7011	6943 6944 7004 7087 7107 7461 7556 7567 7568 7569 7572 8363 8630 8760 procdump 3254 0614 3254 8615 proghdr 1224 1224 6867 9820 9834 PTE_ADDR 1094	REDIR 9009
parseline 9535	7023 7029 7035 7039 7043	7572 8363 8630 8760	9009 9085 9370 9671
9512 9524 9535 9546 9608	7061 7080 7101 8913 9105	procdump 3254	redircmd 9026 9364
parsepipe 9551	9106	0614 3254 8615	9026 9068 9086 9364 9366
9513 9539 9551 9558	PIPE 9010	proghdr 1224	9575 9578 9581 9659 9672
parseredirs 9564	9010 9103 9386 9677	1224 6867 9820 9834	REG_ID 7960
9564 9612 9631 9642	pipealloc 7021	PTE_ADDR 1094	7960 8010
PCINT 7683	0601 6809 7021	1094 2011 2178 2246 2269	7960 8010 REG_TABLE 7962 7962 8017 8018 8031 8032 REG_VER 7961 7961 8009 release 1852 0631 1852 1855 2731 2764 2781 2912 2992 3002 3085 3127 3137 3169 3182 3218 3236 3240 3431 3448 3719 4126 4131 4144 4759 4778 4832 4928 4944 4993 5189 5218 5227 5290 5565 5581 5593 5615 5643 5666 5675 6183 6187 6208 6222 6228
7683 7774	pipeclose 7061	2317 2361	7962 8017 8018 8031 8032
pde_t 0353	0602 6231 7061	PTE_FLAGS 1095	REG_VER 7961
0353 0670 0671 0672 0673	pipecmd 9035 9380	1095 2318	7961 8009
0674 0675 0676 0677 0680	9035 9067 9104 9380 9382	PTE_P 1083	release 1852
0681 1460 1520 1561 1960	9558 9658 9678	1083 1563 1565 2010 2020	0631 1852 1855 2731 2764
2004 2006 2029 2086 2089	piperead 7101	2039 2041 2245 2268 2315	2781 2912 2992 3002 3085
2092 2153 2168 2203 2232	0603 6272 7101	2357	3127 3137 3169 3182 3218
2260 2279 2302 2303 2305	PIPESIZE 7009	PTE_PS 1090	3236 3240 3431 3448 3719
2352 2368 2623 6868	7009 7013 7086 7094 7116	1090 1563 1565	4126 4131 4144 4759 4778
PDX 1062	pipewrite 7080	pte_t 1098	4832 4928 4944 4993 5189
1062 2009	0604 6309 7080	1098 2003 2007 2011 2013	5218 5227 5290 5565 5581
PDXSHIFT 1077	popcli 1916	2032 2171 2234 2281 2306	5593 5615 5643 5666 5675
1062 1068 1077 1565	0633 1871 1916 1919 1921	2354	6183 6187 6208 6222 6228
peek 9501	2134	2354 2354 PTE_U 1085 1085 2020 2161 2222 2286	7072 7075 7088 7097 7108
9501 9525 9540 9544 9556	printint 8376	1085 2020 2161 2222 2286	7119 8451 8613 8631 8651
9569 9605 9609 9624 9632	8376 8426 8430	2359	8666
PGROUNDDOWN 1080	proc 2621	PTE_W 1084	ROOTDEV 0407
1080 2034 2035 2375	0505 0608 0678 1455 1808	1084 1563 1565 2020 2079	0407 3144 3145 6060
PGROUNDUP 1079	1956 1988 2123 2129 2569	2081 2082 2161 2222	ROOTINO 4454
1079 2213 2240 3404 6907	2584 2621 2630 2656 2662	PTX 1065	4454 6060
PGSIZE 1073	2665 2684 2702 2754 2757	1065 2022	run 3364
1073 1079 1080 1560 2016	2761 2775 2776 2777 2813	PTXSHIFT 1076	3261 3364 3365 3371 3417
2044 2045 2094 2157 2160	2857 2859 2862 2865 2866	1065 1068 1076	3427 3440
2161 2173 2175 2179 2182	7061 7080 7101 8913 9105 9106 PIPE 9010 9010 9103 9386 9677 pipealloc 7021 0601 6809 7021 pipeclose 7061 0602 6231 7061 pipecmd 9035 9380 9035 9067 9104 9380 9382 9558 9658 9678 piperead 7101 0603 6272 7101 PIPESIZE 7009 7009 7013 7086 7094 7116 pipewrite 7080 0604 6309 7080 popcli 1916 0633 1871 1916 1919 1921 2134 printint 8376 8376 8426 8430 proc 2621 0505 0608 0678 1455 1808 1956 1988 2123 2129 2569 2584 2621 2630 2656 2662 2665 2684 2702 2754 2757 2761 2775 2776 2777 2813 2857 2859 2862 2865 2866 2877 2884 2890 2891 2892	2359 PTE_W 1084  1084 1563 1565 2020 2079  2081 2082 2161 2222 PTX 1065  1065 2022 PTXSHIFT 1076  1065 1068 1076 pushcli 1905	runcmd 9061

9061 9075 9092 9098 9100	0996 2126 2127 2130	sti 0813 0813 0815 1923 3064 stosb 0742 0742 0744 7160 9840 stosl 0751 0751 0753 7158 strlen 7251 0640 6917 6918 7251 9179 9182 9188 9203 9235 9272 9523 strncmp 7208 9153 0641 5905 7208 9153 9180	sys_close 6439
9112 9119 9130 9282	SEG ICODE 0994	0813 0815 1923 3064	3930 3980 6439
RIINNING 2618	0994 1977 2823	stosh 0742	SYS close 3821
2618 2693 3077 3111 3261	SEG UDATA 0995	0742 0744 7160 9840	3821 3980 4021
3773	0995 1978 2824	stosl 0751	svs date 4158
safestropy 7232	setbuiltin 9175	0751 0753 7158	3951 3982 4158
0639 2831 2905 6936 7232	9175 9225	strlen 7251	SYS date 3823
sb 5324	SETGATE 1171	sti 0813 0813 0815 1923 3064 stosb 0742 0742 0744 7160 9840 stosl 0751 0751 0753 7158 strlen 7251 0640 6917 6918 7251 9179 9182 9188 9203 9235 9272	3823 3982 4023
0536 4504 4510 5111 5113	1171 3672 3673	9182 9188 9203 9235 9272	sys_dup 6401
5114 5115 5324 5328 5333	setupkvm 2087	9523	3931 3969 6401
5360 5361 5362 5384 5385	0670 2087 2109 2310 2818	strncmp 7208 9153	SYS_dup 3810
5471 5472 5473 5509 5510	6884	9523 strncmp 7208 9153 0641 5905 7208 9153 9180	3810 3969 4010
5531 5618 7914 7916 7918	SHIFT 8158	9181 9183 9187 9189 9204	sys_exec 6775
sched 3103	8158 8186 8187 8335	9205 9209 9235	3932 3966 6775
0616 2961 3103 3108 3110	skipelem 6015	strncpy 7218	SYS_exec 3807
3112 3114 3126 3175	6015 6064	0642 5972 7218	3807 3966 4007 8862
scheduler 3058	sleep 3153	STS_IG32 1050	sys_exit 4066
0615 1517 2560 3058 3078	0617 3007 3153 3156 3159	1050 1177	3933 3961 4066
3116	3259 4013 4129 4829 4931	STS_T32A 1047	SYS_exit 3802
SCROLLLOCK 8164	5183 5186 5613 7092 7111	1047 2126	3802 3961 4002 8867
8164 8197	8635 8929	STS_TG32 1051	sys_fork 4060
SECS 7879	spinlock 1751	1051 1177	3934 3960 4060
7879 7902	0507 0617 0627 0629 0630	sum 7476	SYS_fork 3801
SECTOR_SIZE 4664	0631 0659 1751 1809 1812	7476 7478 7480 7482 7483	3801 3960 4001
4664 4731	1824 1852 1894 2657 2661	7495 7542	sys_fstat 6451
SECTSIZE 9812	3153 3359 3369 3658 3663	superblock 4462	3935 3967 6451
9812 9873 9886 9889 9894	4660 4677 4875 4880 5053	0509 0536 4462 5111 5324	SYS_fstat 3808
SEG 1019	5088 5317 5463 6159 6163	5328	3808 3967 4008
1019 1975 1976 1977 1978	7007 7012 8358 8371 8756	SVR 7666	sys_getgid 4177
1981	STA_R 0919 1036	7666 7757	3953 3984 4177
SEG16 1023	0919 1036 1440 1975 1977	switchkym 2116	SYS_getgid 3825
1023 2126	97/84	0679 1504 2110 2116 3079	3825 3984
SEG_ASM 0910	start 1375 8858 9711	SWITCHUVM 2123	sys_getpid 4089
0910 1440 1441 9784 9785	0319 0331 1374 1375 1417	0678 2123 2132 2866 3076	3936 3970 4089
segdesc 1002	1425 1427 5089 5114 5127	6944	SYS_getpid 38II
0/59 0/62 1002 1019 1023	5140 5150 5238 5472 8857	SWICH 33U8	3811 39/0 4011
1901 2502	8858 9/10 9/11 9/6/	0024 3078 3110 3307 3308	sys_getppid 4183
seginit 1900	startothers 1524	syscall 4027	3954 3985 4183
U00/ 14/3 15U5 1900	1458 1480 1524	U05U 3/U/ 305/ 4U2/	S15_getpp1d 3826
0001 1400 107E 2672 2672	000 000 000 000 000 000 000 000 000 00	0010 0011 0010 09II 09IZ 09I3 0	3020 3905
0991 1400 1975 3072 3075	0300 0332 0331 4404 3314 6707 6363 6360 6464 0063	0910 0911 0912 0913 0914	2057 2000 4213
מבר גרטוו 1000 אוסס	0101 0202 0309 0404 0903	8030 8031 8033 8034	CVC getprogg 3830
0002 1001 1004 2616	0551 5787 6256	8075 8076 8077 8078 8070	2020 2000
0993 1901 1904 3010	עדא א 1010 10250 מדא א 1010 1035	8030 8031 8032 8033 8034	ava getuid 4171
0992 1404 1976 2128 3613	0918 1035 1441 1976 1978	8035 8036 8037 8038	3052 3083 A171
9758	1981 9785	ava chdir 6751	SVS detuid 3834
SEG NIII.IASM 0904	STA X 0915 1032	9182 9188 9203 9235 9272 9523  strncmp 7208 9153 0641 5905 7208 9153 9180 9181 9183 9187 9189 9204 9205 9209 9235  strncpy 7218 0642 5972 7218  STS_IG32 1050 1050 1177  STS_T32A 1047 1047 2126  STS_TG32 1051 1051 1177  sum 7476 7476 7478 7480 7482 7483 7495 7542  superblock 4462 0509 0536 4462 5111 5324 5328  SVR 7666 7666 7757  switchkvm 2116 0679 1504 2110 2116 3079  switchuvm 2123 0678 2123 2132 2866 3076 6944  swtch 3308 0624 3078 3116 3307 3308  syscall 4027 0650 3707 3857 4027  SYSCALL 8903 8910 8911 8912 8913 89 8910 8911 8912 8913 8914 8915 8916 8917 8918 8919 8920 8921 8922 8923 8924 8925 8926 8927 8928 8929 8930 8931 8932 8933 8934 8935 8936 8937 8938  sys_chdir 6751 3929 3968 6751 SYS_chdir 3809 3809 3968 4009	3824 3983
0904 1439 9783	0915 1032 1440 1975 1977	SYS chdir 3809	SVS halt 3822
SEG TSS 0996	9784	3809 3968 4009	3822 3981 4022
220_100 0770	> 1 U ±	3007 3700 1007	JUZZ JJUT 10ZZ

sys_kill 4079	SYS_uptime 3814
3937 3965 4079	3814 3973 4014
SYS_kill 3806	sys_wait 4073
3806 3965 4006	3947 3962 4073
sys_link 6463	SYS_wait 3803
3938 3978 6463	3803 3962 4003
SYS_link 3819	sys_write 6427
	3948 3975 6427
sys_mkdir 6701	SYS write 3816
3939 3979 6701	3816 3975 4016
SYS_mkdir 3820	taskstate 1101
3820 3979 4020	1101 2561
sys_mknod 6717	TDCR 7690
3940 3976 6717	7690 7763
SYS mknod 3817	T DEV 4402
3817 3976 4017	4402 5807 5857 6728
sys_open 6651 3941 3974 6651	T_DIR 4400
	4400 5916 6066 6478 6579
SYS_open 3815	6587 6635 6675 6707 6762
3815 3974 4015	T_FILE 4401
sys_pipe 6801	4401 6620 6664
3942 3963 6801	ticks 3664
SYS_pipe 3804	0657 3664 3717 3718 4123
3804 3963 4004	4124 4129 4143
sys_read 6415	tickslock 3663
3943 3964 6415	0659 3663 3675 3716 3719
SYS_read 3805	4122 4126 4129 4131 4142
3805 3964 4005	4144
sys sbrk 4101	TICR 7688
3944 3971 4101	7688 7765
SYS_sbrk 3812	TIMER 7680
3812 3971 4012	7680 7764
sys_setgid 4201	TIMER_16BIT 8721
3956 3987 4201	8721 8727
SYS_setqid 3828	TIMER DIV 8716
3828 3987	8716 8728 8729
sys_setuid 4189	TIMER FREO 8715
3955 3986 4189	8715 8716
SYS setuid 3827	6/15 6/16 timerinit 8724
<del>-</del>	
3827 3986	0653 1485 8724
sys_sleep 4115	TIMER_MODE 8718
3945 3972 4115	8718 8727
SYS_sleep 3813	TIMER_RATEGEN 8720
3813 3972 4013	8720 8727
sys_unlink 6551	TIMER_SELO 8719
3946 3977 6551	8719 8727
SYS_unlink 3818	T_IRQ0 3529
3818 3977 4018	3529 3714 3723 3727 3730
sys_uptime 4138	3734 3738 3739 3773 7757
3949 3973 4138	7764 7777 8017 8031 8097

8116	V2P_WO 0470
TPR 7664	0470 1286 1296
7664 7793	VER 7663
trap 3701	7663 7773
3552 3554 3622 3701 3753	wait 2968
3755 3758	0326 0619 2968 4003 4075
trapframe 0852	8912 8983 9099 9123 9124
0852 2631 2792 3701	9283
trapret 3627	waitdisk 9851
2669 2797 3626 3627	9851 9863 9872
T_SYSCALL 3526	wakeup 3214
3526 3673 3703 8863 8868	0620 3214 3718 4772 4991
8907	5216 5226 5642 5672 7066
tvinit 3667	7069 7091 7096 7118 8607
0658 1480 3667	wakeup1 3203
uart 8765	2671 2945 2955 3203 3217
8765 8786 8805 8815	walkpgdir 2004
uartgetc 8813	2004 2037 2176 2242 2283
8813 8825	2313 2356
uartinit 8768	write_head 5154
0662 1478 8768	5154 5173 5255 5258
uartintr 8823	writei 5852
0663 3735 8823	0552 5852 5974 6326 6585
uartputc 8801	6586
0664 8560 8562 8797 8801	write_log 5233
uproc 0150	5233 5254
0110 0150 2556 2681 4216	xchg 0819
userinit 2811	0819 1516 1833 1869
0618 1488 2811 2819	YEAR 7884
uva2ka 2352	7884 7907
0671 2352 2376	yield 3122
V2P 0467	0621 3122 3774
0467 2080 2081	

```
0150 struct uproc {
0100 #include "types.h"
0101 #include "user.h"
                                                                               0151 int pid;
0102 #include "BC_ps.h"
                                                                               0152 int uid;
                                                                               0153 int gid;
0103
0104 #define MAX_PROCESS_TABLE 64 //64 because this is the max number of active 0154 int ppid;
                                                                               0155 char state[12];
0106 int
                                                                               0156 int size;
0107 main (int argc, char * argv[])
                                                                               0157 char name[12];
0108 {
                                                                               0158 };
0109
       int max = MAX_PROCESS_TABLE;
                                                                               0159
0110
       struct uproc table[max];
                                                                               0160
0111
                                                                               0161
0112
        int procs_recieved = getprocs(max, table);
                                                                               0162
0113
                                                                               0163
0114
        if(procs_recieved == -1) printf(1, "The system call getprocs failed.\n") 0164
0115
                                                                                0165
0116
        if(procs_recieved == 0) printf(1, "System call getprocs() returned no pr(0166
0117
0118
        int i;
                                                                               0168
0119
        for(i = 1; i < procs_recieved; i++) {</pre>
                                                                               0169
0120
       printf(1, "Process %d: Name: %s, ", i, table[i].name);
                                                                               0170
            printf(1, "pid: %d, ", table[i].pid);
0121
                                                                               0171
       printf(1, "uid: %d, ", table[i].uid);
0122
                                                                               0172
0123
       printf(1, "gid: %d, ", table[i].gid);
                                                                               0173
       printf(1, "ppid: %d, ", table[i].ppid);
0124
                                                                               0174
0125
       printf(1, "size: %d, ", table[i].size);
                                                                               0175
       printf(1, "state: %s, \n", table[i].state);
0126
                                                                               0176
0127
                                                                               0177
0128
                                                                               0178
0129
                                                                               0179
       exit();
0130 }
                                                                               0180
0131
                                                                               0181
0132
                                                                               0182
0133
                                                                               0183
0134
                                                                               0184
0135
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0149
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```

Sheet 01 Sheet 01

```
0200 #include "types.h"
                                                                                0250 #include "types.h"
                                                                                0251 #include "user.h"
0201 #include "user.h"
0202 #include "date.h"
                                                                                0252
0203 //#include "lapic.c"
                                                                                0253 int
0204
                                                                                0254 main (int argc, char * argv[])
0205 int
                                                                                0255 {
0206 main (int argc, char * argv[])
                                                                                0256
                                                                                                int uid, gid, ppid;
                                                                                0257
0207 {
0208
        struct rtcdate r;
                                                                                0258
                                                                                        uid = getuid();
0209
                                                                                0259
                                                                                        printf(2, "Current_UID_is : _%d\n", uid);
0210
        if (date(&r)) {
                                                                                0260
                                                                                        printf(2, "Setting_UID_to_100\n" );
0211
            printf (2, "date~failed\n");
                                                                                0261
                                                                                        setuid(100);
0212
        exit();
                                                                                0262
                                                                                        uid = getuid();
0213
                                                                                0263
                                                                                        printf(2, "Current_UID_is: %d\n", uid);
0214
                                                                                0264
0215
                                                                                 0265
                                                                                        gid = getgid();
0216
        printf(1, "\n %d Hours, %d Minutes, %d Seconds, %d Months, %d Day, %d 0266
                                                                                        printf(2, "Current_GID_is : _%d\n", gid);
0217
               r.hour, r.minute, r.second, r.month, r.day, r.year);
                                                                                0267
                                                                                        printf(2, "Setting_GID_to_100\n");
0218
                                                                                0268
                                                                                        setgid(100);
0219
       exit();
                                                                                0269
                                                                                        gid = getgid();
0220 }
                                                                                0270
                                                                                        printf(2, "Current_GID_is: %d\n", gid);
0221
                                                                                0271
0222
                                                                                0272
                                                                                        ppid = getppid();
0223
                                                                                0273
                                                                                                printf(2, "My_parent_process_is: %d\n", ppid);
0224
                                                                                0274
                                                                                        printf(2, "Done!\n");
0225
                                                                                0275
0226
                                                                                0276
                                                                                        exit();
0227
                                                                                0277 }
0228
                                                                                0278
0229
                                                                                0279
0230
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```

Sheet 02 Sheet 02

0300 #include "types.h" 0301 #include "user.h"	0350 typedef unsigned int uint; 0351 typedef unsigned short ushort;
0302 #include "date.h"	0352 typedef unsigned char uchar;
0303 //#include "lapic.c"	0353 typedef uint pde_t;
0304 int get_secs();	0354
0305	0355
0306 int	0356
0307 main (int argc, char * argv[])	0357
0308 {	0358
0309	0359
0310 //error check	0360
0311 if(argc < 2) {	0361
0312 printf(2, "No arguments passed to time\n");	0362
0313 exit();	0363
0314 }	0364
0315 0316	0365
0316	0366 0367
0318 //get start time 0319 int start = get secs();	0368 0369
0319    int start = get_secs(); 0320	0370
0321 if(fork() == 0) {	0370
0322 exec(argv[1], args);	0372
0323 printf(2, "exec failed running %s\n", argv[1]);	0372
0324 exit();	0374
0325 }	0375
0326 wait();	0376
0327	0377
0328 //qet end time	0378
0329 int finish = get_secs();	0379
0330	0380
0331 int total_time = finish - start;	0381
0332	0382
0333 printf(1, "%s ran in %d seconds \n", argv[1], total_time);	0383
0334	0384
0335	0385
0336	0386
0337 exit();	0387
0338 }	0388
0339	0389
0340 int get_secs()	0390
0341 {	0391
0342 struct rtcdate time;	0392
0343 date(&time);	0393
0344 int hour = time.hour * 3600;	0394
0345 int min = time.minute * 60;	0395
0346 return time.second + min + hour;	0396
0347 }	0397
0348	0398
0349	0399

```
0400 #define NPROC
                         64 // maximum number of processes
                                                                                 0450 // Memory layout
0401 #define KSTACKSIZE 4096 // size of per-process kernel stack
                                                                                 0451
                                                                                 0452 #define EXTMEM 0x100000
0402 #define NCPU
                          8 // maximum number of CPUs
                                                                                                                          // Start of extended memory
0403 #define NOFILE
                         16 // open files per process
                                                                                 0453 #define PHYSTOP 0xE000000
                                                                                                                          // Top physical memory
0404 #define NFILE
                         100 // open files per system
                                                                                 0454 #define DEVSPACE 0xFE000000
                                                                                                                          // Other devices are at high addresses
0405 #define NINODE
                         50 // maximum number of active i-nodes
                                                                                 0455
0406 #define NDEV
                         10 // maximum major device number
                                                                                 0456 // Key addresses for address space layout (see kmap in vm.c for layout)
0407 #define ROOTDEV
                          1 // device number of file system root disk
                                                                                 0457 #define KERNBASE 0x80000000
                                                                                                                          // First kernel virtual address
0408 #define MAXARG
                         32 // max exec arguments
                                                                                 0458 #define KERNLINK (KERNBASE+EXTMEM) // Address where kernel is linked
0409 #define MAXOPBLOCKS 10 // max # of blocks any FS op writes
                                                                                 0459
0410 #define LOGSIZE
                          (MAXOPBLOCKS*3) // max data blocks in on-disk log
                                                                                 0460 #ifndef __ASSEMBLER__
0411 #define NBUF
                         (MAXOPBLOCKS*3) // size of disk block cache
0412 #define FSSIZE
                         1000 // size of file system in blocks
                                                                                 0462 static inline uint v2p(void *a) { return ((uint) (a)) - KERNBASE; }
0413
                                                                                 0463 static inline void *p2v(uint a) { return (void *) ((a) + KERNBASE); }
0414
0415
                                                                                 0465 #endif
0416
                                                                                 0466
0417
                                                                                 0467 #define V2P(a) (((uint) (a)) - KERNBASE)
0418
                                                                                 0468 #define P2V(a) (((void *) (a)) + KERNBASE)
0419
0420
                                                                                 0470 #define V2P WO(x) ((x) - KERNBASE)
                                                                                                                           // same as V2P, but without casts
0421
                                                                                 0471 #define P2V_WO(x) ((x) + KERNBASE) // same as P2V, but without casts
0422
                                                                                 0472
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0449
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Sheet 04 Sheet 04

1000   1000	0500 struct buf; 0501 struct context; 0502 struct file; 0503 struct inode;		0550 int 0551 void 0552 int 0553	<pre>readi(struct inode*, char*, uint, uint); stati(struct inode*, struct stat*); writei(struct inode*, char*, uint, uint);</pre>
0505 struct redate:				
0507 struct spinlock:	0505 struct proc;		0555 void	<pre>ideinit(void);</pre>
0.500 struct stat:	0506 struct rtcdate;			<pre>ideintr(void);</pre>
S059 struct superblock;   S059   Vioapsic.c	_	;		<pre>iderw(struct buf*);</pre>
0510				
	-	ck;	-	
0512 void bint(void);   0562 void   ioapicinit(void);   0513 struct bint*   bread(uint, uint);   0563   bread(uint, uint);   0564   // kalloc.c   0565   void   bread(uint, uint);   0565   void   bread(uint, uint);   0566 void   kint(void);   0515 void   0515 void   0566 void   kint(void);   0516 void   kint(void);   0516 void   kint(void);   0518 void   consoleint(t(void);   0567 void   consoleint(t(void);   0567 void   consoleint(t(void);   0569 void   consoleint(t(void);   0570 // kbd.c   0570 // kbd.				
0513 struct buf*   bread(uint, uint);   0563     0564				-
0514 void   brelse(struct buf*);   0556 char*   kalloc.c     0515 void   bwrite(struct buf*);   0566 void   kfree(char*);     0516   0567 void   kinit(void*, void*);     0518 void   consoleint(void);   0568 void   kinit(void*, void*);     0519 void   consoleint(int*,   void);   0579     0520 void   consoleint(int*,   void);   0579   kbd.c     0521 void   panic(char*,);   0577   kbd.c     0522 void   consoleint(int*,   void);   0570   kbd.c     0523 // care.c   0573   lapic.c     0524   lat   exec(char*, char**);   0574 void   constime(struct rtcdate *r);     0525   struct file*   filealloc(void);   0576 extern volatile uint*   lapic;     0527 struct file*   filealloc(void);   0578 void   lapicoint(void);     0528 struct file*   filealloc(struct file*);   0578 void   lapicoint(void);     0529 struct file*   fileiup(struct file*; char*, int n);   0580 void   microdelay(int);     0531 lnt   fileread(struct file*, char*, int n);   0581 void   lapicoint(void);     0532 lnt   filesta(struct file*, char*, int n);   0581 void   log_write(struct buf*);     0536 void   readsb(int dev, struct superblock *sb);   0589 void   log_write(struct buf*);     0538 struct inode*   dirp(struct inode*, char*, uint);   0589   cond   microdelay(void);     0539 struct inode*   dirp(struct inode*);   0591 void   mpstarthem(void);     0540 void   iunlock(struct inode*);   0591 void   mpstarthem(void);     0540 void   iunlock(struct inode*);   0591 void   mpstarthem(void);     0540 void   iunlock(struct inode*);   0591 void   picenable(int);     0540 void   iunlock(struct inode*);   0592 void   picenable(int);     0540 void   iunlock(struct inode*);   0594 void   picenable(int);     0540 void   iunlock(struct inode*);   0594 void   picenable(int);     0540 void   iunlock(struct inode*);   0595 void   picenable(int);     0540 void   iunlock(struct inode*);   0596 void   picenable(int);     0541 void   iunlock(struct inode*);   0596 void   picenable(int);     0542 void   iunlock(struct inode*);   0596 void   picenable(int);     0543 vo				ioapicinit(void);
0515 void   0566 char*   0566 void   0567 void   0568 void   056				
0516				1 77 ( 17)
0517 // console.c         0567 void kinitl(void*, void*)           0518 void consoleinit(void);         0568 void kinit2(void*, void*);           0519 void cyprintf(char*,);         0569           0520 void consoleintr(int(*)(void));         0570 // kbd.c           0521 void panic(char*) _attribute_((noreturn));         0571 void kbdintr(void);           0522 consoleint(int(*)(void));         0572 void consoleint(int(*)(void));           0523 // exec.c         0573 // lapic.c           0524 int exec(char*, char**);         0574 void consoleint(void);           0526 // file.c         0576 extern volatile uint* lapic;           0527 void file.c         0576 extern volatile uint* lapic;           0528 void fileclose(struct file*);         0578 void lapiceoi(void);           0529 struct file* filedup(struct file*);         0578 void lapiceoi(void);           0530 void fileint(void);         0580 void microdelay(int);           0531 int filerad(struct file*, char*, int n);         0581           0532 // fs.c         0582 // log.c           0533 int filestat(struct file*, char*, int n);         0582 // log.c           0535 // fs.c         0584 void log_write(struct buf*);           0536 void readsb(int dev, struct superblock *sb);         0586 void end_op();           0537 void void void void void void void void		<pre>Dwrite(struct bui*);</pre>		
0518 void   consoleinit(void);   consoleinit(r(oid);   consoleinit(r(ant*,);   0569 void   consoleinit(r(ant*,);   0569 void   consoleinit(r(int(*)(void));   0570 // kbd.c   consoleinit(r(int(*)(void));   0570 // kbd.c   consoleinit(r(int(*)(void));   0572   0572   0573 // lapic.c   0573 // lapic.c   consoleinit(r(int(*)(void));   0574 void   consoleinit(r(int(*)(void));   consoleinit(r(int(*)(int(				, ,
0519 void   cprintf(char*,);   0569   0570 // kbd.c   0521 void   consoleintr(int(*)(void));   0571 void   kbdintr(void);   0521 void   panic(char*) _attribute_((noreturn));   0571 void   kbdintr(void);   0522   0573 // lapic.c   0573 // lapic.c   0574 void   cmostime(struct rtodate *r);   0525 // lapic.c   0574 void   cmostime(struct rtodate *r);   0526 // file.c   0575 int   cpunum(void);   0576 extern volatile uint* lapic;   0578 struct file*   filealloc(void);   0578 void   lapiceoi(void);   0578 void   lapiceoi(void);   0528 void   filechose(struct file*);   0578 void   lapiceoi(void);   0530 void   fileinit(void);   0580 void   microdelay(int);   0581 void   dipicitate   0583 void   dipicitate   0584 void   0585 void   dipicitate   0585 void   0585 void   0585 void   0586 void   0586 void   0586 void   0586 void   0586 void   0588 void   058		2 1 1 ( 12)		
0520 void         consoleintr(int(*)(void));         0570 // kbd.c           0521 void         panic(char*) _attribute_((noreturn));         0571 void         kbdintr(void);           0522         0573 // apic.         0572           0523 // exec.c         0573 // lapic.c           0524 int         exec(char*, char**);         0576 syd void         cmostime(struct rtcdate *r);           0525 // file.c         0576 int         opnum(void);           0527 struct file*         filealloc(void);         0577 void         lapicenic(void);           0528 struct file*         filealloc(void);         0578 void         lapicenic(void);           0529 struct file*         filedup(struct file*);         0579 void         lapicatatap(uchar, uint);           0530 void         filerad(struct file*, char*, int n);         0580 void         microdelay(int);           0531 int         filestat(struct file*, struct stat*);         0582 // log.c           0533 int         filewrite(struct file*, char*, int n);         0583 void         initlog(int dev);           0535 // fs.c         0586 void         log_write(struct buf*);           0536 void         readsb(int dev, struct superblock *sb);         0586 void         end_op();           0537 int         dirlokup(struct inode*, char*, uint);         0587 void <t< td=""><td></td><td></td><td></td><td>Kinit2(void*, void*);</td></t<>				Kinit2(void*, void*);
0521 void         panic(char*) _attribute_((noreturn));         0571 void         kbdintr(void);           0523 // exec.c         0573 // lapic.c         0574 void         cmostime(struct rtcdate *r);           0524 int         exec(char*, char**);         0574 void         cmostime(struct rtcdate *r);           0525 column         0575 int         cpunum(void);           0526 // file.c         0575 int         cpunum(void);           0527 struct file*         filealloc(void);         0577 void         lapicenit(void);           0528 void         fileclose(struct file*);         0579 void         lapicenit(void);           0529 struct file*         filedup(struct file*, char*, int n);         0580 void         microdelay(int);           0531 int         filestat(struct file*, char*, int n);         0581         0581           0532 int         filestat(struct file*, char*, int n);         0582 // log.c         0583 void         initlog(int dev);           0534 void         readsb(int dev, struct superblock *sb);         0584 void         log_write(struct buf*);           0536 void         readsb(int dev, struct superblock *sb);         0586 void         end_op();           0537 int         dirlockup(struct inode*, char*, uint);         0587           0538 struct inode*         dirlockup(struct inode*, char*, uint);		=		
0522				14-4/
0523 // exec.c         0573 // lapic.c           0524 int         exec(char*, char**);         0574 void         cmostime(struct rtcdate *r);           0525 cm         0576 extern volatile         cpunum(void);           0526 // file.c         0576 extern volatile         uint* lapic;           0527 struct file*         filealloc(void);         0577 void         lapiceoi(void);           0528 void         filedup(struct file*);         0578 void         lapicinit(void);           0530 void         fileinit(void);         0580 void         microdelay(int);           0531 int         filestat(struct file*, char*, int n);         0581           0532 int         filewrite(struct file*, char*, int n);         0582 // log.c           0533 int         filewrite(struct file*, char*, int n);         0582 // log.c           0534 void         readsb(int dev, struct superblock *sb);         0584 void         log_write(struct buf*);           0535 // fs.c         readsb(int dev, struct superblock *sb);         0586 void         eed_op();           0537 int         dirlookup(struct inode*, char*, uint);         0588 // mp.c           0539 struct inode*         dirlookup(struct inode*, char*, uint*);         0589 // mp.c           0541 void         iiloc((struct inode*);         0590 int         mpbcpu(void);		panic(cnar*)attribute((noreturn));		KDaintr(Vola);
0524 int         exec(char*, char**);         0574 void cpunum(void);         cmostime(struct rtcdate *r); cpunum(void);           0526 // file.c         0576 extern volatile uint* lapic;           0527 struct file*         filealloc(void);         0577 void lapiceoi(void);           0528 void         fileclose(struct file*);         0578 void lapiceoi(void);           0529 struct file*         filedup(struct file*);         0579 void lapicatracp(uchar, uint);           0530 void         fileinit(void);         0580 void microdelay(int);           0531 int         fileerad(struct file*, char*, int n);         0581           0532 int         filestat(struct file*, struct stat*);         0582 // log.c           0533 int         filewrite(struct file*, char*, int n);         0584 void initlog(int dev);           0534 void         readsb(int dev, struct superblock *sb);         0585 void begin.pg();           0535 void         readsb(int dev, struct superblock *sb);         0586 void end_op();           0538 struct inode*         dirlokup(struct inode*, char*, uint);         0587           0539 struct inode*         ialloc(uint, short);         0588 // mp.c           0531 void dirlokup(struct inode*);         0590 int mpbepu(void);           0541 void inint(int dev);         0591 void mpint(void);           0542 void inint(int dev);         0591 void mps				
0525         0576 // file.c         0576 extern volatile uint* lapic;           0527 struct file*         filealloc(void);         0576 extern volatile uint* lapic;           0528 void         fileclose(struct file*);         0578 void         lapicenit(void);           0529 struct file*         filedup(struct file*);         0580 void         lapicinit(void);           0530 void         fileinit(void);         0580 void         microdelay(int);           0531 int         fileread(struct file*, char*, int n);         0581           0532 int         filevite(struct file*, char*, int n);         0582 // log.c           0533 int         filewrite(struct file*, char*, int n);         0583 void         initlog(int dev);           0534 void         readsb(int dev, struct superblock *sb);         0584 void         log_write(struct buf*);           0536 void         readsb(int dev, struct superblock *sb);         0586 void         end_op();           0537 int         dirlookup(struct inode*, char*, uint);         0587           0538 struct inode*         dirlookup(struct inode*, char*, uint*);         0587           0539 struct inode*         divlookup(struct inode*);         0589 extern int         ismp;           0541 void         ilock(struct inode*);         0591 void         mpint(void);           0543 void		orroa/ahoret ahorett\:	_	amogtimo/atmust stadete *s):
0526 // file.c         0576 extern volatile ulnt* lapic;           0527 struct file*         filealoc(void);         0577 void lapiceoi(void);           0528 void         fileclose(struct file*);         0578 void lapicint(void);           0529 struct file*         filedup(struct file*);         0579 void lapicstartap(uchar, uint);           0530 void         fileint(void);         0580 void microdelay(int);           0531 int         filerad(struct file*, char*, int n);         0581           0532 int         filestat(struct file*, char*, int n);         0582 // log.c           0533 int         filewrite(struct file*, char*, int n);         0584 void log_write(struct buf*);           0535 // fs.c         0584 void log_write(struct buf*);           0536 void         readsb(int dev, struct superblock *sb);         0586 void epin_op();           0537 int         dirlink(struct inode*, char*, uint);         0587           0538 struct inode*         dirlokup(struct inode*, char*, uint);         0588 // mp.c           0539 struct inode*         iduo(struct inode*);         0590 int         mpbcup(void);           0541 void         ilock(struct inode*);         0591 void         mpinit(void);           0542 void         ilock(struct inode*);         0592 void         mpinit(void);           0543 void         iunlock(struct inode*		exec(char, char,),		,
0527 struct file*         filealloc(void);         0577 void         lapiceoi(void);           0528 void         fileclose(struct file*);         0578 void         lapicnit(void);           0529 struct file*         filedup(struct file*);         0580 void         lapicstartap(uchar, uint);           0530 void         filerad(struct file*, char*, int n);         0580 void         microdelay(int);           0531 int         filestat(struct file*, struct stat*);         0582 // log.c           0533 int         filewrite(struct file*, char*, int n);         0583 void         initlog(int dev);           0534         filewrite(struct file*, char*, int n);         0584 void         log_write(struct buf*);           0536 void         readsb(int dev, struct superblock *sb);         0585 void         begin_op();           0537 int         dirlokup(struct inode*, char*, uint);         0587         o588 void         end_op();           0539 struct inode*         dirlokup(struct inode*, char*, uint);         0588 //mp.c         ismp;           0540 struct inode*         ialloc(uint, short);         0589 extern int         ismp;           0541 void         iint(int dev);         0591 void         mpstartthem(void);           0542 void         ilock(struct inode*);         0593         picenable(int);           0545 void				
0528 void         fileclose(struct file*);         0578 void         lapicinit(void);           0529 struct file*         filedup(struct file*);         0579 void         lapicstartap(uchar, uint);           0530 void         fileinit(void);         0580 void         microdelay(int);           0531 int         filestat(struct file*, char*, int n);         0581           0532 int         filewrite(struct file*, char*, int n);         0582 // log.c           0533 int         filewrite(struct file*, char*, int n);         0583 void         initlog(int dev);           0535 // fs.c         0584 void         log_write(struct buf*);           0535 void         readsb(int dev, struct superblock *sb);         0586 void         end_op();           0537 int         dirlink(struct inode*, char*, uint);         0587         0587           0538 struct inode*         dirlookup(struct inode*, char*, uint*);         0589 void         end_op();           0539 struct inode*         idulockup(struct inode*);         0590 int         mpbcpu(void);           0541 void         iinit(int dev);         0591 void         mpinit(void);           0542 void         iput(struct inode*);         0592 void         mpstartthem(void);           0543 void         iunlockput(struct inode*);         0594 // picirq.c           0545 v		filealles(reid):		-
0529 struct file*         filedup(struct file*);         0579 void         lapicstartap(uchar, uint);           0530 void         fileinit(void);         0580 void         microdelay(int);           0531 int         fileread(struct file*, char*, int n);         0581           0532 int         filestat(struct file*, char*, int n);         0582 // log.c           0533 int         filewrite(struct file*, char*, int n);         0583 void         initlog(int dev);           0535 // fs.c         0584 void         log_write(struct buf*);           0536 void         readsb(int dev, struct superblock *sb);         0585 void         begin_op();           0537 int         dirlink(struct inode*, char*, uint);         0587         dirlokup(struct inode*, char*, uint*);           0539 struct inode*         dilloc(uint, short);         0588 // mp.c         ialloc(uint, short);           0541 void         iinit(int dev);         0590 int         mpbcpu(void);           0542 void         iinit(int dev);         0591 void         mpinit(void);           0543 void         iiput(struct inode*);         0593         picenable(int);           0544 void         iunlock(struct inode*);         0594 // picirq.c           0545 void         iundex(struct inode*);         0595 void         picenable(int);           0547 int		, ,		
0530 void         fileinit(void);         0580 void         microdelay(int);           0531 int         fileread(struct file*, char*, int n);         0582 // log.c           0533 int         filestat(struct file*, char*, int n);         0582 void         initlog(int dev);           0534 void         log_write(struct buf*);         0584 void         log_write(struct buf*);           0535 // fs.c         0586 void         begin_op();           0536 void         readsb(int dev, struct superblock *sb);         0586 void         end_op();           0537 int         dirlokup(struct inode*, char*, uint);         0587           0538 struct inode*         diloc(uint, short);         0588 // mp.c           0539 struct inode*         ialloc(uint, short);         0588 // mp.c           0541 void         iinit(int dev);         0590 int         mpbcpu(void);           0541 void         iinit(int dev);         0591 void         mpinit(void);           0543 void         iput(struct inode*);         0592 void         mpstartthem(void);           0544 void         iunlock(struct inode*);         0594 // picirq.c           0545 void         iunlockput(struct inode*);         0595 void         picenable(int);           0547 int         namecup(const char*, const char*, const char*);         0597				
0531 int       filerad(struct file*, char*, int n);       0581         0532 int       filestat(struct file*, struct stat*);       0582 // log.c         0533 int       filewrite(struct file*, char*, int n);       0583 void       initlog(int dev);         0534 void       log_write(struct buf*);       0584 void       log_write(struct buf*);         0535 // fs.c       0585 void       begin_op();         0536 void       readsb(int dev, struct superblock *sb);       0586 void       end_op();         0537 int       dirlink(struct inode*, char*, uint);       0587       end_op();         0538 struct inode*       dirlookup(struct inode*, char*, uint*);       0588 // mp.c       ismp;         0539 struct inode*       idup(struct inode*);       0589 extern int       ismp;         0540 struct inode*       idup(struct inode*);       0590 int       mpbcpu(void);         0541 void       iinit(int dev);       0591 void       mpstartthem(void);         0543 void       iput(struct inode*);       0592 void       mpstartthem(void);         0544 void       iunlock(struct inode*);       0595 void       picenable(int);         0546 void       iupdate(struct inode*);       0596 void       picinit(void);         0547 int       nameemp(const char*, const char*);       0598				
0532 int       filestat(struct file*, struct stat*);       0582 // log.c         0533 int       filewrite(struct file*, char*, int n);       0583 void       initlog(int dev);         0534       0584 void       log_write(struct buf*);         0535 // fs.c       0585 void       begin_op();         0536 void       readsb(int dev, struct superblock *sb);       0586 void       end_op();         0537 int       dirlink(struct inode*, char*, uint);       0587         0538 struct inode*       dirlookup(struct inode*, char*, uint*);       0588 // mp.c         0539 struct inode*       ialloc(uint, short);       0589 extern int       ismp;         0541 void       iinit(int dev);       0590 int       mpbcpu(void);         0542 void       ilock(struct inode*);       0591 void       mpstartthem(void);         0543 void       iput(struct inode*);       0593       mpstartthem(void);         0545 void       iunlock(struct inode*);       0594 // picirg.c         0546 void       iupdate(struct inode*);       0595 void       picenable(int);         0546 void       iupdate(struct inode*);       0596 void       picinit(void);         0548 struct inode*       namei(char*);       0598				microderay (inc) /
0533 int       filewrite(struct file*, char*, int n);       0583 void       initlog(int dev);         0534       0584 void       log_write(struct buf*);         0535 // fs.c       0585 void       begin_op();         0536 void       readsb(int dev, struct superblock *sb);       0586 void       end_op();         0537 int       dirlink(struct inode*, char*, uint);       0587       end_op();         0538 struct inode*       dirlookup(struct inode*, char*, uint*);       0588 // mp.c       ismp;         0539 struct inode*       ialloc(uint, short);       0589 extern int       ismp;         0540 struct inode*       idup(struct inode*);       0591 int       mpbcpu(void);         0541 void       ilock(struct inode*);       0592 void       mpstartthem(void);         0543 void       iput(struct inode*);       0593         0544 void       iunlock(struct inode*);       0594 // picirq.c         0545 void       iunlockput(struct inode*);       0595 void       picenable(int);         0546 void       iupdate(struct inode*);       0596 void       picinit(void);         0548 struct inode*       namei(char*);       0598				
0534       0584 void       log_write(struct buf*);         0535 // fs.c       0585 void       begin_op();         0536 void       readsb(int dev, struct superblock *sb);       0586 void       end_op();         0537 int       dirlokup(struct inode*, char*, uint);       0587         0538 struct inode*       dirlookup(struct inode*, char*, uint*);       0588 // mp.c         0539 struct inode*       ialloc(uint, short);       0589 extern int       ismp;         0540 struct inode*       idup(struct inode*);       0590 int       mpbcpu(void);         0541 void       iinit(int dev);       0591 void       mpinit(void);         0542 void       ilock(struct inode*);       0592 void       mpstartthem(void);         0543 void       iput(struct inode*);       0593       picenable(int);         0545 void       iunlockput(struct inode*);       0595 void       picenable(int);         0546 void       iupdate(struct inode*);       0595 void       picinit(void);         0547 int       namecup(const char*, const char*);       0597         0548 struct inode*       namei(char*);       0598			_	initlog(int dev):
0535 // fs.c       0585 void       begin_op();         0536 void       readsb(int dev, struct superblock *sb);       0586 void       end_op();         0537 int       dirlink(struct inode*, char*, uint);       0587         0538 struct inode*       dirlookup(struct inode*, char*, uint*);       0588 // mp.c         0539 struct inode*       ialloc(uint, short);       0589 extern int       ismp;         0540 struct inode*       idup(struct inode*);       0590 int       mpbcpu(void);         0541 void       iinit(int dev);       0591 void       mpinit(void);         0542 void       ilock(struct inode*);       0592 void       mpstartthem(void);         0543 void       iuplock(struct inode*);       0593         0544 void       iunlock(struct inode*);       0594 // picirq.c         0545 void       iupdate(struct inode*);       0595 void       picenable(int);         0546 void       iupdate(struct inode*);       0596 void       picinit(void);         0547 int       namecmp(const char*, const char*);       0597         0548 struct inode*       namei(char*);       0598		THEWHILE BELLE THE TOTAL THE HAT		=
0536 void         readsb(int dev, struct superblock *sb);         0586 void         end_op();           0537 int         dirlink(struct inode*, char*, uint);         0587           0538 struct inode*         dirlookup(struct inode*, char*, uint*);         0588 // mp.c           0539 struct inode*         ialloc(uint, short);         0589 extern int         ismp;           0540 struct inode*         idup(struct inode*);         0590 int         mpbcpu(void);           0541 void         iinit(int dev);         0591 void         mpinit(void);           0542 void         ilock(struct inode*);         0592 void         mpstartthem(void);           0543 void         iput(struct inode*);         0593           0544 void         iunlock(struct inode*);         0594 // picirq.c           0545 void         iunlockput(struct inode*);         0595 void         picenable(int);           0546 void         iupdate(struct inode*);         0596 void         picinit(void);           0547 int         namecmp(const char*, const char*);         0598				
0537 int       dirlink(struct inode*, char*, uint);       0587         0538 struct inode*       dirlookup(struct inode*, char*, uint*);       0588 // mp.c         0539 struct inode*       ialloc(uint, short);       0589 extern int       ismp;         0540 struct inode*       idup(struct inode*);       0590 int       mpbcpu(void);         0541 void       iinit(int dev);       0591 void       mpinit(void);         0542 void       ilock(struct inode*);       0592 void       mpstartthem(void);         0543 void       iput(struct inode*);       0593         0544 void       iunlock(struct inode*);       0594 // picirq.c         0545 void       iunlockput(struct inode*);       0595 void       picenable(int);         0546 void       iupdate(struct inode*);       0596 void       picinit(void);         0547 int       namecmp(const char*, const char*);       0597         0548 struct inode*       namei(char*);       0598		readsh(int dev_struct superblock *sh);		· · ·
0538 struct inode*       dirlookup(struct inode*, char*, uint*);       0588 // mp.c         0539 struct inode*       ialloc(uint, short);       0589 extern int       ismp;         0540 struct inode*       idup(struct inode*);       0590 int       mpbcpu(void);         0541 void       iinit(int dev);       0591 void       mpinit(void);         0542 void       ilock(struct inode*);       0592 void       mpstartthem(void);         0543 void       iput(struct inode*);       0593         0544 void       iunlock(struct inode*);       0594 // picirq.c         0545 void       iunlockput(struct inode*);       0595 void       picenable(int);         0546 void       iupdate(struct inode*);       0596 void       picinit(void);         0547 int       namecmp(const char*, const char*);       0597         0548 struct inode*       namei(char*);       0598				GIA_0P(),
0539 struct inode*       ialloc(uint, short);       0589 extern int       ismp;         0540 struct inode*       idup(struct inode*);       0590 int       mpbcpu(void);         0541 void       iinit(int dev);       0591 void       mpinit(void);         0542 void       ilock(struct inode*);       0592 void       mpstartthem(void);         0543 void       iput(struct inode*);       0593         0544 void       iunlock(struct inode*);       0594 // picirq.c         0545 void       iunlockput(struct inode*);       0595 void       picenable(int);         0546 void       iupdate(struct inode*);       0596 void       picinit(void);         0547 int       namecmp(const char*, const char*);       0597         0548 struct inode*       namei(char*);       0598				
0540 struct inode*       idup(struct inode*);       0590 int       mpbcpu(void);         0541 void       iinit(int dev);       0591 void       mpinit(void);         0542 void       ilock(struct inode*);       0592 void       mpstartthem(void);         0543 void       iput(struct inode*);       0593         0544 void       iunlock(struct inode*);       0594 // picirq.c         0545 void       iunlockput(struct inode*);       0595 void       picenable(int);         0546 void       iupdate(struct inode*);       0596 void       picinit(void);         0547 int       namecmp(const char*, const char*);       0597         0548 struct inode*       namei(char*);       0598			-	ismp;
0541 void       iinit(int dev);       0591 void       mpinit(void);         0542 void       ilock(struct inode*);       0592 void       mpstartthem(void);         0543 void       iput(struct inode*);       0593         0544 void       iunlock(struct inode*);       0594 // picirq.c         0545 void       iunlockput(struct inode*);       0595 void       picenable(int);         0546 void       iupdate(struct inode*);       0596 void       picinit(void);         0547 int       namecmp(const char*, const char*);       0597         0548 struct inode*       namei(char*);       0598				-
0542 void       ilock(struct inode*);       0592 void       mpstartthem(void);         0543 void       iput(struct inode*);       0593         0544 void       iunlock(struct inode*);       0594 // picirq.c         0545 void       iunlockput(struct inode*);       0595 void       picenable(int);         0546 void       iupdate(struct inode*);       0596 void       picinit(void);         0547 int       namecmp(const char*, const char*);       0597         0548 struct inode*       namei(char*);       0598				
0543 void       iput(struct inode*);       0593         0544 void       iunlock(struct inode*);       0594 // picirq.c         0545 void       iunlockput(struct inode*);       0595 void       picenable(int);         0546 void       iupdate(struct inode*);       0596 void       picinit(void);         0547 int       namecmp(const char*, const char*);       0597         0548 struct inode*       namei(char*);       0598				=
0544 void iunlock(struct inode*); 0594 // picirq.c 0545 void iunlockput(struct inode*); 0595 void picenable(int); 0546 void iupdate(struct inode*); 0596 void picinit(void); 0547 int namecmp(const char*, const char*); 0597 0548 struct inode* namei(char*); 0598	0543 void			
0546 void iupdate(struct inode*); 0596 void picinit(void); 0547 int namecmp(const char*, const char*); 0597 0548 struct inode* namei(char*); 0598			0594 // picirg.c	
0546 void iupdate(struct inode*); 0596 void picinit(void); 0547 int namecmp(const char*, const char*); 0597 0548 struct inode* namei(char*); 0598	0545 void	<pre>iunlockput(struct inode*);</pre>	0595 void	<pre>picenable(int);</pre>
0547 int namecmp(const char*, const char*); 0597 0548 struct inode* namei(char*); 0598				
0548 struct inode* namei(char*); 0598	0547 int			
0549 struct inode* nameiparent(char*, char*); 0599	0548 struct inode*		0598	
	0549 struct inode*	<pre>nameiparent(char*, char*);</pre>	0599	

Sheet 05 Sheet 05

0600 // pipe.c		0650 void	<pre>syscall(void);</pre>
0601 int	<pre>pipealloc(struct file**, struct file**);</pre>	0651	
0602 void	<pre>pipeclose(struct pipe*, int);</pre>	0652 // timer.c	
0603 int	<pre>piperead(struct pipe*, char*, int);</pre>	0653 void	<pre>timerinit(void);</pre>
0604 int	<pre>pipewrite(struct pipe*, char*, int);</pre>	0654	
0605		0655 // trap.c	
0606		0656 void	<pre>idtinit(void);</pre>
0607 // proc.c		0657 extern uint	ticks;
0608 struct proc*	<pre>copyproc(struct proc*);</pre>	0658 void	tvinit(void);
0609 void	exit(void);	0659 extern struct	spinlock tickslock;
0610 int	<pre>fork(void);</pre>	0660	
0611 int	<pre>growproc(int);</pre>	0661 // uart.c	
0612 int	kill(int);	0662 void	<pre>uartinit(void);</pre>
0613 void	<pre>pinit(void);</pre>	0663 void	<pre>uartintr(void);</pre>
0614 void	procdump(void);	0664 void	<pre>uartputc(int);</pre>
0615 void	<pre>scheduler(void)attribute((noreturn));</pre>	0665	•
0616 void	sched(void);	0666 // vm.c	
0617 void	<pre>sleep(void*, struct spinlock*);</pre>	0667 void	seginit(void);
0618 void	userinit(void);	0668 void	kvmalloc(void);
0619 int	<pre>wait(void);</pre>	0669 void	vmenable(void);
0620 void	<pre>wakeup(void*);</pre>	0670 pde_t*	setupkvm(void);
0621 void	yield(void);	0671 char*	uva2ka(pde_t*, char*);
0622	/ ICIA(VOIA) /	0672 int	allocuvm(pde_t*, uint, uint);
0623 // swtch.S		0673 int	<pre>dailocuvm(pde_t*, uint, uint);</pre>
0624 void	<pre>swtch(struct context**, struct context*);</pre>	0674 void	freevm(pde_t*);
0625	Switch(Struct Context , Struct Context )/	0675 void	inituvm(pde_t*, char*, uint);
0626 // spinlock.c		0676 int	loaduvm(pde_t*, char*, struct inode*, uint, uint);
0627 void	agging (at must spinlessist):		
	acquire(struct spinlock*);	0677 pde_t*	copyuvm(pde_t*, uint);
0628 void	<pre>getcallerpcs(void*, uint*); holding(struct animlessk*);</pre>	0678 void	<pre>switchuvm(struct proc*);</pre>
0629 int	holding(struct spinlock*);	0679 void	switchkvm(void);
0630 void	<pre>initlock(struct spinlock*, char*);</pre>	0680 int	<pre>copyout(pde_t*, uint, void*, uint);</pre>
0631 void	release(struct spinlock*);	0681 void	<pre>clearpteu(pde_t *pgdir, char *uva);</pre>
0632 void	<pre>pushcli(void);</pre>	0682	
0633 void	<pre>popcli(void);</pre>		elements in fixed-size array
0634			(x) (sizeof(x)/sizeof((x)[0]))
0635 // string.c		0685	
0636 int	<pre>memcmp(const void*, const void*, uint);</pre>	0686	
0637 void*	<pre>memmove(void*, const void*, uint);</pre>	0687	
0638 void*	<pre>memset(void*, int, uint);</pre>	0688	
0639 char*	<pre>safestrcpy(char*, const char*, int);</pre>	0689	
0640 int	strlen(const char*);	0690	
0641 int	strncmp(const char*, const char*, uint);	0691	
0642 char*	strncpy(char*, const char*, int);	0692	
0643		0693	
0644 // syscall.c		0694	
0645 int	<pre>argint(int, int*);</pre>	0695	
0646 int	<pre>argptr(int, char**, int);</pre>	0696	
0647 int	<pre>argstr(int, char**);</pre>	0697	
0648 int	<pre>fetchint(uint, int*);</pre>	0698	
0649 int	<pre>fetchstr(uint, char**);</pre>	0699	

Sheet 06 Sheet 06

```
0700 // Routines to let C code use special x86 instructions.
                                                                               0750 static inline void
0701
                                                                               0751 stosl(void *addr, int data, int cnt)
0702 static inline uchar
0703 inb(ushort port)
                                                                               0753 asm volatile("cld; rep stosl" :
0704 {
                                                                               0754
                                                                                                  "=D" (addr), "=c" (cnt) :
0705 uchar data;
                                                                               0755
                                                                                                  "0" (addr), "1" (cnt), "a" (data) :
0706
                                                                               0756
                                                                                                  "memory", "cc");
0707 asm volatile("in %1,%0" : "=a" (data) : "d" (port));
                                                                               0757 }
0708 return data;
                                                                               0758
0709 }
                                                                               0759 struct segdesc;
0710
                                                                               0760
0711 static inline void
                                                                               0761 static inline void
0712 insl(int port, void *addr, int cnt)
                                                                               0762 lgdt(struct segdesc *p, int size)
0713 {
                                                                               0763 {
0714 asm volatile("cld; rep insl":
                                                                               0764 volatile ushort pd[3];
0715
                 "=D" (addr), "=c" (cnt) :
                                                                               0765
                                                                               0766 pd[0] = size-1;
0716
                  "d" (port), "0" (addr), "1" (cnt) :
0717
                 "memory", "cc");
                                                                               0767 \text{ pd}[1] = (uint)p;
                                                                               0768 pd[2] = (uint)p >> 16;
0718 }
0719
                                                                               0769
0720 static inline void
                                                                               0770 asm volatile("lgdt (%0)" : : "r" (pd));
0721 outb(ushort port, uchar data)
                                                                               0771 }
0722 {
                                                                               0772
0723 asm volatile("out %0,%1" : : "a" (data), "d" (port));
                                                                               0773 struct gatedesc;
0724 }
                                                                               0774
0725
                                                                               0775 static inline void
0726 static inline void
                                                                               0776 lidt(struct gatedesc *p, int size)
0727 outw(ushort port, ushort data)
                                                                               0777 {
                                                                               0778 volatile ushort pd[3];
0729 asm volatile("out %0,%1" : : "a" (data), "d" (port));
                                                                               0779
0730 }
                                                                               0780 pd[0] = size-1;
0731
                                                                               0781 pd[1] = (uint)p;
0732 static inline void
                                                                               0782 	 pd[2] = (uint)p >> 16;
0733 outsl(int port, const void *addr, int cnt)
                                                                               0783
0734 {
                                                                               0784 asm volatile("lidt (%0)" : : "r" (pd));
0735 asm volatile("cld; rep outsl" :
                                                                               0785 }
                   "=S" (addr), "=c" (cnt) :
0736
                                                                               0786
                   "d" (port), "0" (addr), "1" (cnt) :
                                                                               0787 static inline void
0737
0738
                   "cc");
                                                                               0788 ltr(ushort sel)
0739 }
                                                                               0789 {
0740
                                                                               0790 asm volatile("ltr %0" : : "r" (sel));
0741 static inline void
                                                                               0791 }
0742 stosb(void *addr, int data, int cnt)
                                                                               0792
                                                                               0793 static inline uint
0743 {
0744 asm volatile("cld; rep stosb" :
                                                                               0794 readeflags(void)
0745
                   "=D" (addr), "=c" (cnt) :
                                                                               0795 {
                   "0" (addr), "1" (cnt), "a" (data) :
0746
                                                                               0796 uint eflags;
0747
                  "memory", "cc");
                                                                               0797 asm volatile("pushfl; popl %0" : "=r" (eflags));
0748 }
                                                                               0798 return eflags;
0749
                                                                               0799 }
```

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Sheet 08 Sheet 08

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```
0950 // This file contains definitions for the
0900 //
0901 // assembler macros to create x86 segments
                                                                                  0951 // x86 memory management unit (MMU).
0902 //
                                                                                  0952
0903
                                                                                  0953 // Eflags register
0904 #define SEG_NULLASM
                                                                                  0954 #define FL_CF
                                                                                                               0x0000001
                                                                                                                               // Carry Flag
             .word 0, 0;
                                                                                  0955 #define FL PF
                                                                                                               0x00000004
                                                                                                                               // Parity Flag
0906
             .byte 0, 0, 0, 0
                                                                                  0956 #define FL_AF
                                                                                                               0x00000010
                                                                                                                               // Auxiliary carry Flag
0907
                                                                                  0957 #define FL_ZF
                                                                                                               0x00000040
                                                                                                                               // Zero Flag
0908 // The 0xC0 means the limit is in 4096-byte units
                                                                                  0958 #define FL SF
                                                                                                               0x00000080
                                                                                                                               // Sign Flag
                                                                                  0959 #define FL_TF
                                                                                                                                // Trap Flag
0909 // and (for executable segments) 32-bit mode.
                                                                                                               0x00000100
0910 #define SEG_ASM(type,base,lim)
                                                                                  0960 #define FL_IF
                                                                                                               0x00000200
                                                                                                                               // Interrupt Enable
             .word (((lim) >> 12) & 0xffff), ((base) & 0xffff);
                                                                                                                               // Direction Flag
0911
                                                                                  0961 #define FL_DF
                                                                                                               0x00000400
0912
             .byte (((base) >> 16) & 0xff), (0x90 | (type)),
                                                                                  0962 #define FL_OF
                                                                                                               0x00000800
                                                                                                                               // Overflow Flag
0913
                     (0xC0 | (((lim) >> 28) & 0xf)), (((base) >> 24) & 0xff)
                                                                                  0963 #define FL_IOPL_MASK
                                                                                                               0x00003000
                                                                                                                               // I/O Privilege Level bitmask
0914
                                                                                                                               // IOPL == 0
                                                                                  0964 #define FL IOPL 0
                                                                                                               0x00000000
0915 #define STA_X
                       0x8
                                 // Executable segment
                                                                                  0965 #define FL_IOPL_1
                                                                                                                               // IOPL == 1
                                                                                                               0x00001000
0916 #define STA E
                       0x4
                                 // Expand down (non-executable segments)
                                                                                  0966 #define FL IOPL 2
                                                                                                               0x00002000
                                                                                                                               // IOPL == 2
0917 #define STA C
                       0x4
                                 // Conforming code segment (executable only)
                                                                                  0967 #define FL IOPL 3
                                                                                                               0x00003000
                                                                                                                               // IOPL == 3
0918 #define STA_W
                       0x2
                                 // Writeable (non-executable segments)
                                                                                  0968 #define FL_NT
                                                                                                               0x00004000
                                                                                                                               // Nested Task
0919 #define STA R
                       0x2
                                 // Readable (executable segments)
                                                                                  0969 #define FL RF
                                                                                                               0x00010000
                                                                                                                               // Resume Flag
0920 #define STA_A
                       0x1
                                 // Accessed
                                                                                  0970 #define FL VM
                                                                                                               0x00020000
                                                                                                                               // Virtual 8086 mode
0921
                                                                                  0971 #define FL AC
                                                                                                               0x00040000
                                                                                                                               // Alignment Check
0922
                                                                                  0972 #define FL VIF
                                                                                                               0x00080000
                                                                                                                               // Virtual Interrupt Flag
0923
                                                                                  0973 #define FL_VIP
                                                                                                               0x00100000
                                                                                                                               // Virtual Interrupt Pending
0924
                                                                                  0974 #define FL ID
                                                                                                                               // ID flag
                                                                                                               0x00200000
0925
                                                                                  0975
0926
                                                                                  0976 // Control Register flags
0927
                                                                                  0977 #define CRO_PE
                                                                                                                                // Protection Enable
                                                                                                               0x00000001
0928
                                                                                  0978 #define CR0 MP
                                                                                                               0x00000002
                                                                                                                                // Monitor coProcessor
0929
                                                                                  0979 #define CRO_EM
                                                                                                               0x00000004
                                                                                                                               // Emulation
0930
                                                                                  0980 #define CRO_TS
                                                                                                               0x00000008
                                                                                                                               // Task Switched
0931
                                                                                  0981 #define CR0 ET
                                                                                                               0x00000010
                                                                                                                               // Extension Type
0932
                                                                                                                               // Numeric Errror
                                                                                  0982 #define CRO_NE
                                                                                                               0x00000020
0933
                                                                                  0983 #define CRO_WP
                                                                                                               0x00010000
                                                                                                                               // Write Protect
0934
                                                                                  0984 #define CRO AM
                                                                                                               0x00040000
                                                                                                                               // Alignment Mask
0935
                                                                                                                               // Not Writethrough
                                                                                  0985 #define CRO_NW
                                                                                                               0x20000000
0936
                                                                                  0986 #define CRO_CD
                                                                                                               0x40000000
                                                                                                                               // Cache Disable
0937
                                                                                  0987 #define CRO PG
                                                                                                               0x80000000
                                                                                                                               // Paging
0938
                                                                                  0988
0939
                                                                                  0989 #define CR4 PSE
                                                                                                               0x00000010
                                                                                                                               // Page size extension
0940
                                                                                  0990
                                                                                  0991 #define SEG_KCODE 1 // kernel code
0941
0942
                                                                                  0992 #define SEG KDATA 2 // kernel data+stack
0943
                                                                                  0993 #define SEG KCPU 3 // kernel per-cpu data
0944
                                                                                  0994 #define SEG_UCODE 4 // user code
0945
                                                                                  0995 #define SEG UDATA 5 // user data+stack
0946
                                                                                  0996 #define SEG TSS 6 // this process's task state
0947
                                                                                  0997
0948
                                                                                  0998
                                                                                  0999
0949
```

Sheet 09 Sheet 09

```
1000 #ifndef __ASSEMBLER__
                                                                          1001 // Segment Descriptor
                                                                          1002 struct segdesc {
                                                                          1052
1003 uint lim_15_0 : 16; // Low bits of segment limit
                                                                          1053 // A virtual address 'la' has a three-part structure as follows:
1004 uint base_15_0 : 16; // Low bits of segment base address
                                                                          1055 // +-----10-----+
1005 uint base_23_16 : 8; // Middle bits of segment base address
                                                                          1056 // | Page Directory | Page Table | Offset within Page
1006 uint type : 4; // Segment type (see STS_ constants)
                                                                          1057 // Index Index
                      // 0 = system, 1 = application
1007 uint s : 1;
1008 uint dpl : 2;
                    // Descriptor Privilege Level
                                                                          1058 // +------
1009 uint p : 1; // Present
                                                                          1059 // \--- PDX(va) --/ \--- PTX(va) --/
1010 uint lim_19_16 : 4; // High bits of segment limit
                                                                          1060
1011 uint avl : 1; // Unused (available for software use)
                                                                          1061 // page directory index
1012 uint rsv1 : 1;
                        // Reserved
                                                                          1062 #define PDX(va)
                                                                                                  (((uint)(va) >> PDXSHIFT) & 0x3FF)
1013 uint db : 1; // 0 = 16-bit segment, 1 = 32-bit segment 1014 uint g : 1; // Granularity: limit scaled by 4K when set
                                                                          1063
                                                                          1064 // page table index
1015 uint base_31_24 : 8; // High bits of segment base address
                                                                          1065 #define PTX(va)
                                                                                                    (((uint)(va) >> PTXSHIFT) & 0x3FF)
1016 };
                                                                          1066
1017
                                                                          1067 // construct virtual address from indexes and offset
                                                                          1068 #define PGADDR(d, t, o) ((uint)((d) << PDXSHIFT | (t) << PTXSHIFT | (o)))
1018 // Normal segment
1019 #define SEG(type, base, lim, dpl) (struct segdesc)
                                                                          1069
                                                                    1070 // Page directory and
1071 #define NPDENTRIES
1072 #define NPTENTRIES
1073 #define PGSIZE
1074
1075 #define PGSHIFT
1076 #define PTXSHIFT
1020 { ((lim) >> 12) & 0xffff, (uint)(base) & 0xffff,
                                                                          1070 // Page directory and page table constants.
1021 ((uint)(base) >> 16) & 0xff, type, 1, dpl, 1,
                                                                                                 1024 // # directory entries per page directory
1022 (uint)(lim) >> 28, 0, 0, 1, 1, (uint)(base) >> 24 }
                                                                                                    1024 // # PTEs per page table
1023 #define SEG16(type, base, lim, dpl) (struct segdesc) \
                                                                                                   4096 // bytes mapped by a page
1024 { (lim) & 0xffff, (uint)(base) & 0xffff,
1025 ((uint)(base) >> 16) & 0xff, type, 1, dpl, 1,
                                                                                                    12 // log2(PGSIZE)
1026 (uint)(lim) >> 16, 0, 0, 1, 0, (uint)(base) >> 24 }
                                                                          1076 #define PTXSHIFT
                                                                                                   12 // offset of PTX in a linear address
1027 #endif
                                                                          1077 #define PDXSHIFT
                                                                                                    22 // offset of PDX in a linear address
1028
1029 #define DPL_USER 0x3 // User DPL
                                                                          1079 #define PGROUNDUP(sz) (((sz)+PGSIZE-1) & ~(PGSIZE-1))
1030
                                                                          1080 #define PGROUNDDOWN(a) (((a)) & ~(PGSIZE-1))
1031 // Application segment type bits
                      0x8 // Executable segment
1032 #define STA_X
                                                                          1082 // Page table/directory entry flags.
1033 #define STA_E
                      0x4 // Expand down (non-executable segments)
                                                                          1083 #define PTE_P
                                                                                             0x001 // Present
                      0x4 // Conforming code segment (executable only)
1034 #define STA C
                                                                          1084 #define PTE W
                                                                                                    0x002 // Writeable
                      0x2 // Writeable (non-executable segments)
                                                                          1085 #define PTE_U
                                                                                                   0x004 // User
1035 #define STA_W
                      0x2 // Readable (executable segments)
1036 #define STA_R
                                                                          1086 #define PTE_PWT
                                                                                                   0x008 // Write-Through
1037 #define STA A
                      0x1 // Accessed
                                                                          1087 #define PTE PCD
                                                                                                    0x010 // Cache-Disable
1038
                                                                          1088 #define PTE_A
                                                                                                    0x020 // Accessed
1039 // System segment type bits
                                                                          1089 #define PTE_D
                                                                                                    0x040 // Dirty
1040 #define STS T16A 0x1 // Available 16-bit TSS
                                                                          1090 #define PTE PS
                                                                                                   0x080 // Page Size
                                                                          1091 #define PTE MBZ
1041 #define STS_LDT
                      0x2
                             // Local Descriptor Table
                                                                                                   0x180 // Bits must be zero
1042 #define STS_T16B 0x3
                             // Busy 16-bit TSS
                                                                          1092
1043 #define STS CG16
                      0x4
                             // 16-bit Call Gate
                                                                          1093 // Address in page table or page directory entry
                                                                          1094 #define PTE_ADDR(pte) ((uint)(pte) & ~0xFFF)
1044 #define STS_TG
                      0x5
                             // Task Gate / Coum Transmitions
1045 #define STS IG16
                             // 16-bit Interrupt Gate
                                                                          1095 #define PTE FLAGS(pte) ((uint)(pte) & 0xFFF)
                      0x6
1046 #define STS TG16
                      0x7
                             // 16-bit Trap Gate
                                                                          1096
                      0x9
                             // Available 32-bit TSS
                                                                          1097 #ifndef __ASSEMBLER__
1047 #define STS_T32A
1048 #define STS T32B 0xB // Busy 32-bit TSS
                                                                          1098 typedef uint pte t;
1049 #define STS_CG32 0xC // 32-bit Call Gate
                                                                          1099
```

Sheet 10 Sheet 10

```
1100 // Task state segment format
                                                                              1150 // Gate descriptors for interrupts and traps
1101 struct taskstate {
                                                                              1151 struct gatedesc {
1102 uint link;
                        // Old ts selector
                                                                              1152 uint off 15 0 : 16; // low 16 bits of offset in segment
                                                                              1153 uint cs : 16;
1103 uint esp0;
                        // Stack pointers and segment selectors
                                                                                                         // code segment selector
1104 ushort ss0;
                        // after an increase in privilege level
                                                                              1154 uint args : 5;
                                                                                                         // # args, 0 for interrupt/trap gates
1105 ushort padding1;
                                                                              1155 uint rsv1 : 3;
                                                                                                         // reserved(should be zero I quess)
1106 uint *esp1;
                                                                              1156 uint type : 4;
                                                                                                         // type(STS_{TG, IG32, TG32})
1107 ushort ss1;
                                                                              1157 uint s : 1;
                                                                                                         // must be 0 (system)
1108 ushort padding2;
                                                                              1158 uint dpl : 2;
                                                                                                         // descriptor(meaning new) privilege level
1109 uint *esp2;
                                                                              1159 uint p : 1;
                                                                                                         // Present
1110 ushort ss2;
                                                                              1160 uint off_31_16 : 16; // high bits of offset in segment
1111 ushort padding3;
                                                                              1161 };
1112 void *cr3;
                                                                              1162
                        // Page directory base
1113 uint *eip;
                        // Saved state from last task switch
                                                                              1163 // Set up a normal interrupt/trap gate descriptor.
                                                                              1164 // - istrap: 1 for a trap (= exception) gate, 0 for an interrupt gate.
1114 uint eflags;
1115 uint eax;
                        // More saved state (registers)
                                                                              1165 // interrupt gate clears FL_IF, trap gate leaves FL_IF alone
1116 uint ecx;
                                                                              1166 // - sel: Code segment selector for interrupt/trap handler
1117 uint edx;
                                                                              1167 // - off: Offset in code segment for interrupt/trap handler
1118 uint ebx;
                                                                              1168 // - dpl: Descriptor Privilege Level -
1119 uint *esp;
                                                                              1169 //
                                                                                            the privilege level required for software to invoke
1120 uint *ebp;
                                                                                            this interrupt/trap gate explicitly using an int instruction.
1121 uint esi;
                                                                              1171 #define SETGATE(gate, istrap, sel, off, d)
1122 uint edi;
                                                                              1172 {
1123 ushort es;
                        // Even more saved state (segment selectors)
                                                                              1173 (gate).off_15_0 = (uint)(off) & 0xffff;
1124 ushort padding4;
                                                                              1174 (gate).cs = (sel);
1125 ushort cs;
                                                                              1175 (gate).args = 0;
1126 ushort padding5;
                                                                              1176 (gate).rsv1 = 0;
1127 ushort ss;
                                                                                    (gate).type = (istrap) ? STS_TG32 : STS_IG32;
                                                                              1177
1128 ushort padding6;
                                                                              1178
                                                                                    (qate).s = 0;
1129 ushort ds;
                                                                              1179 (gate).dpl = (d);
1130 ushort padding7;
                                                                              1180 (gate).p = 1;
1131 ushort fs;
                                                                              1181
                                                                                    (gate).off_31_16 = (uint)(off) >> 16;
1132 ushort padding8;
                                                                              1182 }
1133 ushort gs;
                                                                              1183
1134 ushort padding9;
                                                                              1184 #endif
1135 ushort ldt;
                                                                              1185
1136 ushort padding10;
                                                                              1186
1137 ushort t;
                        // Trap on task switch
                                                                              1187
1138 ushort iomb;
                        // I/O map base address
                                                                              1188
1139 };
                                                                              1189
1140
                                                                              1190
1141
                                                                              1191
1142
                                                                              1192
1143
                                                                              1193
1144
                                                                              1194
1145
                                                                              1195
1146
                                                                              1196
1147
                                                                              1197
1148
                                                                              1198
                                                                              1199
1149
```

Sheet 11 Sheet 11

```
1200 // Format of an ELF executable file
                                                                              1250 # Multiboot header, for multiboot boot loaders like GNU Grub.
                                                                              1251 # http://www.gnu.org/software/grub/manual/multiboot/multiboot.html
1202 #define ELF_MAGIC 0x464C457FU // "\x7FELF" in little endian
                                                                              1252 #
1203
                                                                              1253 # Using GRUB 2, you can boot xv6 from a file stored in a
1204 // File header
                                                                              1254 # Linux file system by copying kernel or kernelmemfs to /boot
1205 struct elfhdr {
                                                                              1255 # and then adding this menu entry:
1206 uint magic; // must equal ELF_MAGIC
                                                                              1256 #
1207 uchar elf[12];
                                                                              1257 # menuentry "xv6" {
1208 ushort type;
                                                                              1258 # insmod ext2
1209 ushort machine;
                                                                              1259 # set root='(hd0.msdos1)'
1210 uint version;
                                                                              1260 # set kernel='/boot/kernel'
1211 uint entry;
                                                                              1261 # echo "Loading ${kernel}..."
1212 uint phoff;
                                                                              1262 # multiboot ${kernel} ${kernel}
1213 uint shoff;
                                                                              1263 # boot
1214 uint flags;
                                                                              1264 # }
1215 ushort ehsize;
                                                                              1265
1216 ushort phentsize;
                                                                              1266 #include "asm.h"
1217 ushort phnum;
                                                                              1267 #include "memlayout.h"
1218 ushort shentsize;
                                                                              1268 #include "mmu.h"
1219 ushort shnum;
                                                                              1269 #include "param.h"
1220 ushort shstrndx;
                                                                              1270
1221 };
                                                                              1271 # Multiboot header. Data to direct multiboot loader.
1222
                                                                              1272 .p2align 2
1223 // Program section header
                                                                              1273 .text
1224 struct proghdr {
                                                                              1274 .globl multiboot_header
1225 uint type;
                                                                              1275 multiboot header:
1226 uint off;
                                                                              1276 #define magic 0x1badb002
1227 uint vaddr;
                                                                              1277 #define flags 0
1228 uint paddr;
                                                                              1278 .long magic
1229 uint filesz;
                                                                              1279 .long flags
1230 uint memsz;
                                                                              1280 .long (-magic-flags)
1231 uint flags;
                                                                              1281
                                                                              1282 # By convention, the _start symbol specifies the ELF entry point.
1232 uint align;
1233 };
                                                                              1283 # Since we haven't set up virtual memory yet, our entry point is
                                                                              1284 # the physical address of 'entry'.
1234
1235 // Values for Proghdr type
                                                                              1285 .globl _start
1236 #define ELF_PROG_LOAD
                                                                              1286 _start = V2P_WO(entry)
1237
1238 // Flag bits for Proghdr flags
                                                                              1288 # Entering xv6 on boot processor, with paging off.
1239 #define ELF_PROG_FLAG_EXEC
                                   1
                                                                              1289 .globl entry
1240 #define ELF PROG FLAG WRITE
                                                                              1290 entry:
1241 #define ELF_PROG_FLAG_READ
                                                                              1291 # Turn on page size extension for 4Mbyte pages
1242
                                                                              1292 movl %cr4, %eax
1243
                                                                              1293 orl
                                                                                             $(CR4 PSE), %eax
1244
                                                                              1294 movl %eax, %cr4
1245
                                                                              1295 # Set page directory
1246
                                                                              1296 movl $(V2P WO(entrypgdir)), %eax
1247
                                                                              1297 movl
                                                                                            %eax, %cr3
1248
                                                                              1298 # Turn on paging.
1249
                                                                              1299 movl %cr0, %eax
```

```
1300 orl
              $(CR0_PG|CR0_WP), %eax
1301
      movl
              %eax, %cr0
1302
1303 # Set up the stack pointer.
1304 movl $(stack + KSTACKSIZE), %esp
1305
1306 # Jump to main(), and switch to executing at
1307 # high addresses. The indirect call is needed because
1308 # the assembler produces a PC-relative instruction
1309 # for a direct jump.
1310 mov $main, %eax
1311 jmp *%eax
1312
1313 .comm stack, KSTACKSIZE
1314
1315
1316
1317
1318
1319
1320
1321
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```

```
1350 #include "asm.h"
1351 #include "memlayout.h"
1352 #include "mmu.h"
1353
1354 # Each non-boot CPU ("AP") is started up in response to a STARTUP
1355 # IPI from the boot CPU. Section B.4.2 of the Multi-Processor
1356 # Specification says that the AP will start in real mode with CS:IP
1357 # set to XY00:0000, where XY is an 8-bit value sent with the
1358 # STARTUP. Thus this code must start at a 4096-byte boundary.
1359 #
1360 # Because this code sets DS to zero, it must sit
1361 # at an address in the low 2^16 bytes.
1362 #
1363 # Startothers (in main.c) sends the STARTUPs one at a time.
1364 # It copies this code (start) at 0x7000. It puts the address of
1365 # a newly allocated per-core stack in start-4, the address of the
1366 # place to jump to (mpenter) in start-8, and the physical address
1367 # of entrypgdir in start-12.
1368 #
1369 # This code is identical to bootasm.S except:
1370 # - it does not need to enable A20
1371 # - it uses the address at start-4, start-8, and start-12
1372
1373 .code16
1374 .globl start
1375 start:
1376 cli
1377
1378 xorw
               %ax,%ax
1379 movw
               %ax,%ds
1380 movw
               %ax,%es
1381 movw
               %ax,%ss
1382
1383 lgdt
               qdtdesc
1384
      movl
               %cr0. %eax
1385 orl
               $CRO_PE, %eax
1386
      movl
               %eax, %cr0
1387
1388
1389
1390
1391
1392
1393
1394
1395
1396
1397
1398
1399
```

Sheet 13 Sheet 13

```
1400 limpl
              $(SEG_KCODE << 3), $(start 32)
1401
1402 .code32
1403 start32:
1404 movw
             $(SEG_KDATA<<3), %ax
1405 movw
             %ax, %ds
1406 movw
             %ax, %es
1407 movw
             %ax, %ss
1408 movw
             $0, %ax
1409 movw
             %ax, %fs
1410 movw
             %ax, %gs
1411
1412 # Turn on page size extension for 4Mbyte pages
1413 movl %cr4, %eax
1414 orl
             $(CR4 PSE), %eax
1415 movl %eax, %cr4
1416 # Use enterpgdir as our initial page table
1417 movl (start-12), %eax
1418 movl %eax, %cr3
1419 # Turn on paging.
1420 movl %cr0, %eax
1421 orl
             $(CRO_PE|CRO_PG|CRO_WP), %eax
1422 movl %eax, %cr0
1423
1424 # Switch to the stack allocated by startothers()
1425 movl (start-4), %esp
1426 # Call mpenter()
1427 call
              *(start-8)
1428
1429 movw
             $0x8a00, %ax
1430 movw
             %ax, %dx
             %ax. %dx
1431 outw
1432 movw
             $0x8ae0, %ax
1433 outw
             %ax, %dx
1434 spin:
1435 jmp
             spin
1436
1437 .p2align 2
1438 adt:
1439 SEG NULLASM
1440 SEG_ASM(STA_X|STA_R, 0, 0xffffffff)
1441 SEG_ASM(STA_W, 0, 0xfffffffff)
1442
1443
1444 gdtdesc:
1445 .word
             (gdtdesc - gdt - 1)
1446 .long gdt
1447
1448
1449
```

```
1450 #include "types.h"
1451 #include "defs.h"
1452 #include "param.h"
1453 #include "memlayout.h"
1454 #include "mmu.h"
1455 #include "proc.h"
1456 #include "x86.h"
1457
1458 static void startothers(void);
1459 static void mpmain(void) __attribute__((noreturn));
1460 extern pde_t *kpgdir;
1461 extern char end[]; // first address after kernel loaded from ELF file
1462
1463 // Bootstrap processor starts running C code here.
1464 // Allocate a real stack and switch to it, first
1465 // doing some setup required for memory allocator to work.
1466 int.
1467 main(void)
1468 {
1469 kinit1(end, P2V(4*1024*1024)); // phys page allocator
1470 kvmalloc();
                      // kernel page table
1471 mpinit();
                      // collect info about this machine
1472 lapicinit();
1473 seginit();
                     // set up segments
1474 cprintf("\ncpu%d: starting xv6\n\n", cpu->id);
1475 picinit();
                     // interrupt controller
1476 ioapicinit(); // another interrupt controller
1477 consoleinit(); // I/O devices & their interrupts
1478 uartinit(); // serial port
                     // process table
1479 pinit();
1480 tvinit();
                     // trap vectors
1481 binit();
                     // buffer cache
1482 fileinit(); // file table
1483 ideinit();
                     // disk
1484 if(!ismp)
1485 timerinit(); // uniprocessor timer
1486 startothers(); // start other processors
1487 kinit2(P2V(4*1024*1024), P2V(PHYSTOP)); // must come after startothers()
1488 userinit();
                   // first user process
1489 // Finish setting up this processor in mpmain.
1490 mpmain();
1491 }
1492
1493
1494
1495
1496
1497
1498
1499
```

```
1500 // Other CPUs jump here from entryother.S.
                                                                               1550
                                                                                        // wait for cpu to finish mpmain()
1501 static void
                                                                               1551
                                                                                        while(c->started == 0)
1502 mpenter(void)
                                                                               1552
                                                                                         ;
1503 {
                                                                               1553 }
1504 switchkvm();
                                                                               1554 }
1505 seginit();
                                                                               1555
1506 lapicinit();
                                                                               1556 // Boot page table used in entry.S and entryother.S.
1507 mpmain();
                                                                               1557 // Page directories (and page tables), must start on a page boundary,
1508 }
                                                                               1558 // hence the "__aligned__" attribute.
1509
                                                                               1559 // Use PTE_PS in page directory entry to enable 4Mbyte pages.
1510 // Common CPU setup code.
                                                                               1560 __attribute__((__aligned__(PGSIZE)))
1511 static void
                                                                               1561 pde_t entrypgdir[NPDENTRIES] = {
1512 mpmain(void)
                                                                               1562 // Map VA's [0, 4MB) to PA's [0, 4MB)
1513 {
                                                                               1563 [0] = (0) | PTE_P | PTE_W | PTE_PS,
1514 cprintf("cpu%d: starting\n", cpu->id);
                                                                               1564 // Map VA's [KERNBASE, KERNBASE+4MB) to PA's [0, 4MB)
1515 idtinit();
                     // load idt register
                                                                               1565 [KERNBASE>>PDXSHIFT] = (0) | PTE_P | PTE_W | PTE_PS,
1516 xchg(&cpu->started, 1); // tell startothers() we're up
                                                                               1566 };
1517 scheduler(); // start running processes
                                                                               1567
1518 }
                                                                               1568
1519
                                                                               1569
1520 pde_t entrypgdir[]; // For entry.S
                                                                               1570
1521
                                                                               1571
1522 // Start the non-boot (AP) processors.
                                                                               1572
1523 static void
                                                                               1573
1524 startothers(void)
                                                                               1574
1525 {
                                                                               1575
1526 extern uchar _binary_entryother_start[], _binary_entryother_size[];
                                                                               1576
1527 uchar *code;
                                                                               1577
1528 struct cpu *c;
                                                                               1578
1529 char *stack;
                                                                               1579
1530
                                                                               1580
1531 // Write entry code to unused memory at 0x7000.
                                                                               1581
1532 // The linker has placed the image of entryother.S in
                                                                               1582
1533 // _binary_entryother_start.
                                                                               1583
1534 code = p2v(0x7000);
                                                                               1584
1535 memmove(code, _binary_entryother_start, (uint)_binary_entryother_size);
                                                                               1585
1536
                                                                               1586
1537 for(c = cpus; c < cpus+ncpu; c++){
                                                                               1587
1538
       if(c == cpus+cpunum()) // We've started already.
                                                                               1588
1539
          continue;
                                                                               1589
1540
                                                                               1590
1541
        // Tell entryother.S what stack to use, where to enter, and what
                                                                               1591
1542
        // pgdir to use. We cannot use kpgdir yet, because the AP processor
                                                                               1592
1543
        // is running in low memory, so we use entrypgdir for the APs too.
                                                                               1593
1544
        stack = kalloc();
                                                                               1594
1545
        *(void**)(code-4) = stack + KSTACKSIZE;
                                                                               1595
1546
         *(void**)(code-8) = mpenter;
                                                                               1596
1547
        *(int**)(code-12) = (void *) v2p(entrypgdir);
                                                                               1597
1548
                                                                               1598
1549
                                                                               1599
        lapicstartap(c->id, v2p(code));
```

Sheet 15 Sheet 15

1600 // Blank page.	1650 // Blank page.
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Sheet 16

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```

```
1750 // Mutual exclusion lock.
1751 struct spinlock {
1752 uint locked;
                        // Is the lock held?
1753
1754 // For debugging:
1755 char *name;
                        // Name of lock.
1756 struct cpu *cpu;
                        // The cpu holding the lock.
1757
      uint pcs[10];
                        // The call stack (an array of program counters)
1758
                        // that locked the lock.
1759 };
1760
1761
1762
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```

```
1800 // Mutual exclusion spin locks.
1801
1802 #include "types.h"
1803 #include "defs.h"
1804 #include "param.h"
1805 #include "x86.h"
1806 #include "memlayout.h"
1807 #include "mmu.h"
1808 #include "proc.h"
1809 #include "spinlock.h"
1810
1811 void
1812 initlock(struct spinlock *lk, char *name)
1813 {
1814 lk->name = name;
1815 lk \rightarrow locked = 0;
1816 	 1k - cpu = 0;
1817 }
1818
1819 // Acquire the lock.
1820 // Loops (spins) until the lock is acquired.
1821 // Holding a lock for a long time may cause
1822 // other CPUs to waste time spinning to acquire it.
1823 void
1824 acquire(struct spinlock *lk)
1825 {
1826 pushcli(); // disable interrupts to avoid deadlock.
1827 if(holding(lk))
1828
       panic("acquire");
1829
1830 // The xchg is atomic.
1831 // It also serializes, so that reads after acquire are not
1832 // reordered before it.
1833 while(xchg(&lk->locked, 1) != 0)
1834 ;
1835
1836 // Record info about lock acquisition for debugging.
1837 lk->cpu = cpu;
1838 getcallerpcs(&lk, lk->pcs);
1839 }
1840
1841
1842
1843
1844
1845
1846
1847
1848
1849
```

```
1850 // Release the lock.
1851 void
1852 release(struct spinlock *lk)
1853 {
1854 if(!holding(lk))
        panic("release");
1855
1856
1857 	 lk->pcs[0] = 0;
1858 	 1k - \text{cpu} = 0;
1859
1860 // The xchg serializes, so that reads before release are
1861 // not reordered after it. The 1996 PentiumPro manual (Volume 3,
1862 // 7.2) says reads can be carried out speculatively and in
1863 // any order, which implies we need to serialize here.
1864 // But the 2007 Intel 64 Architecture Memory Ordering White
1865 // Paper says that Intel 64 and IA-32 will not move a load
1866 // after a store. So lock->locked = 0 would work here.
1867 // The xchg being asm volatile ensures gcc emits it after
1868 // the above assignments (and after the critical section).
1869 xchq(&lk->locked, 0);
1870
1871 popcli();
1872 }
1874 // Record the current call stack in pcs[] by following the %ebp chain.
1875 void
1876 getcallerpcs(void *v, uint pcs[])
1877 {
1878 uint *ebp;
1879 int i;
1880
1881 ebp = (uint*)v - 2;
1882 for(i = 0; i < 10; i++){
       if(ebp == 0 || ebp < (uint*)KERNBASE || ebp == (uint*)Oxffffffff)</pre>
1883
1884
          break;
1885
        1886
       ebp = (uint*)ebp[0]; // saved %ebp
1887
1888 for(; i < 10; i++)
        pcs[i] = 0;
1889
1890 }
1891
1892 // Check whether this cpu is holding the lock.
1894 holding(struct spinlock *lock)
1895 {
1896 return lock->locked && lock->cpu == cpu;
1897 }
1898
1899
```

```
1900 // Pushcli/popcli are like cli/sti except that they are matched:
                                                                               1950 #include "param.h"
1901 // it takes two popcli to undo two pushcli. Also, if interrupts
                                                                               1951 #include "types.h"
1902 // are off, then pushcli, popcli leaves them off.
                                                                               1952 #include "defs.h"
                                                                               1953 #include "x86.h"
1903
1904 void
1905 pushcli(void)
                                                                               1955 #include "mmu.h"
                                                                               1956 #include "proc.h"
1906 {
1907 int eflags;
                                                                               1957 #include "elf.h"
1908
                                                                               1958
1909 eflags = readeflags();
1910 cli();
1911 if(cpu->ncli++==0)
1912
        cpu->intena = eflags & FL_IF;
                                                                               1962
1913 }
1914
1915 void
                                                                               1965 void
1916 popcli(void)
                                                                               1966 seginit(void)
1917 {
                                                                               1967 {
1918 if(readeflags()&FL_IF)
                                                                               1968 struct cpu *c;
1919
        panic("popcli - interruptible");
                                                                               1969
1920 if(--cpu->ncli < 0)
1921
        panic("popcli");
1922 if(cpu->ncli == 0 && cpu->intena)
1923
        sti();
1924 }
1925
1926
1927
1928
1929
                                                                               1979
1930
1931
1932
                                                                               1982
1933
1934
1935
                                                                               1985
1936
1937
                                                                               1987 cpu = c;
1938
                                                                               1988 proc = 0;
1939
                                                                               1989 }
1940
                                                                               1990
                                                                               1991
1941
1942
                                                                               1992
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1946
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```

```
1954 #include "memlayout.h"
1959 extern char data[]; // defined by kernel.ld
1960 pde_t *kpgdir; // for use in scheduler()
1961 struct segdesc gdt[NSEGS];
1963 // Set up CPU's kernel segment descriptors.
1964 // Run once on entry on each CPU.
1970 // Map "logical" addresses to virtual addresses using identity map.
1971 // Cannot share a CODE descriptor for both kernel and user
1972 // because it would have to have DPL USR, but the CPU forbids
1973 // an interrupt from CPL=0 to DPL=3.
1974 c = &cpus[cpunum()];
1975 c->qdt[SEG KCODE] = SEG(STA X|STA R, 0, 0xfffffffff, 0);
1976 c->qdt[SEG_KDATA] = SEG(STA_W, 0, 0xfffffffff, 0);
1977 c->qdt[SEG_UCODE] = SEG(STA_X|STA_R, 0, 0xffffffff, DPL_USER);
1978 c->qdt[SEG UDATA] = SEG(STA W, 0, 0xfffffffff, DPL USER);
1980 // Map cpu, and curproc
1981 c \rightarrow gdt[SEG_KCPU] = SEG(STA_W, &c \rightarrow cpu, 8, 0);
1983 lgdt(c->gdt, sizeof(c->gdt));
1984 loadgs(SEG_KCPU << 3);
1986 // Initialize cpu-local storage.
```

2000 // Return the address of the PTE in page table pgdir

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```
2001 // that corresponds to virtual address va. If alloc!=0,
2002 // create any required page table pages.
2003 static pte_t *
2004 walkpgdir(pde_t *pgdir, const void *va, int alloc)
2005 {
2006 pde_t *pde;
2007 pte_t *pgtab;
2008
2009 pde = &pgdir[PDX(va)];
2010 if(*pde & PTE_P){
        pgtab = (pte_t*)p2v(PTE_ADDR(*pde));
2011
2012 } else {
2013
        if(!alloc | | (pgtab = (pte_t*)kalloc()) == 0)
2014
          return 0;
2015
        // Make sure all those PTE_P bits are zero.
2016
        memset(pgtab, 0, PGSIZE);
2017
        // The permissions here are overly generous, but they can
2018
        // be further restricted by the permissions in the page table
2019
        // entries, if necessary.
2020
        *pde = v2p(pgtab) | PTE P | PTE W | PTE U;
2021 }
2022 return &pgtab[PTX(va)];
2023 }
2024
2025 // Create PTEs for virtual addresses starting at va that refer to
2026 // physical addresses starting at pa. va and size might not
2027 // be page-aligned.
2028 static int
2029 mappages(pde_t *pgdir, void *va, uint size, uint pa, int perm)
2030 {
2031 char *a, *last;
2032 pte_t *pte;
2033
2034 a = (char*)PGROUNDDOWN((uint)va);
2035 last = (char*)PGROUNDDOWN(((uint)va) + size - 1);
2036 for(;;){
2037
       if((pte = walkpgdir(pgdir, a, 1)) == 0)
2038
          return -1;
2039
        if(*pte & PTE_P)
2040
          panic("remap");
2041
        *pte = pa | perm | PTE_P;
2042
        if(a == last)
2043
          break;
2044
        a += PGSIZE;
        pa += PGSIZE;
2045
2046 }
2047 return 0;
2048 }
2049
```

Sheet 20

2093

2096

2098 2099 return 0;

2094 memset(pgdir, 0, PGSIZE);

2095 if (p2v(PHYSTOP) > (void\*)DEVSPACE)

panic("PHYSTOP too high");

2097 for (k = kmap; k < kmap[NELEM(kmap)]; k++)

if(mappages(pgdir, k->virt, k->phys end - k->phys start,

(uint)k->phys\_start, k->perm) < 0)</pre>

2198

2199

Sheet 21 Sheet 21

2148

2149

```
2200 // Allocate page tables and physical memory to grow process from oldsz to
                                                                                2250
                                                                                          kfree(v);
2201 // newsz, which need not be page aligned. Returns new size or 0 on error.
                                                                               2251
                                                                                          *pte = 0;
2202 int
                                                                                2252
                                                                                2253 }
2203 allocuvm(pde_t *pqdir, uint oldsz, uint newsz)
                                                                                2254 return newsz;
2204 {
2205 char *mem;
                                                                                2255 }
2206 uint a;
                                                                               2256
2207
                                                                                2257 // Free a page table and all the physical memory pages
2208 if(newsz >= KERNBASE)
                                                                                2258 // in the user part.
2209
      return 0;
                                                                               2259 void
2210 if(newsz < oldsz)
                                                                                2260 freevm(pde_t *pgdir)
       return oldsz;
2211
                                                                               2261 {
2212
                                                                               2262 uint i;
2213 a = PGROUNDUP(oldsz);
                                                                                2263
2214 for(; a < newsz; a += PGSIZE){
                                                                               2264 	 if(pqdir == 0)
2215
        mem = kalloc();
                                                                               2265
                                                                                        panic("freevm: no pgdir");
2216
        if(mem == 0)
                                                                                2266 deallocuvm(pgdir, KERNBASE, 0);
                                                                               2267 for(i = 0; i < NPDENTRIES; i++){
2217
          cprintf("allocuvm out of memory\n");
2218
          deallocuvm(pgdir, newsz, oldsz);
                                                                               2268
                                                                                      if(pgdir[i] & PTE_P){
2219
          return 0;
                                                                                2269
                                                                                          char * v = p2v(PTE_ADDR(pgdir[i]));
2220
                                                                               2270
                                                                                          kfree(v);
2221
        memset(mem, 0, PGSIZE);
                                                                                2271
2222
        mappages(pgdir, (char*)a, PGSIZE, v2p(mem), PTE_W | PTE_U);
                                                                                2272
2223 }
                                                                                2273 kfree((char*)pgdir);
2224 return newsz;
                                                                                2274 }
2225 }
                                                                                2275
2226
                                                                                2276 // Clear PTE_U on a page. Used to create an inaccessible
2227 // Deallocate user pages to bring the process size from oldsz to
                                                                                2277 // page beneath the user stack.
2228 // newsz. oldsz and newsz need not be page-aligned, nor does newsz
                                                                                2278 void
2229 // need to be less than oldsz. oldsz can be larger than the actual
                                                                                2279 clearpteu(pde_t *pqdir, char *uva)
2230 // process size. Returns the new process size.
                                                                                2280 {
2231 int
                                                                                2281 pte_t *pte;
2232 deallocuvm(pde_t *pqdir, uint oldsz, uint newsz)
                                                                               2282
2233 {
                                                                                2283 pte = walkpgdir(pgdir, uva, 0);
                                                                                2284 	 if(pte == 0)
2234 pte_t *pte;
2235 uint a, pa;
                                                                                      panic("clearpteu");
                                                                                2285
2236
                                                                                2286 *pte &= ~PTE_U;
2237 if(newsz >= oldsz)
                                                                                2287 }
2238
       return oldsz;
                                                                               2288
2239
                                                                                2289
2240 a = PGROUNDUP(newsz);
                                                                                2290
2241 for(; a < oldsz; a += PGSIZE) \{
                                                                               2291
2242
        pte = walkpgdir(pgdir, (char*)a, 0);
                                                                                2292
2243
        if(!pte)
                                                                                2293
2244
          a += (NPTENTRIES - 1) * PGSIZE;
                                                                                2294
2245
        else if((*pte & PTE_P) != 0){
                                                                                2295
2246
          pa = PTE_ADDR(*pte);
                                                                                2296
2247
                                                                                2297
         if(pa == 0)
2248
          panic("kfree");
                                                                                2298
2249
                                                                               2299
          char *v = p2v(pa);
```

Sheet 22 Sheet 22

```
2300 // Given a parent process's page table, create a copy
2301 // of it for a child.
2302 pde t*
2303 copyuvm(pde_t *pqdir, uint sz)
2304 {
2305 pde t *d;
2306 pte_t *pte;
2307 uint pa, i, flags;
2308 char *mem;
2309
2310 if((d = setupkvm()) == 0)
2311
      return 0;
2312 for(i = 0; i < sz; i += PGSIZE){
2313
       if((pte = walkpgdir(pgdir, (void *) i, 0)) == 0)
2314
          panic("copyuvm: pte should exist");
2315
        if(!(*pte & PTE_P))
2316
        panic("copyuvm: page not present");
2317
        pa = PTE_ADDR(*pte);
2318
        flags = PTE_FLAGS(*pte);
2319
        if((mem = kalloc()) == 0)
2320
         goto bad;
2321
        memmove(mem, (char*)p2v(pa), PGSIZE);
2322
        if(mappages(d, (void*)i, PGSIZE, v2p(mem), flags) < 0)</pre>
2323
          goto bad;
2324 }
2325 return d;
2326
2327 bad:
2328 freevm(d);
2329 return 0;
2330 }
2331
2332
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```

```
2350 // Map user virtual address to kernel address.
2351 char*
2352 uva2ka(pde_t *pgdir, char *uva)
2353 {
2354 pte_t *pte;
2355
2356 pte = walkpgdir(pgdir, uva, 0);
2357 if((*pte & PTE_P) == 0)
2358
      return 0;
2359 if((*pte & PTE_U) == 0)
2360
       return 0;
2361 return (char*)p2v(PTE_ADDR(*pte));
2362 }
2363
2364 // Copy len bytes from p to user address va in page table pgdir.
2365 // Most useful when pgdir is not the current page table.
2366 // uva2ka ensures this only works for PTE_U pages.
2367 int
2368 copyout(pde_t *pgdir, uint va, void *p, uint len)
2369 {
2370 char *buf, *pa0;
2371 uint n, va0;
2372
2373 buf = (char*)p;
2374 while(len > 0){
2375
      va0 = (uint)PGROUNDDOWN(va);
       pa0 = uva2ka(pgdir, (char*)va0);
2376
2377 if(pa0 == 0)
2378
        return -1;
2379
       n = PGSIZE - (va - va0);
2380 if(n > len)
        n = len;
2381
2382 memmove(pa0 + (va - va0), buf, n);
2383
       len -= n;
2384
        buf += n;
2385
       va = va0 + PGSIZE;
2386 }
2387 return 0;
2388 }
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2400 // Blank page.	2450 // Blank page.
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```

```
2550 // Segments in proc->gdt.
2551 #include "BC ps.h"
2552 #define NSEGS
2553 #define FIRST_PROC_GID 0
2554 #define FIRST_PROC_UID 0
2555
2556 int get_current_procs(int, struct uproc* table); //sys_getprocs helper protot
2557 // Per-CPU state
2558 struct cpu {
2559 uchar id;
                                   // Local APIC ID; index into cpus[] below
2560 struct context *scheduler; // swtch() here to enter scheduler
2561 struct taskstate ts;
                                  // Used by x86 to find stack for interrupt
2562 struct segdesc gdt[NSEGS]; // x86 global descriptor table
2563 volatile uint started;
                                  // Has the CPU started?
2564 int ncli;
                                   // Depth of pushcli nesting.
2565 int intena;
                                   // Were interrupts enabled before pushcli?
2566
2567 // Cpu-local storage variables; see below
2568 struct cpu *cpu;
2569 struct proc *proc;
                                   // The currently-running process.
2570 };
2571
2572 extern struct cpu cpus[NCPU];
2573 extern int ncpu;
2574
2575 // Per-CPU variables, holding pointers to the
2576 // current cpu and to the current process.
2577 // The asm suffix tells gcc to use "%gs:0" to refer to cpu
2578 // and "%qs:4" to refer to proc. seginit sets up the
2579 // %gs segment register so that %gs refers to the memory
2580 // holding those two variables in the local cpu's struct cpu.
2581 // This is similar to how thread-local variables are implemented
2582 // in thread libraries such as Linux pthreads.
2583 extern struct cpu *cpu asm("%gs:0");
                                              // &cpus[cpunum()]
2584 extern struct proc *proc asm("%gs:4");
                                              // cpus[cpunum()].proc
2585
2586
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```

```
2600 // Saved registers for kernel context switches.
                                                                                2650 #include "types.h"
2601 // Don't need to save all the segment registers (%cs, etc),
                                                                                2651 #include "defs.h"
2602 // because they are constant across kernel contexts.
                                                                                2652 #include "param.h"
2603 // Don't need to save %eax, %ecx, %edx, because the
                                                                                2653 #include "memlayout.h"
2604 // x86 convention is that the caller has saved them.
                                                                                2654 #include "mmu.h"
2605 // Contexts are stored at the bottom of the stack they
                                                                                2655 #include "x86.h"
2606 // describe; the stack pointer is the address of the context.
                                                                                2656 #include "proc.h"
2607 // The layout of the context matches the layout of the stack in swtch.S
                                                                                2657 #include "spinlock.h"
2608 // at the "Switch stacks" comment. Switch doesn't save eip explicitly,
                                                                                2658;
2609 // but it is on the stack and allocproc() manipulates it.
                                                                                2659
2610 struct context {
                                                                                2660 struct {
2611 uint edi;
                                                                                2661 struct spinlock lock;
2612 uint esi;
                                                                                2662 struct proc proc[NPROC];
2613 uint ebx;
                                                                                2663 } ptable;
2614 uint ebp;
                                                                                2664
2615 uint eip;
                                                                                2665 static struct proc *initproc;
2616 };
                                                                                2666
2617
                                                                                2667 int nextpid = 1;
2618 enum procstate { UNUSED, EMBRYO, SLEEPING, RUNNABLE, RUNNING, ZOMBIE };
                                                                                2668 extern void forkret(void);
                                                                                2669 extern void trapret(void);
2620 // Per-process state
2621 struct proc {
                                                                                2671 static void wakeup1(void *chan);
2622 uint sz;
                                   // Size of process memory (bytes)
                                                                                2672
2623 pde_t* pgdir;
                                   // Page table
                                                                                2673
2624 char *kstack;
                                                                                2674 void
                                   // Bottom of kernel stack for this process
2625 enum procstate state;
                                   // Process state
                                                                                2675 pinit(void)
                                   // Process ID
2626 int pid;
2627 int ppid;
                                      // Parent Process ID
                                                                                2677 initlock(&ptable.lock, "ptable");
2628 int uid;
                                      // User ID
                                                                                2678 }
                                                     ****** Net 2679
2629 int gid;
                                      // Group ID
2630 struct proc *parent;
                                   // Parent process
                                                                                2680 //helper function for sys_getprocs
2631 struct trapframe *tf;
                                   // Trap frame for current syscall
                                                                                2681 int get_current_procs(int max, struct uproc* table)
2632 struct context *context;
                                   // swtch() here to run process
                                                                                2682 {
2633 void *chan;
                                   // If non-zero, sleeping on chan
                                                                                2683
2634 int killed;
                                   // If non-zero, have been killed
                                                                                2684
                                                                                        struct proc *p;
                                                                                                                //temp to get procs from table
2635 struct file *ofile[NOFILE]; // Open files
                                                                                2685
                                                                                        int i = 0;
                                                                                                                //index for max
2636 struct inode *cwd;
                                   // Current directory
                                                                                2686
2637 char name[16];
                                   // Process name (debugging)
                                                                                2687
                                                                                        //To get state string from the proc struct
2638 };
                                                                                2688
                                                                                        static char *states[] = {
2639
                                                                                2689
                                                                                           [UNUSED]
                                                                                                       "UNUSED",
2640 // Process memory is laid out contiguously, low addresses first:
                                                                                2690
                                                                                           [EMBRYO]
                                                                                                       "EMBRYO".
2641 // text
                                                                                2691
                                                                                           [SLEEPING] "SLEEP",
2642 //
         original data and bss
                                                                                2692
                                                                                           [RUNNABLE] "RUNABLE",
                                                                                                       "RUN ",
2643 //
         fixed-size stack
                                                                                2693
                                                                                           [RUNNING]
                                                                                           [ZOMBIE]
                                                                                                       "ZOMBIE"
2644 //
         expandable heap
                                                                                2694
                                                                                2695
                                                                                         };
2645
2646
                                                                                2696
2647
                                                                                2697
                                                                                       acquire(&ptable.lock); //acquire lock when reading from the Ptable to prev
2648
                                                                                2698
                                                                                2699
2649
```

Sheet 26 Sheet 26

```
2750 // Look in the process table for an UNUSED proc.
2700 //run through the ptable and find all procs that are not marked unused.
2701 //stop if end if reached, or the max requested is used
                                                                               2751 // If found, change state to EMBRYO and initialize
2702
      for(p = ptable.proc; p < &ptable.proc[NPROC] && i < max; p++) {</pre>
                                                                               2752 // state required to run in the kernel.
2703
       if(p->state != UNUSED) {
                                                                               2753 // Otherwise return 0.
2704
                                                                               2754 static struct proc*
2705
       table[i].uid = p->uid;
                                                                               2755 allocproc(void)
2706
       table[i].gid = p->gid;
                                                                               2756 {
2707
       table[i].pid = p->pid;
                                                                               2757 struct proc *p;
2708
       table[i].ppid = p->ppid;
                                                                               2758 char *sp;
2709
       table[i].size = p->sz;
                                                                               2759
2710
       ++i;
                                                                               2760 acquire(&ptable.lock);
2711
                                                                               2761 for(p = ptable.proc; p < &ptable.proc[NPROC]; p++)
2712
                                                                               2762
                                                                                      if(p->state == UNUSED)
2713
       //strcpy for name
                                                                               2763
                                                                                          goto found;
2714
                                                                               2764 release(&ptable.lock);
       unsigned k;
2715
       for(k = 0; p-name[k] != '\0'; ++k) {
                                                                               2765 return 0;
                                                                               2766
2716
        table[i].name[k] = p->name[k];
2717
                                                                               2767 found:
       table[i].name[k] = ' \0';
2718
                                                                               2768 p->state = EMBRYO;
2719
                                                                               2769 if(nextpid == 1) {
2720
       //strcpv for state
                                                                               2770
                                                                                      p->ppid = nextpid;
2721
          if(p->state >= 0 && p->state < NELEM(states) && states[p->state]) {
                                                                               2771
                                                                                       p->qid = FIRST PROC GID;
2722
       char * state = states[p->state];
                                                                               2772
                                                                                      p->uid = FIRST PROC UID;
2723
       unsigned j;
                                                                               2773 }
2724
       for (j=0; state[j] != ' \setminus 0'; ++j) {
                                                                               2774 else {
2725
                 table[i].state[j] = state[j];
                                                                               2775
                                                                                        p->ppid = proc->pid;
2726
                                                                               2776
                                                                                        p->qid = proc->qid;
2727
       table[i].state[j] = ' \0';
                                                                               2777
                                                                                        p->uid = proc->uid;
2728
                                                                               2778
2729
                                                                               2779
2730 }
                                                                               2780 p->pid = nextpid++;
2731 release(&ptable.lock);
                                                                               2781 release(&ptable.lock);
2732 return i;
                                                                               2782
2733 }
                                                                               2783 // Allocate kernel stack.
2734
                                                                               2784 if((p->kstack = kalloc()) == 0){
2735
                                                                               2785
                                                                                      p->state = UNUSED;
2736
                                                                               2786
                                                                                      return 0;
                                                                               2787 }
2737
2738
                                                                               2788 sp = p->kstack + KSTACKSIZE;
2739
                                                                               2789
2740
                                                                               2790 // Leave room for trap frame.
                                                                               2791 sp -= sizeof *p->tf;
2741
2742
                                                                               2792 p->tf = (struct trapframe*)sp;
2743
                                                                               2793
2744
                                                                               2794 // Set up new context to start executing at forkret,
2745
                                                                               2795 // which returns to trapret.
2746
                                                                               2796 sp -= 4;
2747
                                                                               2797 *(uint*)sp = (uint)trapret;
2748
                                                                               2798
2749
                                                                               2799
```

Sheet 27 Sheet 27

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Sheet 28 Sheet 28

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Sheet 31 Sheet 31

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Sheet 32

```
3200 // Wake up all processes sleeping on chan.
3201 // The ptable lock must be held.
3202 static void
3203 wakeup1(void *chan)
3204 {
3205 struct proc *p;
3206
3207 for(p = ptable.proc; p < &ptable.proc[NPROC]; p++)</pre>
3208
       if(p->state == SLEEPING && p->chan == chan)
3209
          p->state = RUNNABLE;
3210 }
3211
3212 // Wake up all processes sleeping on chan.
3213 void
3214 wakeup(void *chan)
3215 {
3216 acquire(&ptable.lock);
3217 wakeup1(chan);
3218 release(&ptable.lock);
3219 }
3220
3221 // Kill the process with the given pid.
3222 // Process won't exit until it returns
3223 // to user space (see trap in trap.c).
3224 int
3225 kill(int pid)
3226 {
3227 struct proc *p;
3228
3229 acquire(&ptable.lock);
3230 for(p = ptable.proc; p < &ptable.proc[NPROC]; p++){</pre>
3231
      if(p->pid == pid)
3232
          p->killed = 1;
3233
          // Wake process from sleep if necessary.
3234
          if(p->state == SLEEPING)
3235
          p->state = RUNNABLE;
3236
          release(&ptable.lock);
3237
          return 0;
3238
3239 }
3240 release(&ptable.lock);
3241 return -1;
3242 }
3243
3244
3245
3246
3247
3248
3249
```

```
3250 // Print a process listing to console. For debugging.
3251 // Runs when user types 'P on console.
3252 // No lock to avoid wedging a stuck machine further.
3253 void
3254 procdump(void)
3255 {
3256 static char *states[] = {
3257 [UNUSED]
                  "unused",
3258 [EMBRYO]
                   "embryo",
3259 [SLEEPING] "sleep",
3260 [RUNNABLE] "runble",
                  "run ",
3261 [RUNNING]
3262 [ZOMBIE]
                  "zombie"
3263
      };
3264 int i;
3265 struct proc *p;
3266 char *state;
3267 uint pc[10];
3268
3269 for(p = ptable.proc; p < &ptable.proc[NPROC]; p++){</pre>
3270
       if(p->state == UNUSED)
3271
          continue;
3272
        if(p->state >= 0 && p->state < NELEM(states) && states[p->state])
3273
          state = states[p->state];
3274
        else
3275
          state = "???";
3276
        cprintf("Process name: %s, state: %s, pid: %d, qid: %d, uid: %d \n ", p-
3277
        if(p->state == SLEEPING){
3278
          getcallerpcs((uint*)p->context->ebp+2, pc);
3279
          for(i=0; i<10 && pc[i] != 0; i++)
3280
            cprintf(" %p", pc[i]);
3281
3282
        cprintf("\n");
3283 }
3284 }
3285
3286
3287
3288
3289
3290
3291
3292
3293
3294
3295
3296
3297
3298
3299
```

Sheet 32

3302 # void swtch(struct context *rold, struct context *new); 3303 # Save current register context in old 3305 # and then load register context from new.  3308 swtch: 3309 movl 4(%esp), %eax 3310 movl 8(%esp), %edx 3311 # Save old callee-save registers 3313 pushl %ebp 3314 pushl %ebi 3315 pushl %edi 3317 3318 # Switch stacks 3319 movl %edx, %esp 3320 # Load new callee-save registers 3321 popl %edi 3322 # Load new callee-save registers 3323 popl %edi 3324 popl %esi 3326 popl %ebx 3327 popl %ebx 3328 popl %ebx 3329 3330 3331 3332 3333 3334 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3345 3346 3347 3348 3349	3300 3301		Context switch
3303 # 3304 # Save current register context in old 3305 # and then load register context from new. 3306 3307 .globl swtch 3308 swtch: 3309 movl 4(%esp), %eax 3310 movl 8(%esp), %edx 3311 # Save old callee-save registers 3313 pushl %ebp 3314 pushl %ebx 3315 pushl %edi 3317 3318 # Switch stacks 3319 movl %esp, (%eax) 320 movl %esp, (%eax) 321 movl %esp, (%eax) 322 # Load new callee-save registers 3323 popl %edi 3324 popl %esi 3325 popl %ebx 3326 popl %ebp 327 ret 3328 3329 3330 3331 3332 3333 3334 3335 3337 3338 3339 3340 3341 3342 3343 3344 3345 3346 3347 3348			<pre>void swtch(struct context **old, struct context *new);</pre>
3304 # Save current register context in old 3305 # and then load register context from new. 3306 3307 .globl swtch 3308 swtch: 3309 movl 4(%esp), %eax 3310 movl 8(%esp), %edx 3311 3312 # Save old callee-save registers 3313 pushl %ebp 3314 pushl %ebx 3315 pushl %edi 3316 3317 3318 # Switch stacks 3319 movl %esp, (%eax) 3320 movl %edx, %esp 3321 3322 # Load new callee-save registers 3323 popl %edi 3324 popl %esi 3325 popl %ebx 3326 popl %ebp 3327 ret 3328 3329 3330 3331 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3345 3346 3347 3348			
3305 # and then load register context from new. 3306 3307 .globl swtch 3308 swtch: 3309			
3306 3307 .globl swtch 3308 swtch: 3309 movl 4(%esp), %eax 3310 movl 8(%esp), %edx 3311 3312 # Save old callee-save registers 3313 pushl %ebp 3314 pushl %ebi 3316 pushl %edi 3317 3318 # Switch stacks 3319 movl %esp, (%eax) 3320 movl %edx, %esp 3321 3322 # Load new callee-save registers 3323 popl %edi 3324 popl %edi 3325 popl %ebi 3326 popl %ebi 3327 ret 3328 3329 3330 3331 3331 3331 3332 3333 3334 3334 3334			
3307 .globl swtch 3308 swtch: 3309 movl 4(%esp), %eax 3310 movl 8(%esp), %edx 3311 3312 # Save old callee-save registers 3313 pushl %ebp 3314 pushl %ebx 3315 pushl %edi 3317 3318 # Switch stacks 3319 movl %esp, (%eax) 3320 movl %edx, %esp 3321 3322 # Load new callee-save registers 3323 popl %edi 3324 popl %esi 3325 popl %ebx 3326 popl %ebp 3327 ret 3328 3329 3330 3331 3331 3332 3333 3331 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3345 3346 3347 3348			
3308 swtch: 3319 movl 4(%esp), %eax 3310 movl 8(%esp), %edx 3311 3312 # Save old callee-save registers 3313 pushl %ebp 3314 pushl %ebi 3316 pushl %edi 3317 3318 # Switch stacks 3319 movl %esp, (%eax) 3320 movl %edx, %esp 3321 3322 # Load new callee-save registers 3323 popl %edi 3324 popl %esi 3325 popl %ebx 3326 popl %ebx 3327 ret 3328 3329 3330 3331 3331 3332 3333 3334 3334 3334 3334		. 0	globl swtch
3309 movl 4(%esp), %eax 3310 movl 8(%esp), %edx 3311 3312 # Save old callee-save registers 3313 pushl %ebp 3314 pushl %ebi 3315 pushl %edi 3317 3318 # Switch stacks 3319 movl %esp, (%eax) 3320 movl %edx, %esp 3321 3322 # Load new callee-save registers 3323 popl %edi 3324 popl %edi 3325 popl %ebx 3326 popl %ebbx 3327 ret 3328 3330 3331 3332 3333 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3345 3346 3347 3348			
3310 movl 8(%esp), %edx 3311 # Save old callee-save registers 3313 pushl %ebp 3314 pushl %ebx 3315 pushl %edi 3316 pushl %edi 3317 3318 # Switch stacks 3319 movl %esp, (%eax) 3320 movl %edx, %esp 3321 # Load new callee-save registers 3323 popl %edi 3324 popl %ebx 3325 popl %ebx 3326 popl %ebp 3327 ret 3328 3330 3331 3332 3333 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3345 3346 3347 3348			
3312  # Save old callee-save registers 3313  pushl %ebp 3314  pushl %ebx 3315  pushl %edi 3317 3318  # Switch stacks 3319  movl %esp, (%eax) 3320  movl %edx, %esp 3321 3322  # Load new callee-save registers 3323  popl %edi 3324  popl %esi 3325  popl %ebx 3326  popl %ebx 3327  ret 3328 3330 3331 3332 3333 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3345 3346 3347 3348	3310		movl 8(%esp), %edx
3313 pushl %ebp 3314 pushl %ebx 3315 pushl %edi 3317 3318 # Switch stacks 3319 movl %esp, (%eax) 3320 movl %edx, %esp 3321 3322 # Load new callee-save registers 3323 popl %edi 3324 popl %esi 3325 popl %ebx 3326 popl %ebp 3327 ret 3328 3330 3331 3332 3333 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3345 3346 3347 3348			
3313 pushl %ebp 3314 pushl %ebx 3315 pushl %edi 3317 3318 # Switch stacks 3319 movl %esp, (%eax) 3320 movl %edx, %esp 3321 3322 # Load new callee-save registers 3323 popl %edi 3324 popl %esi 3325 popl %ebx 3326 popl %ebp 3327 ret 3328 3330 3331 3332 3333 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3345 3346 3347 3348	3312		# Save old callee-save registers
3315 pushl %esi 3316 pushl %edi 3317  3318 # Switch stacks 3319 movl %edx, %esp 3321  3322 # Load new callee-save registers 3323 popl %edi 3324 popl %ebx 3326 popl %ebp 3327 ret 3328 3330 3331 3331 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3344 3344 3344 3344	3313		pushl %ebp
3315 pushl %esi 3316 pushl %edi 3317  3318 # Switch stacks 3319 movl %edx, %esp 3321  3322 # Load new callee-save registers 3323 popl %edi 3324 popl %ebx 3326 popl %ebp 3327 ret 3328 3330 3331 3331 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3344 3344 3344 3344	3314		pushl %ebx
3317 3318 # Switch stacks 3319 movl %esp, (%eax) 3320 movl %edx, %esp  3321 3322 # Load new callee-save registers 3323 popl %edi 3324 popl %esi 3325 popl %ebx 3326 popl %ebp 3327 ret 3328 3329 3330 3331 3332 3333 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3344 3344 3344 3345 3346 3347 3348	3315		pushl %esi
3318 # Switch stacks 3319 movl %esp, (%eax) 3320 movl %edx, %esp 3321 3322 # Load new callee-save registers 3323 popl %edi 3324 popl %esi 3325 popl %ebx 3326 popl %ebp 3327 ret 3328 3330 3331 3332 3333 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3342 3343 3344 3345 3346 3347 3348	3316		pushl %edi
3319 movl %esp, (%eax) 3320 movl %edx, %esp  3321 3322  # Load new callee-save registers 3323  popl %edi 3324  popl %esi 3325  popl %ebx 3326  popl %ebp 3327  ret  3328 3329 3330 3331 3332 3333 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3345 3346 3347 3348	3317		
3320 movl %edx, %esp  3321  3322	3318		# Switch stacks
3321 3322  # Load new callee-save registers 3323  popl %edi 3324  popl %esi 3325  popl %ebx 3326  popl %ebp 3327  ret 3328 3330 3331 3332 3333 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3342 3343 3344 3345 3346 3347 3348	3319		movl %esp, (%eax)
3322  # Load new callee-save registers 3323  popl %edi 3324  popl %esi 3325  popl %ebx 3326  popl %ebp 3327  ret 3328 3330 3331 3332 3333 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3343 3344 3345 3346 3347 3348			movl %edx, %esp
3323 popl %edi 3324 popl %esi 3325 popl %ebx 3326 popl %ebp 3327 ret 3328 3330 3331 3332 3333 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3345 3346 3347 3348			
3324 popl %esi 3325 popl %ebx 3326 popl %ebp 3327 ret 3328 3329 3330 3331 3332 3333 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3345 3344 3345 3346 3347 3348			
3325 popl %ebx 3326 popl %ebp 3327 ret 3328 3329 3330 3331 3332 3333 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3345 3344 3345 3346 3347 3348			
3326 popl %ebp 3327 ret 3328 3329 3330 3331 3332 3333 3334 3335 3336 3337 3338 3339 3340 3341 3342 3342 3343 3344 3345 3346 3347 3348	3324		popl %esi
3327 ret 3328 3329 3330 3331 3332 3333 3334 3335 3336 3337 3338 3339 3340 3341 3342 3342 3343 3344 3345 3346 3347 3348			
3328 3329 3330 3331 3332 3333 3334 3335 336 337 338 339 3340 3341 3342 3343 3344 3345 3346 3347 3348			
3329 3330 3331 3332 3333 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3345 3346 3347 3348			ret
3330 3331 3332 3333 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3345 3346 3347 3348			
3331 3332 3333 3334 3335 3336 3337 3338 3340 3341 3342 3342 3343 3344 3345 3346 3347 3348			
3332 3333 3334 3335 3336 3337 3338 3340 3341 3342 3343 3344 3345 3346 3347 3348			
3333 3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3345 3346 3347 3348			
3334 3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3345 3346 3347 3348			
3335 3336 3337 3338 3339 3340 3341 3342 3343 3344 3345 3346 3347 3348			
3336 3337 3338 3339 3340 3341 3342 3343 3344 3345 3346 3347			
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3342 3343 3344 3345 3346 3347 3348	3340		
3343 3344 3345 3346 3347 3348	3341		
3344 3345 3346 3347 3348	3342		
3345 3346 3347 3348	3343		
3346 3347 3348	3344		
3347 3348	3345		
3348	3346		
3349			
	3349		

```
3350 // Physical memory allocator, intended to allocate
3351 // memory for user processes, kernel stacks, page table pages,
3352 // and pipe buffers. Allocates 4096-byte pages.
3353
3354 #include "types.h"
3355 #include "defs.h"
3356 #include "param.h"
3357 #include "memlayout.h"
3358 #include "mmu.h"
3359 #include "spinlock.h"
3360
3361 void freerange(void *vstart, void *vend);
3362 extern char end[]; // first address after kernel loaded from ELF file
3363
3364 struct run {
3365 struct run *next;
3366 };
3367
3368 struct {
3369 struct spinlock lock;
3370 int use_lock;
3371 struct run *freelist;
3372 } kmem;
3373
3374 // Initialization happens in two phases.
3375 // 1. main() calls kinit1() while still using entrypgdir to place just
3376 // the pages mapped by entrypgdir on free list.
3377 // 2. main() calls kinit2() with the rest of the physical pages
3378 // after installing a full page table that maps them on all cores.
3379 void
3380 kinit1(void *vstart, void *vend)
3381 {
3382 initlock(&kmem.lock, "kmem");
3383 kmem.use_lock = 0;
3384 freerange(vstart, vend);
3385 }
3386
3387 void
3388 kinit2(void *vstart, void *vend)
3389 {
3390 freerange(vstart, vend);
3391 kmem.use_lock = 1;
3392 }
3393
3394
3395
3396
3397
3398
3399
```

Sheet 34 Sheet 34

```
3500 // x86 trap and interrupt constants.
                                                                                  3550 #!/usr/bin/perl -w
3501
                                                                                  3551
3502 // Processor-defined:
                                                                                  3552 # Generate vectors.S, the trap/interrupt entry points.
                                     // divide error
3503 #define T_DIVIDE
                              0
                                                                                  3553 # There has to be one entry point per interrupt number
3504 #define T_DEBUG
                             1
                                     // debug exception
                                                                                  3554 # since otherwise there's no way for trap() to discover
3505 #define T NMI
                                     // non-maskable interrupt
                                                                                  3555 # the interrupt number.
3506 #define T_BRKPT
                              3
                                     // breakpoint
                                                                                  3556
3507 #define T_OFLOW
                              4
                                     // overflow
                                                                                  3557 print "# generated by vectors.pl - do not edit\n";
3508 #define T BOUND
                              5
                                     // bounds check
                                                                                  3558 print "# handlers\n";
3509 #define T_ILLOP
                              6
                                     // illegal opcode
                                                                                  3559 print ".globl alltraps\n";
3510 #define T_DEVICE
                              7
                                     // device not available
                                                                                  3560 for(my $i = 0; $i < 256; $i++){}
3511 #define T_DBLFLT
                                     // double fault
                                                                                          print ".globl vector$i\n";
3512 // #define T_COPROC
                             9
                                     // reserved (not used since 486)
                                                                                  3562
                                                                                           print "vector$i:\n";
3513 #define T TSS
                             10
                                     // invalid task switch segment
                                                                                  3563
                                                                                           if(!(\$i == 8 \mid | (\$i >= 10 \&\& \$i <= 14) \mid | \$i == 17))
3514 #define T_SEGNP
                             11
                                     // segment not present
                                                                                  3564
                                                                                               print " pushl \$0\n";
3515 #define T_STACK
                             12
                                     // stack exception
                                                                                  3565
3516 #define T GPFLT
                             13
                                     // general protection fault
                                                                                  3566
                                                                                           print " pushl \$$i\n";
3517 #define T_PGFLT
                             14
                                     // page fault
                                                                                  3567
                                                                                           print " jmp alltraps\n";
3518 // #define T_RES
                             15
                                     // reserved
                                                                                  3568 }
3519 #define T FPERR
                             16
                                     // floating point error
                                                                                  3569
                                     // aligment check
                             17
                                                                                  3570 print "\n# vector table\n";
3520 #define T ALIGN
3521 #define T MCHK
                             18
                                     // machine check
                                                                                  3571 print ".data\n";
                                                                                  3572 print ".globl vectors\n";
3522 #define T SIMDERR
                             19
                                     // SIMD floating point error
3523
                                                                                  3573 print "vectors:\n";
3524 // These are arbitrarily chosen, but with care not to overlap
                                                                                  3574 \text{ for}(\text{my $i = 0; $i < 256; $i++)}
3525 // processor defined exceptions or interrupt vectors.
                                                                                  3575
                                                                                           print " .long vector$i\n";
3526 #define T_SYSCALL
                             64
                                     // system call
                                                                                  3576 }
3527 #define T_DEFAULT
                            500
                                     // catchall
                                                                                  3577
3528
                                                                                  3578 # sample output:
3529 #define T_IRQ0
                                                                                  3579 # # handlers
                             32
                                     // IRQ 0 corresponds to int T_IRQ
3530
                                                                                  3580 # .globl alltraps
                              0
3531 #define IRQ_TIMER
                                                                                  3581 #
                                                                                           .qlobl vector0
                             1
                                                                                  3582 # vector0:
3532 #define IRQ_KBD
3533 #define IRQ_COM1
                              4
                                                                                  3583 #
                                                                                             pushl $0
                                                                                  3584 #
3534 #define IRO IDE
                             14
                                                                                             pushl $0
                             19
                                                                                  3585 #
                                                                                             jmp alltraps
3535 #define IRQ_ERROR
3536 #define IRQ_SPURIOUS
                                                                                  3586 # ...
3537
                                                                                  3587 #
3538
                                                                                  3588 # # vector table
3539
                                                                                  3589 # .data
3540
                                                                                  3590 #
                                                                                          .globl vectors
                                                                                  3591 # vectors:
3541
3542
                                                                                  3592 #
                                                                                            .long vector0
                                                                                             .long vector1
3543
                                                                                  3593 #
                                                                                  3594 #
3544
                                                                                             .long vector2
                                                                                  3595 # ...
3545
3546
                                                                                  3596
3547
                                                                                  3597
3548
                                                                                  3598
3549
                                                                                  3599
```

Sheet 35 Sheet 35

3600	#include "mmu.h"
3601	
3602	<pre># vectors.S sends all traps here.</pre>
3603	.globl alltraps
3604	alltraps:
3605	# Build trap frame.
3606	pushl %ds
3607	pushl %es
3608	pushl %fs
3609	-
3610	pushal
3611	F
3612	# Set up data and per-cpu segments.
3613	movw \$(SEG_KDATA<<3), %ax
3614	movw %ax, %ds
3615	movw %ax, %es
3616	movw \$(SEG_KCPU<<3), %ax
3617	movw %ax, %fs
3618	movw %ax, %qs
3619	
3620	<pre># Call trap(tf), where tf=%esp</pre>
3621	- · · · · · ·
3622	call trap
3623	addl \$4, %esp
3624	
3625	# Return falls through to trapret
3626	
3627	trapret:
3628	popal
3629	popl %gs
3630	popl %fs
3631	popl %es
3632	
3633	addl \$0x8, %esp # trapno and errcode
3634	iret
3635	
3636	
3637	
3638	
3639	
3640	
3641	
3642	
3643	
3644	
3645	
3646	
3647	
3648	
3649	

```
3650 #include "types.h"
3651 #include "defs.h"
3652 #include "param.h"
3653 #include "memlayout.h"
3654 #include "mmu.h"
3655 #include "proc.h"
3656 #include "x86.h"
3657 #include "traps.h"
3658 #include "spinlock.h"
3659
3660 // Interrupt descriptor table (shared by all CPUs).
3661 struct gatedesc idt[256];
3662 extern uint vectors[]; // in vectors.S: array of 256 entry pointers
3663 struct spinlock tickslock;
3664 uint ticks;
3665
3666 void
3667 tvinit(void)
3668 {
3669 int i;
3670
3671 for(i = 0; i < 256; i++)
       SETGATE(idt[i], 0, SEG_KCODE<<3, vectors[i], 0);</pre>
3673 SETGATE(idt[T_SYSCALL], 1, SEG_KCODE<<3, vectors[T_SYSCALL], DPL_USER);
3674
3675 initlock(&tickslock, "time");
3676 }
3677
3678 void
3679 idtinit(void)
3680 {
3681 lidt(idt, sizeof(idt));
3682 }
3683
3684
3685
3686
3687
3688
3689
3690
3691
3692
3693
3694
3695
3696
3697
3698
3699
```

```
3750 default:
3751
        if(proc == 0 || (tf->cs&3) == 0){}
3752
          // In kernel, it must be our mistake.
3753
          cprintf("unexpected trap %d from cpu %d eip %x (cr2=0x%x)\n",
3754
                  tf->trapno, cpu->id, tf->eip, rcr2());
          panic("trap");
3755
3756
3757
        // In user space, assume process misbehaved.
3758
        cprintf("pid %d %s: trap %d err %d on cpu %d "
3759
                "eip 0x%x addr 0x%x--kill proc\n",
3760
                proc->pid, proc->name, tf->trapno, tf->err, cpu->id, tf->eip,
3761
3762
        proc->killed = 1;
3763
3764
3765
     // Force process exit if it has been killed and is in user space.
     // (If it is still executing in the kernel, let it keep running
3767 // until it gets to the regular system call return.)
3768 if(proc && proc->killed && (tf->cs&3) == DPL_USER)
3769
        exit();
3770
3771 // Force process to give up CPU on clock tick.
3772 // If interrupts were on while locks held, would need to check nlock.
3773 if(proc && proc->state == RUNNING && tf->trapno == T_IRQ0+IRQ_TIMER)
3774
        vield();
3775
3776 // Check if the process has been killed since we yielded
3777 if(proc && proc->killed && (tf->cs&3) == DPL_USER)
3778
        exit();
3779 }
3780
3781
3782
3783
3784
3785
3786
3787
3788
3789
3790
3791
3792
3793
3794
3795
3796
3797
3798
3799
```

3749

```
3800 // System call numbers
3801 #define SYS fork
3802 #define SYS exit
3803 #define SYS_wait
                         3
3804 #define SYS_pipe
3805 #define SYS read
3806 #define SYS_kill
3807 #define SYS_exec
3808 #define SYS fstat 8
3809 #define SYS_chdir
                       9
3810 #define SYS_dup
                        10
3811 #define SYS_getpid 11
3812 #define SYS_sbrk 12
3813 #define SYS_sleep 13
3814 #define SYS_uptime 14
3815 #define SYS_open 15
3816 #define SYS_write 16
3817 #define SYS_mknod 17
3818 #define SYS_unlink 18
3819 #define SYS link
3820 #define SYS_mkdir 20
3821 #define SYS_close 21
3822 #define SYS halt
3823 #define SYS_date
3824 #define SYS_getuid 24
3825 #define SYS getgid 25
3826 #define SYS_getppid 26
3827 #define SYS_setuid 27
3828 #define SYS setgid 28
3829 #define SYS_getprocs 29
3830
3831
3832
3833
3834
3835
3836
3837
3838
3839
3840
3841
3842
3843
3844
3845
3846
3847
3848
3849
```

```
3850 #include "types.h"
3851 #include "defs.h"
3852 #include "param.h"
3853 #include "memlayout.h"
3854 #include "mmu.h"
3855 #include "proc.h"
3856 #include "x86.h"
3857 #include "syscall.h"
3858
3859 // User code makes a system call with INT T_SYSCALL.
3860 // System call number in %eax.
3861 // Arguments on the stack, from the user call to the C
3862 // library system call function. The saved user %esp points
3863 // to a saved program counter, and then the first argument.
3865 // Fetch the int at addr from the current process.
3866 int
3867 fetchint(uint addr, int *ip)
3868 {
3869 if(addr \geq proc\geqsz | addr+4 \geq proc\geqsz)
3870
       return -1;
3871 *ip = *(int*)(addr);
3872 return 0;
3873 }
3874
3875 // Fetch the nul-terminated string at addr from the current process.
3876 // Doesn't actually copy the string - just sets *pp to point at it.
3877 // Returns length of string, not including nul.
3878 int
3879 fetchstr(uint addr, char **pp)
3880 {
3881 char *s, *ep;
3882
3883 if(addr >= proc->sz)
3884
       return -1;
3885 *pp = (char*)addr;
3886 ep = (char*)proc->sz;
3887 for(s = *pp; s < ep; s++)
3888
       if(*s == 0)
3889
          return s - *pp;
3890 return -1;
3891 }
3893 // Fetch the nth 32-bit system call argument.
3894 int
3895 argint(int n, int *ip)
3896 {
3897 return fetchint(proc->tf->esp + 4 + 4*n, ip);
3898 }
3899
```

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```
3900 // Fetch the nth word-sized system call argument as a pointer
                                                                                 3950 extern int sys_halt(void);
3901 // to a block of memory of size n bytes. Check that the pointer
                                                                                 3951 extern int sys date(void);
3902 // lies within the process address space.
                                                                                 3952 extern int sys getuid(void);
                                                                                 3953 extern int sys_getgid(void);
3903 int
3904 argptr(int n, char **pp, int size)
                                                                                 3954 extern int sys_getppid(void);
3905 {
                                                                                 3955 extern int sys setuid(void);
3906 int i;
                                                                                 3956 extern int sys_setgid(void);
3907
                                                                                 3957 extern int sys_getprocs(void);
3908 if(argint(n, \&i) < 0)
                                                                                 3958
3909
       return -1;
                                                                                 3959 static int (*syscalls[])(void) = {
3910 if((uint)i >= proc->sz || (uint)i+size > proc->sz)
                                                                                 3960 [SYS fork]
                                                                                                    sys_fork,
3911
       return -1;
                                                                                 3961 [SYS_exit]
                                                                                                    sys_exit,
3912 *pp = (char*)i;
                                                                                 3962 [SYS_wait]
                                                                                                    sys_wait,
3913 return 0;
                                                                                 3963 [SYS_pipe]
                                                                                                    sys_pipe,
3914 }
                                                                                 3964 [SYS_read]
                                                                                                    sys_read,
3915
                                                                                 3965 [SYS_kill]
                                                                                                    sys_kill,
3916 // Fetch the nth word-sized system call argument as a string pointer.
                                                                                 3966 [SYS exec]
                                                                                                    sys_exec,
3917 // Check that the pointer is valid and the string is nul-terminated.
                                                                                 3967 [SYS fstat]
                                                                                                    sys_fstat,
3918 // (There is no shared writable memory, so the string can't change
                                                                                 3968 [SYS_chdir]
                                                                                                    sys_chdir,
3919 // between this check and being used by the kernel.)
                                                                                 3969 [SYS dup]
                                                                                                    sys_dup,
3920 int
                                                                                 3970 [SYS_getpid] sys_getpid,
3921 argstr(int n, char **pp)
                                                                                 3971 [SYS_sbrk]
                                                                                                    sys_sbrk,
3922 {
                                                                                 3972 [SYS sleep]
                                                                                                    sys_sleep,
3923 int addr;
                                                                                 3973 [SYS_uptime] sys_uptime,
3924 if(argint(n, &addr) < 0)
                                                                                 3974 [SYS_open]
                                                                                                    sys_open,
3925
        return -1;
                                                                                 3975 [SYS write]
                                                                                                    sys write,
3926 return fetchstr(addr, pp);
                                                                                 3976 [SYS_mknod]
                                                                                                    sys_mknod,
3927 }
                                                                                 3977 [SYS_unlink] sys_unlink,
3928
                                                                                 3978 [SYS link]
                                                                                                    sys link,
3929 extern int sys_chdir(void);
                                                                                 3979 [SYS_mkdir]
                                                                                                    sys_mkdir,
3930 extern int sys_close(void);
                                                                                 3980 [SYS_close]
                                                                                                    sys_close,
3931 extern int sys dup(void);
                                                                                 3981 [SYS halt]
                                                                                                    sys_halt,
3932 extern int sys_exec(void);
                                                                                 3982 [SYS_date]
                                                                                                    sys_date,
3933 extern int sys_exit(void);
                                                                                 3983 [SYS_getuid] sys_getuid,
3934 extern int sys fork(void);
                                                                                 3984 [SYS_getgid] sys_getgid,
3935 extern int sys_fstat(void);
                                                                                 3985 [SYS_getppid] sys_getppid,
3936 extern int sys_getpid(void);
                                                                                 3986 [SYS_setuid] sys_setuid,
3937 extern int sys kill(void);
                                                                                 3987 [SYS setgid] sys setgid,
3938 extern int sys_link(void);
                                                                                 3988 [SYS_getprocs] sys_getprocs,
3939 extern int sys_mkdir(void);
                                                                                 3989 };
3940 extern int sys mknod(void);
                                                                                 3990
                                                                                 3991
3941 extern int sys_open(void);
3942 extern int sys_pipe(void);
                                                                                 3992
3943 extern int sys read(void);
                                                                                 3993
3944 extern int sys_sbrk(void);
                                                                                 3994
3945 extern int sys sleep(void);
                                                                                 3995
3946 extern int sys unlink(void);
                                                                                 3996
3947 extern int sys_wait(void);
                                                                                 3997
3948 extern int sys write(void);
                                                                                 3998
3949 extern int sys_uptime(void);
                                                                                 3999
```

Sheet 39 Sheet 39

```
4000 char * sysCallNames[] = {
                                                                                   4050 #include "types.h"
4001 [SYS fork]
                                                                                   4051 #include "x86.h"
                   "fork",
4002 [SYS exit]
                   "exit",
                                                                                   4052 #include "defs.h"
4003 [SYS_wait]
                                                                                   4053 #include "date.h"
                   "wait",
4004 [SYS_pipe]
                   "pipe",
                                                                                   4054 #include "param.h"
4005 [SYS_read]
                                                                                   4055 #include "memlayout.h"
                   "read",
4006 [SYS_kill]
                   "kill",
                                                                                   4056 #include "mmu.h"
4007 [SYS_exec]
                   "exec",
                                                                                   4057 #include "proc.h"
4008 [SYS_fstat]
                   "fstat",
                                                                                   4058;
4009 [SYS_chdir]
                   "chdir",
                                                                                   4059 int
4010 [SYS_dup]
                   "dup",
                                                                                   4060 sys_fork(void)
4011 [SYS_getpid]
                   "getpid",
                                                                                   4061 {
4012 [SYS_sbrk]
                   "sbrk",
                                                                                   4062 return fork();
4013 [SYS_sleep]
                   "sleep",
                                                                                   4063 }
4014 [SYS_uptime]
                   "uptime",
                                                                                   4064
4015 [SYS_open]
                   "open",
                                                                                   4065 int
4016 [SYS_write]
                   "write",
                                                                                   4066 sys_exit(void)
4017 [SYS_mknod]
                   "mknod",
                                                                                   4067 {
4018 [SYS_unlink] "unlink",
                                                                                   4068 exit();
4019 [SYS link]
                   "link",
                                                                                   4069 return 0; // not reached
4020 [SYS_mkdir]
                   "mkdir",
                                                                                   4070 }
4021 [SYS_close]
                   "close",
                                                                                   4071
4022 [SYS halt]
                   "halt",
                                                                                   4072 int
4023 [SYS_date]
                   "date",
                                                                                   4073 sys_wait(void)
4024 };
                                                                                   4074 {
4025
                                                                                   4075 return wait();
4026 void
                                                                                   4076 }
4027 syscall(void)
                                                                                   4077
4028 {
                                                                                   4078 int
4029 int num;
                                                                                   4079 sys_kill(void)
4030
                                                                                   4080 {
4031 num = proc->tf->eax;
                                                                                   4081 int pid;
4032 if(num > 0 && num < NELEM(syscalls) && syscalls[num]) {
                                                                                   4082
4033
         proc->tf->eax = syscalls[num]();
                                                                                   4083 if(argint(0, &pid) < 0)
         //int returnValue = proc->tf->eax;
4034
                                                                                   4084
                                                                                           return -1;
4035
         //Omits printing the 'write' system call (16) for readability
                                                                                   4085 return kill(pid);
4036
         //if(num != 16) { cprintf("%s -> %d\n", sysCallNames[num], returnValue); 4086 }
4037
       } else {
                                                                                   4087
4038
         cprintf("%d %s: unknown sys call %d\n",
                                                                                   4088 int
4039
                 proc->pid, proc->name, num);
                                                                                   4089 sys_getpid(void)
4040
         proc \rightarrow tf \rightarrow eax = -1;
                                                                                   4090 {
4041
                                                                                   4091 return proc->pid;
4042 }
                                                                                   4092 }
4043
                                                                                   4093
4044
                                                                                   4094
4045
                                                                                   4095
4046
                                                                                   4096
4047
                                                                                   4097
4048
                                                                                   4098
                                                                                   4099
4049
```

Sheet 40 Sheet 40

```
4100 int
4101 sys_sbrk(void)
4102 {
4103 int addr;
4104 int n;
4105
4106 if(argint(0, &n) < 0)
      return -1;
4107
4108 addr = proc->sz;
4109 if(growproc(n) < 0)
4110
      return -1;
4111 return addr;
4112 }
4113
4114 int
4115 sys_sleep(void)
4116 {
4117 int n;
4118 uint ticks0;
4119
4120 if(argint(0, &n) < 0)
4121
      return -1;
4122 acquire(&tickslock);
4123 ticks0 = ticks;
4124 while(ticks - ticks0 < n){
4125
      if(proc->killed){
4126
        release(&tickslock);
4127
          return -1;
4128
4129
      sleep(&ticks, &tickslock);
4130 }
4131 release(&tickslock);
4132 return 0;
4133 }
4134
4135 // return how many clock tick interrupts have occurred
4136 // since start.
4137 int.
4138 sys_uptime(void)
4139 {
4140 uint xticks;
4141
4142 acquire(&tickslock);
4143 xticks = ticks;
4144 release(&tickslock);
4145 return xticks;
4146 }
4147
4148
4149
```

```
4150 //Turn of the computer
4151 int sys_halt(void){
4152 cprintf("Shutting down ...\n");
4153 outw (0xB004, 0x0 | 0x2000);
4154 return 0;
4155 }
4156
4157 int
4158 sys_date(void)
4159 {
4160
        struct rtcdate * d;
4161
4162
       if(argptr(0, (void*)&d, sizeof(*d)) < 0)</pre>
4163 return -1;
4164
4165
      cmostime(d);
4166
4167
     return 0;
4168 }
4169
4170 int
4171 sys_getuid(void)
4172 {
4173 return proc->uid;
4174 }
4175
4176 int
4177 sys_getgid(void)
4178 {
4179 return proc->gid;
4180 }
4181
4182 int
4183 sys_getppid(void)
4184 {
4185 return proc->ppid;
4186 }
4187
4188 int
4189 sys_setuid(void)
4190 {
4191 int new_uid;
4192
4193 if(argint(0, &new_uid) < 0) //test if argint failed
4194
      return -1;
4195
4196 proc->uid = new uid;
4197
      return 0;
4198 }
4199
```

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Sheet 42 Sheet 42

Sheet 43 Sheet 43

```
4400 #define T_DIR 1 // Directory
4401 #define T FILE 2 // File
4402 #define T DEV 3 // Device
4403
4404 struct stat {
4405 short type; // Type of file
4406 int dev;
                  // File system's disk device
4407 uint ino;
                // Inode number
4408 short nlink; // Number of links to file
4409 uint size; // Size of file in bytes
4410 };
4411
4412
4413
4414
4415
4416
4417
4418
4419
4420
4421
4422
4423
4424
4425
4426
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4430
4431
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4449
```

```
4450 // On-disk file system format.
4451 // Both the kernel and user programs use this header file.
4452
4453
4454 #define ROOTINO 1 // root i-number
4455 #define BSIZE 512 // block size
4456
4457 // Disk layout:
4458 // [ boot block | super block | log | inode blocks | free bit map | data block
4460 // mkfs computes the super block and builds an initial file system. The super
4461 // the disk layout:
4462 struct superblock {
4463 uint size;
                         // Size of file system image (blocks)
4464 uint nblocks;
                         // Number of data blocks
4465 uint ninodes;
                        // Number of inodes.
4466 uint nlog;
                        // Number of log blocks
4467 uint logstart;
                         // Block number of first log block
4468 uint inodestart; // Block number of first inode block
4469 uint bmapstart;
                        // Block number of first free map block
4470 };
4471
4472 #define NDIRECT 12
4473 #define NINDIRECT (BSIZE / sizeof(uint))
4474 #define MAXFILE (NDIRECT + NINDIRECT)
4475
4476 // On-disk inode structure
4477 struct dinode {
4478 short type;
                            // File type
4479 short major;
                            // Major device number (T_DEV only)
4480 short minor;
                            // Minor device number (T_DEV only)
4481 short nlink;
                            // Number of links to inode in file system
4482 uint size;
                            // Size of file (bytes)
4483 uint addrs[NDIRECT+1]; // Data block addresses
4484 };
4485
4486
4487
4488
4489
4490
4491
4492
4493
4494
4495
4496
4497
4498
4499
```

```
4550 struct file {
4500 // Inodes per block.
4501 #define IPB
                          (BSIZE / sizeof(struct dinode))
                                                                                4551 enum { FD_NONE, FD_PIPE, FD_INODE } type;
4502
                                                                                4552 int ref; // reference count
4503 // Block containing inode i
                                                                                4553 char readable;
4504 #define IBLOCK(i, sb) ((i) / IPB + sb.inodestart)
                                                                                4554 char writable;
                                                                                4555 struct pipe *pipe;
                                                                                4556 struct inode *ip;
4506 // Bitmap bits per block
4507 #define BPB
                                                                                4557 uint off;
                          (BSIZE*8)
4508
                                                                                4558 };
4509 // Block of free map containing bit for block b
                                                                                4559
4510 #define BBLOCK(b, sb) (b/BPB + sb.bmapstart)
                                                                                4560
                                                                                4561 // in-memory copy of an inode
4512 // Directory is a file containing a sequence of dirent structures.
                                                                                4562 struct inode {
4513 #define DIRSIZ 14
                                                                                4563 uint dev;
                                                                                                          // Device number
4514
                                                                                4564 uint inum;
                                                                                                          // Inode number
4515 struct dirent {
                                                                                4565 int ref;
                                                                                                          // Reference count
4516 ushort inum;
                                                                                4566 int flags;
                                                                                                         // I_BUSY, I_VALID
4517 char name[DIRSIZ];
                                                                                4567
4518 };
                                                                                4568 short type;
                                                                                                         // copy of disk inode
4519
                                                                                4569 short major;
                                                                                4570 short minor;
4520
4521
                                                                                4571 short nlink;
4522
                                                                                4572 uint size;
4523
                                                                                4573 uint addrs[NDIRECT+1];
4524
                                                                                4574 };
4525
                                                                                4575 #define I BUSY 0x1
4526
                                                                                4576 #define I_VALID 0x2
4527
                                                                                4577
4528
                                                                                4578 // table mapping major device number to
4529
                                                                                4579 // device functions
4530
                                                                                4580 struct devsw {
4531
                                                                                4581 int (*read)(struct inode*, char*, int);
4532
                                                                                4582 int (*write)(struct inode*, char*, int);
4533
                                                                                4583 };
                                                                                4584
4534
4535
                                                                                4585 extern struct devsw devsw[];
4536
4537
                                                                                4587 #define CONSOLE 1
4538
                                                                                4588
4539
                                                                                4589
4540
                                                                                4590
                                                                                4591
4541
4542
                                                                                4592
4543
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4549
                                                                                4599
```

```
4600 // Blank page.
4601
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4649
```

```
4650 // Simple PIO-based (non-DMA) IDE driver code.
4651
4652 #include "types.h"
4653 #include "defs.h"
4654 #include "param.h"
4655 #include "memlayout.h"
4656 #include "mmu.h"
4657 #include "proc.h"
4658 #include "x86.h"
4659 #include "traps.h"
4660 #include "spinlock.h"
4661 #include "fs.h"
4662 #include "buf.h"
4663
4664 #define SECTOR_SIZE
4665 #define IDE_BSY
                           0x80
4666 #define IDE_DRDY
                           0x40
4667 #define IDE_DF
                           0x20
4668 #define IDE_ERR
                           0x01
4669
4670 #define IDE_CMD_READ 0x20
4671 #define IDE_CMD_WRITE 0x30
4672
4673 // idequeue points to the buf now being read/written to the disk.
4674 // idequeue->gnext points to the next buf to be processed.
4675 // You must hold idelock while manipulating queue.
4677 static struct spinlock idelock;
4678 static struct buf *idequeue;
4679
4680 static int havedisk1;
4681 static void idestart(struct buf*);
4683 // Wait for IDE disk to become ready.
4684 static int
4685 idewait(int checkerr)
4686 {
4687 int r;
4688
4689 while(((r = inb(0x1f7)) & (IDE_BSY|IDE_DRDY)) != IDE_DRDY)
4690
4691 if(checkerr && (r & (IDE_DF|IDE_ERR)) != 0)
4692
       return -1;
4693 return 0;
4694 }
4695
4696
4697
4698
4699
```

Sheet 47 Sheet 47

```
4800 // Sync buf with disk.
4801 // If B DIRTY is set, write buf to disk, clear B DIRTY, set B VALID.
4802 // Else if B VALID is not set, read buf from disk, set B VALID.
4803 void
4804 iderw(struct buf *b)
4805 {
4806 struct buf **pp;
4807
4808 if(!(b->flags & B BUSY))
       panic("iderw: buf not busy");
4809
4810 if((b->flags & (B_VALID|B_DIRTY)) == B_VALID)
4811
       panic("iderw: nothing to do");
4812 if(b->dev != 0 && !havedisk1)
4813
        panic("iderw: ide disk 1 not present");
4814
4815 acquire(&idelock);
4816
4817 // Append b to idequeue.
4818 b->gnext = 0;
4819 for(pp=&idequeue; *pp; pp=&(*pp)->qnext)
4820
4821 *pp = b;
4822
4823 // Start disk if necessary.
4824 if(idequeue == b)
4825
       idestart(b);
4826
4827 // Wait for request to finish.
4828 while((b->flags & (B_VALID|B_DIRTY)) != B_VALID){
4829
       sleep(b, &idelock);
4830 }
4831
4832 release(&idelock);
4833 }
4834
4835
4836
4837
4838
4839
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4849
```

```
4850 // Buffer cache.
4851 //
4852 // The buffer cache is a linked list of buf structures holding
4853 // cached copies of disk block contents. Caching disk blocks
4854 // in memory reduces the number of disk reads and also provides
4855 // a synchronization point for disk blocks used by multiple processes.
4856 //
4857 // Interface:
4858 // * To get a buffer for a particular disk block, call bread.
4859 // * After changing buffer data, call bwrite to write it to disk.
4860 // * When done with the buffer, call brelse.
4861 // * Do not use the buffer after calling brelse.
4862 // * Only one process at a time can use a buffer,
4863 //
            so do not keep them longer than necessary.
4864 //
4865 // The implementation uses three state flags internally:
4866 // * B BUSY: the block has been returned from bread
4867 // and has not been passed back to brelse.
4868 // * B_VALID: the buffer data has been read from the disk.
4869 // * B DIRTY: the buffer data has been modified
           and needs to be written to disk.
4870 //
4871
4872 #include "types.h"
4873 #include "defs.h"
4874 #include "param.h"
4875 #include "spinlock.h"
4876 #include "fs.h"
4877 #include "buf.h"
4878
4879 struct {
4880 struct spinlock lock;
4881 struct buf buf[NBUF];
4882
4883 // Linked list of all buffers, through prev/next.
4884 // head.next is most recently used.
4885 struct buf head;
4886 } bcache;
4887
4888 void
4889 binit(void)
4890 {
4891 struct buf *b;
4892
4893 initlock(&bcache.lock, "bcache");
4894
4895
4896
4897
4898
4899
```

```
4900 // Create linked list of buffers
4901 bcache.head.prev = &bcache.head;
4902 bcache.head.next = &bcache.head;
4903 for(b = bcache.buf; b < bcache.buf+NBUF; b++){
4904
       b->next = bcache.head.next;
4905
        b->prev = &bcache.head;
4906
        b->dev = -1;
4907
        bcache.head.next->prev = b;
4908
        bcache.head.next = b;
4909 }
4910 }
4911
4912 // Look through buffer cache for block on device dev.
4913 // If not found, allocate a buffer.
4914 // In either case, return B BUSY buffer.
4915 static struct buf*
4916 bget(uint dev, uint blockno)
4917 {
4918 struct buf *b;
4919
4920 acquire(&bcache.lock);
4921
4922 loop:
4923 // Is the block already cached?
4924 for(b = bcache.head.next; b != &bcache.head; b = b->next){
4925
       if(b->dev == dev && b->blockno == blockno){
4926
          if(!(b->flags & B_BUSY)){
4927
            b->flags |= B_BUSY;
4928
            release(&bcache.lock);
4929
            return b;
4930
4931
          sleep(b, &bcache.lock);
4932
          goto loop;
4933
4934 }
4935
4936 // Not cached; recycle some non-busy and clean buffer.
4937 // "clean" because B DIRTY and !B BUSY means log.c
4938 // hasn't yet committed the changes to the buffer.
4939 for(b = bcache.head.prev; b != &bcache.head; b = b->prev){
4940
       if((b->flags & B BUSY) == 0 && (b->flags & B DIRTY) == 0){
4941
          b->dev = dev;
4942
          b->blockno = blockno;
4943
          b->flags = B BUSY;
4944
          release(&bcache.lock);
4945
          return b;
4946
4947
4948 panic("bget: no buffers");
4949 }
```

```
4950 // Return a B_BUSY buf with the contents of the indicated block.
4951 struct buf*
4952 bread(uint dev, uint blockno)
4953 {
4954 struct buf *b;
4955
4956 b = bget(dev, blockno);
4957 if(!(b->flags & B_VALID)) {
4958
        iderw(b);
4959 }
4960 return b;
4961 }
4962
4963 // Write b's contents to disk. Must be B BUSY.
4964 void
4965 bwrite(struct buf *b)
4966 {
4967 if((b-)flags \& B BUSY) == 0)
4968
        panic("bwrite");
4969 b->flags |= B_DIRTY;
4970 iderw(b);
4971 }
4972
4973 // Release a B_BUSY buffer.
4974 // Move to the head of the MRU list.
4975 void
4976 brelse(struct buf *b)
4977 {
4978 if((b->flags & B_BUSY) == 0)
4979
        panic("brelse");
4980
4981 acquire(&bcache.lock);
4982
4983 b->next->prev = b->prev;
4984 b->prev->next = b->next;
4985 b->next = bcache.head.next;
4986 b->prev = &bcache.head;
4987 bcache.head.next->prev = b;
4988 bcache.head.next = b;
4989
4990 b->flags &= ~B BUSY;
4991 wakeup(b);
4992
4993 release(&bcache.lock);
4994 }
4995
4996
4997
4998
4999
```

```
5000 // Blank page.
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5049
```

```
5050 #include "types.h"
5051 #include "defs.h"
5052 #include "param.h"
5053 #include "spinlock.h"
5054 #include "fs.h"
5055 #include "buf.h"
5057 // Simple logging that allows concurrent FS system calls.
5058 //
5059 // A log transaction contains the updates of multiple FS system
5060 // calls. The logging system only commits when there are
5061 // no FS system calls active. Thus there is never
5062 // any reasoning required about whether a commit might
5063 // write an uncommitted system call's updates to disk.
5065 // A system call should call begin_op()/end_op() to mark
5066 // its start and end. Usually begin_op() just increments
5067 // the count of in-progress FS system calls and returns.
5068 // But if it thinks the log is close to running out, it
5069 // sleeps until the last outstanding end op() commits.
5070 //
5071 // The log is a physical re-do log containing disk blocks.
5072 // The on-disk log format:
5073 // header block, containing block #s for block A, B, C, ...
5074 // block A
5075 // block B
5076 // block C
5077 // ...
5078 // Log appends are synchronous.
5080 // Contents of the header block, used for both the on-disk header block
5081 // and to keep track in memory of logged block# before commit.
5082 struct logheader {
5083 int n;
5084 int block[LOGSIZE];
5085 };
5086
5087 struct log {
5088 struct spinlock lock;
5089 int start;
5090 int size;
5091 int outstanding; // how many FS sys calls are executing.
5092 int committing; // in commit(), please wait.
5093 int dev;
5094 struct logheader lh;
5095 };
5096
5097
5098
5099
```

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Sheet 51 Sheet 51

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```
5250 static void
5200 // called at the end of each FS system call.
5201 // commits if this was the last outstanding operation.
                                                                               5251 commit()
5202 void
                                                                               5252 {
                                                                               5253 if (log.lh.n > 0) {
5203 end_op(void)
5204 {
                                                                               5254
                                                                                        write_log();
                                                                                                       // Write modified blocks from cache to log
5205 int do commit = 0;
                                                                               5255
                                                                                        write head();  // Write header to disk -- the real commit
5206
                                                                               5256
                                                                                        install_trans(); // Now install writes to home locations
5207 acquire(&log.lock);
                                                                               5257
                                                                                        log.lh.n = 0;
5208 log.outstanding -= 1;
                                                                               5258
                                                                                        write head();  // Erase the transaction from the log
                                                                               5259 }
5209 if(log.committing)
5210
      panic("log.committing");
                                                                               5260 }
5211 if(log.outstanding == 0){
                                                                               5261
5212
        do_commit = 1;
                                                                               5262 // Caller has modified b->data and is done with the buffer.
                                                                               5263 // Record the block number and pin in the cache with B_DIRTY.
5213
        log.committing = 1;
5214 } else {
                                                                               5264 // commit()/write_log() will do the disk write.
5215
        // begin_op() may be waiting for log space.
                                                                               5265 //
5216
        wakeup(&log);
                                                                               5266 // log_write() replaces bwrite(); a typical use is:
5217 }
                                                                               5267 // bp = bread(...)
                                                                               5268 // modify bp->data[]
5218 release(&log.lock);
5219
                                                                               5269 // log write(bp)
5220 if(do commit){
                                                                               5270 // brelse(bp)
5221
       // call commit w/o holding locks, since not allowed
                                                                               5271 void
5222
        // to sleep with locks.
                                                                               5272 log write(struct buf *b)
5223
        commit();
                                                                               5273 {
5224
        acquire(&log.lock);
                                                                               5274 int i;
5225
        log.committing = 0;
                                                                               5275
5226
                                                                               5276 if (\log. \ln n) = LOGSIZE \mid \log. \ln n > = \log. size - 1
        wakeup(&log);
5227
        release(&log.lock);
                                                                               5277
                                                                                        panic("too big a transaction");
5228 }
                                                                               5278 if (log.outstanding < 1)
5229 }
                                                                                        panic("log_write outside of trans");
                                                                               5279
5230
                                                                               5280
5231 // Copy modified blocks from cache to log.
                                                                               5281 acquire(&log.lock);
5232 static void
                                                                               5282 for (i = 0; i < log.lh.n; i++) {
5233 write_log(void)
                                                                               5283
                                                                                        if (log.lh.block[i] == b->blockno) // log absorbtion
                                                                               5284
5234 {
5235 int tail;
                                                                               5285 }
5236
                                                                               5286 log.lh.block[i] = b->blockno;
5237 for (tail = 0; tail < log.lh.n; tail++) {
                                                                               5287 if (i == loq.lh.n)
5238
       struct buf *to = bread(log.dev, log.start+tail+1); // log block
                                                                               5288
                                                                                       log.lh.n++;
5239
        struct buf *from = bread(log.dev, log.lh.block[tail]); // cache block
                                                                               5289
                                                                                    b->flags |= B_DIRTY; // prevent eviction
5240
        memmove(to->data, from->data, BSIZE);
                                                                               5290
                                                                                     release(&log.lock);
        bwrite(to); // write the log
                                                                               5291 }
5241
5242
        brelse(from);
                                                                               5292
5243
        brelse(to);
                                                                               5293
5244 }
                                                                               5294
5245 }
                                                                               5295
5246
                                                                               5296
5247
                                                                               5297
5248
                                                                               5298
5249
                                                                               5299
```

Sheet 52 Sheet 52

```
5350 // Blocks.
5300 // File system implementation. Five layers:
5301 // + Blocks: allocator for raw disk blocks.
                                                                                 5351
5302 // + Log: crash recovery for multi-step updates.
                                                                                 5352 // Allocate a zeroed disk block.
5303 // + Files: inode allocator, reading, writing, metadata.
                                                                                 5353 static uint
5304 // + Directories: inode with special contents (list of other inodes!)
                                                                                 5354 balloc(uint dev)
5305 // + Names: paths like /usr/rtm/xv6/fs.c for convenient naming.
                                                                                 5355 {
5306 //
                                                                                 5356 int b, bi, m;
5307 // This file contains the low-level file system manipulation
                                                                                 5357
                                                                                      struct buf *bp;
5308 // routines. The (higher-level) system call implementations
                                                                                 5358
5309 // are in sysfile.c.
                                                                                 5359 bp = 0;
5310
                                                                                 5360
                                                                                       for(b = 0; b < sb.size; b += BPB){
5311 #include "types.h"
                                                                                         bp = bread(dev, BBLOCK(b, sb));
                                                                                 5361
5312 #include "defs.h"
                                                                                 5362
                                                                                         for(bi = 0; bi < BPB && b + bi < sb.size; bi++){
5313 #include "param.h"
                                                                                 5363
                                                                                           m = 1 << (bi % 8);
5314 #include "stat.h"
                                                                                5364
                                                                                           if((bp->data[bi/8] \& m) == 0){ // Is block free?}
5315 #include "mmu.h"
                                                                                 5365
                                                                                             bp->data[bi/8] |= m; // Mark block in use.
5316 #include "proc.h"
                                                                                 5366
                                                                                             log write(bp);
5317 #include "spinlock.h"
                                                                                 5367
                                                                                             brelse(bp);
5318 #include "fs.h"
                                                                                 5368
                                                                                             bzero(dev, b + bi);
5319 #include "buf.h"
                                                                                 5369
                                                                                             return b + bi;
5320 #include "file.h"
                                                                                 5370
5321
                                                                                 5371
5322 #define min(a, b) ((a) < (b) ? (a) : (b))
                                                                                 5372
                                                                                         brelse(bp);
5323 static void itrunc(struct inode*);
                                                                                 5373
5324 struct superblock sb; // there should be one per dev, but we run with one (5374 panic("balloc: out of blocks");
5325
                                                                                 5375 }
5326 // Read the super block.
                                                                                 5376
5327 void
                                                                                5377 // Free a disk block.
5328 readsb(int dev, struct superblock *sb)
                                                                                 5378 static void
                                                                                 5379 bfree(int dev, uint b)
5329 {
5330 struct buf *bp;
                                                                                 5380 {
5331
                                                                                 5381 struct buf *bp;
5332 bp = bread(dev, 1);
                                                                                5382 int bi, m;
5333 memmove(sb, bp->data, sizeof(*sb));
                                                                                 5383
5334 brelse(bp);
                                                                                 5384 readsb(dev, &sb);
5335 }
                                                                                 5385 bp = bread(dev, BBLOCK(b, sb));
5336
                                                                                 5386 bi = b % BPB;
                                                                                 5387 \quad m = 1 \ll (bi \% 8);
5337 // Zero a block.
5338 static void
                                                                                5388 if((bp->data[bi/8] & m) == 0)
5339 bzero(int dev, int bno)
                                                                                5389
                                                                                         panic("freeing free block");
5340 {
                                                                                 5390 bp->data[bi/8] &= ~m;
5341 struct buf *bp;
                                                                                5391 log_write(bp);
5342
                                                                                 5392 brelse(bp);
5343 bp = bread(dev, bno);
                                                                                 5393 }
5344 memset(bp->data, 0, BSIZE);
                                                                                5394
5345 log_write(bp);
                                                                                 5395
5346 brelse(bp);
                                                                                 5396
                                                                                5397
5347 }
5348
                                                                                 5398
5349
                                                                                 5399
```

Sheet 53 Sheet 53

```
5400 // Inodes.
                                                                                5450 //
5401 //
                                                                                5451 // ilock() is separate from iget() so that system calls can
5402 // An inode describes a single unnamed file.
                                                                                5452 // get a long-term reference to an inode (as for an open file)
                                                                                5453 // and only lock it for short periods (e.g., in read()).
5403 // The inode disk structure holds metadata: the file's type,
5404 // its size, the number of links referring to it, and the
                                                                                5454 // The separation also helps avoid deadlock and races during
5405 // list of blocks holding the file's content.
                                                                                5455 // pathname lookup. iqet() increments ip->ref so that the inode
5406 //
                                                                                5456 // stays cached and pointers to it remain valid.
5407 // The inodes are laid out sequentially on disk at
                                                                                5457 //
5408 // sb.startinode. Each inode has a number, indicating its
                                                                                5458 // Many internal file system functions expect the caller to
5409 // position on the disk.
                                                                                5459 // have locked the inodes involved; this lets callers create
5410 //
                                                                                5460 // multi-step atomic operations.
5411 // The kernel keeps a cache of in-use inodes in memory
                                                                                5461
5412 // to provide a place for synchronizing access
                                                                                5462 struct {
5413 // to inodes used by multiple processes. The cached
                                                                                5463 struct spinlock lock;
5414 // inodes include book-keeping information that is
                                                                                5464 struct inode inode[NINODE];
5415 // not stored on disk: ip->ref and ip->flags.
                                                                                5465 } icache;
5416 //
                                                                                5466
5417 // An inode and its in-memory represtative go through a
                                                                                5467 void
5418 // sequence of states before they can be used by the
                                                                                5468 iinit(int dev)
5419 // rest of the file system code.
                                                                                5469 {
5420 //
                                                                                5470 initlock(&icache.lock, "icache");
5421 // * Allocation: an inode is allocated if its type (on disk)
                                                                                5471 readsb(dev, &sb);
5422 // is non-zero. ialloc() allocates, iput() frees if
                                                                                5472 cprintf("sb: size %d nblocks %d ninodes %d nlog %d logstart %d inodestart !
5423 // the link count has fallen to zero.
                                                                                5473
                                                                                               sb.nblocks, sb.ninodes, sb.nlog, sb.logstart, sb.inodestart, sb.bm
5424 //
                                                                                5474 }
5425 // * Referencing in cache: an entry in the inode cache
                                                                                5475
5426 // is free if ip->ref is zero. Otherwise ip->ref tracks
                                                                                5476 static struct inode* iget(uint dev, uint inum);
5427 // the number of in-memory pointers to the entry (open
                                                                                5477
5428 // files and current directories). iget() to find or
                                                                                5478
5429 // create a cache entry and increment its ref, iput()
                                                                                5479
5430 // to decrement ref.
                                                                                5480
5431 //
                                                                                5481
5432 // * Valid: the information (type, size, &c) in an inode
                                                                                5482
5433 // cache entry is only correct when the I_VALID bit
                                                                                5483
5434 // is set in ip->flags. ilock() reads the inode from
                                                                                5484
5435 // the disk and sets I_VALID, while iput() clears
                                                                                5485
5436 // I VALID if ip->ref has fallen to zero.
                                                                                5486
5437 //
                                                                                5487
5438 // * Locked: file system code may only examine and modify
                                                                                5488
5439 // the information in an inode and its content if it
                                                                                5489
         has first locked the inode. The I BUSY flag indicates
                                                                                5490
5441 // that the inode is locked. ilock() sets I BUSY.
                                                                                5491
5442 //
         while iunlock clears it.
                                                                                5492
5443 //
                                                                                5493
5444 // Thus a typical sequence is:
                                                                                5494
5445 // ip = iget(dev, inum)
                                                                                5495
5446 // ilock(ip)
                                                                                5496
5447 // ... examine and modify ip->xxx ...
                                                                                5497
5448 // iunlock(ip)
                                                                                5498
5449 // iput(ip)
                                                                                5499
```

Sheet 54 Sheet 54

```
5500 // Allocate a new inode with the given type on device dev.
5501 // A free inode has a type of zero.
5502 struct inode*
5503 ialloc(uint dev, short type)
5504 {
5505 int inum;
5506 struct buf *bp;
5507 struct dinode *dip;
5508
5509 for(inum = 1; inum < sb.ninodes; inum++){
5510
       bp = bread(dev, IBLOCK(inum, sb));
5511
        dip = (struct dinode*)bp->data + inum%IPB;
5512
      if(dip->type == 0){ // a free inode
5513
        memset(dip, 0, sizeof(*dip));
5514
          dip->type = type;
5515
         log_write(bp); // mark it allocated on the disk
5516
          brelse(bp);
5517
         return iget(dev, inum);
5518
5519
       brelse(bp);
5520 }
5521 panic("ialloc: no inodes");
5522 }
5523
5524 // Copy a modified in-memory inode to disk.
5525 void
5526 iupdate(struct inode *ip)
5527 {
5528 struct buf *bp;
5529 struct dinode *dip;
5530
5531 bp = bread(ip->dev, IBLOCK(ip->inum, sb));
5532 dip = (struct dinode*)bp->data + ip->inum%IPB;
5533 dip->type = ip->type;
5534 dip->major = ip->major;
5535 dip->minor = ip->minor;
5536 dip->nlink = ip->nlink;
5537 dip->size = ip->size;
5538 memmove(dip->addrs, ip->addrs, sizeof(ip->addrs));
5539 log_write(bp);
5540 brelse(bp);
5541 }
5542
5543
5544
5545
5546
5547
5548
5549
```

```
5550 // Find the inode with number inum on device dev
5551 // and return the in-memory copy. Does not lock
5552 // the inode and does not read it from disk.
5553 static struct inode*
5554 iget(uint dev, uint inum)
5555 {
5556 struct inode *ip, *empty;
5557
5558 acquire(&icache.lock);
5559
5560 // Is the inode already cached?
5561 empty = 0;
for(ip = &icache.inode[0]; ip < &icache.inode[NINODE]; ip++){
5563
       if(ip->ref > 0 && ip->dev == dev && ip->inum == inum){
5564
          ip->ref++;
5565
          release(&icache.lock);
5566
        return ip;
5567
if (empty == 0 \&\& ip > ref == 0) // Remember empty slot.
5569
          empty = ip;
5570 }
5571
5572 // Recycle an inode cache entry.
if(empty == 0)
       panic("iget: no inodes");
5574
5575
5576 ip = empty;
5577 ip->dev = dev;
5578 ip->inum = inum;
5579 ip->ref = 1;
5580 ip->flags = 0;
5581 release(&icache.lock);
5582
5583 return ip;
5584 }
5585
5586 // Increment reference count for ip.
5587 // Returns ip to enable ip = idup(ip1) idiom.
5588 struct inode*
5589 idup(struct inode *ip)
5590 {
5591 acquire(&icache.lock);
5592 ip->ref++;
5593 release(&icache.lock);
5594 return ip;
5595 }
5596
5597
5598
5599
```

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Sheet 56 Sheet 56

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```
5750 // Truncate inode (discard contents).
5751 // Only called when the inode has no links
5752 // to it (no directory entries referring to it)
5753 // and has no in-memory reference to it (is
5754 // not an open file or current directory).
5755 static void
5756 itrunc(struct inode *ip)
5757 {
5758 int i, j;
5759 struct buf *bp;
5760 uint *a;
5761
5762 for(i = 0; i < NDIRECT; i++){
5763
       if(ip->addrs[i]){
5764
          bfree(ip->dev, ip->addrs[i]);
5765
          ip->addrs[i] = 0;
5766
5767
     }
5768
5769 if(ip->addrs[NDIRECT]){
5770
        bp = bread(ip->dev, ip->addrs[NDIRECT]);
5771
        a = (uint*)bp->data;
5772
        for(j = 0; j < NINDIRECT; j++)
5773
          if(a[j])
5774
            bfree(ip->dev, a[j]);
5775
5776
        brelse(bp);
5777
        bfree(ip->dev, ip->addrs[NDIRECT]);
5778
        ip->addrs[NDIRECT] = 0;
5779 }
5780
5781 	 ip->size = 0;
5782 iupdate(ip);
5783 }
5784
5785 // Copy stat information from inode.
5786 void
5787 stati(struct inode *ip, struct stat *st)
5788 {
5789 st->dev = ip->dev;
5790 st->ino = ip->inum;
5791 st->type = ip->type;
5792 st->nlink = ip->nlink;
5793 st->size = ip->size;
5794 }
5795
5796
5797
5798
5799
```

5739

5740

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Sheet 58 Sheet 58

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Sheet 59 Sheet 59

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```
6000 // Paths
                                                                                6050 // Look up and return the inode for a path name.
6001
                                                                                6051 // If parent != 0, return the inode for the parent and copy the final
6002 // Copy the next path element from path into name.
                                                                                6052 // path element into name, which must have room for DIRSIZ bytes.
                                                                                6053 // Must be called inside a transaction since it calls iput().
6003 // Return a pointer to the element following the copied one.
6004 // The returned path has no leading slashes,
                                                                                6054 static struct inode*
6005 // so the caller can check *path=='\0' to see if the name is the last one.
                                                                                6055 namex(char *path, int nameiparent, char *name)
6006 // If no name to remove, return 0.
                                                                                6056 {
                                                                                6057 struct inode *ip, *next;
6007 //
6008 // Examples:
                                                                                6058
6009 // skipelem("a/bb/c", name) = "bb/c", setting name = "a"
                                                                                6059 if(*path == '/')
6010 // skipelem("//a//bb", name) = "bb", setting name = "a"
                                                                                6060
                                                                                         ip = iget(ROOTDEV, ROOTINO);
6011 // skipelem("a", name) = "", setting name = "a"
                                                                                6061
6012 // skipelem("", name) = skipelem("///", name) = 0
                                                                                6062
                                                                                        ip = idup(proc->cwd);
6013 //
                                                                                6063
6014 static char*
                                                                                6064 while((path = skipelem(path, name)) != 0){
6015 skipelem(char *path, char *name)
                                                                                6065
                                                                                         ilock(ip);
6016 {
                                                                                6066
                                                                                         if(ip->type != T_DIR){
6017 char *s;
                                                                                6067
                                                                                          iunlockput(ip);
6018 int len;
                                                                                6068
                                                                                          return 0;
6019
                                                                                6069
6020 while(*path == '/')
                                                                                6070
                                                                                         if(nameiparent && *path == '\0'){
6021
        path++;
                                                                                6071
                                                                                          // Stop one level early.
6022 if(*path == 0)
                                                                                6072
                                                                                          iunlock(ip);
6023
       return 0;
                                                                                6073
                                                                                          return ip;
6024 s = path;
                                                                                6074
6025 while(*path != '/' && *path != 0)
                                                                                6075
                                                                                         if((next = dirlookup(ip, name, 0)) == 0){
                                                                                6076
6026
      path++;
                                                                                          iunlockput(ip);
6027 len = path - s;
                                                                                6077
                                                                                          return 0;
6028 if(len >= DIRSIZ)
                                                                                6078
6029
       memmove(name, s, DIRSIZ);
                                                                                6079
                                                                                         iunlockput(ip);
6030 else {
                                                                                6080
                                                                                         ip = next;
6031
        memmove(name, s, len);
                                                                                6081
6032
        name[len] = 0;
                                                                                6082 if(nameiparent){
6033 }
                                                                                6083
                                                                                         iput(ip);
6034 while(*path == '/')
                                                                                6084
                                                                                        return 0;
6035
       path++;
                                                                                6085 }
6036 return path;
                                                                                6086 return ip;
6037 }
                                                                                6087 }
6038
                                                                                6088
6039
                                                                                6089 struct inode*
6040
                                                                                6090 namei(char *path)
6041
                                                                                6091 {
6042
                                                                                6092 char name[DIRSIZ];
6043
                                                                                6093 return namex(path, 0, name);
6044
                                                                                6094 }
6045
                                                                                6095
6046
                                                                                6096
6047
                                                                                6097
6048
                                                                                6098
6049
                                                                                6099
```

Sheet 60 Sheet 60

```
6100 struct inode*
6101 nameiparent(char *path, char *name)
6102 {
6103 return namex(path, 1, name);
6104 }
6105
6106
6107
6108
6109
6110
6111
6112
6113
6114
6115
6116
6117
6118
6119
6120
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6122
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6141
6142
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6144
6145
6146
6147
6148
6149
```

```
6150 //
6151 // File descriptors
6152 //
6153
6154 #include "types.h"
6155 #include "defs.h"
6156 #include "param.h"
6157 #include "fs.h"
6158 #include "file.h"
6159 #include "spinlock.h"
6160
6161 struct devsw devsw[NDEV];
6162 struct {
6163 struct spinlock lock;
6164 struct file file[NFILE];
6165 } ftable;
6166
6167 void
6168 fileinit(void)
6169 {
6170 initlock(&ftable.lock, "ftable");
6171 }
6172
6173 // Allocate a file structure.
6174 struct file*
6175 filealloc(void)
6176 {
6177 struct file *f;
6178
6179 acquire(&ftable.lock);
6180 for(f = ftable.file; f < ftable.file + NFILE; f++){</pre>
6181 if(f->ref == 0)
6182
          f \rightarrow ref = 1;
6183
          release(&ftable.lock);
6184
          return f;
6185
6186 }
6187 release(&ftable.lock);
6188 return 0;
6189 }
6190
6191
6192
6193
6194
6195
6196
6197
6198
6199
```

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Sheet 62 Sheet 62

May 6 16:17 2016 xv6/file.c Page 2

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Sheet 63 Sheet 63

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```
6400 int
                                                                              6450 int
6401 sys_dup(void)
                                                                              6451 sys fstat(void)
6402 {
                                                                              6452 {
6403 struct file *f;
                                                                              6453 struct file *f;
6404 int fd;
                                                                              6454 struct stat *st;
6405
                                                                              6455
6406 if(argfd(0, 0, &f) < 0)
                                                                              6456 if(arqfd(0, 0, &f) < 0 | arqptr(1, (void*)&st, sizeof(*st)) < 0)
      return -1;
                                                                                     return -1;
6407
                                                                              6457
6408 if((fd=fdalloc(f)) < 0)
                                                                              6458 return filestat(f, st);
6409
      return -1;
                                                                              6459 }
6410 filedup(f);
                                                                              6460
6411 return fd;
                                                                              6461 // Create the path new as a link to the same inode as old.
6412 }
                                                                              6462 int
6413
                                                                              6463 sys_link(void)
6414 int
                                                                              6464 {
6415 sys_read(void)
                                                                              6465 char name[DIRSIZ], *new, *old;
6416 {
                                                                              6466 struct inode *dp, *ip;
6417 struct file *f;
                                                                              6467
6418 int n;
                                                                              6468 if(argstr(0, &old) < 0 | argstr(1, &new) < 0)
6419 char *p;
                                                                              6469
                                                                                     return -1;
6420
                                                                              6470
6421 if (arqfd(0, 0, \&f) < 0 \mid arqint(2, \&n) < 0 \mid arqptr(1, \&p, n) < 0)
                                                                              6471 begin op();
6422
      return -1;
                                                                              6472 if((ip = namei(old)) == 0){
6423 return fileread(f, p, n);
                                                                              6473
                                                                                      end_op();
6424 }
                                                                              6474
                                                                                    return -1;
6425
                                                                              6475 }
6426 int
                                                                              6476
6427 sys_write(void)
                                                                              6477 ilock(ip);
6428 {
                                                                              6478 if(ip->type == T_DIR){
6429 struct file *f;
                                                                              6479 iunlockput(ip);
6430 int n;
                                                                              6480 end_op();
6431 char *p;
                                                                              6481
                                                                                      return -1;
                                                                              6482 }
6432
6433 if(argfd(0, 0, &f) < 0 | argint(2, &n) < 0 | argptr(1, &p, n) < 0)
                                                                              6483
      return -1;
6434
                                                                              6484 ip->nlink++;
6435 return filewrite(f, p, n);
                                                                              6485 iupdate(ip);
6436 }
                                                                              6486 iunlock(ip);
6437
                                                                              6487
6438 int
                                                                              6488 if((dp = nameiparent(new, name)) == 0)
6439 sys_close(void)
                                                                              6489
                                                                                     goto bad;
6440 {
                                                                              6490 ilock(dp);
                                                                              if (dp\rightarrow dev != ip\rightarrow dev || dirlink(dp, name, ip\rightarrow inum) < 0)
6441 int fd;
6442 struct file *f;
                                                                              6492
                                                                                       iunlockput(dp);
6443
                                                                              6493
                                                                                       goto bad;
6444 if(argfd(0, &fd, &f) < 0)
                                                                              6494
6445
      return -1;
                                                                              6495 iunlockput(dp);
6446 proc->ofile[fd] = 0;
                                                                              6496 iput(ip);
6447 fileclose(f);
                                                                              6497
6448 return 0;
                                                                              6498 end_op();
                                                                              6499
6449 }
```

Sheet 64 Sheet 64

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Sheet 66 Sheet 66

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6798 } 6799

6748

6749

```
6800 int
6801 sys_pipe(void)
6802 {
6803 int *fd;
6804 struct file *rf, *wf;
6805 int fd0, fd1;
6806
6807 if(argptr(0, (void*)&fd, 2*sizeof(fd[0])) < 0)
6808
      return -1;
6809 if(pipealloc(&rf, &wf) < 0)
6810
      return -1;
6811 fd0 = -1;
6812 if((fd0 = fdalloc(rf)) < 0 | (fd1 = fdalloc(wf)) < 0){
6813
       if(fd0 >= 0)
6814
          proc->ofile[fd0] = 0;
6815
        fileclose(rf);
6816
        fileclose(wf);
6817
       return -1;
6818 }
6819 \quad fd[0] = fd0;
6820 \quad fd[1] = fd1;
6821 return 0;
6822 }
6823
6824
6825
6826
6827
6828
6829
6830
6831
6832
6833
6834
6835
6836
6837
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6840
6841
6842
6843
6844
6845
6846
6847
6848
6849
```

```
6850 #include "types.h"
6851 #include "param.h"
6852 #include "memlayout.h"
6853 #include "mmu.h"
6854 #include "proc.h"
6855 #include "defs.h"
6856 #include "x86.h"
6857 #include "elf.h"
6858
6859 int
6860 exec(char *path, char **argv)
6862 char *s, *last;
6863 int i, off;
6864 uint argc, sz, sp, ustack[3+MAXARG+1];
6865 struct elfhdr elf;
6866 struct inode *ip;
6867 struct proghdr ph;
6868 pde_t *pgdir, *oldpgdir;
6869
6870 begin op();
6871 if((ip = namei(path)) == 0){
6872
       end op();
6873
        return -1;
6874
6875 ilock(ip);
6876 pgdir = 0;
6877
6878 // Check ELF header
6879 if(readi(ip, (char*)&elf, 0, sizeof(elf)) < sizeof(elf))</pre>
       goto bad;
6881 if(elf.magic != ELF_MAGIC)
        goto bad;
6882
6883
6884 if((pgdir = setupkvm()) == 0)
6885
        goto bad;
6886
6887 // Load program into memory.
6888 \text{ sz} = 0;
6889 for(i=0, off=elf.phoff; i<elf.phnum; i++, off+=sizeof(ph)){
6890
        if(readi(ip, (char*)&ph, off, sizeof(ph)) != sizeof(ph))
6891
          goto bad;
6892
         if(ph.type != ELF_PROG_LOAD)
6893
          continue;
6894
        if(ph.memsz < ph.filesz)</pre>
6895
          goto bad;
6896
         if((sz = allocuvm(pgdir, sz, ph.vaddr + ph.memsz)) == 0)
6897
6898
         if(loaduvm(pgdir, (char*)ph.vaddr, ip, ph.off, ph.filesz) < 0)</pre>
6899
          goto bad;
```

Sheet 68 Sheet 68

Sheet 69 Sheet 69

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Sheet 70 Sheet 70

May 6 16:17 2016 xv6/pipe.c Page 1

```
7150 #include "types.h"
7151 #include "x86.h"
7152
7153 void*
7154 memset(void *dst, int c, uint n)
7155 {
7156 if ((int)dst%4 == 0 && n%4 == 0){
7157
      c &= 0xFF;
7158
        stosl(dst, (c<<24)|(c<<16)|(c<<8)|c, n/4);
7159 } else
7160
        stosb(dst, c, n);
7161 return dst;
7162 }
7163
7164 int
7165 memcmp(const void *v1, const void *v2, uint n)
7166 {
7167 const uchar *s1, *s2;
7168
7169 s1 = v1;
7170 	 s2 = v2;
7171 while(n-- > 0){
7172
      if(*s1 != *s2)
7173
        return *s1 - *s2;
7174
      s1++, s2++;
7175 }
7176
7177 return 0;
7178 }
7179
7180 void*
7181 memmove(void *dst, const void *src, uint n)
7182 {
7183 const char *s;
7184 char *d;
7185
7186 s = src;
7187 d = dst;
7188 if (s < d \&\& s + n > d)
7189
     s += n;
7190 d += n;
7191
       while(n-- > 0)
7192
        *--d = *--s;
7193 } else
7194
        while(n-- > 0)
7195
          *d++ = *s++;
7196
7197 return dst;
7198 }
7199
```

7147

7148

7149

	emcpy exists to placate GCC. Use memmove.	7250 int
7201 void*		7251 strlen(const char *s)
7202 memcp	y(void *dst, const void *src, uint n)	7252 {
7203 {		7253 int n;
	curn memmove(dst, src, n);	7254
7205 }		7255 $for(n = 0; s[n]; n++)$
7206		7256 ;
7207 int		7257 return n;
7208 strnc	emp(const char *p, const char *q, uint n)	7258 }
7209 {		7259
	le(n > 0 & p & p == *q)	7260
7211 n	ı, p++, q++;	7261
7212 if(	n == 0)	7262
7213 r	return 0;	7263
7214 ret	urn (uchar)*p - (uchar)*q;	7264
7215 }		7265
7216		7266
7217 char*		7267
7218 strnc	py(char *s, const char *t, int n)	7268
7219 {		7269
7220 cha	ar *os;	7270
7221		7271
7222 os	= S;	7272
7223 whi	le(n > 0 & (*s++ = *t++) != 0)	7273
7224 ;		7274
7225 whi	le(n>0)	7275
7226 *	s++ = 0;	7276
7227 ret	curn os;	7277
7228 }		7278
7229		7279
7230 // Li	ke strncpy but guaranteed to NUL-terminate.	7280
7231 char*		7281
	strcpy(char *s, const char *t, int n)	7282
7233 {		7283
•	ur *os;	7284
7235		7285
	= S;	7286
	$n \ll 0$	7287
	return os;	7288
	le(-n > 0 & (*s++ = *t++) != 0)	7289
7240 ;		7290
	= 0;	7291
	curn os;	7292
7243 }		7293
7244		7294
7245		7295
7246		7296
7247		7297
7248		7298
7249		7299

```
7300 // See MultiProcessor Specification Version 1.[14]
                                                                               7350 // Table entry types
7301
                                                                               7351 #define MPPROC
                                                                                                      0x00 // One per processor
7302 struct mp {
                            // floating pointer
                                                                               7352 #define MPBUS
                                                                                                      0x01 // One per bus
                                    // "_MP_"
                                                                               7353 #define MPIOAPIC
                                                                                                     0x02 // One per I/O APIC
7303 uchar signature[4];
                                    // phys addr of MP config table
7304 void *physaddr;
                                                                               7354 #define MPIOINTR
                                                                                                     0x03 // One per bus interrupt source
7305 uchar length;
                                    // 1
                                                                               7355 #define MPLINTR
                                                                                                      0x04 // One per system interrupt source
                                    // [14]
7306 uchar specrev;
                                                                               7356
                                    // all bytes must add up to 0
                                                                               7357
7307 uchar checksum;
7308 uchar type;
                                    // MP system config type
                                                                               7358
7309 uchar imcrp;
                                                                               7359
7310 uchar reserved[3];
                                                                               7360
7311 };
                                                                               7361
7312
                                                                               7362
7313 struct mpconf {
                            // configuration table header
                                                                               7363
7314 uchar signature[4];
                                                                               7364
                                    // "PCMP"
7315 ushort length;
                                    // total table length
                                                                               7365
7316 uchar version;
                                    // [14]
                                                                               7366
7317 uchar checksum;
                                    // all bytes must add up to 0
                                                                               7367
7318 uchar product[20];
                                    // product id
                                                                               7368
7319 uint *oemtable;
                                    // OEM table pointer
                                                                               7369
7320 ushort oemlength;
                                    // OEM table length
                                                                               7370
7321 ushort entry;
                                    // entry count
                                                                               7371
7322 uint *lapicaddr;
                                    // address of local APIC
                                                                               7372
7323 ushort xlength;
                                    // extended table length
                                                                               7373
7324 uchar xchecksum;
                                    // extended table checksum
                                                                               7374
7325 uchar reserved;
                                                                               7375
7326 };
                                                                               7376
7327
                                                                               7377
7328 struct mpproc {
                            // processor table entry
                                                                               7378
7329 uchar type;
                                    // entry type (0)
                                                                               7379
                                                                               7380
7330 uchar apicid;
                                    // local APIC id
7331 uchar version;
                                    // local APIC verison
                                                                               7381
7332 uchar flags;
                                                                               7382
                                    // CPU flags
7333
        #define MPBOOT 0x02
                                     // This proc is the bootstrap processor.
                                                                               7383
7334 uchar signature[4];
                                    // CPU signature
                                                                                7384
7335 uint feature;
                                    // feature flags from CPUID instruction
                                                                               7385
7336 uchar reserved[8];
                                                                               7386
7337 };
                                                                               7387
7338
                                                                               7388
7339 struct mpioapic {
                            // I/O APIC table entry
                                                                               7389
7340 uchar type;
                                    // entry type (2)
                                                                               7390
7341 uchar apicno;
                                    // I/O APIC id
                                                                               7391
7342 uchar version;
                                    // I/O APIC version
                                                                               7392
7343 uchar flags;
                                   // I/O APIC flags
                                                                               7393
7344 uint *addr;
                                                                               7394
                                   // I/O APIC address
7345 };
                                                                               7395
7346
                                                                               7396
7347
                                                                               7397
7348
                                                                               7398
7349
                                                                               7399
```

Sheet 73 Sheet 73

```
7400 // Blank page.
7401
7402
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7449
```

```
7450 // Multiprocessor support
7451 // Search memory for MP description structures.
7452 // http://developer.intel.com/design/pentium/datashts/24201606.pdf
7453
7454 #include "types.h"
7455 #include "defs.h"
7456 #include "param.h"
7457 #include "memlayout.h"
7458 #include "mp.h"
7459 #include "x86.h"
7460 #include "mmu.h"
7461 #include "proc.h"
7462
7463 struct cpu cpus[NCPU];
7464 static struct cpu *bcpu;
7465 int ismp;
7466 int ncpu;
7467 uchar ioapicid;
7468
7469 int
7470 mpbcpu(void)
7471 {
7472 return bcpu-cpus;
7473 }
7474
7475 static uchar
7476 sum(uchar *addr, int len)
7477 {
7478 int i, sum;
7479
7480 \quad \text{sum} = 0;
7481 for(i=0; i<len; i++)
7482 sum += addr[i];
7483 return sum;
7484 }
7486 // Look for an MP structure in the len bytes at addr.
7487 static struct mp*
7488 mpsearch1(uint a, int len)
7489 {
7490 uchar *e, *p, *addr;
7491
7492 addr = p2v(a);
7493 e = addr + len;
7494 for(p = addr; p < e; p += sizeof(struct mp))
7495
        if(memcmp(p, "_MP_", 4) == 0 && sum(p, sizeof(struct mp)) == 0)
7496
           return (struct mp*)p;
7497 return 0;
7498 }
7499
```

```
7500 // Search for the MP Floating Pointer Structure, which according to the
                                                                               7550 void
7501 // spec is in one of the following three locations:
                                                                               7551 mpinit(void)
7502 // 1) in the first KB of the EBDA;
                                                                               7552 {
7503 // 2) in the last KB of system base memory;
                                                                               7553 uchar *p, *e;
7504 // 3) in the BIOS ROM between 0xE0000 and 0xFFFFF.
                                                                               7554 struct mp *mp;
7505 static struct mp*
                                                                               7555 struct mpconf *conf;
7506 mpsearch(void)
                                                                               7556 struct mpproc *proc;
7507 {
                                                                               7557 struct mpioapic *ioapic;
7508 uchar *bda;
                                                                               7558
7509 uint p;
                                                                               7559 bcpu = &cpus[0];
7510 struct mp *mp;
                                                                               7560 if((conf = mpconfig(\&mp)) == 0)
                                                                                       return;
7511
                                                                               7561
7512 bda = (uchar *) P2V(0x400);
                                                                               7562 ismp = 1;
7513 if((p = ((bda[0x0F]<<8)| bda[0x0E]) << 4)){
                                                                               7563 lapic = (uint*)conf->lapicaddr;
      if((mp = mpsearch1(p, 1024)))
                                                                               7564 for(p=(uchar*)(conf+1), e=(uchar*)conf+conf->length; p<e; ){
7514
7515
          return mp;
                                                                               7565
                                                                                        switch(*p){
7516 } else {
                                                                               7566
                                                                                        case MPPROC:
7517
      p = ((bda[0x14] << 8)|bda[0x13])*1024;
                                                                               7567
                                                                                         proc = (struct mpproc*)p;
7518
      if((mp = mpsearch1(p-1024, 1024)))
                                                                               7568
                                                                                          if(ncpu != proc->apicid){
                                                                                            cprintf("mpinit: ncpu=%d apicid=%d\n", ncpu, proc->apicid);
7519
          return mp;
                                                                               7569
7520 }
                                                                               7570
                                                                                            ismp = 0;
7521 return mpsearch1(0xF0000, 0x10000);
                                                                               7571
7522 }
                                                                               7572
                                                                                          if(proc->flags & MPBOOT)
7523
                                                                               7573
                                                                                            bcpu = &cpus[ncpu];
7524 // Search for an MP configuration table. For now,
                                                                               7574
                                                                                          cpus[ncpu].id = ncpu;
7525 // don't accept the default configurations (physaddr == 0).
                                                                               7575
                                                                                          ncpu++;
7526 // Check for correct signature, calculate the checksum and,
                                                                               7576
                                                                                          p += sizeof(struct mpproc);
7527 // if correct, check the version.
                                                                               7577
                                                                                          continue;
7528 // To do: check extended table checksum.
                                                                               7578
                                                                                        case MPIOAPIC:
7529 static struct mpconf*
                                                                               7579
                                                                                          ioapic = (struct mpioapic*)p;
7530 mpconfig(struct mp **pmp)
                                                                               7580
                                                                                          ioapicid = ioapic->apicno;
7531 {
                                                                               7581
                                                                                          p += sizeof(struct mpioapic);
7532 struct mpconf *conf;
                                                                               7582
                                                                                          continue;
7533 struct mp *mp;
                                                                               7583
                                                                                        case MPBUS:
                                                                                        case MPIOINTR:
7534
                                                                               7584
7535 if((mp = mpsearch()) == 0 || mp->physaddr == 0)
                                                                               7585
                                                                                        case MPLINTR:
7536 return 0;
                                                                               7586
                                                                                         p += 8;
7537 conf = (struct mpconf*) p2v((uint) mp->physaddr);
                                                                               7587
                                                                                          continue;
7538 if(memcmp(conf, "PCMP", 4) != 0)
                                                                               7588
                                                                                        default:
7539
      return 0;
                                                                               7589
                                                                                          cprintf("mpinit: unknown config type %x\n", *p);
7540 if(conf->version != 1 && conf->version != 4)
                                                                               7590
                                                                                          ismp = 0;
                                                                               7591
7541 return 0;
7542 if(sum((uchar*)conf, conf->length) != 0)
                                                                               7592 }
7543
      return 0;
                                                                               7593 if(!ismp){
7544 *pmp = mp;
                                                                                       // Didn't like what we found; fall back to no MP.
                                                                               7594
7545 return conf;
                                                                                        ncpu = 1;
                                                                               7595
                                                                                        lapic = 0;
7546 }
                                                                               7596
7547
                                                                                        ioapicid = 0;
                                                                               7597
7548
                                                                               7598
                                                                                        return;
7549
                                                                               7599 }
```

Sheet 75 Sheet 75

```
7600 if(mp->imcrp){
7601
        // Bochs doesn't support IMCR, so this doesn't run on Bochs.
7602
        // But it would on real hardware.
7603
        outb(0x22, 0x70); // Select IMCR
7604
        outb(0x23, inb(0x23) | 1); // Mask external interrupts.
7605 }
7606 }
7607
7608
7609
7610
7611
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7621
7622
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7645
7646
7647
7648
7649
```

```
7650 // The local APIC manages internal (non-I/O) interrupts.
7651 // See Chapter 8 & Appendix C of Intel processor manual volume 3.
7652
7653 #include "types.h"
7654 #include "defs.h"
7655 #include "date.h"
7656 #include "memlayout.h"
7657 #include "traps.h"
7658 #include "mmu.h"
7659 #include "x86.h"
7660
7661 // Local APIC registers, divided by 4 for use as uint[] indices.
7662 #define ID
                    (0x0020/4) // ID
7663 #define VER
                    (0x0030/4)
                              // Version
7664 #define TPR
                    (0x0080/4) // Task Priority
7665 #define EOI
                    (0x00B0/4) // EOI
7666 #define SVR
                    (0x00F0/4) // Spurious Interrupt Vector
7667 #define ENABLE
                        0x00000100 // Unit Enable
7668 #define ESR
                   (0x0280/4) // Error Status
7669 #define ICRLO (0x0300/4) // Interrupt Command
7670 #define INIT
                        0x00000500 // INIT/RESET
                        0x00000600 // Startup IPI
7671 #define STARTUP
7672 #define DELIVS
                        0x00001000 // Delivery status
7673 #define ASSERT
                        0x00004000 // Assert interrupt (vs deassert)
7674 #define DEASSERT 0x00000000
7675 #define LEVEL
                        0x00008000 // Level triggered
7676 #define BCAST
                        0x00080000 // Send to all APICs, including self.
7677 #define BUSY
                        0x00001000
7678 #define FIXED
                        0x00000000
7679 #define ICRHI (0x0310/4) // Interrupt Command [63:32]
7680 #define TIMER (0x0320/4) // Local Vector Table 0 (TIMER)
7681 #define X1
                        0x0000000B // divide counts by 1
7682 #define PERIODIC 0x00020000 // Periodic
7683 #define PCINT
                   (0x0340/4) // Performance Counter LVT
7684 #define LINTO
                   (0x0350/4) // Local Vector Table 1 (LINT0)
7685 #define LINT1
                   (0x0360/4) // Local Vector Table 2 (LINT1)
7686 #define ERROR
                   (0x0370/4) // Local Vector Table 3 (ERROR)
7687 #define MASKED
                        0x00010000 // Interrupt masked
7688 #define TICR
                    (0x0380/4) // Timer Initial Count
7689 #define TCCR
                    (0x0390/4) // Timer Current Count
7690 #define TDCR
                    (0x03E0/4) // Timer Divide Configuration
7691
7692 volatile uint *lapic; // Initialized in mp.c
7694 static void
7695 lapicw(int index, int value)
7696 {
7697 lapic[index] = value;
7698 lapic[ID]; // wait for write to finish, by reading
7699 }
```

7750 void 7751 lapicinit(void) 7752 { 7753 if(!lapic) return; 7756 // Enable local APIC; set spurious interrupt vector. lapicw(SVR, ENABLE | (T\_IRQ0 + IRQ\_SPURIOUS)); 7759 // The timer repeatedly counts down at bus frequency 7760 // from lapic[TICR] and then issues an interrupt. 7761 // If xv6 cared more about precise timekeeping, 7762 // TICR would be calibrated using an external time source. 7763 lapicw(TDCR, X1); 7764 lapicw(TIMER, PERIODIC | (T\_IRQO + IRQ\_TIMER)); 7765 lapicw(TICR, 10000000); 7767 // Disable logical interrupt lines. 7768 lapicw(LINTO, MASKED); 7769 lapicw(LINT1, MASKED); 7771 // Disable performance counter overflow interrupts // on machines that provide that interrupt entry. 7773 if(((lapic[VER]>>16) & 0xFF) >= 4) lapicw(PCINT, MASKED); 7776 // Map error interrupt to IRQ\_ERROR. 7777 lapicw(ERROR, T\_IRQ0 + IRQ\_ERROR); 7779 // Clear error status register (requires back-to-back writes). 7780 lapicw(ESR, 0); 7781 lapicw(ESR, 0); 7783 // Ack any outstanding interrupts. 7784 lapicw(EOI, 0); 7786 // Send an Init Level De-Assert to synchronise arbitration ID's. 7787 lapicw(ICRHI, 0); 7788 lapicw(ICRLO, BCAST | INIT | LEVEL); 7789 while(lapic[ICRLO] & DELIVS)

7792 // Enable interrupts on the APIC (but not on the processor).

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7794 }

lapicw(TPR, 0);

Sheet 77 Sheet 77

```
7800 int
                                                                                7850 outb(CMOS PORT+1, 0x0A);
7801 cpunum(void)
                                                                                7851 wrv = (ushort*)P2V((0x40 < 4 \mid 0x67)); // Warm reset vector
7802 {
                                                                                7852 \text{ wrv}[0] = 0;
                                                                               7853 wrv[1] = addr >> 4;
7803 // Cannot call cpu when interrupts are enabled:
7804 // result not guaranteed to last long enough to be used!
                                                                                7854
7805 // Would prefer to panic but even printing is chancy here:
                                                                                7855 // "Universal startup algorithm."
7806 // almost everything, including cprintf and panic, calls cpu,
                                                                                7856
                                                                                     // Send INIT (level-triggered) interrupt to reset other CPU.
7807 // often indirectly through acquire and release.
                                                                               7857 lapicw(ICRHI, apicid<<24);
7808 if(readeflags()&FL_IF){
                                                                                7858 lapicw(ICRLO, INIT | LEVEL | ASSERT);
7809
        static int n;
                                                                               7859 microdelay(200);
7810
        if(n++==0)
                                                                                7860
                                                                                      lapicw(ICRLO, INIT | LEVEL);
7811
          cprintf("cpu called from %x with interrupts enabled\n",
                                                                                      microdelay(100); // should be 10ms, but too slow in Bochs!
                                                                                7861
7812
            __builtin_return_address(0));
                                                                                7862
7813 }
                                                                                7863 // Send startup IPI (twice!) to enter code.
7814
                                                                               7864 // Regular hardware is supposed to only accept a STARTUP
7815 if(lapic)
                                                                               7865 // when it is in the halted state due to an INIT. So the second
7816
      return lapic[ID]>>24;
                                                                                    // should be ignored, but it is part of the official Intel algorithm.
7817 return 0;
                                                                                     // Bochs complains about the second one. Too bad for Bochs.
7818 }
                                                                                7868 for(i = 0; i < 2; i++){
7819
                                                                                7869
                                                                                       lapicw(ICRHI, apicid<<24);
                                                                                       lapicw(ICRLO, STARTUP | (addr>>12));
7820 // Acknowledge interrupt.
                                                                               7870
7821 void
                                                                               7871
                                                                                        microdelay(200);
                                                                                7872 }
7822 lapiceoi(void)
7823 {
                                                                               7873 }
7824 if(lapic)
                                                                               7874
7825
        lapicw(EOI, 0);
                                                                                7875 #define CMOS STATA
                                                                                                         0x0a
7826 }
                                                                               7876 #define CMOS_STATB
                                                                                                         0x0b
7827
                                                                               7877 #define CMOS_UIP (1 << 7)
                                                                                                                       // RTC update in progress
7828 // Spin for a given number of microseconds.
                                                                               7878
7829 // On real hardware would want to tune this dynamically.
                                                                               7879 #define SECS
                                                                                                    0x00
7830 void
                                                                               7880 #define MINS
                                                                                                    0x02
7831 microdelay(int us)
                                                                                7881 #define HOURS
                                                                                                    0 \times 04
7832 {
                                                                               7882 #define DAY
                                                                                                    0x07
7833 }
                                                                                7883 #define MONTH
                                                                                                    0x08
7834
                                                                                7884 #define YEAR
                                                                                                    0x09
7835 #define CMOS_PORT
7836 #define CMOS RETURN 0x71
                                                                                7886 static uint cmos_read(uint reg)
                                                                                7887 {
7838 // Start additional processor running entry code at addr.
                                                                               7888 outb(CMOS_PORT, reg);
7839 // See Appendix B of MultiProcessor Specification.
                                                                               7889 microdelay(200);
                                                                               7890
7841 lapicstartap(uchar apicid, uint addr)
                                                                               7891 return inb(CMOS RETURN);
7842 {
                                                                                7892 }
7843 int i;
                                                                                7893
7844 ushort *wrv;
                                                                                7894
                                                                                7895
7845
7846 // "The BSP must initialize CMOS shutdown code to OAH
                                                                                7896
7847 // and the warm reset vector (DWORD based at 40:67) to point at
                                                                                7897
7848 // the AP startup code prior to the [universal startup algorithm]."
                                                                                7898
7849 outb(CMOS_PORT, 0xF); // offset 0xF is shutdown code
                                                                               7899
```

Sheet 78 Sheet 78

```
7900 static void fill_rtcdate(struct rtcdate *r)
7901 {
7902 r->second = cmos_read(SECS);
7903 r->minute = cmos_read(MINS);
7904 r->hour = cmos_read(HOURS);
7905 r\rightarrow day = cmos read(DAY);
7906 r->month = cmos_read(MONTH);
7907 r->year = cmos_read(YEAR);
7908 }
7909
7910 // gemu seems to use 24-hour GWT and the values are BCD encoded
7911 void cmostime(struct rtcdate *r)
7912 {
7913 struct rtcdate t1, t2;
7914 int sb. bcd;
7915
7916    sb = cmos_read(CMOS_STATB);
7917
7918 bcd = (sb \& (1 << 2)) == 0;
7919
7920 // make sure CMOS doesn't modify time while we read it
7921 for (;;) {
7922
        fill rtcdate(&t1);
7923
        if (cmos_read(CMOS_STATA) & CMOS_UIP)
7924
            continue;
7925
        fill rtcdate(&t2);
7926
        if (memcmp(&t1, &t2, sizeof(t1)) == 0)
7927
          break;
7928 }
7929
7930 // convert
7931 if (bcd) {
7932 #define
               CONV(x)
                           (t1.x = ((t1.x >> 4) * 10) + (t1.x & 0xf))
7933
        CONV(second);
7934
        CONV(minute);
7935
        CONV(hour );
7936
        CONV(day);
7937
        CONV(month);
7938
        CONV(year );
7939 #undef
               CONV
7940 }
7941
7942 	 *r = t.1;
7943 r->year += 2000;
7944 }
7945
7946
7947
7948
7949
```

```
7950 // The I/O APIC manages hardware interrupts for an SMP system.
7951 // http://www.intel.com/design/chipsets/datashts/29056601.pdf
7952 // See also picirg.c.
7953
7954 #include "types.h"
7955 #include "defs.h"
7956 #include "traps.h"
7957
7958 #define IOAPIC 0xFEC00000 // Default physical address of IO APIC
7959
7960 #define REG ID
                       0x00 // Register index: ID
7961 #define REG VER
                       0x01 // Register index: version
7962 #define REG_TABLE 0x10 // Redirection table base
7963
7964 // The redirection table starts at REG TABLE and uses
7965 // two registers to configure each interrupt.
7966 // The first (low) register in a pair contains configuration bits.
7967 // The second (high) register contains a bitmask telling which
7968 // CPUs can serve that interrupt.
7969 #define INT DISABLED 0x00010000 // Interrupt disabled
7970 #define INT LEVEL
                           0x00008000 // Level-triggered (vs edge-)
7971 #define INT ACTIVELOW 0x00002000 // Active low (vs high)
                           0x00000800 // Destination is CPU id (vs APIC ID)
7972 #define INT LOGICAL
7974 volatile struct ioapic *ioapic;
7976 // IO APIC MMIO structure: write req, then read or write data.
7977 struct ioapic {
7978 uint reg;
7979 uint pad[3];
7980 uint data;
7981 };
7982
7983 static uint
7984 ioapicread(int reg)
7985 {
7986 ioapic->reg = reg;
7987 return ioapic->data;
7988 }
7989
7990 static void
7991 ioapicwrite(int reg, uint data)
7992 {
7993 ioapic->req = req;
7994 ioapic->data = data;
7995 }
7996
7997
7998
7999
```

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Sheet 80 Sheet 80

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```
8100 // ICW3: (master PIC) bit mask of IR lines connected to slaves
                                                                            8150 // PC keyboard interface constants
8101 //
                (slave PIC) 3-bit # of slave's connection to master
                                                                            8151
8102 outb(IO PIC1+1, 1<<IRO SLAVE);
                                                                            8152 #define KBSTATP
                                                                                                       0x64
                                                                                                             // kbd controller status port(I)
                                                                            8153 #define KBS_DIB
                                                                                                       0x01
8103
                                                                                                            // kbd data in buffer
8104 // ICW4: 000nbmap
                                                                            8154 #define KBDATAP
                                                                                                       0x60
                                                                                                             // kbd data port(I)
8105 // n: 1 = special fully nested mode
                                                                            8155
8106 // b: 1 = buffered mode
                                                                            8156 #define NO
                                                                                                       0
8107 // m: 0 = slave PIC, 1 = master PIC
                                                                            8157
8108 //
           (ignored when b is 0, as the master/slave role
                                                                           8158 #define SHIFT
                                                                                                       (1 << 0)
8109 //
           can be hardwired).
                                                                            8159 #define CTL
                                                                                                       (1 << 1)
8110 // a: 1 = Automatic EOI mode
                                                                            8160 #define ALT
                                                                                                       (1 << 2)
8111 // p: 0 = MCS-80/85 mode, 1 = intel x86 mode
                                                                            8161
8112 outb(IO_PIC1+1, 0x3);
                                                                            8162 #define CAPSLOCK
                                                                                                       (1 << 3)
8113
                                                                            8163 #define NUMLOCK
                                                                                                       (1 << 4)
8114 // Set up slave (8259A-2)
                                                                            8164 #define SCROLLLOCK
                                                                                                       (1 < < 5)
8115 outb(IO_PIC2, 0x11);
                                                                            8165
                                         // ICW1
8116 outb(IO_PIC2+1, T_IRQ0 + 8);
                                     // ICW2
                                                                            8166 #define E0ESC
                                                                                                       (1 < < 6)
8117 outb(IO_PIC2+1, IRQ_SLAVE);
                                  // ICW3
                                                                            8167
8118 // NB Automatic EOI mode doesn't tend to work on the slave.
                                                                            8168 // Special keycodes
8119 // Linux source code says it's "to be investigated".
                                                                            8169 #define KEY HOME
                                                                                                       0xE0
8120 outb(IO_PIC2+1, 0x3); // ICW4
                                                                                                       0xE1
                                                                            8170 #define KEY END
8121
                                                                            8171 #define KEY UP
                                                                                                       0xE2
8122 // OCW3: 0ef01prs
                                                                            8172 #define KEY DN
                                                                                                       0xE3
8123 // ef: 0x = NOP, 10 = clear specific mask, 11 = set specific mask
                                                                            8173 #define KEY_LF
                                                                                                       0xE4
8124 // p: 0 = no polling, 1 = polling mode
                                                                            8174 #define KEY RT
                                                                                                       0xE5
8125 // rs: 0x = NOP, 10 = read IRR, 11 = read ISR
                                                                            8175 #define KEY PGUP
                                                                                                       0xE6
8126 outb(IO_PIC1, 0x68);
                           // clear specific mask
                                                                            8176 #define KEY_PGDN
                                                                                                       0xE7
8127 outb(IO_PIC1, 0x0a);
                                  // read IRR by default
                                                                            8177 #define KEY_INS
                                                                                                       0xE8
8128
                                                                            8178 #define KEY DEL
                                                                                                       0xE9
8129 outb(IO_PIC2, 0x68);
                                   // OCW3
                                                                            8179
8130 outb(IO_PIC2, 0x0a);
                                   // OCW3
                                                                            8180 // C('A') == Control-A
8131
                                                                            8181 #define C(x) (x - '@')
8132 if(irqmask != 0xFFFF)
                                                                            8182
8133
        picsetmask(irqmask);
                                                                            8183 static uchar shiftcode[256] =
8134 }
                                                                            8184 {
8135
                                                                            8185 [0x1D] CTL,
8136
                                                                            8186 [0x2A] SHIFT,
                                                                            8187 [0x36] SHIFT.
8137
8138
                                                                            8188 [0x38] ALT,
8139
                                                                            8189 [0x9D] CTL,
8140
                                                                            8190 [0xB8] ALT
8141
                                                                            8191 };
8142
8143
                                                                            8193 static uchar togglecode[256] =
8144
                                                                            8194 {
                                                                            8195 [0x3A] CAPSLOCK,
8145
8146
                                                                            8196 [0x45] NUMLOCK,
8147
                                                                            8197 [0x46] SCROLLLOCK
8148
                                                                            8198 };
                                                                            8199
8149
```

Sheet 81 Sheet 81

```
8200 static uchar normalmap[256] =
8201 {
8202 NO,
           0x1B, '1', '2', '3', '4', '5', '6', // 0x00
      777, 787,
                 191,
                            '-', '=', '\b', '\t',
8203
                       ′0′,
8204
      'q', 'w',
                 'e',
                       'r', 't',
                                 ′У′,
                                       'u', 'i', // 0x10
8205
      '0', 'p', '[',
                       ']', '\n', NO,
                                        'a', 's',
                 ′g′,
                       'h', 'j', 'k',
                                       'l', ';', // 0x20
8206
      'd', 'f',
                                             ′ν′,
      '\'', '\',
                       '\\', 'Z',
8207
                NO,
                                 'x',
                                       'C',
8208
      'b', 'n', 'm',
                                             '*', // 0x30
                                       NO,
           ′′, NO,
                            NO,
8209
      NO,
                      NO,
                                  NO,
                                       NO,
                                             NO,
8210
      NO,
           NO,
                 NO,
                      NO,
                            NO,
                                 NO,
                                       NO,
                                             '7', // 0x40
      '8', '9', '-', '4', '5', '6', '+', '1',
8211
8212 '2', '3', '0', '.', NO, NO, NO, NO, // 0x50
      [0x9C] '\n',
8213
                      // KP_Enter
      [0xB5] '/',
                       // KP Div
8214
8215
      [0xC8] KEY_UP,
                      [0xD0] KEY_DN,
8216
      [0xC9] KEY PGUP, [0xD1] KEY PGDN,
8217
      [0xCB] KEY_LF,
                       [0xCD] KEY_RT,
8218 [0x97] KEY_HOME,
                      [0xCF] KEY_END,
8219
      [0xD2] KEY_INS,
                       [0xD3] KEY DEL
8220 };
8221
8222 static uchar shiftmap[256] =
8223 {
8224 NO.
           033, '!',
                      '@', '#', '$', '%', '^', // 0x00
8225
      '&',
           1 * 1
                 ′(′,
                      ′)′,
                                 ' + ' .
                                       '\b', '\t',
8226
      'Q', 'W', 'E',
                      'R', 'T', 'Y', 'U', 'I', // 0x10
8227
      'O', 'P', '{',
                       '}', '\n', NO,
                                        'A', 'S',
8228
      'D'.
                            ΊJ',
                                 ′K′,
                                             ':', // 0x20
           'F',
                 'G',
                       Ή',
                                       'L',
      '"', '~', NO,
                       '|', 'Z', 'X',
                                             ′Υ′,
8229
                                       'C',
                                             '*', // 0x30
8230 'B', 'N',
                 'M',
                       '<', '>', '?',
                                       NO,
8231
      NO,
           ′′,
                NO,
                      NO, NO,
                                 NO,
                                       NO,
                                             NO,
                            NO,
                                             '7', // 0x40
8232
     NO.
           NO,
                 NO,
                       NO,
                                 NO,
                                       NO,
                 ′-′,
                                       '+', '1',
8233
      '8', '9',
                      '4', '5', '6',
8234 '2', '3', '0', '.', NO, NO, NO, NO, // 0x50
8235 [0x9C] '\n',
                       // KP_Enter
8236
      [0xB5] '/',
                      // KP_Div
8237
      [0xC8] KEY UP,
                       [0xD0] KEY DN,
8238
      [0xC9] KEY_PGUP, [0xD1] KEY_PGDN,
8239
      [0xCB] KEY_LF,
                       [0xCD] KEY_RT,
8240
      [0x97] KEY HOME,
                      [OxCF] KEY END,
8241
      [0xD2] KEY_INS,
                      [0xD3] KEY_DEL
8242 };
8243
8244
8245
8246
8247
8248
8249
```

```
8250 static uchar ctlmap[256] =
8251 {
8252 NO,
               NO,
                        NO,
                                 NO,
                                          NO,
                                                   NO,
                                                           NO,
                                                                    NO,
8253
      NO,
               NO,
                        NO,
                                 NO,
                                          NO,
                                                   NO,
                                                           NO,
                                                                    NO.
8254
      C('Q'), C('W'), C('E'), C('R'), C('T'), C('Y'), C('U'), C('I'),
     C('O'), C('P'), NO,
                                          '\r',
8255
                                 NO,
                                                  NO,
                                                           C('A'), C('S'),
8256
      C('D'), C('F'), C('G'), C('H'), C('J'), C('K'), C('L'), NO,
8257
               NO,
                        NO,
                                 C('\setminus '), C('Z'), C('X'), C('C'), C('V'),
      NO,
8258
      C('B'), C('N'), C('M'), NO,
                                          NO,
                                                  C('/'), NO,
                                                                    NO,
      [0x9C] '\r',
8259
                        // KP_Enter
8260
      [0xB5] C('/'),
                        // KP_Div
8261
      [0xC8] KEY_UP,
                        [0xD0] KEY_DN,
8262
      [0xC9] KEY_PGUP, [0xD1] KEY_PGDN,
8263
      [0xCB] KEY_LF,
                        [0xCD] KEY_RT,
8264
      [0x97] KEY_HOME, [0xCF] KEY_END,
8265
      [0xD2] KEY_INS,
                        [0xD3] KEY_DEL
8266 };
8267
8268
8269
8270
8271
8272
8273
8274
8275
8276
8277
8278
8279
8280
8281
8282
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8284
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8288
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8292
8293
8294
8295
8296
8297
8298
8299
```

Sheet 82 Sheet 82

```
8350 // Console input and output.
8351 // Input is from the keyboard or serial port.
8352 // Output is written to the screen and serial port.
8353
8354 #include "types.h"
8355 #include "defs.h"
8356 #include "param.h"
8357 #include "traps.h"
8358 #include "spinlock.h"
8359 #include "fs.h"
8360 #include "file.h"
8361 #include "memlayout.h"
8362 #include "mmu.h"
8363 #include "proc.h"
8364 #include "x86.h"
8365
8366 static void consputc(int);
8368 static int panicked = 0;
8369
8370 static struct {
8371 struct spinlock lock;
8372 int locking;
8373 } cons;
8374
8375 static void
8376 printint(int xx, int base, int sign)
8377 {
8378 static char digits[] = "0123456789abcdef";
8379 char buf[16];
8380 int i;
8381 uint x;
8382
8383 if(sign && (sign = xx < 0))
8384
      x = -xx;
8385 else
8386
      x = xx;
8387
8388 i = 0;
8389 do{
8390
      buf[i++] = digits[x % base];
8391 \}while((x /= base) != 0);
8392
8393 if(sign)
       buf[i++] = '-';
8394
8395
8396 while(--i >= 0)
8397
        consputc(buf[i]);
8398 }
8399
```

8347 {

8349 }

8348 consoleintr(kbdgetc);

```
8450 if(locking)
8400 // Print to the console. only understands %d, %x, %p, %s.
8401 void
                                                                             8451
                                                                                     release(&cons.lock);
8402 cprintf(char *fmt, ...)
                                                                             8452 }
8403 {
                                                                             8453
8404 int i, c, locking;
                                                                             8454 void
8405 uint *arqp;
                                                                             8455 panic(char *s)
8406 char *s;
                                                                             8456 {
8407
                                                                             8457 int i;
8408 locking = cons.locking;
                                                                             8458 uint pcs[10];
8409 if(locking)
                                                                             8459
8410
      acquire(&cons.lock);
                                                                             8460 cli();
8411
                                                                             8461 cons.locking = 0;
8412 if (fmt == 0)
                                                                             8462 cprintf("cpu%d: panic: ", cpu->id);
8413
        panic("null fmt");
                                                                             8463 cprintf(s);
8414
                                                                             8464 cprintf("\n");
8415 argp = (uint*)(void*)(&fmt + 1);
                                                                             8465 getcallerpcs(&s, pcs);
8416 for(i = 0; (c = fmt[i] & 0xff) != 0; i++){
                                                                             8466 for(i=0; i<10; i++)
8417
      if(c != '%'){
                                                                             8467
                                                                                  cprintf(" %p", pcs[i]);
8418
          consputc(c);
                                                                             8468 panicked = 1; // freeze other CPU
8419
          continue;
                                                                             8469 for(;;)
8420
                                                                             8470
                                                                                    ;
        c = fmt[++i] & 0xff;
8421
                                                                             8471 }
8422
        if(c == 0)
                                                                             8472
8423
        break;
                                                                             8473
8424
        switch(c){
                                                                             8474
8425
        case 'd':
                                                                             8475
8426
                                                                             8476
          printint(*argp++, 10, 1);
8427
         break;
                                                                             8477
8428
        case 'x':
                                                                             8478
8429
        case 'p':
                                                                             8479
8430
          printint(*argp++, 16, 0);
                                                                             8480
8431
          break;
                                                                             8481
8432
        case 's':
                                                                             8482
8433
        if((s = (char*)*argp++) == 0)
                                                                             8483
8434
          s = "(null)";
                                                                             8484
8435
         for(; *s; s++)
                                                                             8485
8436
          consputc(*s);
                                                                             8486
8437
          break;
                                                                             8487
8438
        case '%':
                                                                             8488
8439
          consputc('%');
                                                                             8489
8440
          break;
                                                                             8490
8441
                                                                             8491
        default:
8442
         // Print unknown % sequence to draw attention.
                                                                             8492
8443
          consputc('%');
                                                                             8493
8444
          consputc(c);
                                                                             8494
8445
          break;
                                                                             8495
8446
                                                                             8496
8447 }
                                                                             8497
8448
                                                                             8498
8449
                                                                             8499
```

Sheet 84

```
8500 #define BACKSPACE 0x100
8501 #define CRTPORT 0x3d4
8502 static ushort *crt = (ushort*)P2V(0xb8000); // CGA memory
8503
8504 static void
8505 cgaputc(int c)
8506 {
8507 int pos;
8508
8509 // Cursor position: col + 80*row.
8510 outb(CRTPORT, 14);
8511 pos = inb(CRTPORT+1) << 8;
8512 outb(CRTPORT, 15);
8513 pos |= inb(CRTPORT+1);
8514
8515 if(c == ' \n')
8516
      pos += 80 - pos%80;
8517 else if(c == BACKSPACE){
8518
      if(pos > 0) --pos;
8519 } else
        crt[pos++] = (c&0xff) \mid 0x0700; // black on white
8520
8521
8522 if (pos < 0 | pos > 25*80)
8523
       panic("pos under/overflow");
8524
8525 if((pos/80) >= 24){ // Scroll up.
8526
       memmove(crt, crt+80, sizeof(crt[0])*23*80);
8527
        pos -= 80;
8528
       memset(crt+pos, 0, sizeof(crt[0])*(24*80 - pos));
8529 }
8530
8531 outb(CRTPORT, 14);
8532 outb(CRTPORT+1, pos>>8);
8533 outb(CRTPORT, 15);
8534 outb(CRTPORT+1, pos);
8535 crt[pos] = ' ' | 0x0700;
8536 }
8537
8538
8539
8540
8541
8542
8543
8544
8545
8546
8547
8548
8549
```

```
8550 void
8551 consputc(int c)
8552 {
8553 if(panicked){
8554
       cli();
8555
     for(;;)
8556
          ;
8557 }
8558
8559 if(c == BACKSPACE){
8560
       uartputc('\b'); uartputc(' '); uartputc('\b');
8561 } else
8562
       uartputc(c);
8563 cgaputc(c);
8564 }
8565
8566 #define INPUT_BUF 128
8567 struct {
8568 char buf[INPUT_BUF];
8569 uint r; // Read index
8570 uint w; // Write index
8571 uint e; // Edit index
8572 } input;
8573
8574 #define C(x) ((x)-'@') // Control-x
8575
8576 void
8577 consoleintr(int (*getc)(void))
8578 {
8579 int c, doprocdump = 0;
8580
8581 acquire(&cons.lock);
8582 while((c = qetc()) >= 0){
8583
        switch(c){
8584
        case C('P'): // Process listing.
8585
          doprocdump = 1;  // procdump() locks cons.lock indirectly; invoke late
8586
          break;
8587
        case C('U'): // Kill line.
8588
          while(input.e != input.w &&
8589
                input.buf[(input.e-1) % INPUT_BUF] != '\n'){
8590
            input.e--;
8591
            consputc(BACKSPACE);
8592
8593
          break;
8594
        case C('H'): case '\x7f': // Backspace
8595
          if(input.e != input.w){
8596
            input.e--;
8597
            consputc(BACKSPACE);
8598
8599
          break;
```

```
8607
              wakeup(&input.r);
8608
8609
8610
          break;
8611
8612
8613 release(&cons.lock);
8614 if(doprocdump) {
8615
        procdump(); // now call procdump() wo. cons.lock held
8623 int c;
8624
```

8616 } 8617 } 8618 8619 int 8620 consoleread(struct inode \*ip, char \*dst, int n) 8621 { 8622 uint target;

8626 target = n; 8627 acquire(&cons.lock); 8628 while(n > 0){ 8629 while(input.r == input.w){ 8630 if(proc->killed){ 8631 release(&cons.lock); 8632 ilock(ip);

return -1;

input.r--;

input.w = input.e;

8635 sleep(&input.r, &cons.lock); 8636 c = input.buf[input.r++ % INPUT\_BUF]; 8637 8638  $if(c == C('D')) \{ // EOF$ 8639 if(n < target){ 8640 // Save ^D for next time, to make sure

// caller gets a 0-byte result.

8643 8644 break; 8645 8646 \*dst++ = c; 8647 --n;

 $if(c == ' \n')$ 

break;

8625 iunlock(ip);

8633

8634

8641

8642

8648

8649

8605

8606

Sheet 86

8655 } 8656 8657 int 8658 consolewrite(struct inode \*ip, char \*buf, int n) 8659 { 8660 int i; 8661 8662 iunlock(ip); 8663 acquire(&cons.lock); 8664 for(i = 0; i < n; i++) 8665 consputc(buf[i] & 0xff); 8666 release(&cons.lock); 8667 ilock(ip); 8668 8669 return n; 8670 } 8671 8672 void 8673 consoleinit(void) 8674 { 8675 initlock(&cons.lock, "console"); 8676 8677 devsw[CONSOLE].write = consolewrite; 8678 devsw[CONSOLE].read = consoleread; 8679 cons.locking = 1;

8680 8681 picenable(IRQ\_KBD); 8682 ioapicenable(IRQ\_KBD, 0); 8683 } 8684 8685

8688 8689 8690 8691 8692 8693 8694

8686

8687

Sheet 86

8799

Sheet 87 Sheet 87

8749

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Sheet 88 Sheet 88

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8900	#include "syscall.h"
8901	<pre>#include "traps.h"</pre>
8902	
8903	<pre>#define SYSCALL(name) \</pre>
8904	.globl name; \
8905	name: \
8906	movl \$SYS_ ## name, %eax;
8907	int \$T_SYSCALL; \
8908	ret
8909	
8910	SYSCALL(fork)
8911	SYSCALL(exit)
8912	SYSCALL(wait)
8913	SYSCALL(pipe)
8914	SYSCALL(read)
8915	SYSCALL(write)
8916	SYSCALL(close)
8917	SYSCALL(kill)
8918	SYSCALL(exec)
8919	SYSCALL(open)
8920	SYSCALL(mknod)
8921	SYSCALL(unlink)
8922	SYSCALL(fstat)
8923	SYSCALL(link)
8924	SYSCALL(mkdir)
8925	SYSCALL(chdir)
8926	SYSCALL(dup)
8927	SYSCALL(getpid)
8928	SYSCALL(sbrk)
8929	SYSCALL(sleep)
8930	SYSCALL(uptime)
8931	SYSCALL(halt)
8932	SYSCALL(date)
8933	SYSCALL(getuid)
8934	SYSCALL(getgid)
8935	SYSCALL(getppid)
8936	SYSCALL(setuid)
8937	SYSCALL(setgid)
8938	SYSCALL(getprocs)
8939	
8940	
8941	
8942	
8943	
8944	
8945	
8946	
8947	
8948	
8949	

```
8950 // init: The initial user-level program
8951
8952 #include "types.h"
8953 #include "stat.h"
8954 #include "user.h"
8955 #include "fcntl.h"
8956
8957 char *argv[] = { "sh", 0 };
8958
8959 int
8960 main(void)
8961 {
8962 int pid, wpid;
8963
8964 if(open("console", O_RDWR) < 0){
8965
        mknod("console", 1, 1);
8966
       open("console", O_RDWR);
8967 }
8968 dup(0); // stdout
8969 dup(0); // stderr
8970
8971 for(;;){
8972
        printf(1, "init: starting sh\n");
8973
        pid = fork();
8974
        if(pid < 0){
8975
          printf(1, "init: fork failed\n");
8976
          exit();
8977
        if(pid == 0){
8978
8979
          exec("sh", argv);
8980
          printf(1, "init: exec sh failed\n");
8981
          exit();
8982
        while((wpid=wait()) >= 0 && wpid != pid)
8983
8984
          printf(1, "zombie!\n");
8985 }
8986 }
8987
8988
8989
8990
8991
8992
8993
8994
8995
8996
8997
8998
8999
```

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Sheet 90 Sheet 90

May 6 16:17 2016 xv6/sh.c Page 1

```
9150 // **** processing for shell builting begins here***
9151
9152 int
9153 strncmp(const char *p, const char *q, uint n)
        while(n > 0 && *p && *p == *q)
9155
9156
         n--, p++, q++;
9157
       if(n == 0)
9158
         return 0;
9159
        return (uchar)*p - (uchar)*g;
9160 }
9161
9162 int
9163 makeint(char *p)
9164 {
9165 int val = 0;
9166
9167 while ((*p >= '0') && (*p <= '9')) {
9168
       val = 10*val + (*p-'0');
9169
        ++p;
9170 }
9171 return val;
9172 }
9173
9174 int
9175 setbuiltin(char *p)
9176 {
9177 int i;
9178
9179 p += strlen("_set");
9180 while (strncmp(p, "", 1) == 0) p++; // chomp spaces
9181 if (strncmp("uid", p, 3) == 0) {
9182 p += strlen("uid");
9183
        while (strncmp(p, "", 1) == 0) p++; // chomp spaces
9184
        i = makeint(p); // ugly
9185
        return (setuid(i));
9186 } else
9187 if (strncmp("gid", p, 3) == 0) {
9188
       p += strlen("gid");
9189
        while (strncmp(p, "", 1) == 0) p++; // chomp spaces
9190
        i = makeint(p); // ugly
9191
        return (setgid(i));
9192 }
9193 printf(2, "Invalid _set parameter\n");
9194 return -1;
9195 }
9196
9197
9198
9199
```

9145 }

9146

9147

9148

9149

9144 return 0;

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Sheet 92 Sheet 92

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```
9300 int
9301 fork1(void)
9302 {
9303 int pid;
9304
9305 pid = fork();
9306 if(pid == -1)
      panic("fork");
9307
9308 return pid;
9309 }
9310
9311
9312
9313
9314
9315
9316
9317
9318
9319
9320
9321
9322
9323
9324
9325
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9342
9343
9344
9345
9346
9347
9348
9349
```

```
9350 // Constructors
9351
9352 struct cmd*
9353 execcmd(void)
9354 {
9355 struct execomd *cmd;
9356
9357 cmd = malloc(sizeof(*cmd));
9358 memset(cmd, 0, sizeof(*cmd));
9359 cmd->type = EXEC;
9360 return (struct cmd*)cmd;
9361 }
9362
9363 struct cmd*
9364 redircmd(struct cmd *subcmd, char *file, char *efile, int mode, int fd)
9365 {
9366 struct redircmd *cmd;
9367
9368 cmd = malloc(sizeof(*cmd));
9369 memset(cmd, 0, sizeof(*cmd));
9370 cmd->type = REDIR;
9371 cmd->cmd = subcmd;
9372 cmd->file = file;
9373 cmd->efile = efile;
9374 cmd->mode = mode;
9375 \quad cmd \rightarrow fd = fd;
9376 return (struct cmd*)cmd;
9377 }
9378
9379 struct cmd*
9380 pipecmd(struct cmd *left, struct cmd *right)
9381 {
9382 struct pipecmd *cmd;
9383
9384 cmd = malloc(sizeof(*cmd));
9385 memset(cmd, 0, sizeof(*cmd));
9386 cmd->type = PIPE;
9387 cmd->left = left;
9388 cmd->right = right;
9389 return (struct cmd*)cmd;
9390 }
9391
9392
9393
9394
9395
9396
9397
9398
9399
```

```
9400 struct cmd*
9401 listcmd(struct cmd *left, struct cmd *right)
9402 {
9403 struct listcmd *cmd;
9404
9405 cmd = malloc(sizeof(*cmd));
9406 memset(cmd, 0, sizeof(*cmd));
9407 cmd->type = LIST;
9408 cmd->left = left;
9409 cmd->right = right;
9410 return (struct cmd*)cmd;
9411 }
9412
9413 struct cmd*
9414 backcmd(struct cmd *subcmd)
9415 {
9416 struct backcmd *cmd;
9417
9418 cmd = malloc(sizeof(*cmd));
9419 memset(cmd, 0, sizeof(*cmd));
9420 cmd->type = BACK;
9421 cmd->cmd = subcmd;
9422 return (struct cmd*)cmd;
9423 }
9424
9425
9426
9427
9428
9429
9430
9431
9432
9433
9434
9435
9436
9437
9438
9439
9440
9441
9442
9443
9444
9445
9446
9447
9448
9449
```

```
9450 // Parsing
9451
9452 char whitespace[] = " t\r\n\v";
9453 char symbols[] = "<|>&;()";
9454
9455 int
9456 gettoken(char **ps, char *es, char **q, char **eq)
9458 char *s;
9459 int ret;
9460
9461 s = *ps;
9462 while(s < es && strchr(whitespace, *s))
9463
        s++;
9464 if(a)
9465
        *q = s;
9466 ret = *s;
9467 switch(*s){
9468 case 0:
9469
       break;
9470 case '|':
9471 case '(':
9472 case ')':
9473 case ';':
9474 case '&':
9475 case '<':
9476
      s++;
9477 break;
9478 case '>':
9479 s++;
9480 if(*s == '>'){
9481
       ret = '+';
9482
          s++;
9483
9484
        break;
9485 default:
9486
      ret = 'a';
9487
        while(s < es && !strchr(whitespace, *s) && !strchr(symbols, *s))</pre>
9488
9489
        break;
9490 }
9491 if(eq)
9492
        *eq = s;
9493
9494 while(s < es && strchr(whitespace, *s))
9495
      s++;
9496 *ps = s;
9497 return ret;
9498 }
9499
```

9550 struct cmd\*

```
9551 parsepipe(char **ps, char *es)
9552 {
9553 struct cmd *cmd;
9554
9555 cmd = parseexec(ps, es);
9556 if(peek(ps, es, "|")){
9557 gettoken(ps, es, 0, 0);
9558 cmd = pipecmd(cmd, parsepipe(ps, es));
9559 }
9560 return cmd;
9561 }
9562
9563 struct cmd*
9564 parseredirs(struct cmd *cmd, char **ps, char *es)
9565 {
9566 int tok;
9567 char *q, *eq;
9568
9569 while(peek(ps, es, "<>")){
9570 tok = gettoken(ps, es, 0, 0);
9571 if(gettoken(ps, es, &g, &eg) != 'a')
9572
       panic("missing file for redirection");
9573 switch(tok){
9574 case '<':
9575
         cmd = redircmd(cmd, q, eq, O_RDONLY, 0);
9576
         break;
9577 case '>':
9578
       cmd = redircmd(cmd, q, eq, O_WRONLY|O_CREATE, 1);
9579
         break;
9580 case '+': // >>
9581
         cmd = redircmd(cmd, q, eq, O_WRONLY|O_CREATE, 1);
9582
          break;
9583
9584 }
9585 return cmd;
9586 }
9587
9588
9589
9590
9591
9592
9593
9594
9595
9596
9597
9598
9599
```

```
9600 struct cmd*
9601 parseblock(char **ps, char *es)
9602 {
9603 struct cmd *cmd;
9604
9605 if(!peek(ps, es, "("))
9606 panic("parseblock");
9607 gettoken(ps, es, 0, 0);
9608 cmd = parseline(ps, es);
9609 if(!peek(ps, es, ")"))
9610 panic("syntax - missing)");
9611 gettoken(ps, es, 0, 0);
9612 cmd = parseredirs(cmd, ps, es);
9613 return cmd;
9614 }
9615
9616 struct cmd*
9617 parseexec(char **ps, char *es)
9618 {
9619 char *q, *eq;
9620 int tok, argc;
9621 struct execomd *cmd;
9622 struct cmd *ret;
9623
9624 if(peek(ps, es, "("))
9625
       return parseblock(ps, es);
9626
9627 ret = execcmd();
9628 cmd = (struct execcmd*)ret;
9629
9630 argc = 0;
9631 ret = parseredirs(ret, ps, es);
9632 while(!peek(ps, es, "|)&;")){
9633
       if((tok=gettoken(ps, es, &q, &eq)) == 0)
9634
          break;
9635
        if(tok != 'a')
9636
          panic("syntax");
9637
        cmd->arqv[arqc] = q;
9638
        cmd->eargv[argc] = eq;
9639
        arqc++;
9640
        if(argc >= MAXARGS)
9641
          panic("too many args");
9642
        ret = parseredirs(ret, ps, es);
9643 }
9644 cmd->argv[argc] = 0;
9645 cmd \rightarrow earqv[arqc] = 0;
9646 return ret;
9647 }
9648
9649
```

```
9650 // NUL-terminate all the counted strings.
9651 struct cmd*
9652 nulterminate(struct cmd *cmd)
9653 {
9654 int i;
9655 struct backemd *bcmd;
9656 struct execomd *ecmd;
9657 struct listcmd *lcmd;
9658 struct pipecmd *pcmd;
9659 struct redircmd *rcmd;
9660
9661 if(cmd == 0)
9662 return 0;
9663
9664 switch(cmd->type){
9665 case EXEC:
9666
        ecmd = (struct execcmd*)cmd;
9667
        for(i=0; ecmd->argv[i]; i++)
9668
        *ecmd->eargv[i] = 0;
9669
        break;
9670
9671 case REDIR:
        rcmd = (struct redircmd*)cmd;
9673
        nulterminate(rcmd->cmd);
9674
       *rcmd->efile = 0;
9675
        break;
9676
9677 case PIPE:
9678
        pcmd = (struct pipecmd*)cmd;
9679
        nulterminate(pcmd->left);
9680
        nulterminate(pcmd->right);
9681
        break;
9682
9683 case LIST:
        lcmd = (struct listcmd*)cmd;
9684
9685
        nulterminate(lcmd->left);
9686
        nulterminate(lcmd->right);
9687
        break;
9688
9689 case BACK:
9690
       bcmd = (struct backcmd*)cmd;
9691
        nulterminate(bcmd->cmd);
9692
       break;
9693 }
9694 return cmd;
9695 }
9696
9697
9698
9699
```

	#include			9750 # Complete transition to 32-bit protected mode by using long jmp					
9701 :	9701 #include "memlayout.h"			9751	9751 # to reload %cs and %eip. The segment descriptors are set up with no				
9702 #include "mmu.h"			9752	9752 # translation, so that the mapping is still the identity mapping.					
9703	9703				9753 ljmp \$(SEG_KCODE<<3), \$start32				
9704 :	Start t	he first CPU: switc	th to 32-bit protected mode, jump into C.	9754					
9705 =	9705 # The BIOS loads this code from the first sector of the hard disk into				9755 .code32 # Tell assembler to generate 32-bit code now.				
	9706 # memory at physical address 0x7c00 and starts executing in real mode				9756 start32:				
		es=0 %ip=7c00.		9757  # Set up the protected-mode data segment registers					
9708					movw	\$(SEG_KDATA<<3), %ax			
	.code16		# Assemble for 16-bit mode	9758 9759	movw	%ax, %ds		S: Data Segment	
	9710 .code10 # Assemble for 10-bit mode			9760	movw	%ax, %es		S: Extra Segment	
	9711 start:			9761	movw	%ax, %ss		S: Stack Segment	
			# DIOC anabled interments: disable					9	
9712	CII		# BIOS enabled interrupts; disable	9762	movw	\$0, %ax		segments not ready for use	
9713	11	4.1	DO TO TO A OR	9763	movw	%ax, %fs	# -> F:		
9714			ers DS, ES, and SS.	9764	movw	%ax, %gs	# -> G	S	
9715	xorw	%ax,%ax	# Set %ax to zero	9765					
9716	movw	%ax,%ds	# -> Data Segment	9766		up the stack pointer and	call in	to C.	
9717	movw	%ax,%es	# -> Extra Segment	9767	movl	\$start, %esp			
9718	movw	%ax,%ss	# -> Stack Segment	9768	call	bootmain			
9719				9769					
9720	9720 # Physical address line A20 is tied to zero so that the first PCs				# If b	ootmain returns (it shou	ldn't), †	trigger a Bochs	
9721	# with	2 MB would run soft	ware that assumed 1 MB. Undo that.	9771	# brea	kpoint if running under B	Bochs, tl	hen loop.	
9722 \$	seta20.1:			9772	movw	\$0x8a00, %ax	# 0x8a	00 -> port 0x8a00	
9723	inb	\$0x64,%al	# Wait for not busy	9773	movw	%ax, %dx			
9724	testb	\$0x2,%al	-	9774	outw	%ax, %dx			
9725	jnz	seta20.1		9775	movw	\$0x8ae0, %ax	# 0x8a	e0 -> port 0x8a00	
9726	3			9776	outw	%ax, %dx		-	
9727	movb	\$0xd1,%al	# 0xd1 -> port 0x64	9777 s	spin:	•			
9728	outb	%al,\$0x64	<u> </u>	9778	jmp	spin			
9729		7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7		9779	21-				
	seta20.2:				Bootst	rap GDT			
9731	inb	\$0x64,%al	# Wait for not busy		p2align	-		# force 4 byte alignment	
9732	testb	\$0x2,%al	" Mate for not baby	9782		2		" Torce i byce drigimene	
9733	jnz	seta20.2		9783	SEG_NU	T.T.A QM		# null seg	
9734	J112	Secazo.z		9784	_		etttt/	# code seg	
9735	movb	\$0xdf,%al	# 0xdf -> port 0x60	9785					
9736	outb	%al,\$0x60	# OXUL -> POIL OXOU	9786	SEG_AS	M(SIA_W, UXU, UXIIIIIIII	)	# data seg	
9737	outb	%a1,\$UX0U			adtdesc:				
	ш оладыл	.h	antad made. The a beat three ODE that makes	-	-	(3-33- 1)		# #!E/#+\ 1	
9738		-	ected mode. Use a bootstrap GDT that makes	9788 9789	.word	(gdtdesc - gdt - 1)		# sizeof(gdt) - 1	
	9739 # virtual addresses map directly to physical addresses so that the				.long	gdt		# address gdt	
9740									
9741	lgdt	gdtdesc		9791					
9742	movl	%cr0, %eax		9792					
9743	orl	\$CRO_PE, %eax		9793					
9744	movl	%eax, %cr0		9794					
9745				9795					
9746				9796					
9747				9797					
9748				9798					
9749				9799					

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```
9800 // Boot loader.
                                                                               9850 void
9801 //
                                                                               9851 waitdisk(void)
9802 // Part of the boot block, along with bootasm.S, which calls bootmain().
                                                                               9852 {
9803 // bootasm.S has put the processor into protected 32-bit mode.
                                                                               9853 // Wait for disk ready.
9804 // bootmain() loads an ELF kernel image from the disk starting at
                                                                               9854 while((inb(0x1F7) & 0xC0) != 0x40)
9805 // sector 1 and then jumps to the kernel entry routine.
                                                                               9855
9806
                                                                               9856 }
9807 #include "types.h"
                                                                               9857
9808 #include "elf.h"
                                                                               9858 // Read a single sector at offset into dst.
9809 #include "x86.h"
                                                                               9859 void
9810 #include "memlayout.h"
                                                                               9860 readsect(void *dst, uint offset)
9812 #define SECTSIZE 512
                                                                               9862 // Issue command.
9813
                                                                               9863 waitdisk();
9814 void readseg(uchar*, uint, uint);
                                                                               9864 outb(0x1F2, 1); // count = 1
9815
                                                                               9865 outb(0x1F3, offset);
9816 void
                                                                               9866 outb(0x1F4, offset >> 8);
9817 bootmain(void)
                                                                               9867 outb(0x1F5, offset >> 16);
9818 {
                                                                               9868 outb(0x1F6, (offset >> 24) | 0xE0);
9819 struct elfhdr *elf;
                                                                               9869 outb(0x1F7, 0x20); // cmd 0x20 - read sectors
9820 struct proghdr *ph, *eph;
                                                                               9870
9821 void (*entry)(void);
                                                                               9871 // Read data.
9822 uchar* pa;
                                                                               9872 waitdisk();
9823
                                                                               9873 insl(0x1F0, dst, SECTSIZE/4);
9824 elf = (struct elfhdr*)0x10000; // scratch space
                                                                               9874 }
9825
                                                                               9875
                                                                               9876 // Read 'count' bytes at 'offset' from kernel into physical address 'pa'.
9826 // Read 1st page off disk
9827 readseg((uchar*)elf, 4096, 0);
                                                                               9877 // Might copy more than asked.
9828
                                                                               9878 void
9829 // Is this an ELF executable?
                                                                              9879 readseq(uchar* pa, uint count, uint offset)
9830 if(elf->magic != ELF_MAGIC)
                                                                               9880 {
9831
      return; // let bootasm.S handle error
                                                                               9881 uchar* epa;
9832
                                                                               9882
9833 // Load each program segment (ignores ph flags).
                                                                               9883 epa = pa + count;
9834 ph = (struct proghdr*)((uchar*)elf + elf->phoff);
                                                                               9884
9835 eph = ph + elf->phnum;
                                                                               9885 // Round down to sector boundary.
9836 for(; ph < eph; ph++){
                                                                               9886 pa -= offset % SECTSIZE;
9837
       pa = (uchar*)ph->paddr;
                                                                               9887
9838
        readseg(pa, ph->filesz, ph->off);
                                                                               9888 // Translate from bytes to sectors; kernel starts at sector 1.
9839
        if(ph->memsz > ph->filesz)
                                                                               9889 offset = (offset / SECTSIZE) + 1;
9840
          stosb(pa + ph->filesz, 0, ph->memsz - ph->filesz);
                                                                               9890
                                                                               9891 // If this is too slow, we could read lots of sectors at a time.
9841 }
9842
                                                                               9892 // We'd write more to memory than asked, but it doesn't matter --
9843 // Call the entry point from the ELF header.
                                                                               9893 // we load in increasing order.
                                                                               9894 for(; pa < epa; pa += SECTSIZE, offset++)
9844 // Does not return!
9845 entry = (void(*)(void))(elf->entry);
                                                                                       readsect(pa, offset);
                                                                               9895
9846 entry();
                                                                               9896 }
9847 }
                                                                               9897
9848
                                                                               9898
9849
                                                                               9899
```

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