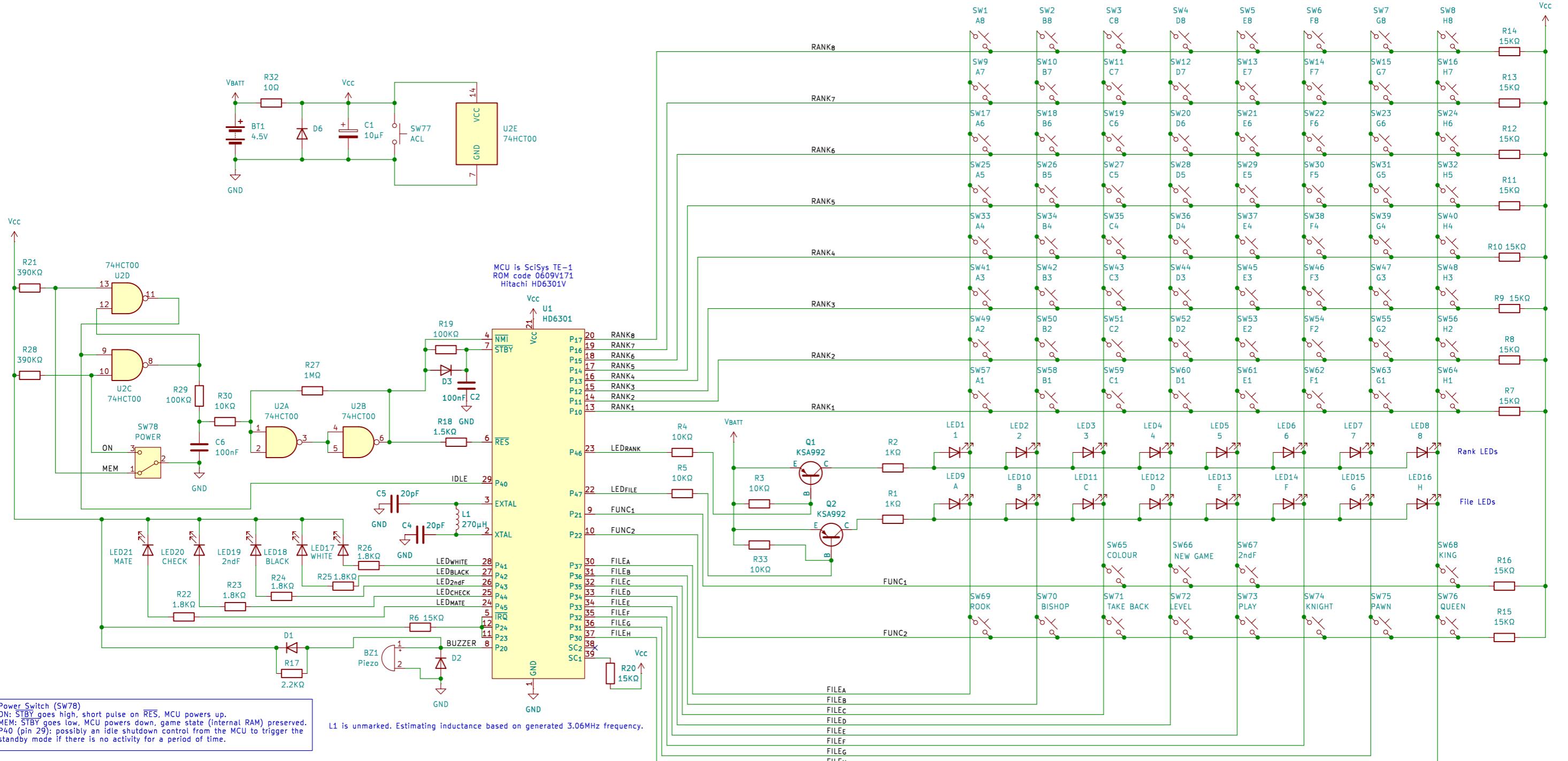


Power Switch (SW78)  
ON: STBY goes high, short pulse on RES, MCU powers up.  
MEM: STBY goes low, MCU powers down, game state (internal RAM) preserved.  
P40 (pin 29): possibly an idle shutdown control from the MCU to trigger the standby mode if there is no activity for a period of time.

L1 is unmarked. Estimating inductance based on generated 3.06MHz frequency.



LogiChess  
SciSys TE-1 0609V171

Brett Hallen

Sheet: /  
File: Tandy\_1650\_Chess.kicad\_sch

Title: Tandy 1650 Electronic Chess

Size: A3 Date: 10/JAN/2026  
KiCad E.D.A. 9.0.5

Rev: A  
Id: 1/1