



Power Switch (SW7B)
ON: STBY goes high, short pulse on RES, MCU powers up.
MEM: STBY goes low, MCU powers down, game state (internal RAM) preserved.
P40 (pin 29): possibly an idle shutdown control from the MCU to trigger the standby mode if there is no activity for a period of time.

L1 is unmarked. Estimating inductance based on generated 3.06MHz frequency

LogiChess
SciSys TE-1 0609V171

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Sheet: / File: Tandy_1650_Chess.kicad_sch

Title: Tandy 1650 Electronic Chess

Size: A3 Date: 10/JAN/2026
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