







BOOT mode selection pins		Boot mode	Aliasing
BOOT1	BOOT0		
x	0	Main Flash memory	Main Flash memory is selected as boot space
0	1	System memory	System memory is selected as boot space
1	1	Embedded SRAM	Embedded SRAM is selected as boot space

Table 2. Data Rate Settings

SPEED PIN	DATA RATE	
	Internal Oscillator	External Clock
0	10SPS	$f_{CLKIN} / 491,520$
1	80SPS	$f_{CLKIN} / 61,440$





