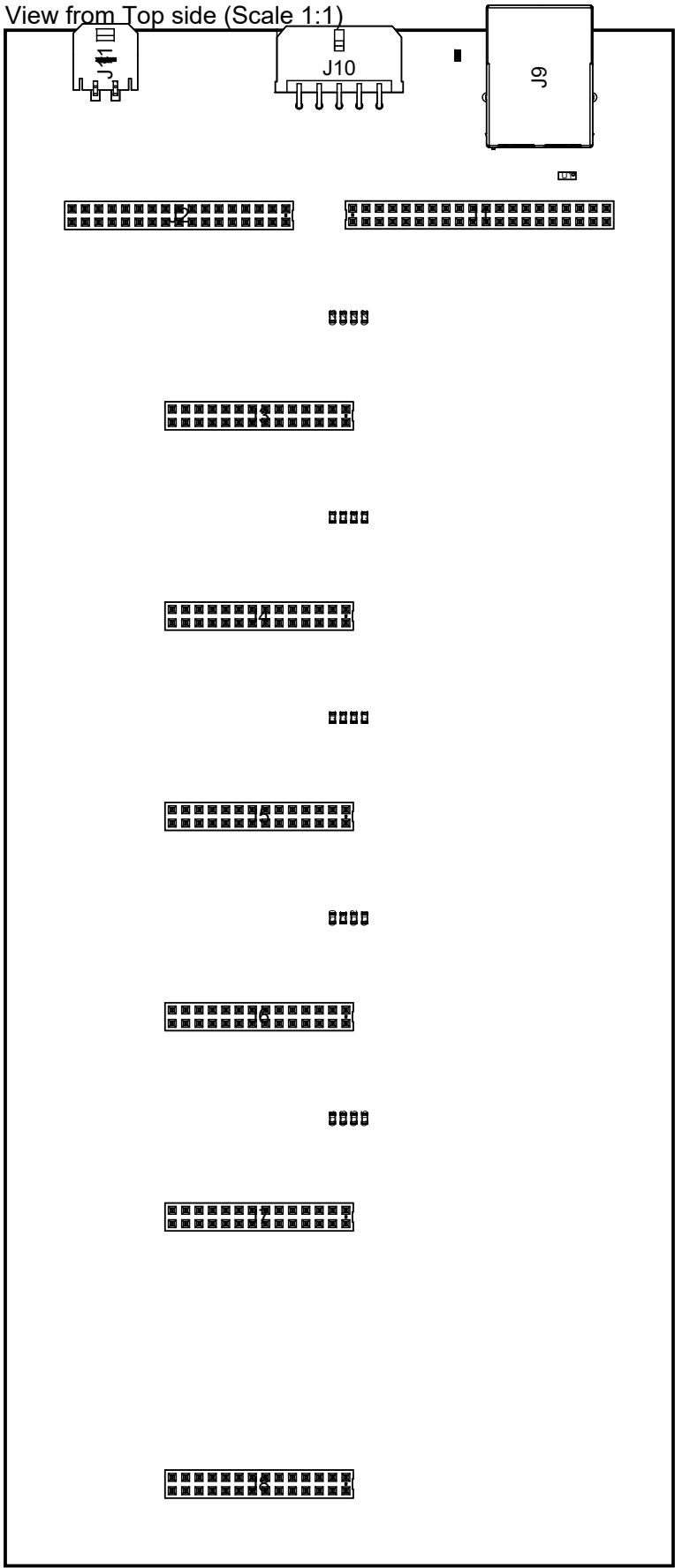
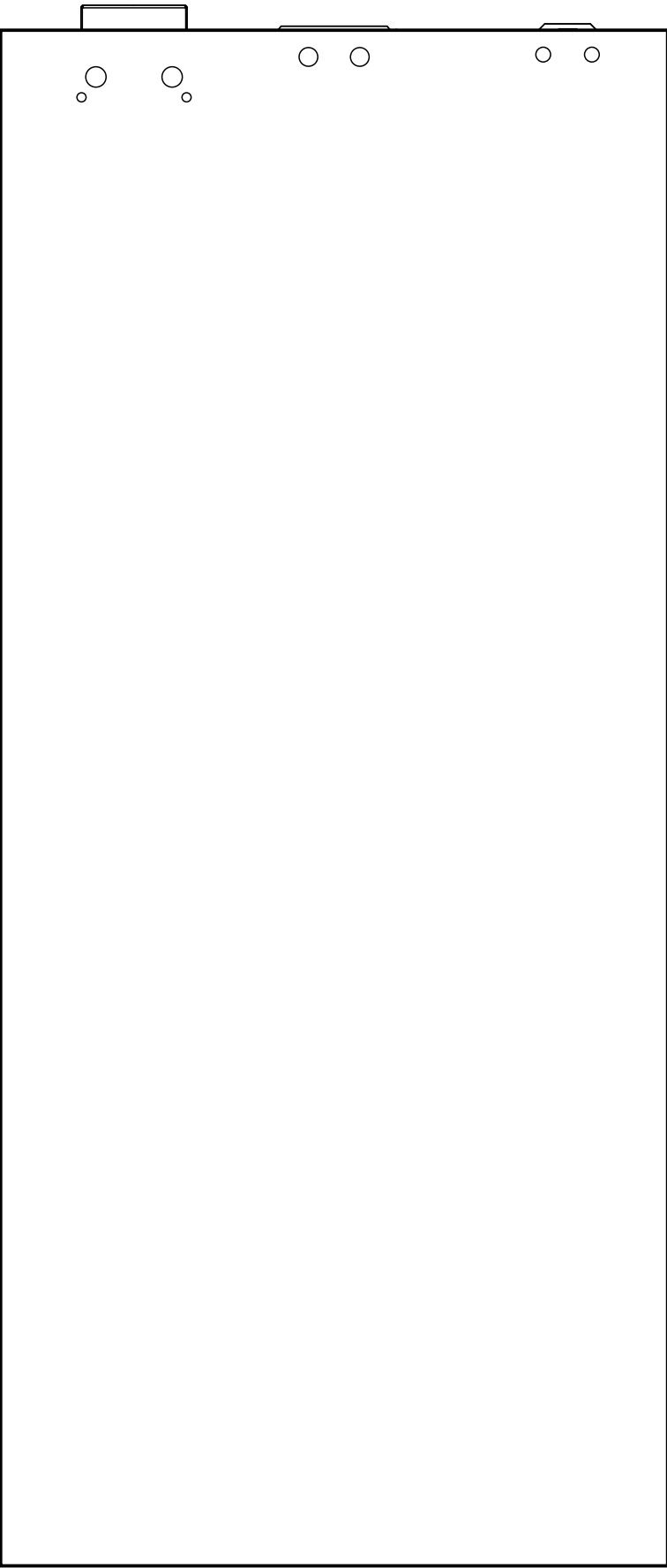
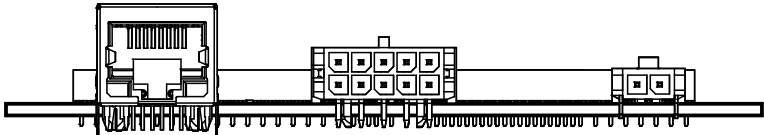


View from Top side (Scale 1:1)



View from Back side (Scale 1:1)

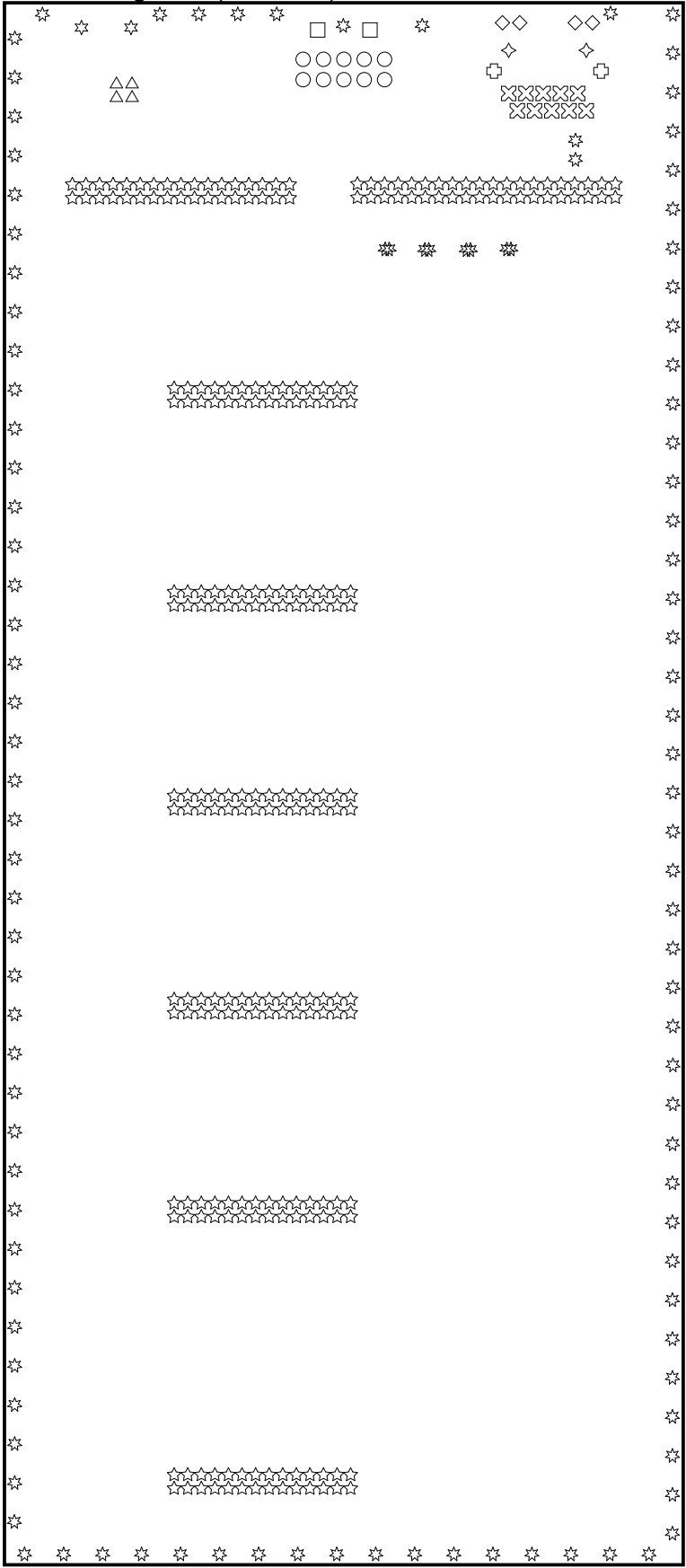


View from Bottom side (Scale 1:1)

100.00mm

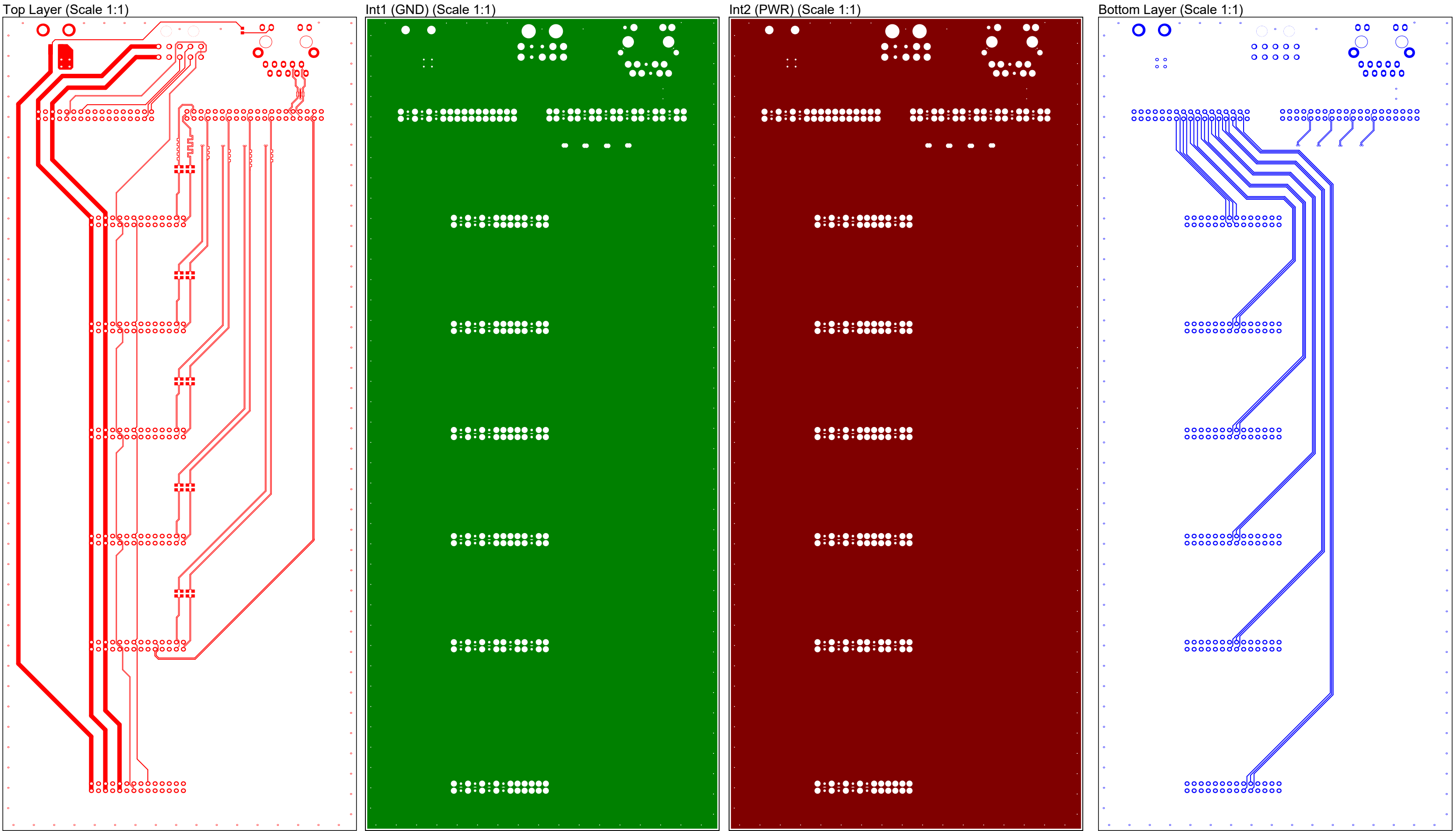
230.00mm

Drill Drawing View (Scale 1:1)



Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
☆	114	0.25mm	Plated	None
△	4	0.51mm	Plated	None
☆	242	0.71mm	Plated	+/-0.08mm
⊗	10	0.90mm	Plated	None
○	10	1.02mm	Plated	+/-0.05mm
◇	4	1.02mm	Plated	None
⊕	2	1.57mm	Plated	None
☆	2	2.41mm	Plated	None
□	2	3.00mm	Plated	+/-0.05mm
◇	2	3.25mm	Plated	None
	392 Total			



Transmission Line Structure Table

Impedance Id	Transmission Line	Target Impedance	Calculated Impedance	Trace layer	Lower Trace Width	Upper Trace Width	Gap	Reference layers	Substack
1	Edge-Coupled Coated Microstrip	100	100.03	Top Layer	0.17mm	0.17mm	0.13mm	Int1 (GND)	Board Layer Stack
2	Edge-Coupled Coated Microstrip	90	89.98	Top Layer	0.24mm	0.24mm	0.13mm	Int1 (GND)	Board Layer Stack
3	Edge-Coupled Coated Microstrip	120	119.98	Top Layer	0.09mm	0.09mm	0.13mm	Int1 (GND)	Board Layer Stack
4	Edge-Coupled Coated Microstrip	100	100.03	Bottom Layer	0.17mm	0.17mm	0.13mm	Int2 (PWR)	Board Layer Stack
5	Edge-Coupled Coated Microstrip	90	89.98	Bottom Layer	0.24mm	0.24mm	0.13mm	Int2 (PWR)	Board Layer Stack
6	Edge-Coupled Coated Microstrip	120	119.98	Bottom Layer	0.09mm	0.09mm	0.13mm	Int2 (PWR)	Board Layer Stack

Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber
	Top Overlay			Legend	GTO
	Surface Material	0.01mm	SM-001	Solder Mask	GTS
Nickel, Gold	Top Surface Finish	0.02mm		Surface Finish	
CF-004	Top Layer	0.04mm		Signal	GTL
Prepreg		0.23mm	PP-018	Dielectric	
CF-004	Int1 (GND)	0.04mm		Internal Plane	GP1
Core		0.93mm	Core-028	Dielectric	
CF-004	Int2 (PWR)	0.04mm		Internal Plane	GP2
Prepreg		0.23mm	PP-018	Dielectric	
CF-004	Bottom Layer	0.04mm		Signal	GBL
Nickel, Gold	Bottom Surface Finish	0.02mm		Surface Finish	
Surface Material	Bottom Solder	0.01mm	SM-001	Solder Mask	GBS
	Bottom Overlay			Legend	GBO

Total thickness: 1.60mm