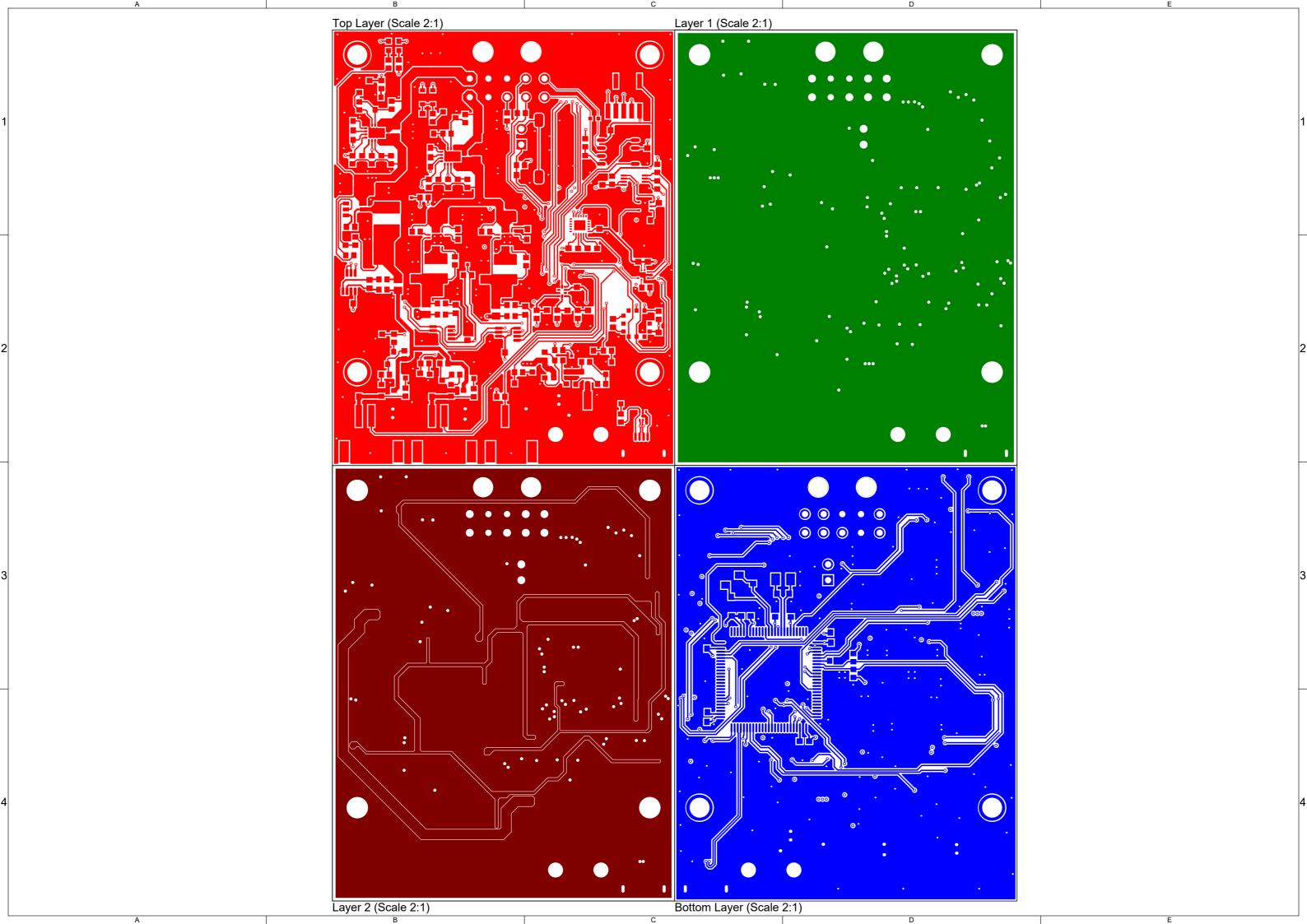


Drill Drawing View (Scale 2:1) **Drill Table** Symbol Count Hole Size Plated Hole Tolerance ☆ ' 0.25mm Plated None ☆ 252 $\Diamond \Diamond \Diamond$ \circ \Diamond 0.50mm | Plated | None ☆ ☆ ₹, Plated None 8 0.51mm ☆ □ 10 1.02mm | Plated | +/-0.05mm ☆ 2 Plated None 1.02mm ☆☆ X 2 2.41mm Plated None 0 2 3.00mm Plated +/-0.05mm ☆ 3.20mm Plated None ☆☆ ☆ ☆ 谷公 282 Total **☆ ☆** ☆ 鈆 ☆ ☆ ☆ ⇔⇔ \boxtimes \bowtie ☆ ☆ ☆ \Diamond \Diamond



Transmission Line Structure Table

Impedance Id	Transmission Line	Target Impedance	Calculated Impedance	Trace layer	Lower Trace Width	Upper Trace Width	Gap	Reference layers	Substack
1	Edge-Coupled Coated Microstrip	90	90.01	Top Layer	0.21mm	0.21mm	0.13mm	Layer 1	Board Layer Stack
2	Edge-Coupled Coated Microstrip	90	90.01	Bottom Layer	0.21mm	0.21mm	0.13mm	Layer 2	Board Layer Stack

_		_		
l)r	ill	Ta	h	Δ
$\boldsymbol{\omega}$		ıa	v	

Symbol	Count	Hole Size	Plated	Hole Tolerance
☆	252	0.25mm	Plated	None
\Diamond	2	0.50mm	Plated	None
\$\$	8	0.51mm	Plated	None
	10	1.02mm	Plated	+/-0.05mm
¢	2	1.02mm	Plated	None
×	2	2.41mm	Plated	None
0	2	3.00mm	Plated	+/-0.05mm
♦	4	3.20mm	Plated	None
	282 Total			

