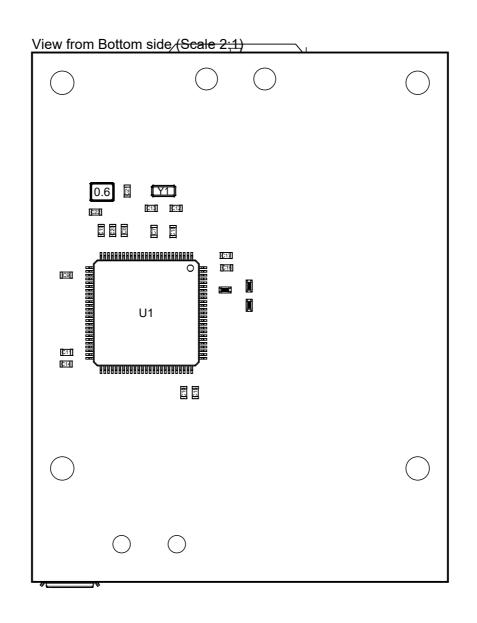
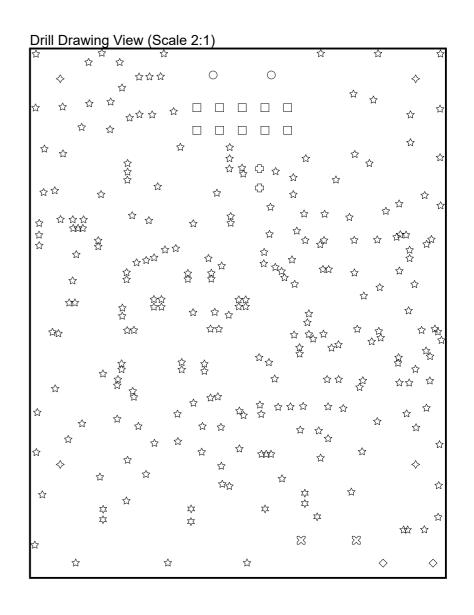
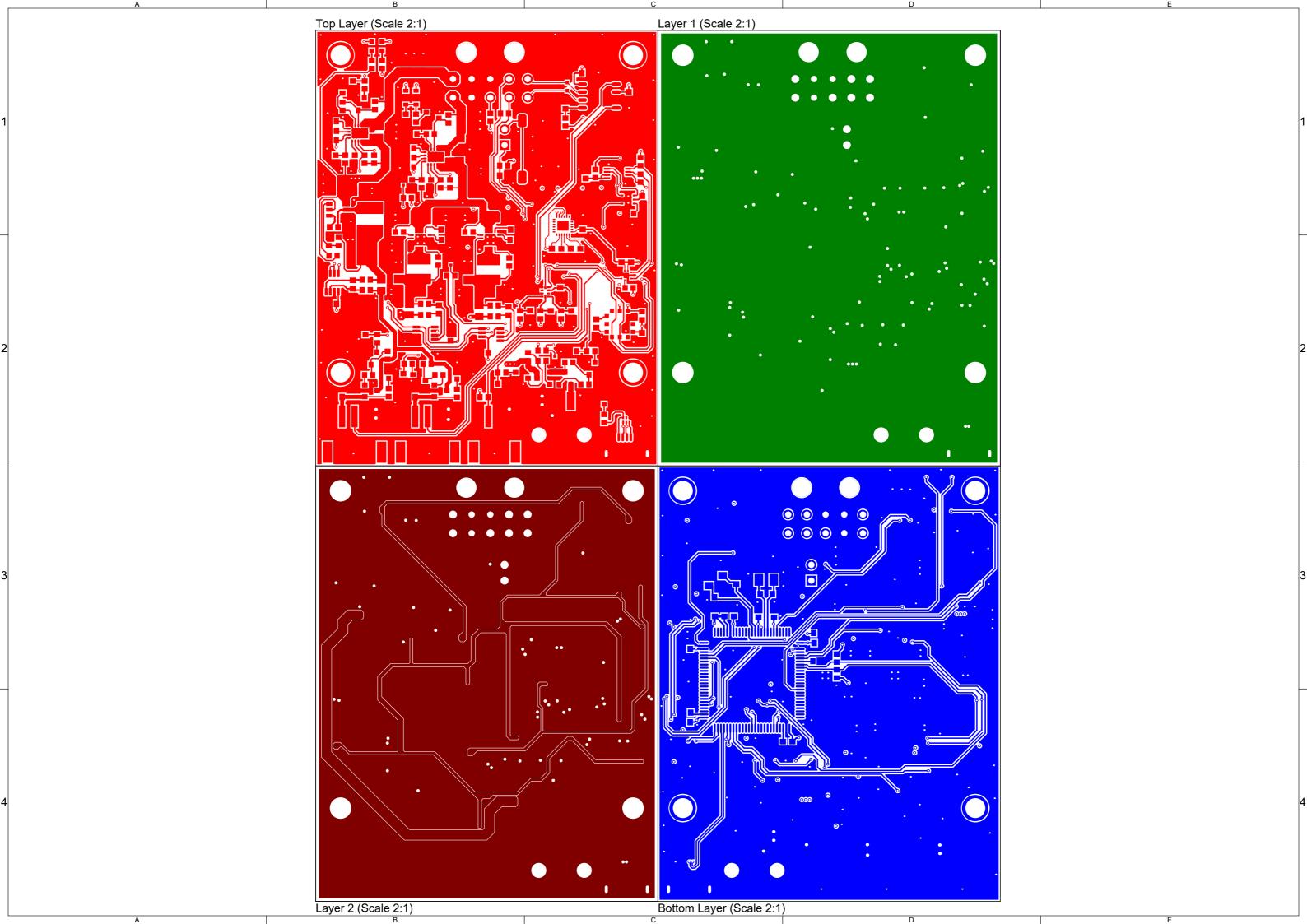
View from Top side (Sçale 2:1) [24] [23] [24] [55] U13 [55] ©2**5** Ф26 U2 **C**64 ¢6\$ ¢6\$ **J**8 ₹ U10 C57 ¢5₿ ¢6ф C48 SS 25 ב <u>`</u> 112 U17 **€**6**6** p J2 p







-	Γransmission L	ansmission Line Structure Table								
	Impedance Id	Transmission Line	Target Impedance	Calculated Impedance	Trace layer	Lower Trace Width	Upper Trace Width	Gap	Reference layers	Substack
	1	Edge-Coupled Coated Microstrip	90	90.01	Top Layer	0.21mm	0.21mm	0.13mm	Layer 1	Board Layer Stack
	2	Edge-Coupled Coated Microstrip	90	90.01	Bottom Layer	0.21mm	0.21mm	0.13mm	Layer 2	Board Layer Stack

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Symbol	Count	Hole Size	Plated	Hole Tolerance
☆	242	0.25mm	Plated	None
\Diamond	2	0.50mm	Plated	None
Σ , ζζ	8	0.51mm	Plated	None
	10	1.02mm	Plated	+/-0.05mm
¢	2	1.02mm	Plated	None
X	2	2.41mm	Plated	None
0	2	3.00mm	Plated	+/-0.05mm
\$	4	3.20mm	Plated	None
	272 Total			

