

Firelight UM+ Release

EA1	EA2 (BU)	GA
Jan '21	April '21	June'21
<ul style="list-style-type: none"> • UM based code <ul style="list-style-type: none"> • Firelight platform and BSP driver code for iProc 15 (including uart, timer, address, cache and MMU init, reboot) • CMICx related code (read/write register/table via schan) • Basic switch init (soc rest and init, misc init, tdm init, mmu init, port mapping, VLAN init, Qos init, MIB counter api, VLAN api) • Integrate PhyMod driver from SDK • CPU TX/RX (packet DCB and DMA, rx channel programming) • SyncE configuration • Interrupt mode for failover • Port APIs based on customer's requirement • Flash driver and persistence • RAM and flash map • Interrupt mode verification 	<ul style="list-style-type: none"> • SKUs and options related code (include configuration file and port configuration error checking) • M0 firmware and related code • Hardware linkscan • Loader for firmware upgrade, dual image and reboot • FTP client for UM firmware upgrade and BS firmware upgrade • Broadsync related code (in UM firmware and BS firmware) • 1-step TC configuration • Port APIs for SyncE • COE APIs • Serdes APIs based on customer's requirement • QoS features requested by customer • packet switch on all ports • MIB counter APIs verification 	<ul style="list-style-type: none"> • DVT test complete • Release notes