

UM 3.8.0 API Functions

	Function	Support in FL UM+	Coding Completed	Verified	Plan for API prototype	Note
Base						
	int board_unit_count (void)	YES	YES	YES	Ready for now	Always return 1 now
	char *board_name (void)	YES	YES	YES	Ready for now	
	uint8 board_uport_count(void)	YES	YES	YES	Ready for now	
	sys_error_t board_uport_to_lport(uint16 uport, uint8 *unit, uint8 *lport)	YES	YES	YES	Ready for now	
	board_lport_to_uport(uint8 unit, uint8 lport, uint16 *uport)	YES	YES	YES	Ready for now	
	sys_error_t board_uplist_to_lbmp(uint8 *uplist, uint8 unit, pbmp_t *pbmp)	YES	YES	YES	Ready for now	
	sys_error_t board_lbmp_to_uplist(uint8 unit, pbmp_t *pbmp, uint8 *uplist)	YES	YES	YES	Ready for now	
	soc_switch_t *board_get_soc_by_unit(uint8 unit)	YES	YES	YES	Ready for now	
	sys_error_t board_chip_revision(uint8 unit, uint16 *dev, uint16 *rev)	YES	YES	YES	Ready for now	
	phy_driver_t *board_get_phy_drv(uint16 uport)	NO	--	--		
TxRx						
	sys_error_t board_rx_set_handler(BOARD_RX_HANDLER fn)	YES	YES	NO	Ready for now	
	sys_error_t board_rx_fill_buffer(sys_pkt_t *pkt)	YES	YES	NO	Ready for now	
	sys_error_t board_tx(sys_pkt_t *pkt, BOARD_TX_CALLBACK cbk)	YES	YES	NO	Ready for now	
	void board_rtx_stop(void)	YES	YES	NO	Ready for now	
VLAN						
	sys_error_t board_vlan_type_set(vlan_type_t type)	YES	YES	NO	Ready for now	Only support 802.1Q VLAN mode in FL UM+
	sys_error_t board_vlan_type_get(vlan_type_t *type)	YES	YES	NO	Ready for now	Only support 802.1Q VLAN mode in FL UM+
	extern sys_error_t board_vlan_create(uint16 vlan_id)	YES	YES	NO	Ready for now	
	sys_error_t board_vlan_destroy(uint16 vlan_id)	YES	YES	NO	Ready for now	
	sys_error_t board_pvlan_port_set(uint16 vlan_id, uint8 *uplist)	NO	--	--		
	sys_error_t board_pvlan_port_get(uint16 vlan_id, uint8 *uplist)	NO	--	--		
	sys_error_t board_pvlan_egress_get(uint16 uport, uint8 *uplist)	NO	--	--		
	sys_error_t board_untagged_vlan_set(uint16 uport, uint16 vlan_id)	YES	YES	NO	Ready for now	
	sys_error_t board_untagged_vlan_get(uint16 uport, uint16 *vlan_id)	YES	YES	NO	Ready for now	
	sys_error_t board_qvlan_port_set(uint16 vlan_id, uint8 *uplist, uint8 *tag_uplist)	YES	YES	NO	Ready for now	
	sys_error_t board_qvlan_port_get(uint16 vlan_id, uint8 *uplist, uint8 *tag_uplist)	YES	YES	NO	Ready for now	
	uint16 board_vlan_count(void)	YES	YES	NO	Ready for now	
	sys_error_t board_qvlan_get_by_index(uint16 index, uint16 *vlan_id, uint8 *uplist, uint8 *tag_uplist, BOOL get_uplist)	YES	YES	NO	Ready for now	
PORT						
	sys_error_t board_port_reinit(uint16 uport)	YES	NO	NO	DEC-PRE1 release	
	sys_error_t board_port_link_status_get(uint16 uport, BOOL *link)	YES	NO	NO	DEC-PRE1 release	Rename from board_get_port_link_status
	sys_error_t board_port_speed_get(uint16 uport, int *speed)	YES	NO	NO	DEC-PRE1 release	Rename from board_port_mode_get
	sys_error_t board_port_speed_set(uint16 uport, int speed)	YES	NO	NO	DEC-PRE1 release	
	sys_error_t board_port_interface_get(uint16 uport, port_if_t *intf)	YES	NO	NO	DEC-PRE1 release	
	sys_error_t board_port_interface_set(uint16 uport, port_if_t *intf)	YES	NO	NO	DEC-PRE1 release	
	sys_error_t board_port_enable_get(uint16 uport, BOOL *portenable)	YES	NO	NO	DEC-PRE1 release	
	sys_error_t board_port_enable_set(uint16 uport, BOOL portenable)	YES	NO	NO	DEC-PRE1 release	
	sys_error_t board_port_an_get(uint16 uport, BOOL *an)	YES	NO	NO	DEC-PRE1 release	
	sys_error_t board_port_an_set(uint16 uport, BOOL an)	YES	NO	NO	DEC-PRE1 release	
	sys_error_t board_port_pause_get(uint16 uport, BOOL *pause_tx, BOOL *pause_rx)	YES	NO	NO	DEC-PRE1 release	
	sys_error_t board_port_pause_set(uint16 uport, BOOL pause_tx, BOOL pause_rx)	YES	NO	NO	DEC-PRE1 release	
	sys_error_t board_port_stat_get(uint16 uport, port_stat_t *stat)	YES	YES	YES	Ready for now	
	sys_error_t board_port_stat_clear(uint16 uport)	YES	YES	YES	Ready for now	
	sys_error_t board_port_stat_clear_all(void)	YES	YES	YES	Ready for now	

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sys_error_t board_port_loopback_get(uint16 uport, port_loopback_t *loopback)	YES	NO	NO	DEC-PRE1 release	
sys_error_t board_port_loopback_set(uint16 uport, port_loopback_t loopback)	YES	NO	NO	DEC-PRE1 release	Rename from baord_port_loopback_enable_set
sys_error_t board_port_ifg_get(uint16 uport, int speed, port_duplex_t duplex, int *ifg)	YES	NO	NO	DEC-PRE1 release	
sys_error_t board_port_ifg_set(uint16 uport, int speed, port_duplex_t duplex, int ifg)	YES	NO	NO	DEC-PRE1 release	
sys_error_t board_port_frame_max_get(uint16 uport, int *size)	YES	NO	NO	DEC-PRE1 release	
sys_error_t board_port_frame_max_set(uint16 uport, int size)	YES	NO	NO	DEC-PRE1 release	
sys_error_t board_port_class_get(uint16 uport, port_class_t pclass, uint32 *pclass_id)	YES	NO	NO	DEC-PRE1 release	
sys_error_t board_port_class_set(uint16 uport, port_class_t pclass, uint32 pclass_id)	YES	NO	NO	DEC-PRE1 release	
sys_error_t board_port_cable_diag(uint16 uport, port_cable_diag_t *status)	NO	--	--		
sys_error_t board_get_cable_diag_support_by_port(uint16 uport, BOOL *support)	NO	--	--		
uint16 board_cable_diag_port_count(void)	NO	--	--		
sys_error_t board_get_port_phy_autoneg(uint16 uport, BOOL *an, BOOL *an_done)	NO	--	--		
sys_error_t board_get_port_phy_eee_ability_remote(uint16 uport, uint8 *types)	NO	--	--		
sys_error_t board_get_port_phy_eee_min_wake_time(uint16 uport, uint8 type, uint16 *wake_t)	NO	--	--		
sys_error_t board_set_port_phy_eee_tx_wake_time(uint16 uport, uint8 type, uint16 wake_t)	NO	--	--		
sys_error_t board_get_port_link_speed(uint16 uport, int16 *speed)	NO	--	--		
/* NEW feature : serdes APIs related */	YES	NO	NO	JAN-EA1 release	
QoS					
sys_error_t board_qos_type_set(qos_type_t type)	YES	NO	NO	DEC-PRE1 release	
sys_error_t board_qos_type_get(qos_type_t *type)	YES	NO	NO	DEC-PRE1 release	
sys_error_t board_untagged_priority_set(uint16 uport, uint8 priority)	YES	NO	NO	DEC-PRE1 release	
sys_error_t board_untagged_priority_get(uint16 uport, uint8 *priority)	YES	NO	NO	DEC-PRE1 release	
/* NEW feature : other QoS APIs related */	YES	NO	NO	DEC-PRE1 release	
COE					
/* NEW feature : COE related APIs */	YES	NO	NO	JAN-EA1 release	
/* NEW feature : pass through APIs if needs */	YES	NO	NO	JAN-EA1 release	
Misc					
flash_dev_t * board_get_flash_dev(void)	YES	YES	YES	Ready for now	
BOOL board_check_image(hsaddr_t address, hsaddr_t *outaddr)	YES	YES	YES	Ready for now	Only for loader
void board_load_program(hsaddr_t entry)	YES	YES	YES	Ready for now	Only for loader
loader_mode_t board_loader_mode_get(bookkeeping_t *bk_data, BOOL reset)	YES	YES	YES	Ready for now	Only for loader
void board_loader_mode_set(loader_mode_t mode, bookkeeping_t *bk_data)	NO	--	--		Only for firmware upgrade
BOOL board_check_imageheader(msaddr_t address)	YES	YES	YES	Ready for now	Only for loader
void board_reset(void *param)	YES	YES	YES	Ready for now	
void board_loop_detect_enable(BOOL enable)	NO	--	--		
uint8 board_loop_detect_status_get(void)	NO	--	--		
BOOL board_phy_led_mode_set(uint8 lport, int mode)	NO	--	--		
void board_firmware_version_get(uint8 *major, uint8 *minor, uint8 *eco, uint8 *misc)	YES	YES	YES	Ready for now	
sys_error_t board_mdns_enable_set(BOOL enable)	NO	--	--		
sys_error_t board_mdns_enable_get(BOOL *enable)	NO	--	--		
void board_active_image_set(uint32 partition)	YES	YES	YES	Ready for now	Only for loader
uint32 board_active_image_get(void)	YES	YES	YES	Ready for now	
BOOL board_select_boot_image(hsaddr_t *outaddr)	YES	YES	YES	Ready for now	Only for loader
BOOL board_reset_button_get(void)	NO	--	--		

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	/* NEW feature : temperature query related */	YES	NO	NO	JAN-EA1 release	
Interrupt	void board_intr_handling_enable(void)	YES	NO	NO	Ready for now	
	void board_intr_handling_disable(void)	YES	NO	NO	Ready for now	
GPIO	int board_gpio_mode_set(uint32 gpio, board_gpio_mode_t mode)	YES	NO	NO	Ready for now	
	int board_gpio_value_set(uint32 gpio, bool val)	YES	NO	NO	Ready for now	
	int board_gpio_value_get(uint32 gpio, bool *val)	YES	NO	NO	Ready for now	
	int board_gpio_intr_func_set(uint32 gpio, board_intr_f intr_func, uint32 intr_param)	YES	NO	NO	Ready for now	
	int board_gpio_intr_enable_set(uint32 gpio, bool enable)	YES	NO	NO	Ready for now	
	int board_gpio_intr_type_set(uint32 gpio, board_gpio_intr_type_t type)	YES	NO	NO	Ready for now	
LED	int board_led_uc_num_get(int *led_uc_num)	YES	NO	NO	JAN-EA1 release	
	int board_led_fw_start_set(int led_uc, bool start)	YES	NO	NO	JAN-EA1 release	
	int board_led_fw_start_get(int led_uc, bool *start)	YES	NO	NO	JAN-EA1 release	
	int board_led_control_data_write(int led_uc, int offset, const uint8 *data, int len)	YES	NO	NO	JAN-EA1 release	
	int board_led_control_data_read(int led_uc, int offset, uint8 *data, int len)	YES	NO	NO	JAN-EA1 release	
Hardware linkscan	int board_hw_linkchange_intr_func_set(board_intr_f)	YES	NO	NO	JAN-EA1 release	
	int board_hw_linkchange_intr_enable_set(bool enable)	YES	NO	NO	JAN-EA1 release	
TIME	/* NEW feature : 1-step TC related */	YES	NO	NO	JAN-EA1 release	
	sys_error_t board_time_sync_clock_source_control_get(uint8 unit, bcm_time_sync_clock_source_config_t *clk_src_config, bcm_time_sync_clock_source_control_t control, int *value)	YES	YES	NO	Ready for now	
	sys_error_t board_time_sync_clock_source_control_set(uint8 unit, bcm_time_sync_clock_source_config_t *clk_src_config, bcm_time_sync_clock_source_control_t control, int value)	YES	YES	NO	Ready for now	
	int board_broadsync_config_set(int mode, int bitclk_hz, int hb_hz)	YES	NO	NO	JAN-EA1 release	
	int board_broadsync_freq_offset_set(int offset_ppb)	YES	NO	NO	JAN-EA1 release	
	int board_broadsync_phase_offset_set(int offset_sec, int offset_nsec, int is_negative)	YES	NO	NO	JAN-EA1 release	
	int board_broadsync_ntp_offset_set(int offset_sec, int offset_nsec, int is_negative)	YES	NO	NO	JAN-EA1 release	
	int board_broadsync_debug_1pps_set(uint8 enableOutput)	YES	NO	NO	JAN-EA1 release	
	int board_broadsync_log_configure(uint32 debug_mask, uint64 udp_log_mask, bcm_mac_t src_mac, bcm_mac_t dest_mac, uint16 tpid, uint16 vid, uint8 ttl, bcm_ip_t src_addr, bcm_ip_t dest_addr, uint16 udp_port)	YES	NO	NO	JAN-EA1 release	
	int board_broadsync_time_set(int time_sec, int time_nsec)	YES	NO	NO	JAN-EA1 release	
L2		NO	--	--		
Mirror		NO	--	--		
Rate		NO	--	--		
Mcast		NO	--	--		
Lag		NO	--	--		