

# **KBP SDK 1.4.13**

## **Release Notes**

## Revision History

Revision	Date	Change Description
KBP-SDK-1.4.13	06/25/17	Bug fixes
KBP-SDK-1.4.12	06/05/17	AVS API bug fixes, Memory optimizations, bug fixes
KBP-SDK-1.4.11	03/30/17	AVS API support, BCM15K related bug fixes and enhancements
KBP-SDK-1.4.10	01/15/17	BCM15K related bug fixes and enhancements
KBP-SDK-1.4.9	10/26/16	Serdes Enhancements
KBP-SDK-1.4.8	09/30/16	Bug fixes enhancements and performance improvements for Optimus Prime, silicon testing.
KBP-SDK-1.4.7	07/29/16	Update rate improvements for Jericho + NLA8865x. Bug fixes and performance improvements for Optimus Prime, silicon testing.
KBP-SDK-1.4.6	06/12/16	Update rate improvements for Jericho + NLA8865x. Bug fixes and performance improvements for Optimus Prime, silicon testing.
KBP-SDK-1.4.5	05/01/16	Bug fix for Crash Recovery
KBP-SDK-1.4.4	03/09/16	Multiple features for Jericho + NLA8865x. Bug fixes and performance improvements for Optimus Prime
KBP-SDK-1.4.3	01/11/16	Optimus Prime full feature support and bug fixes. Increased IPv4 and IPv6 scale for ARAD/Jericho customers using NLA8865x (NLA12K)
KBP-SDK-1.4.2	07/27/15	Optimus Prime full feature support and bug fixes
KBP-SDK-1.4.1	03/16/15	Optimus Prime new feature support and bug fixes
KBP-SDK-1.4.0	12/19/14	Preliminary support for Optimus Prime
KBP-SDK-1.3.5	02/23/15	NetRoute coherency bug fix Handling for algorithmic errors reported by device

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## Release Notes for KBP SDK Rev. 1.4.13

### Description:

KBP SDK 1.4.13 contains the following bug fixes

- [KBPSDK-2108] - OP: SMT, Palladium mixed LPM+ACL case : assert
- [KBPSDK-2123] - OP: Non-DMA mode not working
- [KBPSDK-2146] - OP: NetACL + WB assertion
- [KBPSDK-2169] - OP: Deep power down of unused serdes lanes
- [KBPSDK-2233] - Have to store and restore the device properties for Crash Recovery
- [KBPSDK-2260] - O3S: KBP parallel flush is crashing
- [KBPSDK-2263] - OP: Broadcast with dynamic resources: search miss-match
- [KBPSDK-2267] - OP: PRBS Disable not being done correctly
- [KBPSDK-2269] - OP: Temperature API returning incorrect values
- [KBPSDK-2270] - OP: AVS code needs to be invoked after C\_RST

In addition all fixes and enhancements part of KBP SDK 1.4.12 are also part of this release

- Kbp\_device\_config structure has been enhanced to have the additional fields.  
Setting of the init\_avs field to non-zero value will cause AVS initialization to run as part of kbp\_device\_interface\_init() or kbp\_device\_init()
  - uint32\_t init\_avs;                   /\*\*< Flag to init AVS \*/
  - int32\_t max\_avs\_voltage;       /\*\*< Upper limit for die convergence voltage \*/
  - int32\_t min\_avs\_voltage;       /\*\*< Lower limit for die convergence voltage \*/
- Memory enhancements for NetRoute and ACLs
- [KBPSDK-2250] BCM15K device driver is broken for 32b x86
- BCM15K SMT related bug fixes and improvements
- Improvements to BCM15K update rates

This version of the SDK also includes all enhancements and fixes as of KBP SDK 1.4.11. KBP SDK 1.4.11 introduces support for Adaptive Voltage Scaling (AVS). A new header file "kbp\_avs.h" is added under the "include" directory. Following AVS APIs are supported in this SDK release:

1. ***kbp\_status kbp\_device\_interface\_is\_avs\_enabled(enum kbp\_device\_type type, struct kbp\_avs\_config \*config, uint32\_t \*is\_enabled);***

This API can be used to check if AVS is enabled in the KBP chip or not.

"type" : The device type. Currently KBP\_DEVICE\_OP is the only value supported  
 "\*config" : AVS init configuration can be provided via. Users are expected to pass the MDIO read/write functions via the config structure. All other fields of this Structure other than the Opaque user provided handle can be set to 0  
 "\*is\_enabled" : Will be set to 1 if the AVS is enabled in the chip; else set to 0

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## 2. *kbp\_status kbp\_device\_interface\_init\_avs(enum kbp\_device\_type type, struct kbp\_avs\_config \*config, FILE \*fp);*

This API can be used to compute and set the final convergence voltage of the chip. This API will attempt to change the convergence voltage only if the chip has been enabled for AVS.

“type” : The device type. Currently KBP\_DEVICE\_IP is the only value supported  
 “\*config” : AVS init configuration can be provided via. Users are expected to pass the MDIO read/write functions via the config structure. All other fields of this Structure other than the Opaque user provided handle can be set to 0  
 “\*fp” : Is used for logging. Valid file pointer is expected. As of now is ignored

### Bug Fixes:

- [KBPSDK-2209]: O3S crash issue fix during the kbp\_device\_init ()
- [KBPSDK-2197]: portability changes for linker errors during linking kaps library into vxworks image
- [KBPSDK-2186]: O3S Fixed memtest crash for BCM52311
- [KBPSDK-2182]: O3S kbp\_device\_fix\_errors(), must return the error code if ESR: bits set other than DBA/RPT errors
- [KBPSDK-2161]: O3S ISSU multi-thread support per SMT only during ISSU cache and flush

### KBP SDK 1.4.10 enhancements and bug fixes

New APIs have been added to read the BCM15K die voltage and temperature

- *kbp\_status kbp\_device\_read\_die\_voltage(struct kbp\_device \*device, int32\_t \*voltage);*
- *kbp\_status kbp\_device\_read\_die\_temperature(struct kbp\_device \*device, int32\_t \*temperature);*
- [KBPSDK-2124] Generic and Selective broadcast support for BCM15K
  - When destroying device handles during teardown, only the main device handle should be destroyed
- [KBPSDK-2137] – BCM15K kbp\_diags has no option to connect to other KBP devices on line card
- [KBPSDK-2113] – BCM15K 640b + range cases with search mis-match
- [KBPSDK-2095] – Range expansion heuristics tuning
- [KBPSDK-2081] – KBP Driver: Need a way to specify PCI bus mapping to KBP selection
- [KBPSDK-1753] – Debug Feature Request - Result Register Read

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The packaged version 1.7 of the device driver is the only compatible version with this release. The PCIe device driver must be upgraded along with the KBP SDK for BCM15K.

KBP SDK 1.4.9 resolves symbol conflict between the Broadcom Jericho SDK 5.5.6 and KBP SDK 1.4.8 which use the same Serdes IP code. In addition the following Serdes improvements have been incorporated in this version of the SDK.

- [KBPSDK-2088] - Falcon16 API names clash with version of IP code in DNX SDK
- [KBPSDK-2089] - Temperature monitoring not enabled for serdes
- [KBPSDK-2090] - Unused lanes need to be powered down
- [KBPSDK-2091] - Upgrade Falcon16 microcode to latest version D102\_02

KBP SDK 1.4.9 has been tested for all supported features on Optimus Prime (BCM15K). All features of NLA12K and Jericho internal forwarding solution are GA quality and fully supported.

The SDK contains the following enhancements/fixes delivered as part of KBP SDK 1.4.8

- Warmboot support for NetACL and NetRoute databases with and without associated data
- Aging and Hit Bits
- [KBPSDK-2038] O3S: Need XPT per NP / SMT for 2-to-1 application
- [KBPSDK-2072] BCM15K: Interface initialization enhancements for polarity inversion, asymmetric lane enables and lane swapping

The SDK contains the following bug fixes

- [KBPSDK-1981] BCM15K: PCIe xpt requires byte swapping on big endian systems
- [KBPSDK-2062] BCM15K: S\_RST and C\_RST callbacks have incorrect
- [KBPSDK-2017] BCM15K: Quad8 links not brought up correctly

The enhancement KBPSDK-2072 and KBPSDK-2038 introduced multiple interface related features. The device configuration structures defined in include/init.h passed to kbp\_device\_interface\_init and/or kbp\_device\_init has been enhanced to accept the following fields:

1. The start lane and num\_lanes field can be specified for upto four ports. For four port the following flag KBP\_DEVICE\_QUAD\_PORT should be passed in
2. *uint32\_t reverse\_lanes*: The field is interpreted as a bitmap. Each bit specifies whether the lanes of port are reversed. Bit0 if set to one will reverse all the lanes of port0, etc.

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3. `int32_t (*get_rx_tx_lanes_bmp)(uint64_t *rx_lanes_bmp, uint64_t *tx_lanes_bmp);`  
The callback function is invoked during init if provided. The user is expected to populate the RX and TX lanes bitmap if they wish to configure asymmetric number of lanes for RX and TX. If this feature is used the num\_lanes field in the port map should be provided as maximum value of the two and the callback can be used to shut down specific lanes. The field can be set to NULL if not used
  4. `int32_t (*get_polarity_invert_lanes_bmp)(uint64_t *lanes_bmp);` The following option callback pointer can be provided by the user to invert the polarity of the lanes. Each bit in the bitmap if set to one will invert the polarity of that physical lane number. If this feature is not used the callback function should be NULL.
  5. `uint32_t reset_only:` This bitmap is one hot encoding for each port of the device. If the bit is set all the lanes belonging to that port are configured, however, RX is not enabled. The caller can lazily enable the RX at a later point. This feature is useful if the ports need to be enabled lazily.

Internally discovered bugs through testing on Optimus Prime silicon have been resolved. Please refer to the Optimus Prime Silicon Errata sheet for additional details on the silicon status.

This release also supports crash recovery for NLA12K and Jericho internal forwarding solution and contains all the issues resolved as of KBP SDK 1.3.13 and KBP SDK 1.4.7. All features of NLA12K and Jericho internal forwarding solution are GA quality and fully supported.

This SDK includes –

- APIs to initialize the device, create instructions and keys, and create and manage databases.
- Multiple Reference applications that showcase use of Optimus Prime and NLA12K.
- A C-Model for NLA12K and Optimus Prime. The model is transaction, not cycle accurate.
- The SDK can be used in Blackhole mode without using the C-Model or real device. Pass the transport layer handle as null in `kbp_device_init()`.
- Dynamic resource management of databases for NLA12K and Optimus Prime
- Support for managing multiple KBP's on one line card
- Warmboot and crash recovery

#### Optimus Prime:

- Any existing application coded for NLA12K is expected to work with Optimus Prime C-Model
- All aspects of configuring and managing the device
- NetACL, including associated data.
- NetRoute with associated data

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- Support for Massively parallel ACLs, Power controlled ACLs with associated data.
  - Single cycle lookup of multi access LPM databases.
  - Resolution between lookup results of databases. Typical use is for resolving the result of a match between a public and private forwarding database.
  - Hit bits and Aging
  - Warmboot for all database types

**NLA12K:**

- GA Support for NetRoute, Massively parallel ACLs, Power-controlled ACLs, and Associated data for ACLs, SMT and Cascade.
- Warmboot and crash recovery support for all database types
- Parity error correction (In field soft repair) for entries in DBA and correction of algorithmic errors reported by device

**Devices Supported:**

- NL12000, NLA12000, NLA88650, ERC120 , SD5452A
- SDK has been tested with Rev A2 and Rev B0 and B1 Silicon
- BCM52311 SDK has been verified against A0 and A1 silicon

**Related documents:** (available at <https://support.broadcom.com>) :

- Device Datasheets: NLA12000-DS116-R-KBP, NLA88650-DS107-R-KBP, 41CSERC120-DS115-R-KBP , 48CSSD5452A-DS115-R-KBP, 1CSCSCO-DS116-RDS-KBP 15000-DS100-R, 52311-DS101-R
- KBP SDK Reference Manual, KBP-SDK-SWUM103-R or newer

**Known Limitations / Issues:**

The following features of the Optimus Prime chip are currently not supported

- Capacity and update rates are not optimal for NetACL databases
- The KBP SDK cannot recover if a fatal error occurs when transporting messages to the KBP

**Optimus Prime C-Model Limitations:**

The C-Model is a transaction accurate, not cycle accurate model. Model does not support-

- Network / interface specific functionality such as ILA protocol, Network Byte Order, packet errors.
- ECC, Parity scan / protection, Parity Errors.
- MDIO, JTAG Boundary Tag Registers
- Response to Error Detection, Test Pattern Generator / Checker
- Multiple instances of C-Model may not be instantiated at the same time

**NLA12K C-Model Limitations:**

The C-Model is a transaction accurate, not cycle accurate model. Model does not support-

- Network / interface specific functionality such as ILA protocol, Network Byte Order, packet errors.
- ECC, Parity scan / protection, Parity Errors.
- MDIO, Cascade of processors, JTAG Boundary Tag Registers
- Upper limit on total Associated Data in responses
- Low Power Mode (see Dynamic Power Control section in Datasheet)
- Response to Error Detection, Test Pattern Generator / Checker