# Requirements Document: Multi-Camera, System-on-Chip (SoC) Based, Real-Time Video Processing for UAS and VR/AR Applications

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Abstract

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# **CONTENTS**

| 1 | Introduction          |                                      |   |  |  |  |
|---|-----------------------|--------------------------------------|---|--|--|--|
|   | 1.1                   | Purpose                              | 2 |  |  |  |
|   | 1.2                   | Scope                                | 2 |  |  |  |
|   | 1.3                   | Definitions, Acronyms, Abbreviations |   |  |  |  |
|   |                       | 1.3.1 Definitions                    | 2 |  |  |  |
|   |                       | 1.3.2 Acronyms                       | 3 |  |  |  |
|   |                       | 1.3.3 Abbreviations                  | 3 |  |  |  |
|   | 1.4                   | References                           | 3 |  |  |  |
|   | 1.5                   | Overview                             | 3 |  |  |  |
| 2 | Overa                 | all Description                      | 4 |  |  |  |
|   | 2.1                   | Product Perspective                  | 4 |  |  |  |
|   | 2.2                   | Product Functions                    | 4 |  |  |  |
|   | 2.3                   | Constraints                          | 5 |  |  |  |
|   | 2.4                   | Assumptions & Dependencies           | 5 |  |  |  |
| 3 | Specific Requirements |                                      |   |  |  |  |
|   | 3.1                   | TX1/2                                | 5 |  |  |  |
|   | 3.2                   | CSI Board                            | 5 |  |  |  |
|   | 3.3                   | Cameras                              | 5 |  |  |  |
| 4 | Devel                 | lopment Schedule                     | 6 |  |  |  |
|   | 4.1                   | Development Schedule Tasks           | 6 |  |  |  |

# 1 Introduction

# 1.1 Purpose

This software requirements specification is intended to define the requirements of the project of developing a multicamera, multispectral image processing system, that operates on a System-on-Chip (SoC) at near-real-time, for use in ground and air based applications. Defined requirements will allow for a contract between us, the developers, and Rockwell Collins, our client, on what Rockwell Collins wants us to deliver in their desired software. This document is intended for review and reference by both the developers and the clients.

#### 1.2 Scope

The product outlined in this requirements document will be the multi-camera, SoC based, real-time video processing for UAS and VR/AR applications. This product will need to be able to generate a stitched video output from a multi-camera input. The product is intended to help initialize our client's development of a cheaper alternative to a product that is already offered to their customers.

The software products that will be produced include software for a stitched video output from the NVIDIA TX1/2, receiving the input from two visible band cameras. The video output is expected to be near real-time, and the latency from the camera input to the video output is expected to be improved upon throughout the project. Video output stretch goals is to have software that fuses the video output from the input of three, four, five, and six cameras, and have up to four infrared band inputs.

Output display stretch goals will be to incorporate IMU data, orientation tracking data, GPS data, and geolocate imagery. Two final stretch goals are packaging the hardware for flight, and interfacing the system to support the client's desired cameras for flight use.

The goal of the software is to contribute to a project that will assist pilots during low visibility conditions during the day, night, and inclement weather for all phases of flight. The video input from infrared and visible band cameras combined with on-board sensor input, and databases will enhance a pilot's vision for a UAS.

# 1.3 Definitions, Acronyms, Abbreviations

#### 1.3.1 Definitions

geolocate imagery -

low visibility -

multiple cameras - At least two cameras, but a maximum of six cameras for video input.

Near real-time -

NVIDIA TX1/2 - NVIDIA GPUs, the Jetson TX1 or the Jetson TX2.

spectral bands -

## 1.3.2 Acronyms

| Term                         | Acronym |  |  |
|------------------------------|---------|--|--|
| Augmented Reality            | AR      |  |  |
| Camera Serial Interface      | CSI     |  |  |
| Enhanced Vision System       | EVS     |  |  |
| Global Positioning System    | GPS     |  |  |
| Graphic Processing Unit      | GPU     |  |  |
| Image Signal Processors      | ISP     |  |  |
| Inertial Measurement Unit    | IMU     |  |  |
| Head-up Display              | HUD     |  |  |
| System-on-chip               | SoC     |  |  |
| System-on-module             | SOM     |  |  |
| Size, weight, power and cost | SWaP-C  |  |  |
| Three Dimensional            | 3D      |  |  |
| Two Dimensional              | 2D      |  |  |
| Video Input                  | VI      |  |  |
| Unmanned Aerial Vehicle      | UAV     |  |  |
| Unmanned Aircraft System     | UAS     |  |  |
| Virtual Reality              | VR      |  |  |

# 1.3.3 Abbreviations

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#### 1.4 References

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#### 1.5 Overview

This project aims to create a device that is capable of combining the video input from two or more cameras and produce and output at near real-time. Our proposed solution will use an NVIDIA Jetson device, which we will use for its integrated GPU.

We need this GPU to combine the images from multiple cameras. The end goal is to have a system that uses the input from multiple cameras that operate on the infrared and visible light spectral bands. By using these spectral bands, we

should be able to produce an image that can be used to see in low-visibility situations, such as landing a UAV in fog.

The images we produce will be 2D representations of our collective image captures. In other words, we do not aim to create a 3D image or a dynamic focus image. This is certainly possible when using multiple cameras, but we simply aim to use multiple cameras on different spectral bands to create one image of one subject that is the combination of all images captured by the cameras.

#### 2 Overall Description

#### 2.1 Product Perspective

The system will be self-contained and consists of three parts: one NVIDIA TX1/2, one CSI carrier board, and at least two cameras. The cameras connect to the CSI board, which is connected to the NVIDIA TX1/2. The NVIDIA TX1/2 is responsible for decoding the serial data retrieved by the CSI board from the cameras, and is then be used to execute the software for image processing and combining images from multiple cameras.

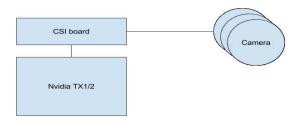


Fig. 1: Product Block Diagram

#### 2.2 Product Functions

The basic functionality of the product will be to produce stitched images on a video output that is provided by multiple camera inputs capable of sensing visible and infrared spectral bands. These images will be relayed in near real-time so that it can be used as a video feed for the pilot of a UAS during low visibility flight conditions.

A functional stretch goal for video output provided by camera input is to fuse the input from the visible and spectral bands, which will overlay the two types of output and enhance the vision for a UAS pilot.

Output display functional stretch goals will be to provide indications from IMU data, orientation tracking data, GPS data, and geolocate imagery and have them displayed with the video output provided by the camera input.

#### 5

#### 2.3 Constraints

The system must operate in near real-time. In other words, the camera feed(s) must be processed quickly enough for the user to make snap decisions based on the feed. The NVIDIA board should process each frame before the next one arrives to be processed. If were recording at 30 frames per second (fps), then each output frame should be processed in less than 1/30 of a second.

#### 2.4 Assumptions & Dependencies

LIST ITEMS, WRONG, THIS SHOULD BE IN PARAGRAPH FORM

Software deployed on NVIDIA TX1/2 with NVIDIA Jetpack from Ubuntu machine Adequate power supplies being used Cameras being aimed at same subject; capturing mostly the same image Each camera works independently of the system

#### 3 Specific Requirements

THIS NEEDS TO BE IN PARAGRAPH FORM, WITH SECTIONS FROM THE IEEE STD 830-1998

#### 3.1 TX1/2

Adequate power supply

NVIDIA Jetpack software + Ubuntu system to deploy it

## 3.2 CSI Board

Camera serial interface is the hardware that interfaces with different cameras or image sensors, which provides output for a computer with pixel data and signals that can be used for subsequent image processing. Therefore, the CSI is the way to be used to transfer camera input to computer in this project.

In order to carry up to six cameras, a carrier board with CSI such as J106 is required to connect cameras and NVIDIA TX1/2, and the video output format from CSI is depending on different chosen cameras.

## 3.3 Cameras

Visible light

Infrared

UV

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# 4 DEVELOPMENT SCHEDULE

|        | 2017     |          | 2018    |          |       |       |     |  |
|--------|----------|----------|---------|----------|-------|-------|-----|--|
|        | November | December | January | February | March | April | May |  |
| Task 1 |          | ]        |         |          |       |       |     |  |
| Task 2 |          |          |         |          |       |       |     |  |
| Task 3 |          | [        |         | ]        |       |       |     |  |
| Task 4 |          |          |         |          |       |       |     |  |
| Task 5 |          |          |         | [        |       |       |     |  |
| Task 6 |          |          |         |          |       |       |     |  |
| Task 7 |          |          |         |          |       | [     |     |  |
|        |          |          |         |          |       |       |     |  |

Fig. 2: Project Schedule

# 4.1 Development Schedule Tasks

- Task 1: Have hardware procured and assembled.
- Task 2: Produce a tiled video output from the input of six cameras.
- Task 3: Produce stitched video output from the input of two and three cameras, and have latency estimates produced.
- Task 4: Produce a dual stitched video output that is combined into a fused five-camera output (stretch goal).
- Task 5: Incorporate IMU data, orientation tracking data, GPS data, and geolocate imagery into the video output (stretch goals).
- Task 6: Package the system hardware for flight (stretch goal).
- Task 7: Produce a software interface for the system to accommodate higher quality cameras (stretch goal).