

ELECTRONICS AND COMMUNICATION TECHNOLOGIES: ELECTRONICS SYSTEMS

LM Cyber Security – Fall 2024

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Dip. Ing. Informazione

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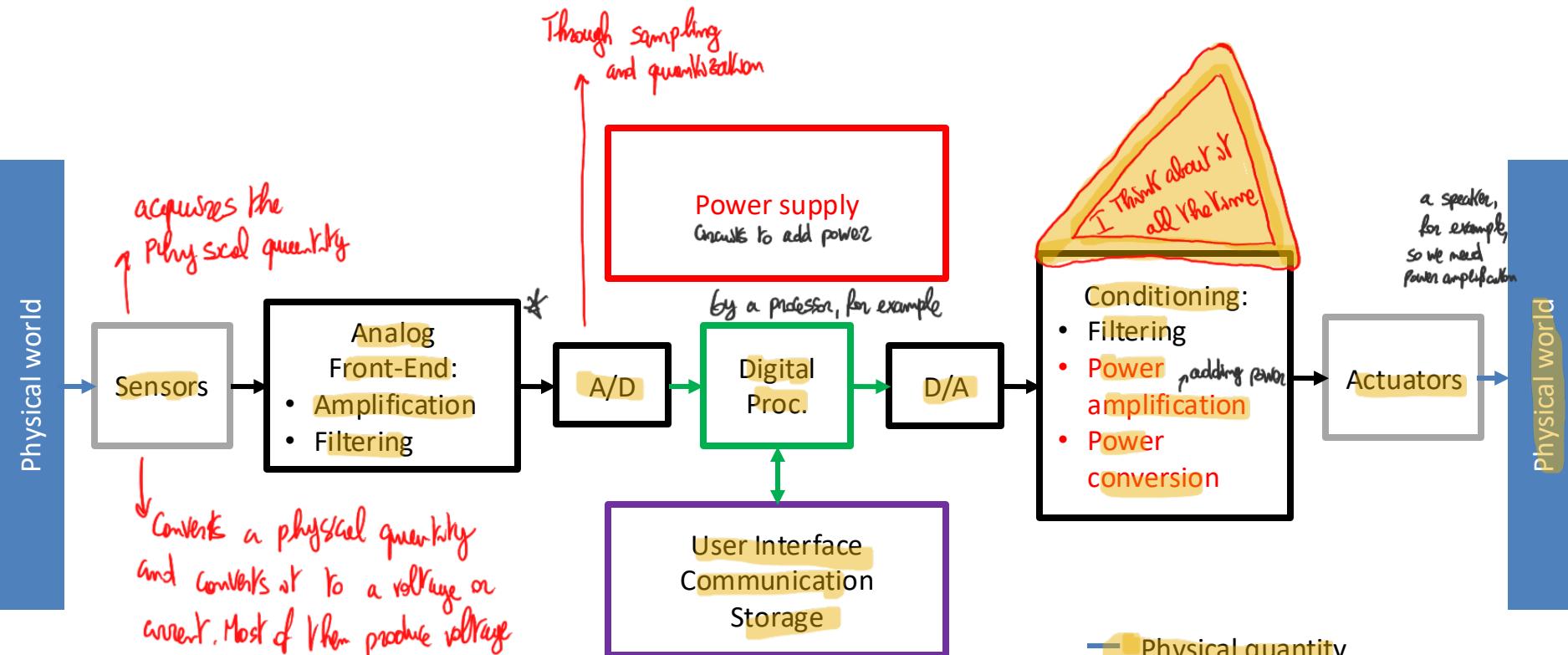
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Office hours:

Friday 14-16. Please, contact me in advance before showing up.
We can also arrange an appointment remotely on Microsoft
Teams.



Electronic Processing System



A/D: Analog to Digital converter
D/A: Digital to Analog converter

* It's possible that the output of a sensor is small and has to be amplified. Keep in mind that the circuits are analog.

Physical quantity
Analog signal
Digital signal

My is represented by a continuous function of time. Values continuously inside a range and continuously through time. But can't be processed by digital system.

NOTE: every circuit has its own specifications for power supply needed.

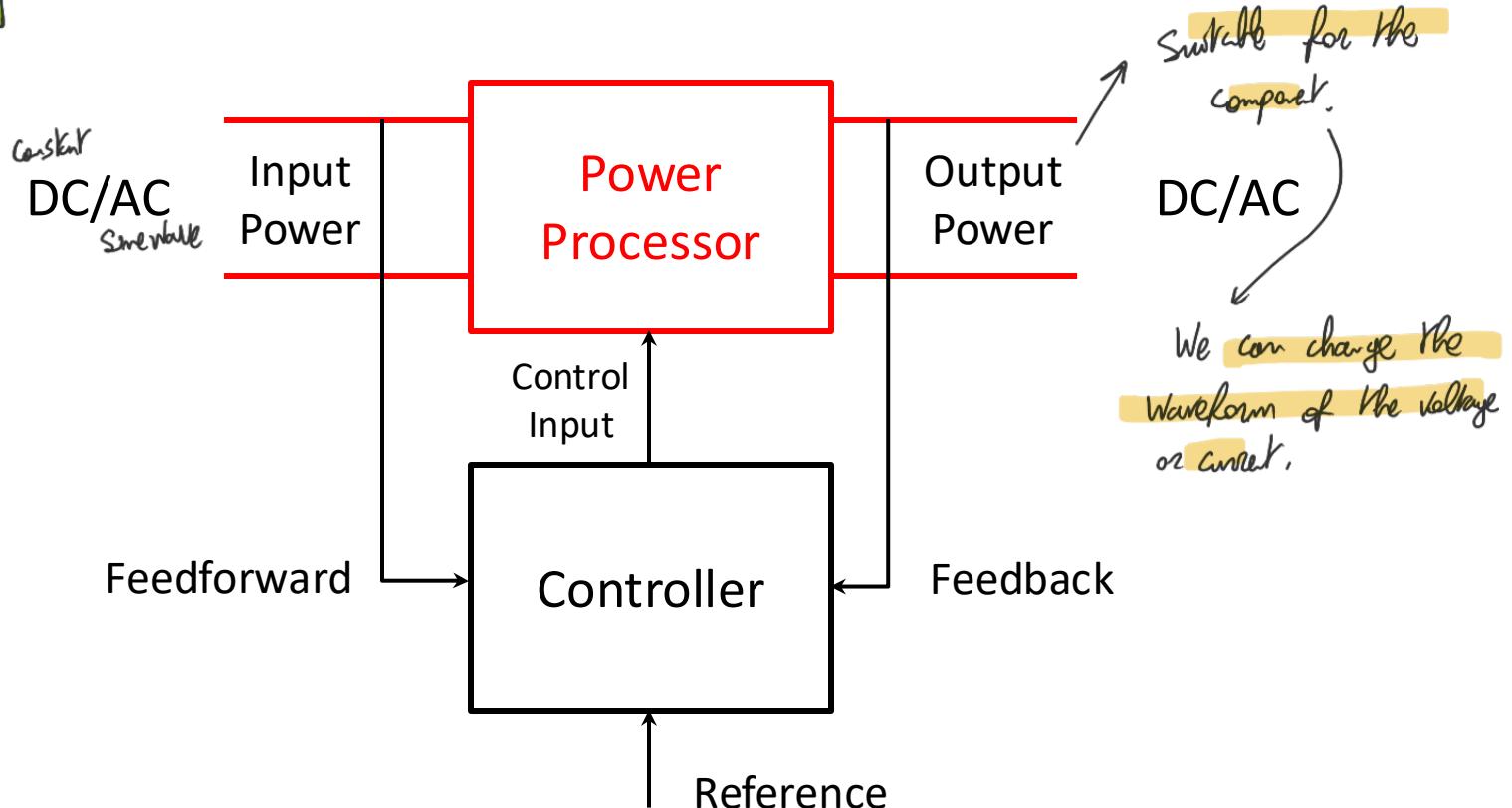
Lithium cell provides $\approx 3.7V$ when fully charged, $\approx 3V$ when out of charge.

In general, for our pieces we need a stable supply voltage and each needs
their own supply voltage.

Power conversion is important: example, a PC charger in Europe takes 130V, 50 Hz voltage as input
and converts that to the right power supply needed for the PC.

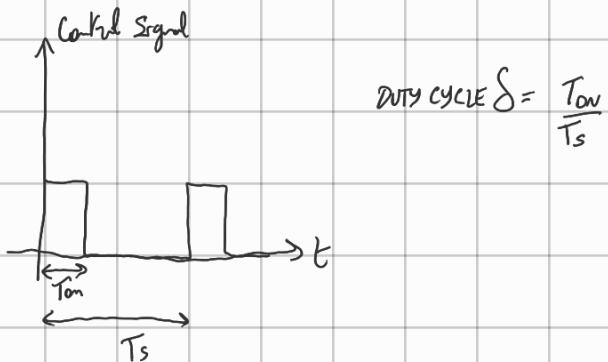
Power Converter

brat



The power processor treats power and needs to be controlled. It is usually done with switches. The control input is a rectangle wave with a duty cycle. Controller measures the output, knows what we want (Reference) and operate its control (Feedback). Sometimes we can also do the opposite. Feedback often better performance.

Control input is a network of switches (not so many). The control input decides if the switch is open or closed, and like input is a rectangular wave. The control is encoded in the duty cycle, which is the ratio between the time a signal is high and the overall signal period.

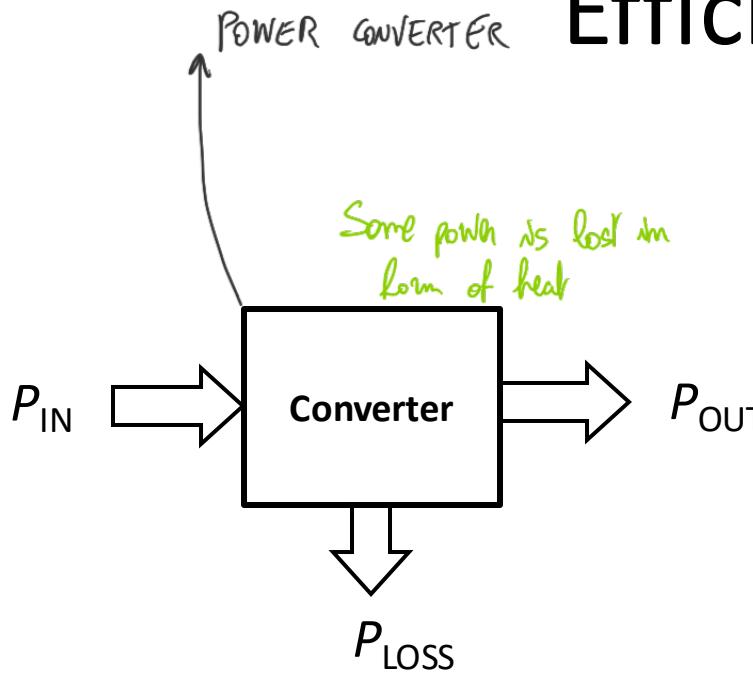


Classificazione convertitori di potenza

Typology	Input	Output	Characteristics
DC-DC	DC	DC	<ul style="list-style-type: none">Different voltageRegulationGalvanic insulation \Rightarrow No electrical connection between input and output.
Inverter <i>Solar power</i>	DC	AC	
Rectifier <i>(a diode can be one)</i>	AC	DC	
Cycloconverter <i>Wind power</i>	AC	AC	<ul style="list-style-type: none">Different frequency and amplitude

because frequency depends on the wind

Efficiency



$$\eta = \frac{P_{\text{OUT}}}{P_{\text{IN}}}$$

$$P_{\text{LOSS}} = P_{\text{IN}} - P_{\text{OUT}} = P_{\text{OUT}} \left(\frac{1}{\eta} - 1 \right)$$

- Why we aim at having efficiency as close as possible to 1?
 1. Simplification of the cooling system in terms of size and weight and costs
 2. Higher reliability
 3. Energy saving

↓
Our devices heats up and have a max operating temperature. We need to remove the heat.

Let's say we have $0.98 = \eta$ and maybe we could improve to 0.99. How much we are losing?

$$P_{\text{LOSS}} = P_{\text{IN}} - P_{\text{OUT}} = P_{\text{OUT}} \left(\frac{1}{\eta} - 1 \right), \text{ maybe } P_{\text{OUT}} = 100W$$

$$P_{\text{LOSS}} = P_{\text{OUT}} \left(\frac{1-\eta}{\eta} \right) \approx P_{\text{OUT}} (1-\eta) = 2W \text{ if } 0.98 \\ 1W \text{ if } 0.99$$

Reducing to almost a half the loss = reducing the area of the heat sink (of a computer for example).
 ↓ But if we keep the same size of our component we have lower temperatures ⇒ device works at colder temperatures, better.

INFORMATION CODING RECAP

Info is stored in bits. With N bits, the array is: $(a_{N-1} a_{N-2} \dots a_1 a_0)_2 = \sum_{n=0}^{N-1} a_n \cdot 2^n = A \in [0, 2^N - 1]$

We can say info is encoded in natural numbers (ex. ASCII)

- How to represent integers? 2's complement.

$b \in [-2^{N-1}, 2^{N-1}]$ is the range of integer numbers we have with N bits.

$B = \begin{cases} b, & b \geq 0 \\ b + 2^N, & b < 0 \end{cases}$ If number is positive, we just need to represent how we would do normally

Also written as $|b|_2$

- If we have the strings of bits and want to write it as easy: If $b \geq 0$, the most significant bit is 0, otherwise 1. The rule is:

$$b = -2^{N-1}b_{N-1} + 2^{N-2}b_{N-2} + \dots + 2^0b_0 \quad (\text{if } b_{N-1} = 1, b = B - 2^N = -2^{N-1}b_{N-1} - \dots)$$

$$S = |a+b|_2^n = |a|_2^n + |b|_2^n |_2^n = |A+B|_2^n$$

\uparrow Representation

There is overflow if the carry is different as the most sig. bit.