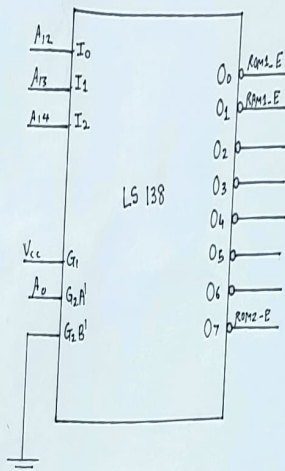
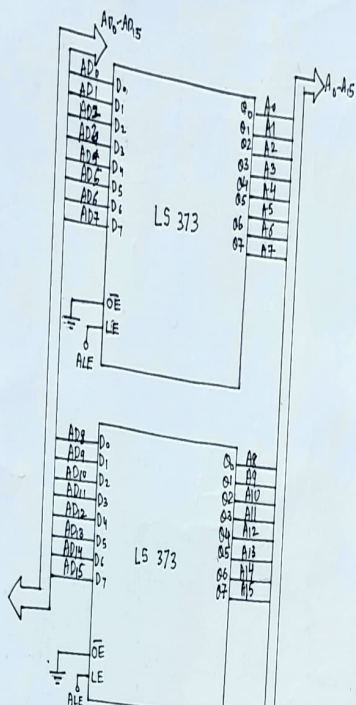


$CLK \Rightarrow 5MHz$   
 $PCLK \Rightarrow 2.5MHz$



Logic Circuit

